

PATENT ASSIGNMENT

Electronic Version v1.1
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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE BY SECURED PARTY
CONVEYING PARTY DATA	
Name	Execution Date
LINCOLN VENTURES, LLC	01/24/2012
RECEIVING PARTY DATA	
Name:	Patriot Scientific Corporation
Street Address:	701 Palomar Airport Road
Internal Address:	Suite 170
City:	Carlsbad
State/Country:	CALIFORNIA
Postal Code:	92011
PROPERTY NUMBERS Total: 6	
Property Type	Number
Patent Number:	5809336
Patent Number:	5784584
Patent Number:	5659703
Patent Number:	5604915
Patent Number:	5530890
Patent Number:	5440749
CORRESPONDENCE DATA	
Fax Number:	(714)427-7799
Phone:	714-427-7482
Email:	cneu@swlaw.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
Correspondent Name:	Erik C. Squier, Snell & Wilmer L.L.P.
Address Line 1:	600 Anton Boulevard
Address Line 2:	Suite 1400
Address Line 4:	Costa Mesa, CALIFORNIA 92626

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ATTORNEY DOCKET NUMBER:	50477-0001
NAME OF SUBMITTER:	Erik C. Squier
Total Attachments: 2 source=LINCOLN VENTURES RELEASE OF SECURITY INTEREST#page1.tif source=LINCOLN VENTURES RELEASE OF SECURITY INTEREST#page2.tif	

RELEASE OF SECURITY INTEREST IN INTELLECTUAL PROPERTY

THIS RELEASE OF SECURITY INTEREST IN INTELLECTUAL PROPERTY (the "Release") is made as of this 24th day of January, 2012, between PATRIOT SCIENTIFIC CORPORATION (the "Debtor") and LINCOLN VENTURES, LLC (the "Secured Party").

WHEREAS, the Debtor and the Secured Party have entered into certain Convertible Debenture, executed as of April 23, 2002 (the "Convertible Debenture 1") and certain Convertible Debenture, executed as of June 10, 2002 (the "Convertible Debenture 2"), collectively, the "Agreements" pursuant to which the Debtor granted to the Secured Party a security interest in the Debtor's Patents (as defined in the Agreements) (collectively hereinafter the "Intellectual Property").

WHEREAS, on May 23, 2002, the Convertible Debenture 1 was recorded with the United States Patent and Trademark Office (the "USPTO") and on August 5, 2002, the Convertible Debenture 2 was recorded with the USPTO, in order to give notice that the Intellectual Property had been pledged under the Agreements.

WHEREAS, the Debtor subsequently paid in full all indebtedness (as defined in the Agreements) and now desires to record this Release with the USPTO in order to release any and all right, title and interest in and to the Intellectual Property that the Secured Party may have, and the Secured Party wishes to record this Release.

NOW, THEREFORE, in consideration of the foregoing and for other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, intending to be legally bound, the Secured Party does hereby irrevocably and unconditionally release any and all right, title or interest of any kind that exists today and may exist in the future in and to the Intellectual Property. This release shall be governed by and construed in accordance with the laws of the State of California, without regard to its conflicts of law doctrine.

Additionally, the Secured Party hereby authorizes the Debtor or its authorized representative to record this Release with the USPTO in order to memorialize the release of any security interest of the Secured Party in the Intellectual Property.

IN WITNESS WHEREOF, the Secured Party has caused this Release to be executed by its duly authorized representative as of the date set forth above.

"SECURED PARTY"
LINCOLN VENTURES, LLC

By: 

Name: Roy A. Adams

Title: Trustee

EXHIBIT A - PATENTS

1. United States Patent No. 5809336, issued on September 15, 1998, entitled "High Performance Microprocessor Having Variable Speed System Clock."
2. United States Patent No. 5784584, issued on July 21, 1998, entitled "High Performance Microprocessor Using Instructions That Operate Within Instruction Groups."
3. United States Patent No. 5659703, issued on August 19, 1997, entitled "Microprocessor System With Hierarchical Stack And Method of Operation."
4. United States Patent No. 5604915, issued on February 18, 1997, entitled "Data Processing System Having Load Dependent Bus Timing."
5. United States Patent No. 5530890, issued on June 25, 1996, entitled "High Performance, Low Cost Microprocessor."
6. United States Patent No. 5440749, issued on August 8, 1995, entitled "High Performance, Low Cost Microprocessor With Combination Stack And Register Architecture."

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