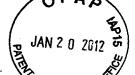
Form **PTO-1595** (Rev. 03-09) OMB No. 0651-0027 (exp. 03/31/2009)

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ULS_DEPARTMENT OF COMMERCE 01/24/2012



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	Internal Address:
Additional name(s) of conveying party(ies) attached? Yes XNo	
3. Nature of conveyance/Execution Date(s): Execution Date(s) November 15, 2011	Street Address: 1753, Shimonumabe, Nakahara-ku
Assignment	
☐ Security Agreement ☐ Change of Name	City: _Kawasaki-shi
☐ Joint Research Agreement	State: Kanagawa
Government Interest Assignment	
Executive Order 9424, Confirmatory License	Country: Japan Zip:
Other	Additional name(s) & address(es) attached? Yes X No
A. Patent Application No.(s)	s document is being filed together with a new application. B. Patent No.(s)
13/355,061	
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McGinn Intellectual Property Law Group, PLLC	☒ Authorized to be charged to deposit account
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State: Virginia Zip: 22182-3817	
Phone Number: (703) 761-4100	-]
Fax Number: (703) 761-2375 or 76	Deposit Account Number 50-0481
Email Address: ADMIN@MCGINNIPLAW.COM	Authorized User Name Sean M. McGinn
9. Signature:	January 20, 2012
Signature	01/24/2012 LMARTIN 000@319 500481 1335 00
	81/C4/C016 CHRKITH SECONDE SOS
Sean M. McGinn, Esq. Name of Person Signing	Fotal number of pages including cover sheet, attachments, and documents:

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PATENT

REEL: 027713 FRAME: 0469

ASSIGNMENT (譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Renesas Electronics Corporation,

a corporation organized under the laws of Japan,

located at 1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa, Japan,

receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Electronics Corporation, its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR INTEGRATED CIRCUIT MOUNTABLE ON RECORDING DEVICE AND METHOD OF OPERATING THE SAME

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted,

to be held and enjoyed by said Renesas Electronics Corporation,

its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Electronics Corporation,

Signed on the date(s) indicated aside our signatures:

INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
1) Yutaka Tunalashi Yutaka FUNABASHI	November 15 2011
2)	
3)	
4)	
5)	·
6)	
7)	<u> </u>
8>	
9)	
10)	

PATENT REEL: 027713 FRAME: 0470

RECORDED: 01/20/2012