

## PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT								
NATURE OF CONVEYANCE:	ASSIGNMENT								
CONVEYING PARTY DATA									
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Nedeljko Varnica</td> <td>01/20/2012</td> </tr> <tr> <td>Xueshi Yang</td> <td>01/19/2012</td> </tr> <tr> <td>Shashi Kiran Chilappagari</td> <td>01/23/2012</td> </tr> </tbody> </table>		Name	Execution Date	Nedeljko Varnica	01/20/2012	Xueshi Yang	01/19/2012	Shashi Kiran Chilappagari	01/23/2012
Name	Execution Date								
Nedeljko Varnica	01/20/2012								
Xueshi Yang	01/19/2012								
Shashi Kiran Chilappagari	01/23/2012								
RECEIVING PARTY DATA									
Name:	Marvell Semiconductor, Inc.								
Street Address:	5488 Marvell Lane								
City:	Santa Clara								
State/Country:	CALIFORNIA								
Postal Code:	95054								
PROPERTY NUMBERS Total: 1									
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>13359705</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	13359705				
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Application Number:	13359705								
CORRESPONDENCE DATA									
Fax Number:	(408)222-2755								
Phone:	408-222-2500								
Email:	kimberly@marvell.com								
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>									
Correspondent Name:	Horace Ng								
Address Line 1:	5488 Marvell Lane								
Address Line 4:	Santa Clara, CALIFORNIA 95054								
ATTORNEY DOCKET NUMBER:	MP4026								
NAME OF SUBMITTER:	Horace Ng								
Total Attachments: 4 source=MP4026_Assgn INV-MSI (executed)#page1.tif source=MP4026_Assgn INV-MSI (executed)#page2.tif source=MP4026_Assgn INV-MSI (executed)#page3.tif source=MP4026_Assgn INV-MSI (executed)#page4.tif									

OP \$40.00 13359705

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, I, the undersigned,

**Nedeljko Varnica; Xueshi Yang; and Shashi Kiran Chilappagari**

who have created a certain invention for which an application for United States Letters Patent was executed by me concurrently herewith and entitled:

**Methods and Systems for Efficient Decoding of Concatenated Error Correction Codes**

Do hereby sell, assign and transfer to Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, CA 95054, its successors, assigns, and legal representatives, the full and exclusive right to said invention and said application and to any and all inventions described in said application for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be granted therefor in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Do hereby further sell, assign and transfer to Marvell Semiconductor, Inc., its successors, assigns, and legal representatives, the full and exclusive right to United States Provisional Patent Applications(s) No(s). 61/437,954, filed January 31, 2011, entitled "Power And Throughput Efficient Decoding In Systems With Concatenated Error Correction Codes" and to any and all inventions described in said provisional patent application(s) for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be entitled to the benefit thereof in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Agree that Marvell Semiconductor, Inc., hereinafter referred to as Assignee, may apply for and receive Letters Patent for said invention and said inventions, hereinafter referred to as said invention, in its own name, in the United States, its territorial possessions, and all foreign countries; and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of said Assignee, its successors, assigns and legal representatives, the undersigned will execute all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, execute all rightful oaths, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to said Assignee, its successors, assigns or legal representatives all facts known to the undersigned relating to said invention and the history thereof; and generally do everything possible which said Assignee, its successors, assigns, or legal representatives shall consider desirable for aiding in securing, maintaining and enforcing proper patent protection for said invention and for vesting title to said invention and all applications for patents on said invention in said Assignee, its successors, assigns, or legal representatives; and

Covenant with said Assignee, its successors, assigns, or legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

IN TESTIMONY WHEREOF I have hereunto set MY signature on the date indicated below.

Full Name of Sole/First Inventor:	
Nedeljko Varnica;	
Inventor's Signature:	Date: Month/Day/Year
	01/20/2012

Docket#: MP4026

PATENT

Full Name of Second Inventor, if any: Xueshi Yang	
Inventor's Signature:	Date: Month/Day/Year

Full Name of Third Inventor, if any: Shashi Kiran Chilappagari	
Inventor's Signature: <i>C. Shashi Kiran</i>	Date: Month/Day/Year <i>01/23/2012</i>

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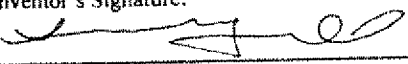
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Full Name of Third Inventor, if any:	
Shashi Kiran Chilappagari	
Inventor's Signature:	Date: Month/Day/Year