

PATENT ASSIGNMENT

Electronic Version v1.1
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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Packard Bell NEC, Inc.	02/23/2000
RECEIVING PARTY DATA	
Name:	NEC Corporation
Street Address:	7-1, Shiba 5-Chome
Internal Address:	Minato-ku
City:	Tokyo
State/Country:	JAPAN
Postal Code:	108-8001
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7120433
CORRESPONDENCE DATA	
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Address Line 4:	Washington, DISTRICT OF COLUMBIA 20007-5118
ATTORNEY DOCKET NUMBER:	211465-00001
NAME OF SUBMITTER:	John S. Paniaguas
Total Attachments: 21 source=Assignment#page1.tif	

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OVER SHEET

U.S. DEPARTMENT OF COMMERCE

Patent and Trademark Office

7-31-00

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Packard Bell NEC, Inc.

Additional name(s) of conveying party(ies) attached? _ Yes X No

2. Name and address of receiving party(ies)

Name: NEC Corporation

Internal Address: _____

Street Address: 7-1, Shiba 5-ChomeMinato-ku 108-8001City: Tokyo Country: JapanAdditional name(s) & address(es) attached? _ Yes X No

3. Nature of conveyance:

X Assignment

___ Merger

___ Security Agreement

___ Change of Name

___ Other _____

Execution Date: February 23, 2000Application number(s) or patent number(s): SEE EXHIBIT A and B

If this document is being filed together with a new application, the execution date of the application is: _____

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? x Yes (Exhibit A and B attached) ___ No

4390292

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: JOHN S. PANIAGUASInternal Address: KATTEN MUCHIN ZAVISStreet Address: 525 W. Monroe Street, Suite 1600City: Chicago State: IL ZIP: 60661-36936. Total number of applications and patents involved: 1657. Total Fee (37 CFR 3.41) \$ 6600.00X EnclosedX Authorized to be charged to deposit account (only if check is missing or improper)

8. Deposit Account No.

50-1214

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9. Statement and signature

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

John S. Paniaguas
Name of Person Signing

Signature

Date

7/27/00

Total number of pages including cover sheet, attachments, and document: 21

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PATENT
REEL: 011007 FRAME: 0153PATENT
REEL: 027728 FRAME: 0234

EXHIBIT A

Patent Number	Issue Date	Title	Counsel
RE35480	03/18/97	Hardware Based Interface for Mode Switching to Access Memory Above One Megabyte DeLisle; David J. (Spring, TX); Fakhruddin; Salfuddin (Tomball, TX); Gauthier; Lloyd (Austin, TX); Kohtz; Robert A. (Cedar Rapids, IA) 317757 October 4, 1994	FETF

PATENT
REEL: 011007 FRAME: 0154

PATENT
REEL: 027728 FRAME: 0235

4390292	06/28/83	Means and Method for Compensating for Print Medium Thickness in Line Printers Krenz; Horst M. (St. Joseph, MI) 317810 November 3, 1981	FETF
4395151	07/26/83	Print Head Drive Belt Tensioning Means and Method for a Line Printer Krenz; Horst M. (St. Joseph, MI) 317663 November 2, 1981	FETF
4408172	10/04/83	Variable Speed Tuning Selector Perdue; Terry A. (St. Joseph, MI) 309666 October 8, 1981	FETF
4414298	11/08/83	Printed Circuit Board Mount for Batteries and the Like Krenz; Horst M. (St. Joseph, MI) 396927 July 9, 1982	FETF
4422700	12/27/83	Grounded Multi-Pin Connector for Shielded Flat Cable Krenz; Horst M. (St. Joseph, MI) 337360 January 6, 1982	FETF
4423975	01/03/84	Form Trimming Apparatus and Method for Line Printer Krenz; Horst M. (St. Joseph, MI) 387092 June 10, 1982	FETF
4430012	02/07/84	Paper Guide for Line Printer Kooy; Wayne J. (Galein, MI); Krenz; Horst M. (St. Joseph, MI); Unger; Lawrence C. (Benton Harbor, MI) 471767 March 3, 1983	FETF
4490741	12/25/84	Synchronization Signal Stabilization for Video Image Overlay Hornback; Donald L. (St. Joseph, MI) 433343 October 8, 1982	FETF
4501458	02/26/85	Locking Hinge for a Printed Circuit Board Cubbage; William R. (Coloma, MI) 537779 September 29, 1983	FETF
4511965	04/16/85	Video RAM Accessing System Rajaram; Babu (St. Joseph, MI) 446296 March 21, 1983	FETF
4527766	07/09/85	Video Display Tilt Apparatus Krenz; Horst M. (St. Joseph, MI) 500125 June 1, 1983	FETF
4538875	09/03/85	Strain Relief Clip for Electrical Cable Krenz; Horst M. (St. Joseph, MI) 630150 July 12, 1984	FETF

PATENT

REEL: 011007 FRAME: 0155

PATENT

REEL: 027728 FRAME: 0236

4542326	09/17/85	Automatic Antenna Positioning System Hornback; Donald L. (St. Joseph, MI) 433568 October 8, 1982	FETF
4546348	10/08/85	Dual Channel Curve Tracer Ashton; Robert H. (Watervliet, MI) 498608 May 27, 1983	FETF
4574277	03/04/86	Selective Page Disable for a Video Display Krause; Charles A. (Stevensville, MI); Rajaram; Babu (St. Joseph, MI) 527944 August 30, 1983	FETF
4594587	06/10/86	Character Oriented RAM Mapping System and Method Therefor Chandler; Gregg (St. Joseph, MI); Rajaram; Babu (St. Joseph, MI) 527945 August 30, 1983	FETF
4600357	07/15/86	Gripper Force Sensor/Controller for Robotic Arm Coules; Russell G. (St. Joseph, MI) 582200 February 21, 1984	FETF
4611343	09/09/86	Audio Spectrum Analyzer Operable in a Difference Mode Rapaich; Mark (Stevensville, MI) 625940 June 29, 1984	FETF
4612632	09/16/86	Power Transition Write Protection for PROM Olson; Anthony M. (Stevensville, MI) 679784 December 10, 1984	FETF
4617554	10/14/86	Keyboard Scanning Krause; Charles A. (Stevensville, MI); Vander Heyden; Randy J. (Garland, TX) 527131 August 26, 1983	FETF
4620186	10/28/86	Multi-Bit Write Feature for Video RAM Krause; Charles A. (Stevensville, MI); Rajaram; Babu (St. Joseph, MI) 527943 August 30, 1983	FETF
4620285	10/28/86	Sonar Ranging/Light Detection System for Use in a Robot Perdue; Terry A. (St. Joseph, MI) 603471 April 24, 1984	FETF
4631749	12/23/86	ROM Compensated Microphone Rapaich; Mark (Stevensville, MI) 623647 June 22, 1984	FETF
4669053	05/26/87	Portable Personal Computer Krenz; Horst M. (St. Joseph, MI) 615987 May 31, 1984	FETF

PATENT
REEL: 011007 FRAME: 0156

PATENT
REEL: 027728 FRAME: 0237

4679152	07/07/87	Navigation System and Method for a Mobile Robot Perdue; Terry A. (St. Joseph, MI) 703354 February 20, 1985	FETF
4703419	10/27/87	Switchcover Means and Method for Dual Mode Microprocessor System Krause; Charles A. (Stevensville, MI); Rajaram; Babu (St. Joseph, MI); Watzman; Barry A. (Benton Harbor, MI) 886956 July 24, 1986	FETF
4704604	11/03/87	Pivoting Mount for Detachable Keyboard Fuhs; Eric D. (Stevensville, MI) 675488 November 28, 1984	FETF
4714919	12/22/87	Video Display with Improved Smooth Scrolling Foster; Mark J. (Stevensville, MI) 635385 July 30, 1984	FETF
4744046	05/10/88	Video Display Terminal with Paging and Scrolling Foster; Mark J. (Stevensville, MI) 667790 November 2, 1984	FETF
4792929	12/20/88	Data Processing System with Extended Memory Access Olson; Anthony M. (Stevensville, MI); Rajaram; Babu (St. Joseph, MI) 029299 March 23, 1987	FETF
4821229	04/11/89	Dual Operating Speed Switchover Arrangement for CPU Jauregui; Luis H. (St. Joseph, MI) 808394 December 12, 1985	FETF
4847758	07/11/89	Main Memory Access in a Microprocessor System With a Cache Memory Olson; Anthony M. (Stevensville, MI); Robinson; Thomas N. (St. Joseph, MI); Rajaram; Babu (St. Joseph, MI) 115278 October 30, 1987	FETF
4901261	02/13/90	Retractable Handle and Latch for Portable Computers Fuhs; Eric D. (Stevensville, MI) 265110 October 31, 1988	FETF
4903221	02/20/90	Keyboard Latching Arrangement for Portable Computers Krenz; Horst M. (Bridgman, MI) 265078 October 31, 1988	FETF
4933910	06/12/90	Method for Improving the Page Hit Ratio of a Page Mode Main Memory System Olson; Anthony M. (Stevensville, MI); Rajaram; Babu (St. Joseph, MI); Robinson; Thomas N. (St. Joseph, MI) 215652 July 6, 1988	FETF
4949934	08/21/90	Computer Stand Krenz; Horst M. (Bridgman, MI); Wahlemeier; Fred E. (San Antonio, TX) 333373 April 5, 1989	FETF

PATENT
REEL: 011007 FRAME: 0157

PATENT
REEL: 027728 FRAME: 0238

4980848	12/25/90	Heat Exchange Display Panel for Portable Computer Griffin; Wayne L. (St. Joseph, MI); Fuhs; Eric D. (Stevensville, MI); Kohtz; Robert A. (St. Joseph, MI); Ojeda; Peter A. (St. Joseph, MI) 265180 October 31, 1988	FETF
4982303	01/01/91	Removable Hard Disk Drive Having a Combination Latch, Lever and Handle Krenz; Horst M. (Bridgman, MI) 265077 October 31, 1988	FETF
4984209	01/08/91	Burst Refresh of Dynamic Random Access Memory for Personal Computers Rajaram; Babu (St. Joseph, MI); Foster; Mark J. (Stevensville, MI) 114770 October 30, 1987	FETF
5010426	04/23/91	Installation Mechanism for Removable Computer Drive Module Krenz; Horst M. (Bridgman, MI) 306613 February 3, 1989	FETF
5027294	06/25/91	Method and Apparatus for Battery-Power Management using Load-Compensating Monitoring of Battery Discharge Fakruddin; Saifee (St. Joseph, MI); Foster; Mark J. (Stevensville, MI) 303604 January 27, 1989	FETF
5042003	08/20/91	Memory Usage System Belt; Steve L. (St. Joseph, MI); Kohtz; Robert A. (St. Joseph, MI) 215651 July 6, 1988	FETF
5045960	09/03/91	Self-Aligning Guide and Track for Removable Disk Drive Module Eding; Lon (Zeeland, MI) 365322 June 13, 1989	FETF
5064378	11/12/91	Mounting of DRAMs of Different Sizes and Pinouts Within Limited Footprint Olson; Anthony M. (Stevensville, MI); Rajaram; Babu (St. Joseph, MI) 294925 December 30, 1988	FETF
5072450	12/10/91	Method and Apparatus for Error Detection and Localization Helm; Gordon L. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); Olson; Anthony M. (Stevensville, MI) 386340 July 27, 1989	FETF
5123092	06/16/92	External Expansion Bus Interface Buxton; Clark (St. Joseph, MI); Fakruddin; Saifee (St. Joseph, MI); Foster; Mark (Stevensville, MI); Kohtz; Robert (St. Joseph, MI); Schindler; Jeff (St. Joseph, MI) 260964 October 21, 1988	FETF
5129069	07/07/92	Method and Apparatus for Automatic Memory Configuration by a Computer Helm; Gordon L. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); McAuliffe; William J. (Clay Township, St. Joseph County, IN); Olson; Anthony M. (Stevensville, MI); Witkowski; Todd R. (Stevensville, MI) 301607 January 24, 1989	FETF

PATENT

REEL: 011007 FRAME: 0158

PATENT

REEL: 027728 FRAME: 0239

5136694	08/04/92	Method and Apparatus Facilitating Communication Between Two Keyboards and a Single Processor Belt; Steven L. (Stevensville, MI); Ruthenbeck; Mark A. (Lincoln Township, Berrien County, MI); Foster; Mark J. (Lincoln Township, Berrien County, MI); Barnes; Brian C. (Benton Township, Berrien County, MI); VanderHeyden; Randy J. (St. Joseph Township, Berrien County, MI) 334841 April 7, 1989	FETF
5146578	09/08/92	Method of Varying the Amount of Data Prefetched to a Cache Memory in Dependence on the History of Data Requests Zangenehpour; Saied (Stevensville, MI) 685157 April 12, 1991	FETF
5157776	10/20/92	High Speed Memory for Microprocessor Systems Foster; Mark J. (Stevensville, MI) 426489 October 23, 1989	FETF
5184117	02/02/93	Fluorescent Backlight Flicker Control in an LCD Display Gauthier; Lloyd W. (St. Joseph, MI) 373017 June 28, 1989	FETF
5214364	05/25/93	Microprocessor-Based Antenna Rotor Controller Perdue; Terry A. (St. Joseph, MI); Poplewski; David (Dowagiac, MI) 703464 May 21, 1991	FETF
5224217	06/29/93	Computer System which uses a Least-Recently-Used Algorithm for Manipulating Data Tags when Performing Cache Replacement Zangenehpour; Saied (4756 Notredame, Stevensville, MI 49127) 924391 August 3, 1992	FETF
5253350	10/12/93	Method of Combining Lower Order and Translated Upper Order Bits to Address ROM within a Range Reserved for Other Devices Foster; Mark J. (Stevensville, MI); Rajaram; Babu (St. Joseph, MI); Olson; Anthony M. (Stevensville, MI) 555778 July 19, 1990	FETF
5253352	10/12/93	Method & Apparatus for Pipelining Cache Accesses Using Anticipatory Initiation of Cache Read Olson; Anthony M. (Stevensville, MI) 435880 November 13, 1989	FETF
5257143	10/26/93	Method and Apparatus for Positioning Head of Disk Drive Using Zone-Bit-Recording Zangenehpour; Saied (Lincoln Township, Berrien County, MI) 641264 January 15, 1991	FETF
5261083	11/09/93	Floppy Disk Controller Interface For Suppressing False Verify Cycle Errors Witkowski; Todd R. (Bainbridge Township, MI); Olson; Anthony M. (Stevensville, MI); Robinson; Thomas N. (Lincoln Township, MI); Smith; Jimmy D. (Lincoln Township, MI) 692371 April 26, 1991	FETF

PATENT
REEL: 011007 FRAME: 0159

PATENT
REEL: 027728 FRAME: 0240

5280621	01/18/94	Personal Computer Having Dedicated Processors for Peripheral Devices Interconnected to the CPU by Way of a System Control Processor Barnes; Brian C. (Benton Harbor, MI); Foster; Mark J. (Stevensville, MI); Gauthier; Lloyd W. (St. Joseph, MI); Fakhruddin; Saifee (St. Joseph, MI); DeLisle; David J. (Berrien Springs, MI); Veit; David R. (Granger, IN) 019997 February 17, 1993	FETF
5283889	02/01/94	Hardware Based Interface for Mode Switching To Access Memory above One Megabyte DeLisle; David J. (Berrien Springs, MI); Fakhruddin; Saifee (St. Joseph, MI); Gauthier; Lloyd W. (St. Joseph, MI); Kohtz; Robert A. (St. Joseph, MI) 031029 March 11, 1993	
5291588	03/01/94	Control System and Method for Multiple Rate Disk Drive Data Transfer with a Single Oscillator Kohtz; Robert A. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI) 455818 December 18, 1989	FETF
5297270	03/22/94	Programmable Cache Memory Which Associates Each Section of Main Memory to be Cached with a Status Bit which Enables/Disables the Caching Accessibility of the Particular Section, and with the Capability of Functioning with Memory Areas of Varying Size Olson; Anthony M. (Stevensville, MI) 017972 February 12, 1993	FETF
5303352	04/12/94	Dual Connector Port For Bus Master Card Robinson; Thomas N. (Lincoln Township, Berrien County, MI); Nicol; Mark D. (Stevensville, MI); Olson; Anthony M. (Stevensville, MI); Witkowski; Todd R. (Stevensville, MI) 433057 November 6, 1989	FETF
5317721	05/31/94	Method and Apparatus to Disable ISA Devices for EISA Addresses Outside the ISA Range Robinson; Thomas N. (Stevensville, MI) 432688 November 6, 1989	FETF
5353416	10/04/94	CPU Lock Logic for Corrected Operation of a posted write array Olson; Anthony M. (Stevensville, MI) 999189 December 30, 1992	FETF
5363486	11/08/94	Variable Size Queue Circuit for Buffering Data Transfers from a Processor to a Memory Olson; Anthony M. (Stevensville, MI); Rajaram; Babu (St. Joseph, MI) 091462 July 13, 1993	FETF
5446403	08/29/95	Power On Reset Signal Circuit With Clock Inhibit And Delayed Reset Witkowski; Todd R. (Benton Harbor, MI) 192178 February 4, 1994	FETF

PATENT
REEL: 011007 FRAME: 0160

PATENT
REEL: 027728 FRAME: 0241

5460547	10/24/95	Port Replicator Belt; Steven (St. Joseph, MI); Schindler; Jeffrey (Lindenhurst, IL); Stobert; Norman (St. Joseph, MI) 104950 August 10, 1993	FETF
5498971	03/12/96	Method and Control Circuit for Measuring the Temperature of an Integrated Circuit Turnbull; Robert R. (Buchanan, MI); DeLisle; David J. (Berrien Springs, MI); Kohtz; Robert A. (St. Joseph, MI) 470639 June 6, 1995	FETF
5517648	05/14/96	Symmetric Multiprocessing system with Unified Environment and Distributed System Functions Bertone; James F. (Quincy, MA); DiPlacido, Jr.; Bruno (Dedham, MA); Joyce; Thomas F. (Westford, MA); Massucci; Martin (Burlington, MA); McNally; Lance J. (Townsend, MA); Murray, Jr.; Thomas L. (Hollis, NH); Nibby, Jr.; Chester M. (Beverly, MA); Pence; Michelle A. (Chelmsford, MA); Sanfacon; Marc (North Chelmsford, MA); Shen; Jian-Kuo (Belmont, MA); Somers; Jeffrey S. (Lowell, MA); Steiner; G. Lewis (Milford, MA) 405550 March 16, 1995	FETF
5522069	05/28/96	Symmetric Multiprocessing System With Unified Environment and Distributed System Functions Bertone; James F. (Quincy, MA); DiPlacido, Jr.; Bruno (Dedham, MA); Joyce; Thomas F. (Westford, MA); Massucci; Martin (Burlington, MA); McNally; Lance J. (Townsend, MA); Murray, Jr.; Thomas L. (Hollis, NH); Nibby, Jr.; Chester M. (Beverly, MA); Pence; Michelle A. (Chelmsford, MA); Sanfacon; Marc (North Chelmsford, MA); Shen; Jian-Kuo (Belmont, MA); Somers; Jeffrey S. (Lowell, MA); Steiner; G. Lewis (Milford, MA); Wu; William S. (Cupertino, CA); Rasmussen; Norman J. (Hillsboro, OR); Marisetty; Suresh K. (San Jose, CA); Nizar; Puthiya K. (El Darado Hills, CA) 258750 June 10, 1994	FETF
5541490	07/30/96	Computer Power Supply System Sengupta; Upal (St. Joseph, MI); Turnbull; Robert R. (Buchanan, MI); Shah; Rajesh A. (St. Joseph, MI); Fritz; Brian C. (St. Joseph, MI) 245004 May 18, 1994	FETF
5557782	09/17/96	Flexible Deterministic State Machine Witkowski; Todd R. (Bainbridge Township, MI); Messina; Vincent J. (Pittsfield Township, MI); Ball; Richard D. (Austin, TX); Fillion; John T. (Lawrenceville, GA) 273763 July 12, 1994	FETF
5561361	10/01/96	Computer Power Supply and Battery Recharging System Sengupta; Upal (St. Joseph, MI); Turnbull; Robert R. (Buchanan, MI); Shah; Rajesh A. (St. Joseph, MI); Fritz; Brian C. (St. Joseph, MI) 245684 May 18, 1994	FETF

PATENT
REEL: 011007 FRAME: 0161

PATENT
REEL: 027728 FRAME: 0242

5566383	10/15/96	Drive Platform Assembly with Rotatable Mounting Brackets and Automatic Grounding Bracket Gildea; Larry A. (Kalamazoo, MI); Marwah; Ramesh (St. Joseph, MI) 406777 March 17, 1995	FETF
5569052	10/29/96	Computer for use with a Port Replicator Belt; Steven (St. Joseph, MI); Schindler; Jeffrey (Lindenhurst, IL); Stobert; Norman (St. Joseph, MI) 505042 July 21, 1995	FETF
5574866	11/12/96	Method and Apparatus for Providing a Data Write Signal With a Programmable Duration Smith; Jimmy D. (Stevensville, MI); Nicol; Mark D. (Stevensville, MI); O'Brien; Terence P. (Saint Joseph, MI) 389559 February 15, 1995	FETF
5596480	01/21/97	Split Rotatable Keyboard Manser; Brian E. (Salem, WI); Ojeda, IV; Peter A. (St. Joseph, MI); Poole; Anton R. (St. Joseph, MI); Guoming; Frank W. (Arlington Heights, IL) 218625 March 28, 1994	FETF
5596713	01/21/97	Method to Prevent Data Loss in an Electrically Erasable Read Only Memory DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); DeLisle; David J. (Berrien Springs, MI); Fakhruddin; Saifuddin (St. Joseph, MI); Gauthier; Lloyd W. (Austin, TX); Kohtz; Robert A. (St. Joseph, MI) 478363 June 7, 1995	FETF
5629604	05/13/97	Computer Power Supply System Sengupta; Upal (St. Joseph, MI); Turnbull; Robert R. (Buchanan, MI); Shah; Rajesh A. (St. Joseph, MI); Fritz; Brian C. (St. Joseph, MI) 975879 November 13, 1992	FETF
5634079	05/27/97	System for Providing for a Parallel Port with Standard Signals and a Flash Recovery Mode with Second Predetermined Signals Redefining Parallel Port with Alternate Functions Buxton; Clark L. (St. Joseph, MI) 885805 May 15, 1992	FETF
5636348	06/03/97	Control System for a Multiple Mode Standard Parallel Port for a Personal Computer Buxton; Clark L. (St. Joseph, MI); Kohtz; Robert A. (St. Joseph, MI) 361463 December 21, 1994	FETF
5646817	07/08/97	Adjustable Keyboard Manser; Brian E. (Salem, WI); Ojeda, IV; Peter A. (St. Joseph, MI); Poole; Anton R. (St. Joseph, MI); Wu; Guoming Frank (Arlington Heights, IL) 614481 March 13, 1996	FETF

PATENT
REEL: 011007 FRAME: 0162

PATENT
REEL: 027728 FRAME: 0243

5687379	11/11/97	Method and Apparatus for Preventing Unauthorized Access to Peripheral Devices Smith; Jimmy Dean (Stevensville, MI); Nicol; Mark D. (Stevensville, MI); Straup; Brian K. (South Bend, IN); O'Brien; Terence Paul (Saint Joseph, MI); Krau; Michael P. (Benton Harbor, MI); Ball; Richard David (Saint Joseph, MI) 583277 January 5, 1996	FETF
5687387	11/11/97	Enhanced Active Port Replicator Having Expansion and Upgrade Capabilities Endejan; Edward B. (St. Joseph, MI); Manning; Richard S. (Hartford, MI); Sparling; Larry A. (Grand Haven, MI); Wagner; John P. (Round Rock, TX) 412505 March 24, 1995	FETF
5696987	12/09/97	Hardware Based Interface for Emulation of a Standard System Control Processor DeLisle; David J. (Spring, TX); Fakhruddin; Saifuddin (Tonball, TX); Gauthier; Lloyd (Austin, TX); Kohtz; Robert A. (St. Joseph, MI) 622924 March 27, 1996	FETF
5729431	03/17/98	Heat Sink For A Portable Personal Computer Marwah; Ramesh (San Diego, CA); Zappacosta; Elisa E. (Issaquah, WA) 420377 April 7, 1995	FETF
5742841	04/21/98	Alternate I/O Port Access to Standard Register Set DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); Krau; Michael P. (Benton Harbor, MI) 579037 December 19, 1995	FETF
5747964	05/05/98	Rechargeable Battery and Charging System Turnbull; Robert R. (Buchanan, MI) 785474 January 17, 1997	FETF
5748922	05/05/98	Method and Apparatus for Reading Data From A Write Only Port Smith; Jimmy Dean (Stevensville, MI); Nicol; Mark D. (Stevensville, MI); O'Brien; Terence Paul (Saint Joseph, MI) 387370 February 13, 1995	FETF
5748982	05/05/98	Apparatus for Selecting a User Programmable Address for an I/O Device Smith; Jimmy Dean (Stevensville, MI); Nicol; Mark D. (Stevensville, MI); Straup; Brian K. (South Bend, IN); O'Brien; Terence Paul (Saint Joseph, MI); Herman; Mark Layne (Saint Joseph, MI); Hussey; Terrence A. (Saint Joseph, MI) 616430 March 15, 1996	FETF
5752063	05/12/98	Write Inhibited Registers DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); DeLisle; David J. (Berrien Springs, MI); Krau; Michael P. (Benton Harbor, MI); Fakhruddin; Saifuddin (St. Joseph, MI); Gauthier; Lloyd W. (Austin, TX); Kohtz; Robert A. (St. Joseph, MI) 575004 December 19, 1995	FETF

PATENT
REEL: 011007 FRAME: 0163

PATENT
REEL: 027728 FRAME: 0244

5764995 06/09/98

Write Once Read Only Registers

FETF

DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); Krau;

Michael P. (Benton Harbor, MI)

680099

July 12, 1996

PATENT

REEL: 011007 FRAME: 0164

PATENT

REEL: 027728 FRAME: 0245

5784642	07/21/98	System for Establishing a Transfer Mode Between System Controller and Peripheral Device Smith; Jimmy Dean (Stevensville, MI); Nicol; Mark D. (Stevensville, MI); Straup; Brian K. (South Bend, IN); O'Brien; Terence Paul (Saint Joseph, MI); Herman; Mark Layne (Saint Joseph, MI) 484446 June 7, 1995	FETF
5786984	06/28/98	Modular Portable Personal Computer Bonardi; Timothy A. (Buchanan, MI); Fuhs; Eric D. (Stevensville, MI); Ojeda; Peter A. (Mundelein, IL); Griffin; Wayne L. (St. Joseph, MI); Hallowell; William C. (Spring, TX); Wagner; John P. (Round Rock, TX); Wang; Bruce (Livonia, MI); Zappacosta; Elisa E. (Issaquah, WA) 415229 March 24, 1995	FETF
5793606	08/11/98	Removable LCD and Stand Assembly Cabbage; William R. (Coloma, MI); Fuhs; Eric D. (Stevensville, MI); Ojeda; Peter A. (Mundelein, IL); Plangger; David L. (Stevensville, MI); Wagner; John P. (Round Rock, TX) 410634 March 24, 1995	FETF
5802376	09/01/98	Write Once Read Only Registers DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); Krau; Michael P. (Benton Harbor, MI) 874446 June 16, 1997	FETF
5809290	09/15/98	Programmable Hardware Counter DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); Krau; Michael P. (Benton Harbor, MI) 847399 April 24, 1997	FETF
5809340	09/15/98	Adaptively Generating Timing Signals for Access to Various memory Devices Based on Stored Profiles Bertone; James F. (Quincy, MA); DiPlacido, Jr.; Bruno (Dedham, MA); Joyce; Thomas F. (Westford, MA); Massucci; Martin (Burlington, MA); McNally; Lance J. (Townsend, MA); Murray, Jr.; Thomas L. (Hollis, NH); Nibby, Jr.; Chester M. (Beverly, MA); Pence; Michelle A. (Chelmsford, MA); Sanfacon; Marc (North Chelmsford, MA); Shen; Jian-Kuo (Belmont, MA); Somers; Jeffrey S. (Lowell, MA); Steiner; G. Lewis (Milford, MA); Wu; William S. (Cupertino, CA); Rasmussen; Norman J. (Hillsboro, OR); Marisetty; Suresh K. (San Jose, CA); Nizar; Puthiya K. (El Darado Hills, CA) 909745 August 12, 1997	FETF
5821733	10/13/98	Multiple Cell and Serially Connected Rechargeable Batteries and Charging System Turnbull; Robert R. (Buchanan, MI) 767170 December 16, 1996	FETF

PATENT
REEL: 011007 FRAME: 0165

PATENT
REEL: 027728 FRAME: 0246

5822601	10/13/98	Apparatus to Allow A CPU to Control the Relocation of Code Blocks for Other CPUs DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); DeLisle; David J. (Berrien Springs, MI); Fakhruddin; Saifuddin (St. Joseph, MI); Gauthier; Lloyd W. (Austin, TX); Kohtz; Robert A. (St. Joseph, MI) 549304 October 27, 1995	FETF
5838141	11/17/98	Method for Generating a Continuously Variable Reference Signal for Controlling Battery Cell Charging Sengupta; Upal (St. Joseph, MI); Turnbull; Robert R. (Buchanan, MI); Shah; Rajesh A. (St. Joseph, MI); Fritz; Brian Carl (St. Joseph, MI) 856488 May 14, 1997	FETF
5847546	12/08/98	System and Apparatus for Generating a Continuously Variable Reference Signal for Controlling Battery Cell Charging Sengupta; Upal (St. Joseph, MI); Turnbull; Robert R. (Buchanan, MI); Shah; Rajesh A. (St. Joseph, MI); Fritz; Brian Carl (St. Joseph, MI) 858202 May 14, 1997	FETF
5848250	12/08/98	Processor Upgrade System for a Personal Computer Smith; Jimmy D. (Stevensville, MI); Hamersley; Richard A. (St. Joseph, MI); Olson; Anthony M. (Stevensville, MI) 468181 June 6, 1995	FETF
5854736	12/29/98	Peripheral Card Locking Device Fuhs; Eric D. (Stevensville, MI); Ojeda; Peter A. (Mundelein, IL); Zappacosta; Elisa E. (Issaquah, WA) 410633 March 24, 1995	FETF
5864698	01/26/99	Disk Based Bios Krau; Michael Paul (Benton Harbor, MI); Gohr; Donald William (Benton Harbor, MI) 932808 September 15, 1997	FETF
5867106	02/02/99	Password Switch to Override Remote Control Bi; Depeng (Wheeling, IL); Gladwin; S. Christopher (Chicago, IL); Wilson; James Y. (Crystal Lake, IL) 543785 October 16, 1995	FETF
5867655	02/02/99	Method to Store Privileged Data within the Primary CPU Memory Space DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); DeLisle; David J. (Berrien Springs, MI); Krau; Michael P. (Benton Harbor, MI); Fakhruddin; Saifuddin (St. Joseph, MI); Gauthier; Lloyd W. (Austin, TX); Kohtz; Robert A. (St. Joseph, MI) 914733 August 19, 1997	FETF
5870520	02/09/99	Flash Disaster Recovery ROM and Utility to Reprogram Multiple ROMs Lee; Min Eig (Stevensville, MI); Sun; Jiming (St. Joseph, MI) 615637 March 13, 1996	FETF

PATENT

REEL: 011007 FRAME: 0166

PATENT

REEL: 027728 FRAME: 0247

5872967 02/16/99 Method for Warm Boot from Reset FETF
DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); DeLisle;
David J. (Bernien Springs, MI); Krau; Michael P. (Benton Harbor, MI);
Fakhruddin; Saifuddin (St. Joseph, MI); Gauthier; Lloyd W. (Austin, TX);
Kohtz; Robert A. (St. Joseph, MI)
607445
February 27, 1996

PATENT
REEL: 011007 FRAME: 0167

PATENT
REEL: 027728 FRAME: 0248

5883497	03/16/99	Battery Fuel Gauge Turnbull; Robert R. (Buchanan, MI) 956618 September 18, 1997	FETF
5956522	09/21/99	Symmetric Multiprocessing System with Unified Environment and Distributed System Functions Bertone; James F. (Quincy, MA); DiPlacido, Jr.; Bruno (Dedham, MA); Joyce; Thomas F. (Westford, MA); Massucci; Martin (Burlington, MA); McNally; Lance T. (Townsend, MA); Murray, Jr.; Thomas L. (Hollis, NH); Nibby, Jr.; Chester M. (Beverly, MA); Pence; Michelle A. (Chelmsford, MA); Sanfacon; Marc (North Chelmsford, MA); Shen; Jian-Kuo (Belmont, MA); Somers; Jeffrey S. (Lowell, MA); Steiner; G. Lewis (Milford, MA) 405510 March 16, 1995	FETF
5974558	10/26/99	Resume on Pen Contact Cortopassi; Michael (Buffalo Grove, IL); Gladwin; S. Christopher (Chicago, IL); Voegeli; Derick W. (Wheeling, IL) 543510 October 16, 1995	FETF
5987618	11/16/99	Programmable Hardware Timer Including Two Programmable Hardware Downcounters wherein the Second Downcounter Counts in Multiples of the First Downcounter DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); Krau; Michael P. (Benton Harbor, MI) 481850 June 7, 1995	FETF
5990875	11/23/99	Double Pen Up Event Bi; Depeng (Wheeling, IL); George; Jose T. (Bloomington, IL) 543787 October 16, 1995	
5996082	11/30/99	System and Method For Delaying A Wake-Up Signal Cortopassi; Michael (Buffalo Grove, IL) 543697 October 16, 1995	FETF
5999991	12/07/99	Programmably Selectable Addresses for Expansion for a Motherboard Smith; Jimmy Dean (Stevensville, MI); Nicol; Mark D. (Stevensville, MI); Straup; Brian K. (South Bend, IN); O'Brien; Terence Paul (Saint Joseph, MI); Herman; Mark Layne (Saint Joseph, MI) 906398 August 5, 1997	FETF
6005533	12/21/99	Remote Occlusion Region Wilson; James Y. (Crystal Lake, IL) 543701 October 16, 1995	
6009495	12/28/99	Protected Address Range in an Electrically Programmable Read Only Memory DeRoo; David T. (St. Joseph, MI); Nicol; Mark D. (Stevensville, MI); DeLisle; David J. (Berrien Springs, MI); Fakhruddin; Saifuddin (St. Joseph, MI); Gauthier; Lloyd W. (Austin, TX); Kohtz; Robert A. (St. Joseph, MI) 554667 November 8, 1995	FETF

PATENT
REEL: 011007 FRAME: 0168

PATENT
REEL: 027728 FRAME: 0249

6018806	01/25/00	Method and System for Rebooting A Computer Having Corrupted Memory Using An Extended Jumper Cortopassi; Michael (Buffalo Grove, IL); George; Jose T. (Bloomingtondale, IL); Parham; John Allan (Buffalo Grove, IL); Voegeli; Derick W. (Wheeling, IL) 543423 October 16, 1995	FETF
6026454	02/15/00	Dedicated network Expansion Device McCann 08/169792 December 17, 1993	FETF

PATENT
REEL: 011007 FRAME: 0169

PATENT
REEL: 027728 FRAME: 0250

EXHIBIT B

(NOTE: If an application is a "Continued Prosecution Application," the "Filing Date" set forth here is the date upon which the Continued Prosecution Application was filed, rather than the earlier date upon which the "Serial Number" set forth here was assigned to a parent application of the Continued Prosecution Application.)

Serial Number	Filing Date	Title	Country	Counsel	Inventors
08/131,029	10/04/93	Two Stage Cache Memory System and Method	US	FETF	Zangenehpour
08/082,263	06/30/99	Method & Apparatus for Battery Power Management Using Load Compensation Monitoring of Battery Discharge	US	FETF	Fakhruddin
08/258,752	06/10/94	Symmetric Multiprocessing System With Unfinished Environment and Distributed System Functions	US	FETF	BERTONE
08/300,500	09/02/94	Structure and Method For Controlling A Host Computer Using A Remote Hand-Held Interface Device	US	SMMFF	Banerjee Gladwin Maskatia Ojeda
29/028,322	09/12/94	Portable Multimedia Unit For A Portable Personal Computer	US	FETF	Ojeda
08/314,346	09/29/94	Pivoting Mount for Detachable Keyboard	US	FETF	Fuhs
08/393,467	11/03/99	Processor Independent Architecture (Inventor James C. Lin has refused to sign an assignment)	US	FETF	Lin
08/411,379	02/23/98	Apparatus and Method for Automatically Disconnecting Address and Data Buses in a Multimedia System When Docking With A Portable Personal Computer	US	FETF	Carter Cubbage Griffin Niedzielski Sparling Unithoven
08/437,077	05/05/95	Microcomputer System with Color Coded Components	US	WSGR	Friesen, Mark B.
29/039,623	06/01/95	Tower Computer	US	WSGR	Friesen, Mark B. Esslinger, Hartmut
08/480,047	12/16/95	A Multiprocessor System for Enabling Shared Access To A Memory	US	FETF	Ball DeRoo Nicol Smith
8/479,245	06/07/95	Power Management Architecture for a Reconfigurable Write Back Cache (continuation of 08/205,217 filed 03/02/94) (In condition for allowance, issue fee due 4/13/00)	US	SMMFF	Lin, James C. Manser, Bryan L.
08/526,297	09/11/95	An Improved Bus Interface Controller	US	FETF	Bertone
08/543,599	07/07/99	Broadcast Search for Available Host	US	FETF	Bi Gladwin Gladwin
08/543,425	10/16/95	System and Method for Automatically Reconnecting A Wireless Interface Device To A Host Computer	US	FETF	Gladwin
08/543,788	08/05/99	Remote Keyboard Macros Activated by Hot Icons	US	FETF	Gladwin

PATENT
REEL: 011007 FRAME: 0170

PATENT
REEL: 027728 FRAME: 0251

08/553,808	06/09/98	System and Method for Enabling An IPX Driver To Accommodate Multiple LAN Adapters	US	FETF	Bi
08/543,463	10/16/95	Radio Flash Update	US	FETF	Bi Hsiung
08/551,936	11/29/99	Host/Remote Control Mode	US	FETF	Cortopassi George Hsiung Lim Parham Voegeli Wilson
08/543,700	04/16/99	Remote Control Interface	US	FETF	Banerjee Gladwin Maakatia
08/543,698	12/15/99	Screen Saver Disabler	US	FETF	Bi Gladwin Wilson
08/616,572	03/15/96	Method of Mapping Interrupts and DMA's	US	FETF	Herman Nicol O'Brien Smith Straup
08/621,672	03/26/96	Hard Disk Password Lock	US	FETF	Dumas
08/776,220	09/23/99	An Extendible and Portable Network Protocol Based System Management Architecture	US	FETF	Peters
08/771,616	12/20/96	Graphic User Interface For Managing A Server System	US	FETF	Bertone Coelho
08/784,275	12/28/99	Wireless Enumeration	US	FETF	Bi Gladwin Gopalan Wilson
08/784,688	12/15/99	Ink Trails On A Wireless Remote Interface Tablet And Wireless Remote Ink Field Object	US	FETF	Bi Boals George Gladwin Merkle Wilson
08/784,276	01/15/97	Dynamic Server Allocation for Load Balancing Wireless Remote Interface Processing	US	FETF	Bi
08/784,211	01/15/97	Data Compression	US	FETF	Boals
08/784,034	01/15/97	Local Handwriting Recognition in a Wireless Remote Interface Tablet	US	FETF	Bi
08/784,141	01/15/97	Audio Compression in a Wireless Remote Interface Tablet	US	FETF	Bi
08/784,243	11/30/99	Multi-User on Screen Keyboard	US	FETF	Bi
08/784,212	01/15/97	Mode Switching for Pen-Based Computer System	US	FETF	Bi

PATENT
REEL: 011007 FRAME: 0171

PATENT
REEL: 027728 FRAME: 0252

08/855,810	07/02/99	Symmetric Multiprocessing System with Unified Environment and Distributed System Functions Wherein Bus Operations Related Storage Spaces Are ...	US	FETF	Bertone DiPlacido Joyce Massucci McNally Murray
08/905,912	12/03/99	External Flexible Bay	US	FETF	Bailey
08/958,721	01/13/00	Modular Portable Personal Computer	US	FETF	Bonardi Fuhs Griffin Hallowell Ojeda Wang
08/971,994	12/16/99	Door for Computer Case	US	FETF	Chen Herron Nguyen Williams You
09/120,866	07/20/98	Expansion Module Including Programmable Chip Selects	US	FETF	Nicol O'Brien Smith Straup
08/958,767	01/13/00	Dual Slope A-D Converter	US	FETF	Turnbull
08/957,398	01/05/99	Mouse Emulation with Passive Pen	US	FETF	Cohen Cortopassi George Gladwin Hsiung Lim
08/783,708	04/08/99	Multiple Wireless Remote Interfaces To A Single Server	US	FETF	Gladwin Soucy
08/783,080	04/16/99	Multi-User Flash ROM Update	US	FETF	Bi
08/916,880	06/25/99	Peripheral Card Locking Device	US	FETF	Fuchs Ojeda
08/971,993	11/17/97	Removable Drive Bay	US	FETF	Chen Herron Ngugen You

PATENT
REEL: 011007 FRAME: 0172

PATENT
REEL: 027728 FRAME: 0253

ASSIGNMENT

ASSIGNMENT made by Packard Bell NEC, Inc., a Delaware corporation, located and doing business at 6000 Florin - Perkins Road, Sacramento, California 95828-1037 (hereinafter the ASSIGNOR) to NEC Corporation, a Japanese corporation, located and doing business at 7-1, Shiba 5-Chome, Minato-ku, Tokyo 108-8001, Japan (hereinafter the ASSIGNEE).

IN CONSIDERATION of good and valuable consideration, the receipt of which is acknowledged, the ASSIGNOR sells, assigns and transfers to the ASSIGNEE and its successors Assignor's entire right, title and interest in and to the patents and patent applications as set forth in the attached Exhibits A and B; the same to be held and enjoyed by the ASSIGNEE, for its own use, and for its legal representatives and assigns, as full and entire as the same would have been held by the ASSIGNOR had this assignment and sale not been made.

Executed and delivered this 23 day of February, 2000.

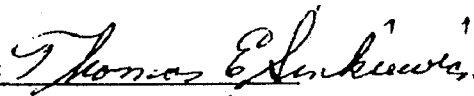
ASSIGNOR:

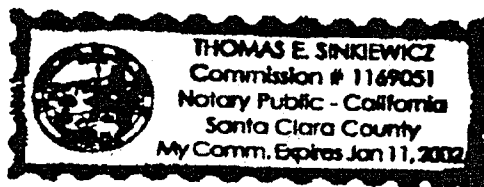

Norio Tanoue, President
Packard Bell NEC, Inc.

STATE OF CALIFORNIA)

Santa Clara) SS.
COUNTY OF ~~SACRAMENTO~~)

Personally appeared before me this 23rd day of February, 2000, Norio Tanoue, to me known to be the person who signed this instrument, who acknowledged that the same was signed as a free act on behalf of the identified corporation as its president with authority to do so.

Notary Public 
My Commission Expires 1/11/2002



RECORDED: 07/31/2000

PATENT
REEL: 011007 FRAME: 0173

RECORDED: 02/17/2012

PATENT
REEL: 027728 FRAME: 0254