# PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the Assignee's name and state of incorporation previously recorded on Reel 008011 Frame 0464. Assignor(s) hereby confirms the sell, assign, and transfer to Cypress Semiconductor Corporation of the entire right, title and interest.

#### CONVEYING PARTY DATA

Name	Execution Date
David B. Rees	02/28/2012

### RECEIVING PARTY DATA

Name:	Cypress Semiconductor Corporation
Street Address:	3901 North First Street
City:	San Jose
State/Country:	CALIFORNIA
Postal Code:	95134

## PROPERTY NUMBERS Total: 1

Property Type	Number
Patent Number:	5821770

#### CORRESPONDENCE DATA

 Fax Number:
 (408)545-6911

 Phone:
 408-943-6878

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 andb@cypress.com

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US

Mail.

Correspondent Name: Cypress Semiconductor Corporation

Address Line 1: 198 Champion Court

Address Line 4: San Jose, CALIFORNIA 95134

ATTORNEY DOCKET NUMBER:	CD96004
NAME OF SUBMITTER:	Andrew J. Bateman

Total Attachments: 3

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PATENT REEL: 027779 FRAME: 0341 CH \$40.00 582

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTORS: David REES

SERIAL NO.: 08/640.032

PATENT NO.: 5,821,770

ISSUE DATE: October 13, 1998

FOR:

OPTION DECODING WITH ON-CHIP ELECTRICAL FUSES

# **DECLARATION UNDER MPEP § 323.01(b)**

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

The undersigned is an authorized signatory of Cypress Semiconductor Corporation. I have personal knowledge of the facts recited in this Declaration.

In the assignment recorded at Reel/Frame number 008011/0464 on 04/30/1996 for the above-identified patent, the Assignee, Cypress Semiconductor Corporation, is identified in error as "Cypress Semiconductor, Inc., a corporation organized and existing under the laws of the State of California." The Assignee requests correction in the assignment records of the United States Patent and Trademark Office to show Assignee's name as "Cypress Semiconductor Corporation, a corporation organized and existing under the laws of the State of Delaware" and is including a copy of the originally recorded papers, a cover sheet, and the required fee for recordation.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001, of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the above-identified patent.

Date: February 28, 2012

By:

Cathal G. Phelan

Title:

Cypress Semiconductor Corporation

Chief Technical Officer

PATENT REEL: 027779 FRAME: 0342

	Exp	ress Mail No. EM135053298US
FC 12.970, 1595 Re 6.93	07-02-1996	VER SHEET US DEPARTMENT OF COMMERCE
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Tab settings □ □ □ ▼  To the Honoran a Commission	<b>                                   </b>	J the attached original documents or copy thereof.
1 Name of the Neyman party (18): (N		Name and address of receiving party(ies)
30		Name: Cypress Semiconductor, Inc.
David Brian Rees		Internal Address: 3901 North First Street
Additional name(s) of anyties) att	acned? I Yes \$ No	San Jose, CA 95134-1599
Nature of conveyance:		
Assignment	☐ Merger	Street Address: 3901 North First Street
☐ Security Agreement	☐ Change of Name	
☐ Other		City: San Jose State: CA ZIP: 95134
Execution Date: April 26, 1	996	Additional name(s) & address(es) attached? 그 Yes 환 No
A. Patent Application No.(s)		B. Patent No.(s)  657@5 U.S. PTO
		B. Patent No.(s) 657Ø5 U.S. PTO
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A. Patent Application No.(s)  5. Name and address of party to wh concerning document should be read to the Name: Randy W. Tung	Additional numbers att orn correspondence mailed: sselle, Raisch,	B. Patent No.(s)  657Ø5 U.S. PTO  106/27/96  6. Total number of applications and patents involved:  7. Total fee (37 CFR 3.41)\$40.00
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A. Patent Application No.(s)  5. Name and address of party to wh concerning document should be represented by the Name: Randy W. Tung  Internal Address: Barnes, Kit Choate, Whittemore & Street Address: 645 Griswood City: Detroit State:  9. Statement and signature.	Additional numbers attorn correspondence mailed:  Sselle, Raisch, Hulbert, P.C.  Old, 3500  MI ZIP: 48226  DO NOT US	B. Patent No.(s)  65705 U.S. PTO  106/27/96  6. Total number of applications and patents involved:  7. Total fee (37 CFR 3.41)\$40.00  3. Enclosed  3. Authorized to be charged to deposit account  8. Deposit account number:  02-1000  (Attach duplicate copy of this page if paying by deposit account)

Mail documents to be recorded with required cover sheet info@nation@nT.

Commissioner of Patents & Trademarks, Box Assignments, FRAME: 0464

Washington, D.C. 20231



64,663-044

### **ASSIGNMENT**

WHEREAS, I, DAVID B REES, have invented certain improvements in OPTION DECODING WITH ON-CHIP ELECTRICAL FUSES for which I am about to make application for Letters Patent of the United States; and

WHEREAS, CYPRESS SEMICONDUCTOR, INC., a corporation organized and existing under the laws of the State of California and having its principal place of business at 3901 North First Street, San Jose, California 95134-1599, is desirous of acquiring the entire right, title and interest in and to said invention;

NOW, THEREFORE, in consideration of One Dollar (\$1.00) and other good and valuable consideration, the receipt of which is hereby acknowledged, I, DAVID B REES, by these presents, do hereby sell, assign and transfer unto the said corporation and its assigns, for the territory of the United States of America and all foreign countries, the entire right, title and interest, including all priority rights under the International Convention associated with each country of the Union, in and to said invention as described in the patent application executed by me on the day of day of 19 1/2, preparatory to obtaining Letters Patent of the United States thereon, and in and to said application and any Letters Patent that may be granted in pursuance of said application and any divisional, continuation or continuation-in-part application thereof, and in and to any reissue of any such patent, and in and to any patent applications which may be filed on said invention in countries foreign to the United States and any Letters Patent granted thereon.

I further authorize said corporation to apply for foreign patents on said invention in its own name or through its designees, including subsidiaries, related companies or assignees, under the International Convention or otherwise, and I further agree to execute all papers, including those required for the United States and foreign applications, and to perform such other proper acts as said corporation or its designees the rights herein assigned.

David Brian Rees

BARNES, KISSELLE, RAISCH, CHOATE, WHITTEMORE & HULBERT, P.C. 3500 Penobscot Building Detroit, Michigan 48226

RECORDED: 04/30/1996

PATENT REEL: 8011 FRAME: 0465

> PATENT REEL: 027779 FRAME: 0344

REC ORIJED, 04/30/1990

RECORDED: 02/28/2012