

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Yoshimasa HORII	06/29/2006
RECEIVING PARTY DATA	
Name:	FUJITSU SEMICONDUCTOR LIMITED
Street Address:	2-10-23 Shin-Yokohama Kohoku-ku
City:	Yokohama-shi, Kanagawa
State/Country:	JAPAN
Postal Code:	222-0033
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	13403321
CORRESPONDENCE DATA	
Fax Number:	(202)822-1111
Phone:	202-822-1100
Email:	PatentMail@whdapatentlaw.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
Correspondent Name:	Westerman, Hattori, Daniels & Adrian, LL
Address Line 1:	1250 Connecticut Avenue, NW
Address Line 2:	Suite-700
Address Line 4:	Washington, DISTRICT OF COLUMBIA 20036-2657
ATTORNEY DOCKET NUMBER:	062968A
NAME OF SUBMITTER:	Dennis M. Hubbs
Total Attachments: 7 source=062968A_assignment#page1.tif source=062968A_assignment#page2.tif source=062968A_assignment#page3.tif source=062968A_assignment#page4.tif source=062968A_assignment#page5.tif source=062968A_assignment#page6.tif source=062968A_assignment#page7.tif	

OP \$40.00 13403321

TO: WESTERMAN, HATTORI, DANIELS & ADRIAN, LL COMPANY: 1250 CONNECTICUT AVENUE,

**UNITED STATES PATENT AND TRADEMARK OFFICE**

UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE



JULY 09, 2010

501228198A

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WESTERMAN, HATTORI, DANIELS & ADRIAN, LL
1250 CONNECTICUT AVENUE, NW
SUITE-700
WASHINGTON, DC 20036-2657

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RECORDATION DATE: 07/09/2010

REEL/FRAME: 024651/0744

NUMBER OF PAGES: 46

BRIEF: CHANGE OF NAME (SEE DOCUMENT FOR DETAILS).
DOCKET NUMBER: 100607

ASSIGNOR:

FUJITSU MICROELECTRONICS LIMITED

DOC DATE: 04/01/2010

ASSIGNEE:

FUJITSU SEMICONDUCTOR LIMITED
2-10-23 SHIN-YOKOHAMA KOHOKU-KU
YOKOHAMA-SHI, KANAGAWA, JAPAN

222-0033

SERIAL NUMBER: 10336716

FILING DATE: 01/06/2003

PATENT NUMBER:

ISSUE DATE:

TITLE: LEAD FRAME AND MANUFACTURING METHOD THEREOF AND A SEMICONDUCTOR
DEVICE

O:WESTERMAN, HATTORI, DANIELS & ADRIAN, LL COMPANY:1250 CONNECTICUT AVENUE,

024651/0744 PAGE 6

SERIAL NUMBER: 11503907 FILING DATE: 08/15/2006
PATENT NUMBER: ISSUE DATE:
TITLE: BONDING APPARATUS AND BONDING METHOD FOR ELECTRONIC COMPONENT

SERIAL NUMBER: 11504115 FILING DATE: 08/15/2006
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF, AND THIN FILM DEVICE

SERIAL NUMBER: 11507536 FILING DATE: 08/22/2006
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR SUBSTRATE AND METHOD OF FABRICATING SEMICONDUCTOR DEVICE

SERIAL NUMBER: 11508289 FILING DATE: 08/23/2006
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE WITH BUILT-IN CAPACITOR, AND METHOD OF PRODUCING THE SAME

SERIAL NUMBER: 11511472 FILING DATE: 08/29/2006
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

SERIAL NUMBER: 11512229 FILING DATE: 08/30/2006
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

SERIAL NUMBER: 11515875 FILING DATE: 09/06/2006
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

SERIAL NUMBER: 11516584 FILING DATE: 09/07/2006
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME

SERIAL NUMBER: 11518237 FILING DATE: 09/11/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD FOR REDUCING METAL, MULTILAYER INTERCONNECTION STRUCTURE AND MANUFACTURING METHOD FOR THE SAME, AND SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD FOR THE SAME

SERIAL NUMBER: 11520766 FILING DATE: 09/14/2006
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME

SERIAL NUMBER: 11541726 FILING DATE: 10/03/2006
PATENT NUMBER: ISSUE DATE:
TITLE: HIGHLY INTEGRATED AND RELIABLE DRAM AND ITS MANUFACTURE

SERIAL NUMBER: 11541727 FILING DATE: 10/03/2006
PATENT NUMBER: ISSUE DATE:
TITLE: COVER TAPE FOR PACKAGING SEMICONDUCTOR DEVICE AND PACKAGE FOR SEMICONDUCTOR DEVICE

062968

TO: WESTERMAN, HATTORI, DANIELS & ADRIAN, LL COMPANY: 1250 CONNECTICUT AVENUE,



062968

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DECEMBER 15, 2008

*500730047A

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WESTERMAN, HATTORI, DANIELS & ADRIAN, LL
1250 CONNECTICUT AVENUE, NW
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WASHINGTON, DC 20036-2657UNITED STATES PATENT AND TRADEMARK OFFICE
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MAIL STOP: ASSIGNMENT SERVICES BRANCH, P.O. BOX 1450, ALEXANDRIA, VA 22313.

RECORDATION DATE: 12/09/2008

REEL/FRAME: 021976/0089
NUMBER OF PAGES: 57BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).
DOCKET NUMBER: 082621

ASSIGNOR:

FUJITSU LIMITED

DOC DATE: 11/04/2008

ASSIGNEE:

FUJITSU MICROELECTRONICS LIMITED
7-1, NISHI-SHINJUKU 2-CHOME,
SHINJUKU-KU
TOKYO, JAPAN 163-0722

SERIAL NUMBER: 11125398

FILING DATE: 05/06/2005

PATENT NUMBER:

ISSUE DATE:

TITLE: SEMICONDUCTOR DEVICE HAVING GATE ELECTRODE ON BOTH SIDES OF CHANNEL
REGION

SERIAL NUMBER: 11082831

FILING DATE: 03/18/2005

PATENT NUMBER:

ISSUE DATE:

TITLE: NETWORK SWITCHING DEVICE AND METHOD USING SHARED BUFFER

TO:WESTERMAN, HATTORI, DANIELS & ADRIAN, LL COMPANY:1250 CONNECTICUT AVENUE,

021976/0089 PAGE 28

SERIAL NUMBER: 11300276 FILING DATE: 12/15/2005
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR MANUFACTURE METHOD

SERIAL NUMBER: 11515875 # 062968 FILING DATE: 09/06/2006
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

SERIAL NUMBER: 12061408 FILING DATE: 04/02/2008
PATENT NUMBER: ISSUE DATE:
TITLE: FERROELECTRIC MEMORY DEVICE AND FABRICATION PROCESS THEREOF,
FABRICATION PROCESS OF A SEMICONDUCTOR DEVICE

SERIAL NUMBER: 12037286 FILING DATE: 02/26/2008
PATENT NUMBER: ISSUE DATE:
TITLE: FERROELECTRIC MEMORY DEVICE AND FABRICATION PROCESS THEREOF,
FABRICATION PROCESS OF A SEMICONDUCTOR DEVICE

SERIAL NUMBER: 12126357 FILING DATE: 05/23/2008
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND FABRICATION PROCESS THEREOF

SERIAL NUMBER: 11315212 FILING DATE: 12/23/2005
PATENT NUMBER: ISSUE DATE:
TITLE: FERROELECTRIC MEMORY DEVICE AND FABRICATION PROCESS THEREOF,
FABRICATION PROCESS OF A SEMICONDUCTOR DEVICE

SERIAL NUMBER: 12177621 FILING DATE: 07/22/2008
PATENT NUMBER: ISSUE DATE:
TITLE: FERROELECTRIC MEMORY DEVICE, FERROELECTRIC MEMORY MANUFACTURING
METHOD, AND SEMICONDUCTOR MANUFACTURING METHOD

SERIAL NUMBER: 12163340 FILING DATE: 06/27/2008
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE WITH IMPROVED PADS

SERIAL NUMBER: 11397852 FILING DATE: 04/05/2006
PATENT NUMBER: ISSUE DATE:
TITLE: INSPECTING METHOD, INSPECTING APPARATUS, AND METHOD OF
MANUFACTURING SEMICONDUCTOR DEVICE

SERIAL NUMBER: 11498169 FILING DATE: 08/03/2006
PATENT NUMBER: ISSUE DATE:
TITLE: INSPECTION DEVICE AND METHOD

SERIAL NUMBER: 12128088 FILING DATE: 05/28/2008
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE WITH FERRO-ELECTRIC CAPACITOR

SERIAL NUMBER: 12127902 FILING DATE: 05/28/2008
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME



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SEPTEMBER 19, 2006

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DANIELS & ADRIAN, LLP

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RECORDATION DATE: 09/06/2006

REEL/FRAME: 018273/0220

NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).
DOCKET NUMBER: 062968

ASSIGNOR:
HORII, YOSHIMASA

DOC DATE: 06/29/2006

ASSIGNEE:
FUJITSU LIMITED
1-1, KAMIKODANAKA 4-CHOME,
NAKAHARA-KU, KAWASAKI-SHI
KANAGAWA 211-8588, JAPAN

SERIAL NUMBER: 11515875
PATENT NUMBER:
TITLE:

FILING DATE:
ISSUE DATE:

018273/0220 PAGE 2

LAZENA MARTIN, EXAMINER
ASSIGNMENT SERVICES BRANCH
PUBLIC RECORDS DIVISION

U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter, "ASSIGNOR") by
(Insert Name(s) & Address(es) of ASSIGNEE(S))

FUJITSU LIMITED, 1-1, Kamikodanaka 4-chome, Nakahara-ku,

Kawasaki-shi, Kanagawa 211-8588 Japan

(hereinafter, "ASSIGNEE"), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled
(Title of Invention)

SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

relating to International Patent Application PCT/JP _____ / _____ and/or for which application for Letters Patent of the United States was executed on even date herewith or, if not so executed, was:

(a) executed on _____ ; *(Insert date of execution of application, if not concurrent)*

(b) filed on _____,

Serial No. / ;

Assignee's attorney is hereby authorized to insert in (b) the specified data, when known.

and to said application and all Letters Patent(s) of the United States granted on said application and any continuation, division, renewal, substitute, reissue or reexamination application based thereon, for the full term or terms for which the said Letters Patent(s) may be granted and including any extensions thereof (collectively, hereinafter, "said application(s) and Letters Patent(s)").

The ASSIGNOR agree(s), when requested by said ASSIGNEE and without charge to but at the expense of said ASSIGNEE, to do all acts which the ASSIGNEE may deem necessary, desirable or expedient, for securing, maintaining and enforcing protection for said invention, including in the preparation and prosecution of said application(s) and the issuance of said Letters Patent(s), in any interference, reissue, reexamination, or public use proceeding, and in any litigation or other legal proceeding which may arise or be declared in relation to same, such acts to include but not be limited to executing all papers, including separate assignments and declarations, taking all rightful oaths, providing sworn testimony, and obtaining and producing evidence.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

1) <u>堀井 義正</u> (Signature)	<u>Yoshimasa HORII</u> (Type Name)	<u>Jun. 29, 2006</u> (Date)
2) _____ (Signature)	_____ (Type Name)	_____ (Date)
3) _____ (Signature)	_____ (Type Name)	_____ (Date)
4) _____ (Signature)	_____ (Type Name)	_____ (Date)
5) _____ (Signature)	_____ (Type Name)	_____ (Date)

PATENT