

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT										
NATURE OF CONVEYANCE:	ASSIGNMENT										
CONVEYING PARTY DATA											
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Ahmad Hamzehdoost</td> <td>04/21/1992</td> </tr> <tr> <td>Leonard L. Mora</td> <td>04/08/1992</td> </tr> </tbody> </table>		Name	Execution Date	Ahmad Hamzehdoost	04/21/1992	Leonard L. Mora	04/08/1992				
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RECEIVING PARTY DATA											
<table border="1"> <tr> <td>Name:</td> <td>VLSI Technology, Inc.</td> </tr> <tr> <td>Street Address:</td> <td>1109 McKay Drive</td> </tr> <tr> <td>City:</td> <td>San Jose</td> </tr> <tr> <td>State/Country:</td> <td>CALIFORNIA</td> </tr> <tr> <td>Postal Code:</td> <td>95131</td> </tr> </table>		Name:	VLSI Technology, Inc.	Street Address:	1109 McKay Drive	City:	San Jose	State/Country:	CALIFORNIA	Postal Code:	95131
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CORRESPONDENCE DATA											
<p>Fax Number: (408)518-5671</p> <p>Phone: 3474431592</p> <p>Email: david.schaeffer@nxp.com</p> <p><i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i></p> <p>Correspondent Name: David L. Schaeffer</p> <p>Address Line 1: 411 East Plumeria Drive, MS41</p> <p>Address Line 2: NXP Semiconductors, IP&L</p> <p>Address Line 4: San Jose, CALIFORNIA 95134</p>											
NAME OF SUBMITTER:	David L. Schaeffer										
<p>Total Attachments: 1</p> <p>source=07876249ParentInventorsAssignment#page1.tif</p>											

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07/876249

Patent

VLSI Case No.: 1463

Assignment

Whereas, we, Ahmad Hamzehdoost and Leonard L. Mora
executed employment agreements with VLSI Technology, Inc. (hereinafter called VLSI), a Delaware Corporation
having its principal place of business at 1109 McKay Drive, San Jose, California, 95131, whereby we have agreed
to assign to VLSI all inventions (except as otherwise limited by law) which relate to VLSI's business and which were
first conceived or actually reduced to practice during our employment by VLSI;

Now, therefor, pursuant to the terms of said employment agreements and for good and valuable consideration,
receipt of which is hereby acknowledged, we do hereby sell, assign and transfer unto VLSI, its successors and
assigns, the entire right, title and interest, including the right of priority in, to and under an application for Letters
Patent of the United States entitled:

PACKAGE STRUCTURE WITH ACCESSIBLE CHIP

X filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the
United States and of countries foreign thereto which may be granted thereon or therefor; or

Serial No.: _____ filed on _____ and the inventions set forth
and described therein, and any and all Letters Patent of the United States and of countries foreign thereto
which may be granted thereon or therefor;

And for the above consideration, we agree promptly upon request of VLSI, its successors or assigns, to execute
and deliver without further compensation any power of attorney, assignment, application, whether original,
continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to VLSI, its
successors and assigns, the inventions described in said application and all patent rights therein, in the United
States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference
proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the
expenses which may be incurred by me in lending such cooperation and assistance are paid by VLSI.

In witness whereof, we hereunto set our hands and seal:

Inventor's Signature: <u>Ahmad Hamzehdoost</u>	Date: <u>4,21,92</u>
Inventor's Signature: <u>Leonard L. Mora</u>	Date: <u>4/8/92</u>
Inventor's Signature: _____	Date: _____
Inventor's Signature: _____	Date: _____
Inventor's Signature: _____	Date: _____
Inventor's Signature: _____	Date: _____
Inventor's Signature: _____	Date: _____
Inventor's Signature: _____	Date: _____
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