PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
HEWLETT-PACKARD DEVELOPMENT COMPANY, L.P.	12/13/2011

RECEIVING PARTY DATA

Name:	HTC Corporation
Street Address:	No.23, Xinghua Rd., Taoyuan City
City:	Taoyuan County
State/Country:	TAIWAN

PROPERTY NUMBERS Total: 2

Property Type	Number
Patent Number:	6163780
Patent Number:	7714858

CH $80.00 6163780

CORRESPONDENCE DATA

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NAME OF SUBMITTER:	JANINE CHANG

Total Attachments: 11
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Exhibit B

ASSIGNMENT OF PATENTS AND PATENT APPLICATIONS

WHEREAS, Hewlett-Packard Development Company, L.P., a limited partnership established and existing under the laws of the State of Texas and having its registered place of business at 20555 S.H. 249 Houston, Texas 77070, U.S.A. and Hewlett-Packard Company, a corporation organized and existing under the laws of the State of Delaware and having its principal place of business at 3000 Hanover Street, Palo Alto, California 94304, U.S.A. (collectively "HP") are the owners of record, either individually or collectively, of the Assigned Patents (as defined below);

WHEREAS, HTC Corporation ("Purchaser"), a corporation duly organized and existing under and by virtue of the laws of Taiwan, and having a place of business at No. 23 Xinghua Road, Taoyuan City, Taoyuan County 330, Taiwan, is desirous of acquiring the entire interest in and to the Assigned Patents (as defined below);

WHEREAS, HP and Purchaser have entered into a Patent Purchase and Sale Agreement for certain patents and patent applications dated November 11, 2011 ("Purchase and Sale Agreement") wherein HP has agreed to sell and Purchaser has agreed to purchase the Assigned Patents subject to all prior encumbrances and licenses;

WHEREAS, Purchaser has agreed and covenanted in said Purchase and Sale Agreement to license back to HP certain rights under the Assigned Patents, as set forth in Sections 6.1.2 and 7.2 thereof, as a condition of and as part of the consideration for the Parties entering into the Purchase and Sale Agreement;

WHEREAS, this Assignment is made by HP subject to and contingent upon Purchaser concurrently providing to HP a grant-back license to the Assigned Patents and upon Purchaser and its Affiliates making certain covenants not to sue or assert the Assigned Patents, in accordance with the Purchase and Sale Agreement; and

WHEREAS, for the purpose of this Assignment, the following terms, whether in singular or in plural form, when used with a capital initial letter shall have the respective meanings as follows.

"**Affiliate**" means with respect to any person, any other Person that directly, or indirectly through one or more intermediaries, controls, is controlled by, or is under the common control of the Person in question; provided, however, that in any country where the local law or regulation does not permit foreign equity participation of more than fifty percent (50%), an "Affiliate" shall include any Person in which the Person in question owns or controls, directly or indirectly, the maximum percentage of such outstanding stock or voting rights permitted by such local law or regulation. For purposes of the foregoing, "control," including the terms "controlling," "controlled by" and "under common control with," means the possession, direct or indirect, of the power to direct or cause the direction of the management and policies of a Person, whether through the ownership of voting securities, by contract or otherwise.

"Assigned Patents" means the issued patents and patent applications listed in Appendix A of this Assignment.

"Encumbrances" means any commitments, licenses or other rights relating to any of the Assigned Patents, whether express, implied or otherwise, that are made, entered into or granted by, or that arise from the actions taken by, HP, any current or former Affiliate of HP, or any Person, prior to the Effective Date including, but not limited to, the commitments, licenses and rights described in Sections 5 and 6.1 of the Purchase and Sale Agreement.

"Person" means any natural person, corporation, company, partnership, association, sole proprietorship, trust, joint venture, non-profit entity, institute, governmental authority, trust association or other form of entity not specifically listed herein including, without limitation, HP or any of its Affiliates, or Purchaser or any of its Affiliates.

NOW, THEREFORE, to all whom it may concern, be it known that for good and valuable consideration to HP in hand paid, the receipt of which is hereby acknowledged, HP has sold, assigned, transferred, and set over, and by these presents does sell, assign, transfer, and set over unto said Purchaser, subject to all Encumbrances, its whole right, title, and interest in and to all of the Assigned Patents, said whole right, title, and interest in and to said Assigned Patents including all past, present, and future causes of action and claims for damages derived by reason of patent infringement thereof (to the extent such damages are not already paid, awarded or contractually owed to HP, its Affiliates or any predecessor of HP or HP's Affiliates), for said Purchaser's own use and for the use of its assigns, successors, and legal representatives to the full end of the term of each of the Assigned Patents. For clarity, the foregoing assignment does not include (i) any trademarks, trade dress, trade names, or other indicia of origin; (ii) except for inventions of the Assigned Patents, any inventions or discoveries, whether patentable or not, and registrations, invention disclosures, patents and applications therefor; (iii) any trade secrets, confidential information or know-how; (iv) any works of authorship, whether copyrightable or not; and (v) any other intellectual property or proprietary rights of HP, its Affiliates or any predecessor of HP or HP's Affiliates.

In Testimony Whereof, HP by its fully authorized representatives has executed this Assignment as of the dates indicated below.

HEWLETT-PACKARD DEVELOPMENT COMPANY, L.P.

By: HPQ Holdings, LLC, its General Partner

By: _____ Date: DEC 13 2011

Bruce Ives., Manager
HPQ Holdings, LLC

HEWLETT-PACKARD COMPANY

By: _____ Date: December 13, 2011

Susan Goodhue
VP & AGC, Intellectual Property Transactions
Hewlett-Packard Company

Appendix A of Exhibit B: List of Assigned Patents

United States Patents

Item #	Lot ID	Lot Title	Patent #	Patent Title
1	W091106-B	PC - Audio System	US5666263	Attaching a speaker to a computer component
2	W091106-B	PC - Audio System	US5701347	Audio system for a personal computer
3	W091106-B	PC - Audio System	US7035086	Removable storage of speakers within cavities of electronic device housing
4	W100226-A	PC Power Supply	US5550729	Power sequencing control
5	W100226-A	PC Power Supply	US5682306	Switched mode power supply with power factor correction
6	W100226-A	PC Power Supply	US5828204	Power supply with minimal dissipation output stage
7	W100226-A	PC Power Supply	US6659779	Electronic assembly having a removable power supply
8	W100226-A	PC Power Supply	US6773267	Electronic assembly having a removable power supply
9	W110603-A	PCs and Notebook Design	US5777628	Method and apparatus for detecting cache collisions in a two dimensional memory
10	W110603-A	PCs and Notebook Design	US5781407	Portable personal computers with multi-directional infrared communication
11	W110603-A	PCs and Notebook Design	US5896524	Off-line clock synchronization for multiprocessor event traces
12	W110603-A	PCs and Notebook Design	US6256193	Vertical docking and positioning apparatus for a portable computer
13	W110603-A	PCs and Notebook Design	US6404626	Integrated connector module for personal computers
14	W110603-A	PCs and Notebook Design	US7143321	System and method for multi processor memory testing
15	W110603-A	PCs and Notebook Design	US7145767	Support
16	W100312-A	Antennas and modems	US5640689	Communications apparatus with antenna switching based on antenna rotation
17	W100312-A	Antennas and modems	US6141690	Computer network address mapping
18	W100312-A	Antennas and modems	US6150992	Traceable self-contained programmable frequency source for performing alternate test site and open area test site comparisons
19	W090424-C	Battery Circuitry and Backup Power Technologies	US5416403	Current stabilizing circuit
20	W090424-C	Battery Circuitry and Backup Power Technologies	US5488531	REDUNDANT POWER MIXING ELEMENT WITH FAULT DETECTION.....
21	W090424-C	Battery Circuitry and Backup Power Technologies	US6014014	State-of-charge-measurable batteries
22	W090424-C	Battery Circuitry and Backup Power Technologies	US6259971	Portable fuel-cell-powered system with ultrasonic atomization of H2O by-product
23	W090424-C	Battery Circuitry and Backup Power Technologies	US6274949	Back-Up Power Accessory For A Computer
24	W090424-C	Battery Circuitry and Backup Power	US6311279	INTERNAL BATTERY BACKUP

		Technologies		
25	W090424-C	Battery Circuitry and Backup Power Technologies	US6635383	Conical coiled spring contact for minimizing battery-to-device contact resistance stemming form insulating contaminant layer on same
26	W090424-C	Battery Circuitry and Backup Power Technologies	US6641952	Battery arrangement for reducing battery terminal contact resistance stemming from insulating contaminant layer on same
27	W090424-C	Battery Circuitry and Backup Power Technologies	US6950729	Portable fuel-cell-powered system with ultrasonic atomization of H2O by-product
28	W090424-C	Battery Circuitry and Backup Power Technologies	US7351497	Reducing battery terminal contact resistance stemming from insulating contaminant layer on same
29	W090501-A	Battery Pack, Charger and Battery Management	US5659238	Computer battery pack charge current sensor with gain control
30	W090501-A	Battery Pack, Charger and Battery Management	US5677077	Sensor circuit for providing maximum and minimum cell voltages of a battery
31	W090501-A	Battery Pack, Charger and Battery Management	US5717937	IMPROVED CIRCUIT FOR SELECTING AND DESIGNATING A MASTER BATTERY PACK IN A COMPUTER
32	W090501-A	Battery Pack, Charger and Battery Management	US6160378	Battery Charger With Detachable Mechanical Adapters And Fold-Out Connectors
33	PSL54	Epen	US7193618	Electronic ink ball point pen with memory
34	PSL54	Epen	US7342575	Electronic writing systems and methods
35	W110204-A	Power Management	US5777503	Pulse width modulation bias to minimize effect of noise due to ramp switching
36	W110204-A	Power Management	US5786687	Transformer-isolated pulse drive circuit
37	W110204-A	Power Management	US5789904	Computer battery pack charge current sensor with gain control
38	W110204-A	Power Management	US5907197	AC/DC portable power connecting architecture
39	W110204-A	Power Management	US5911529	Typing power
40	W110204-A	Power Management	US6026495	Nonintrusive monitoring of a computer sys- tem's downtime due to a supply power outage condition
41	W110204-A	Power Management	US6046662	Low profile surface mount transformer
42	PSL87	Microprocessor Architecture	US5495569	Circuit for ensuring that a local interrupt controller in a microprocessor is powered up active
43	PSL87	Microprocessor Architecture	US5689653	Vector memory operations
44	PSL87	Microprocessor Architecture	US5751932	Fail-fast, fail-functional, fault-tolerant multiprocessor system
45	PSL87	Microprocessor Architecture	US5832290	Apparatus, systems and method for improving memory bandwidth utilization in vector processing systems
46	PSL87	Microprocessor Architecture	US5838894	Logical, fail-functional, dual central processor units formed from three processor units

47	PSL87	Microprocessor Architecture	US5870576	Method and apparatus for storing and expanding variable-length program instructions upon detection of a miss condition within an instruction cache containing pointers to compressed instructions for wide instruction word processor architectures
48	PSL87	Microprocessor Architecture	US5964867	Method for inserting memory prefetch operations based on measured latencies in a program optimizer
49	PSL87	Microprocessor Architecture	US6026479	Apparatus and method for efficient switching of CPU mode between regions of high instruction level parallism and low instruction level parallism in computer programs
50	PSL87	Microprocessor Architecture	US6195754	Method and apparatus for tolerating power outages of variable duration in a multi-processor system
51	PSL87	Microprocessor Architecture	US6308261	Computer system having an instruction for probing memory latency
52	PSL87	Microprocessor Architecture	US6799263	Prefetch instruction for an unpredicted path including a flush field for indicating whether earlier prefetches are to be discarded and whether in-progress prefetches are to be aborted
53	PSL87	Microprocessor Instructions	US5721893	Exploiting untagged branch prediction cache by relocating branches
54	PSL87	Microprocessor Instructions	US5809450	Method for estimating statistics of properties of instructions processed by a processor pipeline
55	PSL87	Microprocessor Instructions	US6189141	Control path evaluating trace designator with dynamically adjustable thresholds for activation of tracing for high (hot) activity and low (cold) activity of flow control
56	PSL87	Microprocessor Instructions	US6219833	Method of using primary and secondary processors
57	PSL87	Microprocessor Instructions	US6463523	Method and apparatus for delaying the execution of dependent loads
58	PSL87	Microprocessor Instructions	US6651176	Systems and methods for variable control of power dissipation in a pipelined processor
59	PSL87	Microprocessor Instructions	US6691207	Method and apparatus for implementing loop compression in a program counter trace
60	PSL87	Microprocessor Instructions	US6845501	Method and apparatus for enabling a compiler to reduce cache misses by performing pre-fetches in the event of context switch
61	W110415-A	Computer Network and System Management	US5819042	Method and apparatus for guided configuration of unconfigured network and internetwork devices
62	W110415-A	Computer Network and System Management	US6505256	Automatic synchronization of state colors across a web-based system
63	W110415-A	Computer Network and System Management	US7010717	Facility creation process for clustered servers
64	W110415-A	Computer Network and System Management	US7111202	Autonomous boot failure detection and recovery
65	W110415-A	Computer Network and System Management	US7120684	Method and system for central management of a computer network

66	W110415-A	Computer Network and System Management	US7240090	Data queueing
67	W110415-A	Computer Network and System Management	US7249115	Network modelling
68	W110415-A	Computer Network and System Management	US7359978	Providing secure access through network firewalls
69	W110415-A	Computer Network and System Management	US7366857	Internal disk array mirror architecture
70	W110415-A	Computer Network and System Management	US7383379	Manipulating data in a data storage device using an auxiliary memory device
71	W110415-A	Computer Network and System Management	US7444679	Network, method and computer readable medium for distributing security updates to select nodes on a network
72	W100702-A	Network and System Management	US7185111	Available server determination
73	W100702-A	Network and System Management	US7320032	Methods and structure for reducing resource hogging
74	W100702-A	Network and System Management	US7346808	Diagnostic method, system, and program that isolates and resolves partnership problems between a portable device and a host computer
75	W100702-A	Network and System Management	US7404205	System for controlling client-server connection requests
76	W100702-A	Network and System Management	US7434141	Network-based memory error decoding system and method
77	W100702-A	Network and System Management	US7447764	Peripheral devices, systems for providing job operations for a plurality of host devices, and peripheral device monitoring methods
78	W100702-A	Network and System Management	US7457881	Method and apparatus for sending data from one protocol layer to another
79	W100702-A	Network and System Management	US7508763	Method to regulate traffic congestion in a network
80	W100702-A	Network and System Management	US7571221	Installation of network services in an embedded network server
81	W091113-A	Network and Systems Management	US5991897	Diagnostic module dispatcher
82	W091113-A	Network and Systems Management	US6687762	Network operating system adapted for simultaneous use by different operating systems
83	W091113-A	Network and Systems Management	US6711621	System and method of implementing netware core protocol within a sockets model
84	W091113-B	Network Data Transfer	US5742602	Adaptive repeater system
85	W091113-B	Network Data Transfer	US6198727	Method and apparatus for providing 10Base-T/100Base-TX link assurance

86	W091113-B	Network Data Transfer	US6381288	Method and apparatus for recovering data from a differential phase shift keyed signal
87	W091113-B	Network Data Transfer	US6865231	High-speed interconnection adapter having automated crossed differential pair correction
88	W110506-A	Networking	US5920698	AUTOMATIC DETECTION OF A SIMILAR DEVICE AT THE OTHER END OF A WIRE IN A COMPUTER NETWORK
89	W110506-A	Networking	US5923654	NETWORK SWITCH THAT INCLUDES A PLURALITY OF SHARED PACKET BUFFERS
90	W101210-A	Networking	US5923663	Method and apparatus for automatically detecting media connected to a network port
91	W101210-A	Networking	US5983269	Method and apparatus for configuring routing paths of a network communicatively interconnecting a number of processing elements
92	W101210-A	Networking	US6041065	Flexible multi-frequency repeater
93	W101210-A	Networking	US6049889	High performance recoverable communication method and apparatus for write-only networks
94	W110701-A	Networking	US6429762	Data communication isolation transformer with improved common-mode attenuation
95	W110506-A	Networking	US6603808	DUAL MODE PHONE LINE NETWORKING MODEM UTILIZING CONVENTIONAL TELEPHONE WIRING
96	W110506-A	Networking	US6631131	TRANSPOSE TABLE BIASED ARBITRATION SCHEME
97	W101210-A	Networking	US6647099	Administrative control and security of modems
98	W110506-A	Networking	US6744812	DUAL MODE PHONE LINE NETWORKING MODEM UTILIZING CONVENTIONAL TELEPHONE WIRING
99	W110701-A	Networking	US7173926	Method to eliminate user setup for installation of broadband modems, routers, and integrated modem-routers
100	W101210-A	Networking	US7308494	Reprovisioning technique for an interconnect fabric design
101	W110128-A	Web Server	US5941959	System for transferring a data stream to a requestor without copying data segments to each one of multiple data source/sinks during data stream building
102	W110128-A	Web Server	US5961598	System And Method For Internet Gateway Performance Charting
103	W110128-A	Web Server	US5974463	A scaleable network system for remote acce ss of a local network
104	W110128-A	Web Server	US7076796	Virtual media from a directory service
105	W110128-A	Web Server	US7203764	System and method for distributing load among redundant independent stateful world wide web server sites
106	W110128-A	Web Server	US7222177	Methods and structure for implementing web server quality-of-service control
107	W110128-A	Web Server	US7376741	System For Aborting Response To Client Request If Detecting Connection Between Client Server Is Closed By Examining Local Server Information
108	W090417-A	Database Management Technology	US5440732	Key-range locking with index trees
109	W090417-A	Database Management	US5485607	Concurrency-control method and apparatus in a database management system utilizing key-valued

		Technology		locking
110	W090417-A	Database Management Technology	US5504900	Commitment ordering for guaranteeing serializability across distributed transactions
111	W090417-A	Database Management Technology	US6044375	Automatic extraction of metadata using a neural network
112	W090417-A	Database Management Technology	US6785687	System for and method of efficient, expandable storage and retrieval of small datasets
113	W090417-A	Database Management Technology	US6816856	System for and method of data compression in a valueless digital tree representing a bitset
114	W090417-A	Database Management Technology	US6954757	Framework, architecture, method and system for reducing latency of business operations of an enterprise
115	W090612-B	Embedded Software Creation	US6163780	System and apparatus for condensing executable computer software code
116	W090612-B	Embedded Software Creation	US6856994	System and method for condensing application software
117	W090612-B	Embedded Software Creation	US7089251	Methods for processing condensed computer code
118	W090612-B	Embedded Software Creation	US7093245	System and apparatus for upgrading concentrated executable computer software code without reconcentration
119	W090612-B	Embedded Software Creation	US7096463	System and apparatus for dynamically upgrading concentrated executable computer software code
120	W090612-A	Embedded Software Execution	US7036111	Code verification system and method
121	W090612-A	Embedded Software Execution	US7069396	Deferred memory allocation for application threads
122	W090612-A	Embedded Software Execution	US7320129	Native language verification system and method
123	W110311-A	Graphics Software	US5889994	Method for cataloging graphics primitives by rendering state
124	W090925-A	Graphics Software	US6052132	Technique for providing a computer generated face having coordinated eye and head movement
125	W090925-A	Graphics Software	US6172682	Detecting insideness of a rectangle to an arbitrary polygon
126	W110311-A	Graphics Software	US6175373	Method and apparatus for presenting video on a display monitor associated with a computer
127	W110311-A	Graphics Software	US6300959	Method and system condensing animated images
128	W090925-A	Graphics Software	US6359618	Using irradiance textures for photorealistic image generation
129	W110311-A	Graphics Software	US6753878	Parallel pipelined merge engines
130	W110311-A	Graphics Software	US7151864	Information research initiated from a scanned image media
131	W110311-A	Graphics Software	US7254279	Method for image stabilization by adaptive filtering
132	W110311-A	Graphics Software	US7714858	Distributed rendering of interactive soft shadows

133	W100108-A	Graphics Software - Motion Analysis	US6683968	Method for visual tracking using switching linear dynamic system models
134	W100108-A	Graphics Software - Motion Analysis	US6694044	Method for motion classification using switching linear dynamic system models
135	W100108-A	Graphics Software - Motion Analysis	US6778704	Method and apparatus for pattern recognition using a recognition dictionary partitioned into subcategories
136	W100108-A	Graphics Software - Motion Analysis	US6944317	Method for motion classification using switching linear dynamic systems models
137	W100108-A	Graphics Software - Motion Analysis	US6999601	Method for visual tracking using switching linear dynamic systems models
138	W091211-A	Secure Software Distribution	US5615061	Method of preventng software piracy by uniquely identifying the specific magnetic storage device the software is stored on
139	W091211-A	Secure Software Distribution	US6324691	Manufacture of software distribution media packages from components resident on a remote server source
140	W100702-B	Security	US7058685	Validation and audit of e-media delivery
141	W100702-B	Security	US7187772	Anonymous transactions based on distributed processing
142	W100122-A	Security	US7308707	Communication and authentication of a composite credential utilizing obfuscation
143	W100702-B	Security	US7472271	Methods and devices relating to distributed computing environments
144	W100115-B	Video Signal Processing	US5539473	Dot clock generation with minimal clock skew
145	W100115-B	Video Signal Processing	US5552783	Constant current voltage restoration
146	W100115-B	Video Signal Processing	US5629720	Display mode processor
147	W100115-B	Video Signal Processing	US7023470	Self-testing video display devices and method of use thereof
148	W100115-B	Video Signal Processing	US7038669	System and method for providing a reference video signal
149	W100402-A	Cooling System	US6422814	Fan brake for removable module
150	W100402-A	Cooling System	US6972956	Collapsible fan and system and method incorporating same
151	W100402-A	Cooling System	US7164582	Cooling system with submerged fan
152	W111021-A	Operating System	US5579478	System Administration Module For An Operating System Affords Graded Restricted Access Privileges
153	W111021-A	Operating System	US6453461	A METHOD AND APPARATUS FOR INTERFACING A GENERIC PROGRAM WITH ASL PLUG AND PLAY CODE IN AN ACPI OPERATING SYSTEM
154	W111021-A	Operating System	US6505258	COMPREHENSIVE INTERFACE BETWEEN BIOS AND DEVICE DRIVERS TO SIGNAL EVENTS
155	W111021-A	Operating System	US6549930	METHOD FOR SCHEDULING THREADS IN A MULTITHREADED PROCESSOR
156	W111021-A	Operating System	US7099978	Method And System Of Completing Pending I/O Device Reads In A Multiple-processor Computer System
157	W111021-A	Operating System	US7107579	Preserving Program Context When Adding Probe Routine Calls For Program Instrumentation
158	W111021-A	Operating System	US7149873	Methods And Apparatus For A Dual Address Space Operating System

159	W111021-A	Operating System	US7203775	System And Method For Avoiding Deadlock

Foreign Patents

Item #	Lot ID	Lot Title	Patent #	Patent Title
160			None (intentionally left blank)	
161	W091113-A	Network and Systems Management	JP3410748	COMPUTER SYSTEM MANAGER
162	W091113-A	Network and Systems Management	CH520769	COMPUTER SYSTEM MANAGER
163	W091113-A	Network and Systems Management	DE520769	COMPUTER SYSTEM MANAGER
164	W091113-A	Network and Systems Management	FR520769	COMPUTER SYSTEM MANAGER
165	W091113-A	Network and Systems Management	GB520769	COMPUTER SYSTEM MANAGER
166	W110415-A	Computer Network and System Management	MX PA/a/2004/007787	Method and System for Central Management of a Computer Network
167	PSL87	Microprocessor Instructions	JP3711206	Supertree
168	PSL87	Microprocessor Instructions	DE926594	Supertree
169	PSL87	Microprocessor Instructions	FR926594	Supertree
170	PSL87	Microprocessor Instructions	GB926594	Supertree
171	W100702-B	Security	EP1202202	Validation and audit of e-media delivery
172	PSL87	Microprocessor Architecture	FR752656	FAULT-TOLERANT MULTIPLE PROCESSOR SYSTEM WITH DUPLEXED PROCESSOR PAIRS
173	PSL87	Microprocessor Architecture	GB752656	FAULT-TOLERANT MULTIPLE PROCESSOR SYSTEM WITH DUPLEXED PROCESSOR PAIRS