PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the incorporation state of the assignee (mistakenly given as California) as set out in the assignment previously recorded on Reel 009944 Frame 0007. Assignor(s) hereby confirms the assignee's state of incorporation is Delaware.

CONVEYING PARTY DATA

Name	Execution Date
Milind Weling	04/29/1999

RECEIVING PARTY DATA

Name:	VLSI Technology, Inc.
Street Address:	1109 McKay Drive
City:	San Jose
State/Country:	CALIFORNIA
Postal Code:	95131

PROPERTY NUMBERS Total: 1

Property Type	Number
Patent Number:	6353261

CORRESPONDENCE DATA

Fax Number: (408)518-5671 Phone: 3474431592

Email: david.schaeffer@nxp.com

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

via US Mail.

Correspondent Name: David L. Schaeffer

411 East Plumeria Drive, MS41 Address Line 1: Address Line 2: NXP Semiconductors, IP&L Address Line 4: San Jose, CALIFORNIA 95134

NAME OF SUBMITTER: David L. Schaeffer

Total Attachments: 2

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REEL: 027924 FRAME: 0575

05-17-1999

UNITED S.



K OFFICE

Docket No.: VLSI-3285

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Assignment Branch, North Tower Building, Suite 10C35, Washington, D.C., 20231, on the below date of deposit. ame of Person

05/03/99

Making the Deposit:

TONY CHOU

Signature of the Person Making the Deposit:

The Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Deposit:

Please record the attached original document(s) or copy(ies) thereof:

Transmittal of an Assignment

1. Name of conveying Milind Weling

party(ies): Serial No.:

Filed:

05/03/99

Group Art Unit:

Examiner:

Name and Address of VLSI TECHNOLOGY, INC.

Receiving party(ies)

1109 McKay Drive, San Jose, California 95131

Nature of Conveyance: Assignment

Execution Date:

04/29/99

4. New Patent Application entitled:

METHOD AND APPARATUS FOR REDUCING INTERCONNECT

RESISTANCE USING AN INTERCONNECT WELL

5. Name and address of party to whom correspondence concerning document should be mailed:

Name:

Glenn D. Barnes

Wagner, Murabito & Hao Address

Two North Market Street, Third Floor, San Jose, CA 95113

Phone

(408) 938-9060

09/303871

6. Total Number of applications and patents involved: ONE

7. Fee Calculation (for other than a small entity) Assignment Recordation Fee, per property Total Fees (37 CFR 3.41)

is enclosed

1 X \$40.00

\$40.00

8. At any time during the pendency of this application, please charge any additional fees required or credit any E FOVE PRAYMANTS to Deposit Account 23-0085. A duplicate copy of this transmittal is enclosed. 05/12/1999 1

02 FC:581

9. Statement and signature:

X The amount of \$ 40.00

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date:

05/03/99

Name Glenn D. Barnes

Reg. No. 42,293

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PATENT REEL: 994**♠ हR≜M**E: 0006

REEL: 027924 FRAME: 0576

Docket No.: VLSI-3285

Assignment to VLSI Technology, Inc.

initials In consideration of good and valuable consideration, receipt of which is hereby acknowledged. I/we 23/2012date Delaware Milind Weling do hereby sell, assign and transfer unto VLSI Technology, Inc. (hereinafter called VLSI), a California Corporation having its principal place of business at 1109 McKay Drive, San Jose, California 95131, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled: METHOD AND APPARATUS FOR REDUCING INTERCONNECT RESISTANCE USING AN INTERCONNECT WELL x filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore; or and the inventions set forth filed on and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore; Further, I/we have agreed to assign to VLSI all Inventions (except as otherwise limited by law) which relate to VLSI business and which were first conceived or actually reduced to practice during my/our employment by VLSI; And for the above consideration, I/we agree promptly upon request of VLSI, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to VLSI, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by VLSI; I/we further covenant with VLSI, its successors, assigns, and legal representatives that no assignment, grant. mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned: In witness whereof, I/we hereunto set my/our hand(s) and seal. Inventor's Signature: Shiling Welling Date: April 29, 1999

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PATENT REEL: 9944-ቬዋ**ራ**ነላ**ፍ:** 0007

REEL: 027924 FRAME: 0577

RECORDED: 03/26/2012