## PATENT ASSIGNMENT

# Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

## **CONVEYING PARTY DATA**

Name	Execution Date
VirtenSys Limited	01/31/2012

## RECEIVING PARTY DATA

Name:	Micron Technology, Inc	
Street Address:	8000 S. Federal Way	
Internal Address:	MS 1-525	
City:	Boise	
State/Country:	IDAHO	
Postal Code:	83707	

## PROPERTY NUMBERS Total: 29

Property Type	Number
Patent Number:	6608829
Patent Number:	6791990
Patent Number:	6970469
Patent Number:	7088710
Patent Number:	7099355
Patent Number:	6822965
Patent Number:	7050448
Patent Number:	6704365
Patent Number:	7006580
Patent Number:	6876663
Patent Number:	6937133
Patent Number:	7002981
Patent Number:	7137122
Patent Number:	6622202
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Patent Number:	7016350
Patent Number:	7894563
Patent Number:	8085800
Patent Number:	8059671
Patent Number:	8050265
Patent Number:	8040907
Application Number:	11138325
Application Number:	12250994
Application Number:	12609930
Application Number:	12328381
Application Number:	12252835
Application Number:	12481312
Application Number:	12430480
Application Number:	12410704
Application Number:	12315723

#### **CORRESPONDENCE DATA**

Fax Number: (208)368-5606 Phone: 208-368-4547

Email: monicawood@micron.com

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

via US Mail.

Correspondent Name: Micron Technology, Inc. Address Line 1: 8000 S. Federal Way

Address Line 2: MS 1-525

Address Line 4: Boise, IDAHO 83707

ATTORNEY DOCKET NUMBER:	VTYS
NAME OF SUBMITTER:	Scott N. Barker

#### Total Attachments: 15

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#### ASSIGNMENT OF SELLER REGISTERED INTELLECTUAL PROPERTY

This Assignment of Seller Registered Intellectual Property effective as of January 31, 2012 (this "Assignment"), is made by VirtenSys Limited, a Private company incorporated in England and Wales whose registered number is 05640059 and whose registered office is at 5500 Lakeside, Cheadle Royal Business Park, Stockport SK8 3GR, United Kingdom (the "Company"), VirtenSys, Inc., a Delaware Corporation ("Sub", and collectively, with the Company, "Sellers") in favor of Micron Technology, Inc., a Delaware corporation ("Buyer"). All capitalized words and terms used in this Assignment and not defined herein shall have the respective meanings ascribed to them in the Asset Purchase Agreement effective as of January 19, 2012 by and between Seller, Buyer, and the other parties named therein (the "Agreement").

WHEREAS, pursuant to the Agreement, Sellers have agreed to sell, transfer, convey, assign and deliver to Buyer certain assets of Sellers, including all of the registered intellectual property assets of Sellers listed on <u>Exhibit A</u> hereto and all divisionals, continuations, continuations-in-part, reexaminations, reissues, and related international applications and patents thereof (collectively, the "Assigned IP"); and

WHEREAS, in consideration therefor, the Agreement requires that Buyer pay, and Buyer has agreed to the Purchase Price

NOW, THEREFORE, in consideration of the mutual promises set forth in the Agreement and other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Sellers hereby agree as follows:

- 1. Sellers do hereby sell, assign, transfer, and convey to Buyer all of Sellers' right, title and interest in and to the Assigned IP, the same to be held and enjoyed by Buyer for its own use and enjoyment and the use and enjoyment of its successors, assigns or other legal representatives, as fully and entirely as the same would have been held and enjoyed by Sellers if this assignment and sale had not been made. The foregoing includes, without limitation, the assignment, transfer and conveyance of all causes of actions, claims, and demands or other rights for, or arising from, any infringement, including past infringement, all rights of priority under any international conventions and any other international agreements to which the United States adheres, all income, royalties, damages, claims, and payments now or hereafter due or payable with respect to the Assigned IP, and all rights corresponding thereto throughout the world.
- 2. Sellers hereby covenant and agree that Sellers will not execute any writing or do any act whatsoever conflicting with this Assignment, and that Sellers will, at any time upon request, without further or additional consideration but at the sole expense of Buyer, execute such additional assignments and other writings and do such additional acts as Buyer, in its reasonable discretion, may deem necessary to perfect Buyer's enjoyment of this grant, and at the sole expense of Buyer, render reasonably necessary assistance in making application for and obtaining original, divisional, continuations, continuation-in-part, reexamined, reissued, or extended letters patent or of any and all foreign countries on said inventions, and, at Buyer's sole expense, in enforcing any rights or causes of action accruing as a result of such applications or

patents, by giving testimony in any proceedings or transactions involving such applications or patents, and/or by executing preliminary statements and other affidavits.

- 3. Buyer and Sellers authorize and request that the Commissioner of Patents and Trademarks of the United States, and the corresponding entities or agencies in any applicable foreign countries, record Buyer as the owner of record of the Assigned IP.
- 4. This Assignment, and all claims or actions that may be based upon, arise out of or relate to this Assignment, shall be governed by and construed in accordance with the internal laws of the State of Delaware, without giving effect to principles governing conflicts of laws.
- 5. This Assignment shall be binding upon and inure to the benefit of the Buyer and the Sellers and their respective successors and assigns.
- 6. If any provision of this Assignment or the application of any such provision to any person or circumstance shall be held invalid, illegal or unenforceable in any respect by a court of competent jurisdiction, such invalidity, illegality or unenforceability shall not affect any other provision hereof.
- 7. This Assignment may be executed in any number of counterparts, each of which shall be deemed to be one and the same instrument.

[Remainder of Page Left Intentionally Blank]

	eller and Buyer has caused this Assignment of igned by a duly authorized officer to be effective as
ACCEPTED: MICRON TECHNOLOGY, INC.	REVIEWED MTI Legal RIL
Name: Edward M Doller Title: UP Architecture Rid	
VIRTENYS LIMITED	
By:	
England and Wales whose registered number is 0: Lakeside, Cheadle Royal Business Park, Stockporday in person and acknowledged that he signed the	of VirtenSys Limited a Private company incorporated in 5640059 and whose registered office is at 5500 rt SK8 3GR, United Kingdom, appeared before me this he above and foregoing instrument as his free and each said entity pursuant to authority granted to him by
IN WITNESS WHEREOF, I have hereun, 2012.	to set my hand and notarial seal this day of
Notary Public	

[Signature Page to Assignment of Seller Registered Intellectual Property]

My commission expires on:

IN WITNESS WHEREOF, each of Seller and Buyer has caused this Assignment of Seller Registered Intellectual Property to be signed by a duly authorized officer to be effective as of the 31st day of January, 2012

ACCEPTED:

MICRON TECHNOLOGY, INC.

By: \_\_\_\_\_\_Name: Title:

NJAG" Notary V*IK TENSYS* <del>VIRTENYS</del> LIMITED

Name: 104 - 1 Nic 172122

Title: CHARRAN

I, a notary public, in and for the county and state aforesaid, do hereby certify that JOHN M NICHOLSON personally known to me to be the DIRECTOR of VirtenSys Limited a Private company incorporated in England and Wales whose registered number is 05640059 and whose registered office is at 5500 Lakeside, Cheadle Royal Business Park, Stockport SK8 3GR, United Kingdom, appeared before me this day in person and acknowledged that he signed the above and foregoing instrument as his free and voluntary act and as the free and voluntary act of each said entity pursuant to authority granted to him by the board of directors of each said entity for the uses and purposes therein set forth

IN WITNESS WHEREOF, I have hereunto set my hand and notarial seal this 30 day of January, 2012

Notary Public

My commission expires on: is fer life and

my current practising contificate expires

on 31sr October 2012

NOTAY OF PUBLIC PUBLIC

Nigel J A. Glassey Notary Public Stockport, Chashire, England SK6 880116

Signature Page to Assignment of the

Profocol No N 1500 /012/1610

lectual Property

VIRTENYS, INC.	
By: D	
Name! Stave Colkins	D. O'DONNELL
Name Stave Colkins Title: Director	Notary Public, State of Michigan
courses (NT	County of Berrien
STATE OF	My Commission Expires Dec. 11, 2012
) SS.	Acting in the County of GEELTEN
COUNTY OF BEERITEN )	
personally known to me to be the <u>Divictor</u> before me this day in person and acknowledged the free and voluntary act and as the free and voluntary him by the board of directors of each said entity for	and state aforesaid, do hereby certify that STRVE CALKEN of VirtenSys, Inc., a Delaware Corporation, appeared that he signed the above and foregoing instrument as his ry act of each said entity pursuant to authority granted to for the uses and purposes therein set forth.  Ato set my hand and notarial seal this 30 day of
Notary Public D. OWNA	VRL

My commission expires on: 13-11-12

## Exhibit A

## Patents:

Sellers Internal Patent No.	Patent/Application No.	Title
PX1	EP(UK)0974212	CLOSED-LOOP SYNCHRONISATION ARRANGEMENT FOR DATA TRANSMISSION SYSTEM
PX1	US6608829	Closed-loop synchronization arrangement for data transmission system
PX1	JP Application No. H10-542484 (1998) (JP Unexamined Publication No. 2001-519114A)	Synchronising Arrangements
PX1	DE69815521	SYNCHRONISATIONSVORRICHTUNG MIT GESCHLOSSENER SCHLEIFE FÜR EIN DATENÜBERTRAGUNGSSYTEM
PX1	CN Application No. 98805086.2 (CN Publication No. 1256039)	CLOSED-LOOP SYNCHRONISATION ARRANGEMENT FOR DATA TRANSMISSION SYSTEM
PX1	CA2285954	CLOSED-LOOP SYNCHRONISATION ARRANGEMENT FOR DATA TRANSMISSION SYSTEM
PX1	AU6846298	CLOSED-LOOP SYNCHRONISATION ARRANGEMENT FOR DATA TRANSMISSION SYSTEM
PX1	AT242942	SYNCHRONISATIONSVORRICHTUNG MIT GESCHLOSSENER SCHLEIFE FÜR EIN DATENÜBERTRAGUNGSSYTEM
PX1	WO9845973	CLOSED-LOOP SYNCHRONISATION ARRANGEMENT FOR DATA TRANSMISSION SYSTEM
PX1	GB0974212	CLOSED-LOOP SYNCHRONISATION ARRANGEMENT FOR DATA TRANSMISSION SYSTEM
PX1	GB2324214	CLOSED-LOOP SYNCHRONISATION ARRANGEMENT FOR DATA TRANSMISSION SYSTEM
PX2	GB2328590	DATA SWITCHING APPARATUS
PX2	AU8450998	Data switching apparatus
PX2	WO9909715	DATA SWITCHING APPARATUS
PX3	EP(UK)1013048	PRIORITY SELECTION MEANS FOR DATA TRANSMISSION APPARATUS
PX3	US6791990	Priority selection means for data transmission apparatus

[Signature Page to Assignment of Seller Registered Intellectual Property

Sellers Internal Patent No.	Patent/Application No.	Title
PX3	WO9914916	PRIORITY SELECTION MEANS FOR DATA TRANSMISSION APPARATUS
PX3	JP Registration No. 3825254B / JP Application No. 2000-512330 (JP Unexamined Publication No. 2001-517026A)	PRIORITY SELECTION MEANS FOR DATA TRANSMISSION APPARATUS
PX3	DE69815865	PRIORITÄTSAUSWAHLMITTEL FÜR DATENÜBERTRAGUNGSGERÄT
PX3	CN Application No. 98808999.8 (CN Publication No. 1269938A)	PRIORITY SELECTION MEANS FOR DATA TRANSMISSION APPARATUS
РХЗ	CN1115024C	PRIORITY SELECTION MEANS FOR DATA TRANSMISSION APPARATUS
PX3	CA2300379	PRIORITY SELECTION MEANS FOR DATA TRANSMISSION APPARATUS
PX3	AU8814298	Priority selection means for data transmission apparatus
PX3	AT243903	PRIORITÄTSAUSWAHLMITTEL FÜR DATENÜBERTRAGUNGSGERÄT
PX3	GB1013048	PRIORITY SELECTION MEANS FOR DATA TRANSMISSION APPARATUS
PX4	EP(UK)1057307	SCHEDULING MEANS FOR DATA SWITCHING APPARATUS
PX4	US6970469	Scheduling means for data switching apparatus
PX4	JP Application No. 2000-532953 (JP Unexamined Publication No. 2002-504779A)	SCHEDULING MEANS FOR DATA SWITCHING APPARATUS
	CN Application No. 99802997.1 (CN Publication No. 1291395A)	SCHEDULING MEANS FOR DATA SWITCHING APPARATUS
PX4	WO9943131	SCHEDULING MEANS FOR DATA SWITCHING APPARATUS
PX4	GB2334651	SCHEDULING MEANS FOR DATA SWITCHING APPARATUS
PX4	GB1057307	SCHEDULING MEANS FOR DATA SWITCHING APPARATUS
PX5	EP(UK)1142219	DATA SWITCHING METHOD AND

Sellers Internal Patent No.	Patent/Application No.	Title
		APPARATUS
PX5	US7088710	Method of transmitting information through data switching apparatus and apparatus therefor
PX5	WO0038375	DATA SWITCHING METHOD AND APPARATUS
PX5	JP Application No. 2000-590345 (JP Unexamined Publication No. 2002-533994A)	DATA SWITCHING METHOD AND APPARATUS
PX5	DE69908821	DATENVERMITTLUNGSVERFAHREN UND - VORRICHTUNG
PX5	CN Application No. 99814901.2 (CN Publication No. 1357188)	DATA SWITCHING METHOD AND APPARATUS
PX5	CA2353621	DATA SWITCHING METHOD AND APPARATUS
PX5	AU1062800	DATA SWITCHING METHOD AND APPARATUS
PX5	AT242946	DATENVERMITTLUNGSVERFAHREN UND - VORRICHTUNG
PX5	GB1142219	DATA SWITCHING METHOD AND APPARATUS
PX6	US7099355	Distributed hierarchical scheduling and arbitration for bandwidth allocation
PX6	EP(UK)1142229	DISTRIBUTED HIERARCHICAL SCHEDULING AND ARBITRATION FOR BANDWIDTH ALLOCATION
PX6	WO0038376	DISTRIBUTED HIERARCHICAL SCHEDULING AND ARBITRATION FOR BANDWIDTH ALLOCATION
PX6	JP Application No. 2000-590346 (JP Unexamined Publication No. 2002-533995A)	DISTRIBUTED HIERARCHICAL SCHEDULING AND ARBITRATION FOR BANDWIDTH ALLOCATION
PX6	CN Application No. 99814689.7 (CN Publication No. 1338168)	DISTRIBUTED HIERARCHICAL SCHEDULING AND ARBITRATION FOR BANDWIDTH ALLOCATION
PX6	CA2353622	DISTRIBUTED HIERARCHICAL SCHEDULING AND ARBITRATION FOR BANDWIDTH ALLOCATION

Sellers Internal Patent No.	Patent/Application No.	Title
PX6	AU1398900	DISTRIBUTED HIERARCHICAL SCHEDULING AND ARBITRATION FOR BANDWIDTH ALLOCATION
PX6	AT334534	VERTEILTE HIERARCHISCHE FOLGEPLANUNG UND ARBITRIERUNG ZUR BANDBREITENZUORDNUNG
PX6	GB1142229	DISTRIBUTED HIERARCHICAL SCHEDULING AND ARBITRATION FOR BANDWIDTH ALLOCATION
PX7	EP(UK)1142220	APPROXIMATE STATE CONTROL MECHANISM
PX7	US6822965	Approximate state control mechanism
PX7	WO0038377	APPROXIMATE STATE CONTROL MECHANISM
PX7	JP Application No. 2000-590347 (JP Unexamined Publication No. 2002-533996A)	APPROXIMATE STATE CONTROL MECHANISM
PX7	DE69909518	APPROXIMATIVER ZUSTAND- STEUERUNGSMECHANISMUS
PX7	CN Application No. 99814799.0 (CN Publication No. 1331875)	APPROXIMATE STATE CONTROL MECHANISM
PX7	CA2353423	APPROXIMATE STATE CONTROL MECHANISM
PX7	AU1673300	APPROXIMATE STATE CONTROL MECHANISM
PX7	AT244967	APPROXIMATIVER ZUSTAND- STEUERUNGSMECHANISMUS
PX7	GB1142220	APPROXIMATE STATE CONTROL MECHANISM
PX8	EP(UK)1269693	DATA SWITCHING ARBITRATION ARRANGEMENTS
PX8	US7050448	Data switching arbitration arrangements
PX8	WO0176157	DATA SWITCHING ARBITRATION ARRANGEMENTS
PX8	AU3764201	DATA SWITCHING ARBITRATION ARRANGEMENTS
PX8	AT371320	VORRICHTUNG ZUR DATENVERMITTLUNGSARBITRIERUNG
PX8	GB1269693	DATA SWITCHING ARBITRATION ARRANGEMENTS

Sellers Internal Patent No.	Patent/Application No.	Title	
PX9	GB2360427	DATA TRANSMISSION DRIVER DEVICE	
PX9	US6704365	Data transmission driver device	
PX9	US7006580	Method for controlling a data transmission pre-emphasis unit and a data transmission system employing the method	
PX9	WO0169871	VARIABLE GAIN LINE DRIVER DEPENDING ON FREQUENCY	
PX9	EP1264454	VARIABLE GAIN LINE DRIVER DEPENDING ON FREQUENCY	
PX9	AU3764501	VARIABLE GAIN LINE DRIVER DEPENDING ON FREQUENCY	
PX10	EP(UK)1287648	SWITCHING SYSTEM	
PX10	US6876663	Switching system	
PX10	WO0195576	SWITCHING SYSTEM	
PX10	JP Application No. 2002-502991 (JP Unexamined Publication No. 2003-536323A)	SWITCHING SYSTEM	
PX10	DE60126934	VERMITTLUNGSSYSTEM	
PX10	CN Application No. 01813144.1 (CN Publication No. 1443413)	SWITCHING SYSTEM	
PX10	AU6250701	SWITCHING SYSTEM	
PX10	GB1287648	SWITCHING SYSTEM	
PX11	EP(UK)1284071	APPARATUS AND METHOD FOR RESOURCE ARBITRATION	
PX11	US6937133	Apparatus and method for resource arbitration	
PX11	WO0189159	APPARATUS AND METHOD FOR RESOURCE ARBITRATION	
PX11	AU5497401	APPARATUS AND METHOD FOR RESOURCE ARBITRATION	
PX11	GB1284071	APPARATUS AND METHOD FOR RESOURCE ARBITRATION	
PX12	EP(UK)1284070	METHOD AND ARBITRATION UNIT FOR DIGITAL SWITCH	
PX12	US7002981	Method and arbitration unit for digital switch	
PX12	WO0191386	METHOD AND ARBITRATION UNIT FOR DIGITAL SWITCH	
PX12	AU4084801	METHOD AND ARBITRATION UNIT FOR DIGITAL SWITCH	

Sellers Internal Patent No.	Patent/Application No.	Title	
PX12	GB1284070	METHOD AND ARBITRATION UNIT FOR DIGITAL SWITCH	
PX13	EP1399817	METHODS AND APPARATUS FOR REGULATING PROCESS STATE CONTROL MESSAGES	
PX13	US7137122	Methods and apparatus for regulating process state control messages	
PX13	GB2368247	METHODS AND APPARATUS FOR REGULATING PROCESS STATE CONTROL MESSAGES	
PX13	WO0233548	METHODS AND APPARATUS FOR REGULATING PROCESS STATE CONTROL MESSAGES	
PX13	AU9009201	METHODS AND APPARATUS FOR REGULATING PROCESS STATE CONTROL MESSAGES	
PX13	AT486317	VERFAHREN UND GERÄT ZUM STEUERN VON PROZESSZUSTANDS- STEUERUNGSNACHRICHTEN	
PX13	GB1399817	METHODS AND APPARATUS FOR REGULATING PROCESS STATE CONTROL MESSAGES	
PX14	EP1346365	METHOD AND DEVICE FOR OPERATING A RAM MEMORY	
PX14	US6622202	Method and device for operating a RAM memory	
PX14	WO02054406	METHOD AND DEVICE FOR OPERATING A RAM MEMORY	
PX15	EP1356647	A DATA SWITCH AND A METHOD FOR CONTROLLING THE DATA SWITCH	
PX15	US7016350	Data switch and a method for controlling the data switch	
PX15	WO02063826	A DATA SWITCH AND A METHOD FOR CONTROLLING THE DATA SWITCH	
PX15	AU2002222309	A DATA SWITCH AND A METHOD FOR CONTROLLING THE DATA SWITCH	
PX16	US7006580	Method for controlling a data transmission pre-emphasis unit and a data transmission system employing the method	
PX16	GB2360427	DATA TRANSMISSION DRIVER DEVICE	
PX16	US6704365	Data transmission driver device	
PX16	WO0169871	VARIABLE GAIN LINE DRIVER DEPENDING ON FREQUENCY	
PX16	EP1264454	VARIABLE GAIN LINE DRIVER DEPENDING	

Sellers Internal Patent No.	Patent/Application No.	Title	
		ON FREQUENCY	
PX16	AU3764501	VARIABLE GAIN LINE DRIVER DEPENDING ON FREQUENCY	
PX19	US Application No. 11/138325 (US Publication No. 2005/0265487	Method of Sampling Data and a Circuit for Sampling Data	
PX20	US7894563	Clock recovery circuit and a method of generating a recovered clock signal	
V001	US8085800	Queuing method	
V001	GB2452913	QUEUING METHOD	
V001	WO2009037422	QUEUING METHOD	
V001	JP Application No. 2010-525417 (JP Unexamined Publication No. 2010-539837A)	Transmitting queued data packets according to priority	
V001	CN Application No. 2008-80115667.9 (CN Publication No. 101878621A)	QUEUING METHOD	
V002	US8059671	Switching device	
V002	GB Application No. 0812909.0 (GB Publication No. 2461881)	Switching device	
V002	WO2010007339	SWITCHING DEVICE	
V003	US8050265	Multicast data packet switching method	
V003	GB2456180	SWITCHING METHOD FOR DATA PACKETS	
V003	WO2009087344	SWITCHING METHOD	
V004	US8040907	Switching method	
V004	GB Application No. 0812404.2 (GB Publication No. 2461693)		
V004	WO2010004257	SWITCHING DEVICE, METHOD AND COMPUTER PROGRAM	
V005	US7894563	Clock recovery circuit and a method of generating a recovered clock signal	
V006	US Application No. 12/609930 (2011/0069710	Switching Method	
V006	GB2473675	SWITCHING METHOD	
	1		

Sellers Internal Patent No.	Patent/Application No.	Title	
V007	US Application No. 12/328381 (US Publication No. 2010/0146160)	METHOD AND APPARATUS FOR PROVIDING DATA ACCESS	
V007	GB Application No. 0821886.9 (GB Publication No. 2465801)	METHOD AND APPARATUS FOR PROVIDING DATA ACCESS	
V007	WO2010063985	METHOD AND APPARATUS FOR I/O DEVICE VIRTUALIZATION	
V008	US Application No. 12/252835 (US Publication No. 2010/0098104)	SWITCHING DEVICE	
V008	GB Application No. 0818634.8 (GB Publication No. 2464310)	SWITCHING DEVICE	
V008	WO2010040983	SWITCHING DEVICE	
V009	US Application No. 12/481312 (US Publication No. 2009/0307388)	METHODS OF PROVIDING ACCESS TO I/O DEVICES	
V009	GB2460841	METHODS OF PROVIDING ACCESS TO I/O DEVICES	
V009	WO2009150414	METHODS OF PROVIDING ACCESS TO I/O DEVICES	
V010	US Application No. 12/430480 (US Publication No. 2009/0268738)	METHOD OF PROCESSING DATA PACKETS	
V010	GB2460014	METHOD OF PROCESSING DATA PACKETS	
V010	WO2009133337	METHOD OF PROCESSING DATA PACKETS	
V011	US Application No. 12/410704 (US Publication No. 2009/0252167)	QUEUE PROCESSING METHOD	
V011	GB Application No. 0806145.9 (GB Publication No. GB2458952)	QUEUE PROCESSING METHOD	
V011	WO2009122122	QUEUE PROCESSING METHOD	
V012	US Application No. 12/315723 (US	Control path I/O virtualisation	

Sellers Internal Patent No.	Patent/Application No.	Title
	Publication No. 2009/0150563)	
V012	GB Application No 0723939.5 (GB Publication No. 2455347)	CONTROL PATH I/O VIRTUALISATION
V012	WO2009071941	CONTROL PATH I/O VIRTUALISATION
V012	CN Application No. 2008-80119626.7 (CN Publication No. 101889263A)	CONTROL PATH I/O VIRTUALISATION
V012	JP Application No. 2010-536539 (JP Unexamined Publication No. 2011-507065A)	CONTROL PATH I/O VIRTUALISATION
V013	US Application No. 12/250994 (US Publication No. 2009/0103556)	DATA SWITCH
V013	GB Application No. 0720148.6 (GB Publication No. 2453732)	Data Switch
V013	WO2009050501	TIMING IN A DATA SWITCH
	US Application No. 13/275296	Connection Method
	GB Application No. 1117230.1	Connection Method

# Trademarks:

Domain	Number	Trademark	Classes
US	3964139	VIRTENSYS VIO	9, 42
US	3994611	VIRTENSYS	9, 42
US	3964140	Virtensys logo	9, 42
CTM	8551061	VIRTENSYS VIO	9, 42

СТМ	8551707	Virtensys logo	9, 42
CTM	8551244	VIRTENSYS	9, 42
UK	2450827A	VirtenSys	9, 42
UK	2450827B	Virtensys	9, 42
UK	2450827C	Vir10sys	9, 42
UK	2450827D	Virtensys logo	9, 42

Domain Names:

virtensys.com

vir10sys.com

vertensys.com

RECORDED: 04/12/2012 REEL: 028037 FRAME: 0018