

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT														
NATURE OF CONVEYANCE:	ASSIGNMENT														
CONVEYING PARTY DATA															
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Jusuke OGURA</td> <td>01/07/2009</td> </tr> <tr> <td>Hikaru KOKURA</td> <td>01/07/2009</td> </tr> <tr> <td>Hideyuki KOJIMA</td> <td>01/09/2009</td> </tr> <tr> <td>Toru ANEZAKI</td> <td>01/08/2009</td> </tr> <tr> <td>Hiroyuki OGAWA</td> <td>01/07/2009</td> </tr> <tr> <td>Junichi ARIYOSHI</td> <td>01/07/2009</td> </tr> </tbody> </table>		Name	Execution Date	Jusuke OGURA	01/07/2009	Hikaru KOKURA	01/07/2009	Hideyuki KOJIMA	01/09/2009	Toru ANEZAKI	01/08/2009	Hiroyuki OGAWA	01/07/2009	Junichi ARIYOSHI	01/07/2009
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Hiroyuki OGAWA	01/07/2009														
Junichi ARIYOSHI	01/07/2009														
RECEIVING PARTY DATA															
<table border="1"> <tr> <td>Name:</td> <td>FUJITSU SEMICONDUCTOR LIMITED</td> </tr> <tr> <td>Street Address:</td> <td>2-10-23 Shin-Yokohama Kohoku-ku</td> </tr> <tr> <td>City:</td> <td>Yokohama-shi, Kanagawa</td> </tr> <tr> <td>State/Country:</td> <td>JAPAN</td> </tr> <tr> <td>Postal Code:</td> <td>222-0033</td> </tr> </table>		Name:	FUJITSU SEMICONDUCTOR LIMITED	Street Address:	2-10-23 Shin-Yokohama Kohoku-ku	City:	Yokohama-shi, Kanagawa	State/Country:	JAPAN	Postal Code:	222-0033				
Name:	FUJITSU SEMICONDUCTOR LIMITED														
Street Address:	2-10-23 Shin-Yokohama Kohoku-ku														
City:	Yokohama-shi, Kanagawa														
State/Country:	JAPAN														
Postal Code:	222-0033														
PROPERTY NUMBERS Total: 1															
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>13455633</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	13455633										
Property Type	Number														
Application Number:	13455633														
CORRESPONDENCE DATA															
Fax Number:	(202)822-1111														
Phone:	202-822-1100														
Email:	PatentMail@whdapatentlaw.com														
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>															
Correspondent Name:	Westerman, Hattori, Daniels & Adrian, LL														
Address Line 1:	1250 Connecticut Avenue, NW														
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ATTORNEY DOCKET NUMBER:	090095A														

OP \$40.00 13455633

NAME OF SUBMITTER:

Stephen G. Adrian

Total Attachments: 5

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U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter, "ASSIGNOR") by
(Insert Name(s) & Address(es) of ASSIGNEE(S))

FUJITSU MICROELECTRONICS LIMITED

7-1, Nishi-Shinjuku 2-chome, Shinjuku-ku, Tokyo 163-0722 Japan

(hereinafter, "ASSIGNEE"), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled
(Title of Invention)

METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

relating to International Patent Application PCT/JP _____ / _____ and/or for which application for Letters Patent of the United States was executed on even date herewith or, if not so executed, was:

(a) executed on _____ (Insert date of execution of application, if not concurrent)

(b) filed on _____

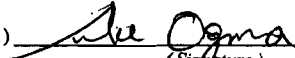

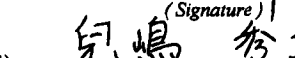
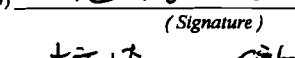
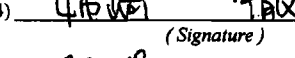
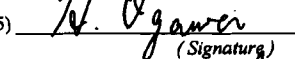
Serial No. _____ / _____

Assignee's attorney is hereby authorized to insert in (b) the specified data, when known.

and to said application and all Letters Patent(s) of the United States granted on said application and any continuation, division, renewal, substitute, reissue or reexamination application based thereon, for the full term or terms for which the said Letters Patent(s) may be granted and including any extensions thereof (collectively, hereinafter, "said application(s) and Letters Patent(s)").

The ASSIGNOR agree(s), when requested by said ASSIGNEE and without charge to but at the expense of said ASSIGNEE, to do all acts which the ASSIGNEE may deem necessary, desirable or expedient, for securing, maintaining and enforcing protection for said invention, including in the preparation and prosecution of said application(s) and the issuance of said Letters Patent(s), in any interference, reissue, reexamination, or public use proceeding, and in any litigation or other legal proceeding which may arise or be declared in relation to same, such acts to include but not be limited to executing all papers, including separate assignments and declarations, taking all rightful oaths, providing sworn testimony, and obtaining and producing evidence.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

1) <u></u> (Signature)	<u>Jusuke OGURA</u> (Typed Name)	<u>January 7, 2009</u> (Date)
2) <u></u> (Signature)	<u>Hikaru KOKURA</u> (Typed Name)	<u>Jan. 7, 2009</u> (Date)
3) <u></u> (Signature)	<u>Hideyuki KOJIMA</u> (Typed Name)	<u>Jan. 9, 2009</u> (Date)
4) <u></u> (Signature)	<u>Toru ANEZAKI</u> (Typed Name)	<u>Jan. 8, 2009</u> (Date)
5) <u></u> (Signature)	<u>Hiroyuki OGAWA</u> (Typed Name)	<u>Jan. 7, 2009</u> (Date)
6) <u></u> (Signature)	<u>Junichi ARIYOSHI</u> (Typed Name)	<u>Jan. 7, 2009</u> (Date)

TO:WESTERMAN, HATTORI, DANIELS & ADRIAN COMPANY:1250 CONNECTICUT AVE., N.W.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE



500779891A

FEBRUARY 12, 2009

PTAS

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WASHINGTON, DC 20036

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 571-272-3350. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, MAIL STOP: ASSIGNMENT SERVICES BRANCH, P.O. BOX 1450, ALEXANDRIA, VA 22313.

RECORDATION DATE: 02/12/2009

REEL/FRAME: 022249/0906

NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).
DOCKET NUMBER: 090095

ASSIGNOR:

OGURA, JUSUKE

DOC DATE: 01/07/2009

ASSIGNOR:

KOKURA, HIKARU

DOC DATE: 01/07/2009

ASSIGNOR:

KOJIMA, HIDEYUKI

DOC DATE: 01/09/2009

ASSIGNOR:

ANEZAKI, TORU

DOC DATE: 01/08/2009

ASSIGNOR:

OGAWA, HIROYUKI

DOC DATE: 01/07/2009

ASSIGNOR:

ARIYOSHI, JUNICHI

DOC DATE: 01/07/2009

P.O. Box 1450, Alexandria, Virginia 22313-1450 - www.uspto.gov

TO:WESTERMAN, HATTORI, DANIELS & ADRIAN COMPANY:1250 CONNECTICUT AVE., N.W.

022249/0906 PAGE 2

ASSIGNEE:

FUJITSU MICROELECTRONICS LIMITED
7-1, NISHI-SHINJUKU 2-CHOME
SHINJUKU-KU, TOKYO, JAPAN 163-0722

SERIAL NUMBER: 12369859

FILING DATE:

PATENT NUMBER:

ISSUE DATE:

TITLE: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

ASSIGNMENT SERVICES BRANCH
PUBLIC RECORDS DIVISION

TO:WESTERMAN, HATTORI, DANIELS & ADRIAN, LL COMPANY:1250 CONNECTICUT AVENUE,



UNITED STATES PATENT AND TRADEMARK OFFICE

UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE



501228198A

JULY 09, 2010

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WESTERMAN, HATTORI, DANIELS & ADRIAN, LL
1250 CONNECTICUT AVENUE, NW
SUITE-700
WASHINGTON, DC 20036-2657

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RECORDATION DATE: 07/09/2010

REEL/FRAME: 024651/0744

NUMBER OF PAGES: 46

BRIEF: CHANGE OF NAME (SEE DOCUMENT FOR DETAILS).

DOCKET NUMBER: 100607

ASSIGNOR:

FUJITSU MICROELECTRONICS LIMITED

DOC DATE: 04/01/2010

ASSIGNEE:

FUJITSU SEMICONDUCTOR LIMITED
2-10-23 SHIN-YOKOHAMA KOHOKU-KU
YOKOHAMA-SHI, KANAGAWA, JAPAN

222-0033

SERIAL NUMBER: 10336716

FILING DATE: 01/06/2003

PATENT NUMBER:

ISSUE DATE:

TITLE: LEAD FRAME AND MANUFACTURING METHOD THEREOF AND A SEMICONDUCTOR
DEVICE

TO: WESTERMAN, HATTORI, DANIELS & ADRIAN, LL COMPANY: 1250 CONNECTICUT AVENUE,

024651/0744 PAGE 25

SERIAL NUMBER: 12364830 FILING DATE: 02/03/2009
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR
DEVICE MANUFACTURING SYSTEM

SERIAL NUMBER: 12365276 FILING DATE: 02/04/2009
PATENT NUMBER: ISSUE DATE:
TITLE: MANUFACTURE METHOD FOR SEMICONDUCTOR DEVICE HAVING MIM CAPACITOR,
AND SEMICONDUCTOR DEVICE

SERIAL NUMBER: 12366015 FILING DATE: 02/05/2009
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE CONTAINING THIN FILM CAPACITOR AND MANUFACTURE
METHOD FOR THIN FILM CAPACITOR

SERIAL NUMBER: 12366020 FILING DATE: 02/05/2009
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING SEMICONDUCTOR
DEVICE

SERIAL NUMBER: 12369297 FILING DATE: 02/11/2009
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME

SERIAL NUMBER: 12369859 FILING DATE: 02/12/2009
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

SERIAL NUMBER: 12379488 FILING DATE: 02/24/2009
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

SERIAL NUMBER: 12379832 FILING DATE: 03/03/2009
PATENT NUMBER: ISSUE DATE:
TITLE: P-CHANNEL MOS TRANSISTOR AND FABRICATION PROCESS THEREOF

SERIAL NUMBER: 12379967 FILING DATE: 03/05/2009
PATENT NUMBER: ISSUE DATE:
TITLE: MONITOR PATTERN OF SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING
SEMICONDUCTOR DEVICE

SERIAL NUMBER: 12382532 FILING DATE: 03/18/2009
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE FOR FINGERPRINT RECOGNITION

SERIAL NUMBER: 12392699 FILING DATE: 02/25/2009
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

SERIAL NUMBER: 12395233 FILING DATE: 02/27/2009
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF THE SEMICONDUCTOR
DEVICE

PATENT