501922707 05/16/2012

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	EXECUTIVE ORDER 9424, CONFIRMATORY LICENSE

CONVEYING PARTY DATA

Name	Execution Date
Purdue University	03/03/2012

RECEIVING PARTY DATA

Name:	National Science Foundation
Street Address:	4201 Wilson Blvd
Internal Address:	Room 1265
City:	Arlington
State/Country:	VIRGINIA
Postal Code:	22230

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	13345588

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PATENT 501922707 REEL: 028222 FRAME: 0079

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RECORDED: 05/16/2012

Sign and submit the executed document to the appropriate funding agency (e.g. upload in iEdison).
Invention Title: Proposal for an All-Spin Transistor with Built-in Memory
Inventor(s): Behtash Behin-Aein, Supriyo Datta, Sayeef Salahuddin, Srikant Srinivasan, Angik Sarka
U.S. Filing/Issue Date: 1/6/2012
Patent or Application Serial No.: 13/345,588
Grant/Contract Number(s): EEC0228390; EEC0738513
Foreign Applications filed/intended in (countries):
The invention identified above is a Subject Invention under 35 U.S.C. 200, et seq., and the Standard Patent Rights clause at 37 CFR 401.14, FAR 52.227-11 or FAR 52.227-12 (if applicable) which are included among the terms of the above identified grant or contract award from the United State Government. This document is confirmatory of:
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For PURDUE UNIVERSITY (Grantee/Contractor Organization)
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