

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT												
NATURE OF CONVEYANCE:	ASSIGNMENT												
CONVEYING PARTY DATA													
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Yoshihiro YONEDA</td> <td>05/16/2012</td> </tr> <tr> <td>Hirohiko KOBAYASHI</td> <td>05/17/2012</td> </tr> <tr> <td>Kenji KOYAMA</td> <td>05/21/2012</td> </tr> <tr> <td>Masaki YANAGISAWA</td> <td>05/16/2012</td> </tr> <tr> <td>Kenji HIRATSUKA</td> <td>05/22/2012</td> </tr> </tbody> </table>		Name	Execution Date	Yoshihiro YONEDA	05/16/2012	Hirohiko KOBAYASHI	05/17/2012	Kenji KOYAMA	05/21/2012	Masaki YANAGISAWA	05/16/2012	Kenji HIRATSUKA	05/22/2012
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RECEIVING PARTY DATA													
Name:	SUMITOMO ELECTRIC INDUSTRIES, LTD.												
Street Address:	5-33, Kitahama 4-chome, Chuo-ku												
City:	Osaka-shi, Osaka												
State/Country:	JAPAN												
Postal Code:	541-0041												
PROPERTY NUMBERS Total: 1													
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>13479567</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	13479567								
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Application Number:	13479567												
CORRESPONDENCE DATA													
Fax Number:	(202)263-4329												
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Email:	aroberts@sgrlaw.com												
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>													
Correspondent Name:	Michael A. Makuch												
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Address Line 2:	Smith, Gambrell & Russell, llp												
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ATTORNEY DOCKET NUMBER:	033035 M 387												
NAME OF SUBMITTER:	Ayisha M. Roberts												
Total Attachments: 2 source=033035M387_Assignment#page1.tif source=033035M387_Assignment#page2.tif													

OP \$40.00 13479567

ASSIGNMENT

This assignment agreement is applicable to an invention entitled (Invention Title) _____

METHOD OF MAKING SEMICONDUCTOR OPTICAL INTEGRATED DEVICE

The PATENT RIGHTS referred to in this agreement are:

- (check one) a patent application for this invention, executed by the ASSIGNOR(S) concurrently with this assignment.
- U.S. patent application Serial No. _____, filed on _____.
- a U.S. patent application based on PCT International Application No. _____ filed on (date) _____ (U.S. patent application Serial No. _____, if known).
- U.S. patent No. _____, issued _____.

The PATENT RIGHTS also include all divisions, reissues, continuations and extensions of the patents and patent applications identified above.

The PATENT RIGHTS assigned under this agreement are:

- (check one) U.S. patent rights only.
- Worldwide patent rights. In this case, the assignee shall have the right to claim the benefit of the filing date of any U.S. or foreign patent application for this invention.

The ASSIGNOR(S) referred to in this agreement is (or are) the inventor(s) whose signatures appear on page 2 of this Assignment and any Supplemental Sheet(s).

The ASSIGNEE referred to in this agreement is:

(Name of Assignee) SUMITOMO ELECTRIC INDUSTRIES, LTD.

(Address) 5-33, Kitahama 4-chome, Chuo-ku, Osaka-shi, Osaka 541-0041, Japan

The ASSIGNEE is:

- (check one) An Individual.
- A Partnership
- A Corporation of Japan (specify state or country)
- (other)

The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and valuable consideration, receipt of which is acknowledged, hereby assign(s) the following rights to the ASSIGNEE, its successors and assigns:

- the full and exclusive right to the invention;
- the entire right, title and interest in and to the PATENT RIGHTS;
- the right to sue and recover for any past infringement; and,
- the right to claim priority under 35 USC 119, 35 USC 120, or any other applicable provisions, based on any earlier patent applications for this invention.

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO
ASSIGNEE: SUMITOMO ELECTRIC INDUSTRIES, LTD.
INVENTION TITLE: METHOD OF MAKING SEMICONDUCTOR OPTICAL INTEGRATED
DEVICE

As to all U.S. patent applications assigned under this agreement, the ASSIGNOR(S) hereby authorize(s) and request(s) the Commissioner of Patents and Trademarks to issue all Letters Patent to the ASSIGNEE as the assignee of the entire right, title and interest, for the sole use and enjoyment of said ASSIGNEE, its successors and assigns.

Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any facts known to the ASSIGNOR(S) respecting said invention, and testify in any legal proceedings, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause any and all Letters Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

The ASSIGNORS authorize any attorneys and agents who have a power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

Yoshihiro YONEDA
Name of sole or first inventor

Yoshihiro Yoneda
Signature

May 16, 2012
Date

Hirohiko KOBAYASHI
Name of second inventor, if any

Hirohiko Kobayashi
Signature

May 17, 2012
Date

Kenji KOYAMA
Name of third inventor, if any

Kenji Koyama
Signature

May 21, 2012
Date

Masaki YANAGISAWA
Name of fourth inventor, if any

Masaki Yanagisawa
Signature

May 16, 2012
Date

Kenji HIRATSUKA
Name of fifth inventor, if any

Kenji Hiratsuka
Signature

May 22, 2012
Date