

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
NXP B.V.	09/26/2011
RECEIVING PARTY DATA	
Name:	Callahan Cellular L.L.C.
Street Address:	2711 Centerville Road
Internal Address:	Suite 400
City:	Wilmington
State/Country:	DELAWARE
Postal Code:	19808
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	13481914
CORRESPONDENCE DATA	
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ATTORNEY DOCKET NUMBER:	6657-04303
NAME OF SUBMITTER:	Dean M. Munyon
Total Attachments: 84 source=NXP Multiple Assgt to Callahan Cellular L.L.C. (Exh B) Reduced#page1.tif	

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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, NXP B.V., a company formed in The Netherlands, with an office at High Tech Campus 60, 5656 AG Eindhoven, The Netherlands ("*Assignor*"), does hereby sell, assign, transfer, and convey unto Callahan Cellular L.L.C., a Delaware limited liability company, with an address at 2711 Centerville Rd, Suite 400, Wilmington, DE 19808 ("*Assignee*"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "*Patent Rights*");

(a) the provisional patent applications, patent applications and patents listed in the table below (the "*Patents*");

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
7750866	US	05/16/2006	Diversity antenna assembly for wireless communication equipment Rambem, Vincent
DE602006007992.3	DE	05/17/2006	Integrated circuit with internal communication network Rijkema, Edwin
FR1889419	FR	05/17/2006	Integrated circuit with internal communication network Rijkema, Edwin
GB1889419	GB	05/17/2006	Integrated circuit with internal communication network Rijkema, Edwin
7907610	US	05/17/2006	Integrated circuit with internal communication network Rijkema, Edwin
CN/ZL200680018032.8	CN	05/17/2006	Integrated circuit with internal communication network Rijkema, Edwin
7728754	US	11/08/2006	Integrating analog to digital converter Gerfers, Friedel
7570097	US	12/12/2006	Electronic circuit with low noise delay circuit Frambach, Johannes Petrus Antonius
CN200680047943.3	CN	12/12/2006	Electronic circuit with low noise delay circuit Frambach, Johannes P. A.
EP06842462.1	EP	12/12/2006	Electronic circuit with low noise delay circuit Frambach, Johannes P. A.
JP2008-546728	JP	12/12/2006	Electronic circuit with low noise delay circuit Johannes Petrus Antonius Frambach
DE602007002487.0	DE	03/26/2007	Radio frequency PWM & PPM modulator Vromans, Jan
FR2005574	FR	03/26/2007	Radio frequency PWM & PPM modulator Vromans, Jan

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
GB2005574	GB	03/26/2007	Radio frequency PWM & PPM modulator Vromans, Jan
7898352	US	03/26/2007	Radio frequency PWM & PPM modulator Vromans, Jan
CN200780010863.5	CN	03/26/2007	Radio frequency PWM & PPM modulator Vromans, Jan
JP2009-502295	JP	03/26/2007	Radio frequency PWM & PPM modulator Vromans, Jan
7215940	US	02/28/2003	Integrated circuit Goddard, Simon Peter
GB0729606	GB	08/24/1995	Microcontroller with a reconfigurable program status word Wang, Johannes
5664156	US	09/16/1994	Microcontroller with a reconfigurable program status word Wang, Johannes
JP3705811	JP	08/24/1995	Microcontroller with a reconfigurable program status word John Wang [Ata Khan]
KR10-0385493	KR	08/24/1995	Microcontroller with a reconfigurable program status word Wang, Johannes
5487045	US	09/16/1994	Sense amplifier having variable sensing load for non-volatile memory Trodden, Thomas J.
JP3706135	JP	08/28/1995	Sense amplifier having variable sensing load for non-volatile memory Thomas John Trodden
DE69524200.8	DE	08/28/1995	Sense amplifier for non-volatile semiconductor memory Trodden, Thomas J.
FR0729633	FR	08/28/1995	Sense amplifier for non-volatile semiconductor memory Trodden, Thomas J.
GB0729633	GB	08/28/1995	Sense amplifier for non-volatile semiconductor memory Trodden, Thomas J.
5619663	US	09/16/1994	Computer instruction prefetch system Mizrahi-Shalom, Ori K.
6308241	US	12/22/1997	On-chip cache file register for minimizing CPU idle cycles during cache refill Simovich, Slobodan
CNZL98804158.8	CN	10/05/1998	Extra register minimizes CPU idle cycles during cache refill Simovich, S.
KR10-0618057	KR	10/05/1998	Extra register minimizes CPU idle cycles during cache refill Simovich, Slobodan
DE69813196.7	DE	10/05/1998	Extra register minimizes CPU idle cycles during cache refill

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
FR0968470	FR	10/05/1998	Simovich, Slobodan Extra register minimizes CPU idle cycles during cache refill
GB0968470	GB	10/05/1998	Simovich, Slobodan Extra register minimizes CPU idle cycles during cache refill
5510972	US	06/29/1994	Simovich, Slobodan Bridge rectifier circuit having active switches and an active control circuit
DE69505633.6	DE	06/12/1995	Wong, Stephen Low loss rectifier bridge
FR0715780	FR	06/12/1995	Wong, Stephen Low loss rectifier bridge
GB0715780	GB	06/12/1995	Wong, Stephen Low loss rectifier bridge
JP3425151	JP	06/12/1995	Wong, Stephen Bridge rectifier circuit having active switches and an active control circuit
5687065	US	11/28/1994	Wong, Stephen L. Pre-regulator with light switch to limit voltage ringing on turn-off
KR10-0429323	KR	09/13/1996	Majid, Naveed Register file read/write cell
DE69619794.4	DE	09/13/1996	Ang, Michael Anthony Register file read/write cell
FR0793847	FR	09/13/1996	Ang, Michael Register file read/write cell
GB0793847	GB	09/13/1996	Ang, Michael Register file read/write cell
GB0935843	GB	07/27/1998	Ang, Anthony Michael Low power stand-by for switched-mode power supply circuit with burst mode operation
5812383	US	07/31/1997	Majid, Naveed Low power stand-by for switched-mode power supply circuit with burst mode operation
JP4315296	JP	07/27/1998	Majid, Naveed Low power stand-by for switched-mode power supply circuit with burst mode operation
5995384	US	07/31/1997	Naveed Majid [Antonius Moberg] [Erwin Seinen] Functional on/off switch for switched-mode power supply circuit with burst mode operation
			Majid, Naveed

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
GB0941573	GB	07/27/1998	Circuit with burst mode operation Malid, Naveed
JP4430746	JP	07/27/1998	Functional on/off switch for switched-mode power supply circuit with burst mode operation Malid, Naveed
FR1040468	FR	05/31/1999	Opcode-driven buffer manager for use in data storage devices Ross, Kevin
GB1040468	GB	05/31/1999	Opcode-driven buffer manager for use in data storage devices Ross, Kevin
6185640	US	06/19/1998	Minimal frame buffer manager allowing simultaneous read/write access by alternately filling and emptying a first and second buffer one packet at a time Ross, Kevin
KR10-0673568	KR	06/03/1999	Minimal frame buffer manager for use in data storage devices Ross, Kevin
MY125924	MY	06/16/1999	Minimal frame buffer manager for use in data storage devices Kevin Ross
CNZL99801390.0	CN	06/03/1999	Minimal frame buffer manager for use in data storage device Ross, K.
6100738	US	12/22/1998	High-speed current switch with complementary stages Blegems, Paul F.
6668285	US	05/12/1999	Object oriented processing with dedicated pointer memories Ross, Kevin
CNZL00801392.6	CN	05/10/2000	Process facing to object using special pointer memory Ross, K.
6480967	US	05/21/1999	Multiple module processing system with reset system independent of reset characteristics of the modules Jensen, Rune Hartung
CNZL00800843.4	CN	05/01/2000	Reset system for multiple component system Jensen, R. H.
DE60012660.9	DE	05/01/2000	Reset system for multiple component system Jensen, Rune
FR1099150	FR	05/01/2000	Reset system for multiple component system Jensen, Rune
GB1099150	GB	05/01/2000	Reset system for multiple component system Jensen, Rune
JP4288011	JP	05/01/2000	Reset system for multiple component system Rune Jensen [Michael Gartlan
KR10-0607535	KR	05/01/2000	Reset system for multiple component system

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
TW1175830	TW	05/19/2000	Jensen, Rune H. A processing system and method of resetting a plurality of processing modules
6374319	US	06/22/1999	Jensen, Rune Hartung Flag-controlled arbitration of requesting agents
KR10-0753690	KR	06/16/2000	Singh, Alok Flag-controlled arbitration of requesting agents
FR1118150	FR	07/17/2000	Singh, Alok A resonant mode power supply having over-power and over-current protection
GB1118150	GB	07/17/2000	Majid, Naveed A resonant mode power supply having over-power and over-current protection
6087782	US	07/28/1999	Majid, Naveed Resonant mode power supply having over-power and over-current protection
TW1161817	TW	07/26/2000	Majid, Naveed A resonant mode power supply having over-power and over-current protection
6493781	US	08/19/1999	Majid, Naveed Servicing of interrupts with stored and restored flags
CNZL00801734.4	CN	07/31/2000	Saville, Winthrop L. Servicing of interrupts with stored and restored flags
KR10-0817947	KR	07/31/2000	Saville, W. L. Servicing of interrupts with stored and restored flags
6232754	US	08/15/1999	Saville, Winthrop L. Sleep-mode-ready switching power converter
KR10-0702278	KR	07/31/2000	Liebler, Jerome Edgar Switching power converter
6922555	US	09/02/1999	Liebler, Jerome Phase interpolation receiver for angle modulated RF signals
CNZL00802496.0	CN	08/15/2000	Mohindra, Rishi Phase interpolation receiver for angle modulated RF signals
KR10-0780117	KR	08/15/2000	Mohindra, Rishi Phase interpolation receiver for angle modulated signals
7376814	US	09/07/1999	Mohindra, Rishi Method for forming variable length instructions in a processing system
CNZL00802561.4	CN	08/30/2000	Saville, Winthrop L. Variable-instruction-length processing

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
KR10-0764260	KR	08/30/2000	Saville, W. L. Variable-instruction-length processing
6782447	US	12/17/1999	Saville, Winthrop L. Circular address register
EP00991150.4	EP	12/05/2000	Ostler, Farrell L. Circular address register
KR10-0841548	KR	12/05/2000	Ostler, Farrell L. Processing system
6820193	US	12/17/1999	Ostler, Farrell L. Branch instructions with decoupled condition and address
KR10-0764261	KR	12/05/2000	Ostler, Farrell L. Branch instructions with decoupled condition and address
FR1208435	FR	12/12/2000	Ostler, Farrell L. Shared resource arbitration method and apparatus
6678774	US	12/16/1999	Providenza, John R. Shared resource arbitration method and apparatus
5343058	US	06/22/1992	Providenza, John R. Gate array bases with flexible routing
5313079	US	09/25/1992	Shiffer, II, James D. Gate array bases with flexible routing
5686171	US	12/30/1993	Brasen, Daniel R. Integrated circuit scribe line structures and methods for making same
5795815	US	08/19/1996	Vokoun, Edward R. Integrated circuit scribe line structures and methods for making same
5943591	US	07/10/1997	Vokoun, Edward R. Integrated circuit scribe line structures and methods for making same
5613151	US	12/05/1995	Vokoun, Edward R. Data processor with flexible register mapping scheme
6133111	US	09/30/1998	Dockser, Kenneth A. Method of making photo alignment structure
6397279	US	01/07/1998	Sur, Harlan Smart retry system that reduces wasted bus transactions associated with master retries
6076125	US	01/09/1998	Jaramillo, Ken Single address queue for handling multiple priority requests
6154800	US	09/01/1999	Anand, Vishal Single address queue for handling multiple priority requests

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
6546479	US	02/10/1998	Anand, Vishal Reduced instruction fetch latency in a system including a pipelined processor
6208004	US	08/19/1998	Niot, Francois Semiconductor device with high-temperature-stable gate electrode for sub-micron applications and fabrication thereof
6479362	US	02/14/2001	Cunningham, James A. Semiconductor device with high-temperature-stable gate electrode for sub-micron applications and fabrication thereof
6507887	US	04/18/2000	Cunningham, James A. Binary data memory design with data stored in low-power sense
DE60003387.2	DE	01/06/2000	Pontius, Timothy A. System and method for maximizing dma transfers of arbitrary
FR1145130	FR	01/06/2000	Wu, Shih-ho Direct memory access controller in a computer system
GB1145130	GB	01/06/2000	Wu, Shih-ho Direct memory access controller in a computer system
6330623	US	01/08/1999	Wu, Shih-ho System and method for maximizing DMA transfers of arbitrarily aligned data
6378044	US	09/22/1999	Wu, Shih-ho Method and system for cache replacement among configurable cache sets
EP00961803.4	EP	09/13/2000	Luce, Roger W. Method and system for cache replacement
6484271	US	09/16/1999	Luce, Roger W. Memory redundancy techniques
6084464	US	10/29/1999	Gray, Kenneth S. On-chip decoupling capacitor system with parallel fuse
FR1142119	FR	09/28/2000	Lin, Xi-Wei On-chip decoupling capacitor system with parallel fuse
GB1142119	GB	09/28/2000	Lin, Xi-Wei On-chip decoupling capacitor system with parallel fuse
DE60013230.7	DE	05/25/2000	Lin, Xi-Wei Noise coding in a variable rate vocoder
FR1105868	FR	05/25/2000	Reagan, John Terrence Noise coding in a variable rate vocoder
GB1105868	GB	05/25/2000	Reagan, John Terrence Noise coding in a variable rate vocoder

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
6954727	US	05/28/1999	Reagan, John Terrence Reducing artifact generation in a vocoder
5588128	US	02/27/1996	Reagan, John Terrence Dynamic direction look ahead read buffer
6057224	US	07/24/1997	Hicok, Gary D. Methods for making semiconductor devices having air dielectric interconnect structures
5798559	US	09/03/1997	Bothra, Subhas Integrated circuit structure having an air dielectric and dielectric support pillars
5814544	US	07/25/1996	Bothra, Subhas Forming a MOS transistor with a recessed channel
5802073	US	06/04/1997	Huang, Tiao-Yuan Built-in self test functional system block for UTOPIA interface
5781802	US	07/09/1997	Platt, Alfred First-in-first-out (FIFO) controller for buffering data between systems which are asynchronous and free of false flags and internal metastability
6629206	US	12/31/1999	Cassetti, David K. Set-associative cache-management using parallel reads and serial reads initiated during a wait state
DE60044336.1	DE	12/19/2000	Johnson, Mark W. Set-associative cache-management method using
FR1163588	FR	12/19/2000	Johnson, Mark W. Set-associative cache-management method using parallel reads and serial reads initiated while processor is waited
GB1163588	GB	12/19/2000	Johnson, Mark W. Parallel reads and serial reads initiated while processor is waited
KR10-0710922	KR	12/19/2000	Johnson, Mark W. Set-associative cache-management method using parallel reads and serial reads initiated while processor is waited
6366165	US	11/12/1999	Johnson, Mark W. Amplifier with stabilization means
6542030	US	09/28/2001	Huijsing, Johan H. Amplifier with stabilization means
7021551	US	05/16/2003	Huijsing, Johan H. Data carrier comprising evaluation means for evaluating information represented by an information voltage
CNZL03813115.3	CN	05/16/2003	Bergler, Ewald Data carrier comprising evaluation means for evaluating information represented by an information voltage

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			Bergler, Ewald
DE60322535.7	DE	05/16/2003	Data carrier comprising evaluation means for evaluating information represented by an information voltage
			Bergler, Ewald
FR1514232	FR	05/16/2003	Data carrier comprising evaluation means for evaluating information represented by an information voltage
			Bergler, Ewald
GB1514232	GB	05/16/2003	Data carrier comprising evaluation means for evaluating information represented by an information voltage
			Bergler, Ewald
FR0551940	FR	01/11/1993	A semiconductor device comprising a multigate MOSFET
			Battersby, Steve
GB0551940	GB	01/11/1993	A semiconductor device comprising a multigate MOSFET
			Battersby, Steve
5528065	US	06/01/1995	Dual-gate insulated gate field effect device
			Battersby, Stephen J.
5801573	US	06/27/1997	Protected switch having a power semiconductor device
			Kelly, Brendan P.
GB9420372.1	GB	10/12/1994	Unable to verify
			Unable to verify
5754074	US	11/30/1995	Protected switch
			Kelly, Brendan P.
GB9424666.7	GB	12/07/1994	Unable to verify
			Unable to verify
6075979	US	03/05/1997	Radio receivers
			Holtvoeth, Knud
DE69722494.5	DE	03/11/1997	Improvements in or relating to radio receivers
			Holtvoeth, Knud
FR0797304	FR	03/11/1997	Improvements in or relating to radio receivers
			Holtvoeth, Knud
GB0797304	GB	03/11/1997	Improvements in or relating to radio receivers
			Holtvoeth, Knud
JP3974679	JP	03/17/1997	Receiver provided with piezoelectric crystal oscillator circuit
			Holtvoeth, Knud
DE69815010.4	DE	02/02/1998	Power device with a short-circuit detector
			Kelly, Brendan P.
FR0904636	FR	02/02/1998	Power device with a short-circuit detector
			Kelly, Brendan P.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
GB0904636	GB	02/02/1998	Power device with a short-circuit detector Kelly, Brendan P.
5942886	US	02/18/1998	Power device with a short-circuit detector Kelly, Brendan P.
JP4256476	JP	02/02/1998	Power device with a short-circuit detector Brendan Kelly
KR10-0510045	KR	02/02/1998	Power device with a short-circuit detector Kelly, Brendan Patrick
FR0910870	FR	01/29/1998	Power semiconductor devices with a temperature sensor circuit Kelly, Brendan P.
GB0910870	GB	01/29/1998	Power semiconductor devices with a temperature sensor circuit Kelly, Brendan P.
IT0910870	IT	01/29/1998	S/C power switch with thermal overload detector Kelly, Brendan P.
7424316	US	07/26/2000	Body-worn personal communications apparatus Boyle, Kevin R.
6972755	US	01/08/2002	Driver circuit for a display device Plangger, Guido
JP4290370	JP	01/04/2002	Display device driving, display and driver circuit testing method guido plangger
6876255	US	06/18/2002	Linear amplifier using non-linear CMOS capacitors Reber, Martin
JP4272053	JP	06/18/2002	Linear amplifier using non-linear CMOS capacitors Reber, Martin
6947337	US	08/07/2002	Random-access memory devices comprising a dioded buffer Stojanov, Nikola
CNZL02813476.2	CN	08/07/2002	Random-access memory devices comprising deoded buffer Stojanov, N.
JP4570359	JP	08/07/2002	Random-access memory devices comprising a dioded buffer Stojanov, Nikola
DE60224406.4	DE	08/07/2002	Random-access memory devices comprising a dioded buffer Stojanov, Nikola
FR1417686	FR	08/07/2002	Random-access memory devices comprising a dioded buffer Stojanov, Nikola
GB1417686	GB	08/07/2002	Random-access memory devices comprising a dioded buffer Stojanov, Nikola
6885155	US	12/18/2002	Arrangement for activating a display device with voltage multiplier

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
7504846	US	03/31/2003	Speirs, Christopher Rodd Testable cascode circuit and method for testing the same using a group of switching elements
JP3980560	JP	03/31/2003	Plangger, Guido Testable cascode circuit and method for testing the same
DE60337159.0	DE	03/31/2003	guido plangger Joachim Christian Reiner (Meike Pingel) Testable cascode circuit and method for testing the same
FR1499904	FR	03/31/2003	Plangger, Guido Testable cascode circuit and method for testing the same
GB1499904	GB	03/31/2003	Plangger, Guido Testable cascode circuit and method for testing the same
7257009	US	04/14/2003	Plangger, Guido Voltage converter for converting an input voltage to an output voltage and driving circuit comprising a voltage converter
CNZL03808658.1	CN	04/14/2003	Negoï, Andy C. Voltage converter for converting an input voltage to an output voltage and driving circuit comprising a voltage converter
DE60333453.9	DE	04/14/2003	Negoï, Andy C. Software re-configurable charge pump with improved current...
FR1506610	FR	04/14/2003	Negoï, Andy C. Voltage converter for converting an input voltage to an output voltage and driving circuit comprising a voltage converter
GB1506610	GB	04/14/2003	Negoï, Andy C. Voltage converter for converting an input voltage to an output voltage and driving circuit comprising a voltage converter
TWI323072	TW	04/16/2003	Negoï, Andy C. Voltage converter for and method of converting an input voltage to an output voltage and driving circuit comprising a voltage converter
7057965	US	04/14/2003	Negoï, Andy C. Method of performing access to a single-port memory device, memory access device, integrated circuit device and method of use of an integrated circuit device
CNZL03808984.X	CN	04/14/2003	Tobescu, Corneliu Method of performing access to a single-port memory device, memory access device, integrated circuit device and method of use of an integrated circuit device
TWI301275	TW	04/18/2003	Tobescu, C. Method of performing access to a single-port memory device, memory access device, integrated circuit device and method of use of an integrated circuit device
DE60315985.0	DE	06/25/2003	Corneliu, Tobescu Erasable and programmable non-volatile cell

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
			Solo De Zaldivar, Jos��
FR1522078	FR	06/25/2003	Erasable and programmable non-volatile cell
			Solo de Zaldivar, Jose
GB1522078	GB	06/25/2003	Erasable and programmable non-volatile cell
			Solo De Zaldivar, Jos��
7289362	US	06/25/2003	Erasable and programmable non-volatile cell
			Solo de Zaldivar, Jose
CNZL03816168.0	CN	06/25/2003	Erasable and programmable non-volatile cell
			Solo De Zaldivar, J.
JP4749714	JP	06/25/2003	Erasable and programmable non-volatile cell
			Solo de Zaldivar, Jose
7248104	US	08/12/2003	Operational amplifier
			Wang, Zhenhua
5231351	US	09/18/1991	Magnetoresistive speed sensor processing circuit utilizing a symmetrical hysteresis signal
			Kordis, Jergen
FR0624976	FR	05/02/1994	Circuit for demodulating the sound signals of a television signal
			Brilka, Joachim
GB0624976	GB	05/02/1994	Circuit for demodulating the sound signals of a television signal
			Brilka, Joachim
5483298	US	05/05/1994	Circuit arrangement for demodulating the sound signals in a television signal in accordance with at least two different transmission standards
			Brilka, Joachim
5640688	US	04/20/1995	Tuner and splitter-modulator circuit having a common quartz crystal
			Ruitenb��rg, Leo
DEP4413927.6	DE	04/21/1994	Unable to verify
			Unable to verify
SG032338	SG	04/21/1995	Circuit arrangement comprising a tuner and a splitter-modulator
			Leonardus, Ruitenb��rg
DE59507857.5	DE	04/18/1995	Circuit with a high frequency receiver stage and a modulator for transmission
			Ruitenb��rg, Leo
FR0679024	FR	04/18/1995	Circuit with a high frequency receiver stage and a modulator for transmission
			Ruitenb��rg, Leo
GB0679024	GB	04/18/1995	Circuit with a high frequency receiver stage and a modulator for transmission
			Ruitenb��rg, Leo

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JP3806160	JP	04/19/1995	Circuit device with tuner and separator-modulator Leonardus Joseph Michael Ruitenburg
5631546	US	07/11/1995	Power supply for generating at least two regulated interdependent supply voltages Heinke, Friedhelm
5724006	US	09/05/1995	Circuit Arrangement with Controllable Transmission Characteristics Kilian, Ernst-August
5714915	US	05/30/1996	Oscillator Brilka, Joachim
DE59609618.6	DE	05/24/1996	Oscillator for use on IC Brilka, Joachim
FR0746092	FR	05/24/1996	Oscillator Brilka, Joachim
GB0746092	GB	05/24/1996	Oscillator Brilka, Joachim
5867771	US	10/17/1996	Circuit arrangement comprising a tuner and a splitter-modulator Ruitenburg, Leo
JP3816995	JP	10/16/1996	Circuit device with tuner and branching filter-modulator Leonardus Joseph Michael Ruitenburg
KR10-0441183	KR	10/17/1996	Circuit arrangement comprising a tuner and a splitter-modulator Ruitenburg, Leo
5859873	US	12/18/1996	Method and arrangement for non-contact transmission of measured values Ritter, Siegfried
JP3842834	JP	12/17/1996	Method and apparatus for non-contact transmission of measured value Ritter, Siegfried
6081560	US	02/27/1997	Production of a frequency control signal in an FSK receiver Manfred, Biehl
CNZL97190380.8	CN	02/27/1997	Circuit device for generating control signal Biehl, M.
DE59712313.6	DE	02/27/1997	AFC circuit for pagers Manfred Biehl / Winfried Jansen / Ralf Pilaski
FR0824818	FR	02/27/1997	Production of a frequency control signal in an FSK receiver Biehl, Manfred
GB0824818	GB	02/27/1997	Production of a frequency control signal in an FSK receiver Biehl, Manfred
IT0824818	IT	02/27/1997	Production of a frequency control signal in an FSK receiver Biehl, Manfred

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JP3833259	JP	02/27/1997	Production of a frequency control signal in an fsk receiver Manfred, Biehl
5912594	US	03/20/1997	Compact crystal oscillator having no large capacitor element Burkhard, Dick
CNZ197190523.1	CN	03/20/1997	Oscillator Dick, B.
DE69730085.4	DE	03/20/1997	Differential oscillator Dick, Burkhard
FR0827641	FR	03/20/1997	Oscillator Dick, Burkhard
GB0827641	GB	03/20/1997	Oscillator Dick, Burkhard
JP4182178	JP	03/20/1997	Oscillator Dick, Burkhard
08/890451	US	07/09/1997	Starting circuit for high pass filters Dick, Burkhard
5978662	US	06/26/1997	Circuit arrangement comprising a Cartesian amplifier Swales, Nicholas D.
DE69734271.9	DE	06/23/1997	Setting-up cartesian loop transmitter Swales, Nicolas D.
FR0847619	FR	06/23/1997	Circuit arrangement comprising a cartesian amplifier Swales, Nicolas D.
GB0847619	GB	06/23/1997	Circuit arrangement comprising a cartesian amplifier Swales, Nicolas D.
JP3850448	JP	06/23/1997	Circuit arrangement comprising a cartesian amplifier Nicholas D. Swales
5977824	US	06/18/1997	Circuit feedback loop with monitored loop gain Sparks, Geoffrey S.
DE69734092.9	DE	06/18/1997	Monitoring the loop of a cartesian loop Sparks, Geoffrey S.
FR0847620	FR	06/18/1997	Circuit arrangement comprising a feedback loop Sparks, Geoffrey S.
JP4040098	JP	06/18/1997	Circuit arrangement comprising a feedback loop Sparks, Geoffrey S.
5943100	US	07/03/1997	Circuit arrangement for frequency conversion of a color signal Hildebrandt, Gunter
FR0822609	FR	07/21/1997	Device for receiving and/or transmitting an electromagnetic wave

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GB0822609	GB	07/21/1997	Massey, Peter Johan Device for receiving and/or transmitting an electromagnetic wave
6150983	US	07/24/1997	Massey, Peter Johan Device for receiving and/or transmitting electromagnetic radiation
GB0829916	GB	09/02/1997	Massey, Peter J. Microwave component
6005451	US	09/08/1997	Wendel, Ralf Microwave element
6111443	US	01/22/1998	Wendel, Ralf Accelerated switching by selection of various threshold levels
DE59813036.5	DE	01/14/1998	Mores, Robert Signal forming circuit with short delay
FR0855798	FR	01/14/1998	Mores, Robert Circuit arrangement for generating an output signal
GB0855798	GB	01/14/1998	Mores, Robert Circuit arrangement for generating an output
JP4159129	JP	01/21/1998	Mores, Robert Circuit device for output signal generation
6118352	US	02/19/1998	Mores, Robert Microwave component comprising gyromagnetic material exposed to adjustable magnetic field strength
CN2198105222.3	CN	02/20/1998	Wendel, Ralf Microwave component
JP4067624	JP	02/19/1998	Wendel, R. Microwave parts
DE59809440.7	DE	02/13/1998	Wendel, Ralf Microwave component
FR0860891	FR	02/13/1998	Wendel, Ralf Microwave component
GB0860891	GB	02/13/1998	Wendel, Ralf Microwave component
6385503	US	06/16/1998	Wendel, Ralf Reactor for the processing of wafers, with a protection device
6252633	US	07/01/1998	Volle, Wolfgang Television signal receiver
DE59813329.5	DE	06/26/1998	Ruitenburg, Leo TV-tuner with switchable outputs
			Ruitenburg, Leo

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FR0889646	FR	06/26/1998	Television signal receiver Ruitenburg, Leo
GB0889646	GB	06/26/1998	Television signal receiver Ruitenburg, Leo
6046779	US	04/20/1998	Circuit arrangement for phase shifting a color signal prior to comb filtering and then cancelling the phase shift after comb filtering Heinemann, Herbert
CNZL98107429.4	CN	04/24/1998	Circuit arrangement for color signal processing Heinemann, H.
DE59813454.9	DE	04/15/1998	Circuit for filtering a color-under-signal Herbert Heinemann/Shin-Ichi Uchiyama
FR0876064	FR	04/15/1998	Colour signal processing circuit Heinemann, Herbert
GB0876064	GB	04/15/1998	Colour signal processing circuit Heinemann, Herbert
6046651	US	08/06/1998	Microwave component Wendel, Ralf
CNZL98118016.7	CN	08/06/1998	Microwave component Wendel, R.
JP4157918	JP	08/05/1998	Microwave parts Wendel, R.
GB0896381	GB	08/04/1998	Microwave component Wendel, Ralf
6097261	US	08/06/1998	Nonreciprocal microwave component having adjustable magnetic field strength Wendel, Ralf
CNZL98118017.5	CN	08/06/1998	Microwave component Wendel, R.
6163207	US	01/06/1999	Integrator-filter circuit Kattner, Axel
6118332	US	01/14/1999	Circuit arrangement for an adjustable high-pass filter Jabs, Hermann
6169383	US	02/23/1999	Circuit arrangement for demodulating an intermediate-frequency video signal Hafemeister, Thomas
FR0948202	FR	03/25/1999	Demodulator circuit for video signals at intermediate frequency Hafemeister, Thomas
GB0948202	GB	03/25/1999	Demodulator circuit for video signals at intermediate frequency

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6385717	US	08/17/1999	Hafemeister, Thomas Programmable 1-bit Data Processing Arrangement
DE59914464.5	DE	08/09/1999	Mayer-Lindenberg, Georg-Friedrich Programmable one bit data processing apparatus
FR0981079	FR	08/09/1999	Mayer-Lindenberg, Georg-Friedrich Programmable one bit data processing apparatus
GB0981079	GB	08/09/1999	Mayer-Lindenberg, Georg-Friedrich Programmable one bit data processing apparatus
6519008	US	05/12/1999	Mayer-Lindenberg, Georg-Friedrich Filter circuit
DE59915092.0	DE	05/07/1999	Kattner, Axel Filtering circuit
FR0957635	FR	05/07/1999	Kattner, Axel Filtering circuit
GB0957635	GB	05/07/1999	Kattner, Axel Filtering circuit
6388463	US	09/02/1999	Kattner, Axel Circuit arrangement for bias adjustment of bus levels
DE59914619.2	DE	09/01/1999	Mores, Robert Circuit arrangement for bias adjustment for bus level
FR0984361	FR	09/01/1999	Mores, Robert Circuit arrangement for bias adjustment for bus level
GB0984361	GB	09/01/1999	Mores, Robert Circuit arrangement for bias adjustment for bus level
6444920	US	01/26/2000	Mores, Robert Thin film circuit with component
6384673	US	06/29/2000	Klee, Mareike K. Current mirror arrangement
DE60017862.5	DE	06/29/2000	Dick, Burkhard Current mirror arrangement
FR1110133	FR	06/29/2000	Dick, Burkhard Current mirror arrangement
GB1110133	GB	06/29/2000	Dick, Burkhard Current mirror arrangement
6771142	US	09/13/2000	Dick, Burkhard Network coupler
6919775	US	05/25/2004	Wendt, Matthias Network coupler

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KR10-0762537	KR	09/14/2000	Wendi, Matthias Network coupler
DE50108772.9	DE	03/29/2001	Wendi, Matthias Resolving medium access conflict in networks
FR1143668	FR	03/29/2001	Fuhrmann, Peter Resolving media access conflicts in networks with a plurality of network nodes and at least one star node
GB1143668	GB	03/29/2001	Fuhrmann, Peter Resolving media access conflicts in networks with a plurality of network nodes and at least one star node
7269178	US	04/05/2001	Fuhrmann, Peter Resolution of media access contentions in networks comprising a plurality of network nodes and at least one star node
7672321	US	04/27/2007	Fuhrmann, Peter Data communication in networks comprising a plurality of network nodes and at least one star node
6646704	US	09/18/2001	Fuhrmann, Peter Apparatus for projection utilizing liquid crystal light modulator and method of manufacturing said liquid crystal light modulator
6762655	US	09/21/2001	Liebscher, Gerhard Circuit arrangement
DE50113112.4	DE	09/20/2001	Wichern, Andreas Circuit arrangement
FR1202450	FR	09/20/2001	Wichern, Andreas Circuit arrangement
GB1202450	GB	09/20/2001	Wichern, Andreas Circuit arrangement
6483367	US	12/03/2001	Wichern, Andreas Circuit arrangement for generating and amplifying an amplitude limited DC signal
FR1217731	FR	12/05/2001	Kohsiek, Cord-Heinrich Electric circuit
GB1217731	GB	12/05/2001	Kohsiek, Cord-Heinrich Electric circuit
7143211	US	10/15/2001	Kohsiek, Cord-Heinrich Memory configuration with I/O support
CNZL01817319.5	CN	10/15/2001	Drescher, Wolfram Memory configuration with I/O support
			Drescher, Wolfram

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DE50106472.9	DE	10/15/2001	Memory configuration with I/O support Drescher, Wolfram
FR1328862	FR	10/15/2001	Memory configuration with I/O support Drescher, Wolfram
GB1328862	GB	10/15/2001	Memory configuration with I/O support Drescher, Wolfram
KR10-0777497	KR	10/15/2001	Method for data-input and data-output at a processor memory and device for performing of this method Drescher, Wolfram
7719850	US	02/13/2002	Arrangement with an integrated circuit mounted on a bearing means and a power supply module arrangement Duerbaum, Thomas
CN2102800315.2	CN	02/13/2002	Arrangement with integrated circuit mounted on bearing means and power supply module arrangement Duerbaum, T.
DE60236253.9	DE	02/13/2002	Power supply on microelectronic chip Thomas Duerbaum [Reinhold Efferich
FR1362502	FR	02/13/2002	Arrangement with an integrated circuit mounted on a bearing means and a power supply module arrangement Duerbaum, Thomas
GB1362502	GB	02/13/2002	Arrangement with an integrated circuit mounted on a bearing means and a power supply module arrangement Duerbaum, Thomas
6697267	US	06/12/2002	Voltage converter Tolle, Tobias Georg
7680045	US	06/25/2002	Packet switching device with a feedback method of the arbiter Van Wageningen, Andries
DE50206361.0	DE	06/25/2002	Packet switching apparatus with a feedback process of the metering unit Van Wageningen, Andries
FR1280303	FR	06/25/2002	Packet switching device with a feedback method for its access unit Van Wageningen, Andries
GB1280303	GB	06/25/2002	Packet switching device with a feedback method for its access unit Van Wageningen, Andries
JP3929837	JP	06/24/2002	Packet switching device, line card and switch card used for the switching device Van Wageningen, Andries
7030601	US	09/11/2002	Circuit configuration for a gradiometric current sensor with a bridge circuit for measuring gradients of magnetic field strength and a sensor equipped with this circuit configuration

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6912615	US	09/06/2002	Buchhold, Reinhard Control means for burst access control
EP02102326.2	EP	09/06/2002	Nicolai, Volker Control device for control of burst access
7136442	US	11/12/2002	Nicolai, Volker Receiving circuit receiving message signals
FR1315304	FR	11/14/2002	Budde, Wolfgang Otto Receiver circuit for communications signals
GB1315304	GB	11/14/2002	Budde, Wolfgang Otto Receiver circuit for communications signals
7570946	US	11/25/2002	Budde, Wolfgang Otto Method for determining the signal strength at the input of a tuner
7187250	US	12/05/2002	Van Der Wijst, Hendricus Martinus Coupler, integrated electronic component and electronic device
CNZL02825426.0	CN	12/05/2002	Matters-Kammerer, Marion Kornelia Coupler, integrated electronic component and electronic device
JP4101763	JP	12/05/2002	Matters-Kammerer, M. K. Coupler, integrated electronic component and electronic device
KR10-0981524	KR	12/05/2002	Matters-Kammerer, Marion Coupler, integrated electronic component and electronic device
DE60226493.6	DE	12/05/2002	Matters-Kammerer, Marion K. Coupler, integrated electronic component and electronic device
FR1459444	FR	12/05/2002	Matters-Kammerer, Marion Kornelia Coupler, integrated electronic component and electronic device
GB1459444	GB	12/05/2002	Matters-Kammerer, Marion Coupler, integrated electronic component and electronic device
6690229	US	12/18/2002	Matters-Kammerer, Marion Feed back current-source circuit
JP4157928	JP	12/18/2002	Rudolph, Jochen Current source circuit
7259800	US	12/19/2002	Rudolph, Jochen Television signal receiver with a universal output for surface-acoustic wave filters of different types
7136892	US	12/20/2002	Van Der Wijst, Hendricus Martinus Method for multiplying two factors from the Galois field and multiplier for performing the method
JP2002-375919	JP	12/26/2002	Feuser, Markus Method for multiplying two factors from galois field and multiplier for performing the same

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			Feuser, Markus
7003538	US	02/08/2002	Process and apparatus for finite field multiplication (FFM)
			Drescher, Wolfram
7069418	US	02/14/2002	Method and arrangement for instruction word generation in the controlling of functional units in a processor
			Weiss, Matthias
DE10107102.7	DE	02/14/2001	Method and apparatus for generating instruction words in control of functional units in a processor
			Fettweis, Gerhard
FR2824154	FR	02/13/2002	Method and apparatus for generating instruction words in control of functional units in a processor
			Fettweis, Gerhard
GB2375197	GB	02/11/2002	A method of generating instruction words for driving functional units in a processor
			Fettweis, Gerhard
JP4494701	JP	02/08/2002	Method and device for generating instruction word in controlling function unit in processor
			Fettweis, Gerhard
7577818	US	05/06/2002	Microprocessor program addressing arrangement having multiple independent complete address generators
			Drescher, Wolfram
JP3668464	JP	04/03/2002	Address generating unit
			Drescher, Wolfram
7039844	US	01/14/2003	Integrated circuit with self-testing circuit
			Hapke, Friedrich
6734710	US	01/28/2003	Circuit arrangement for pulse generation
			Beier, Ralf
6861893	US	02/19/2003	Circuit arrangement for a current-controlled resistor having an enlarged linear range
			Kohlsiek, Cord-Heinrich
6867636	US	02/19/2003	Circuit arrangement for a current-controlled resistor having an enlarged linear range
			Kohlsiek, Cord-Heinrich
7139953	US	02/26/2003	Integrated circuit with test circuit
			Hapke, Friedrich
CN2103804878.7	CN	02/26/2003	Integrated circuit with test circuit
			Hapke, F.
DE60318820.6	DE	02/26/2003	Integrated circuit with test circuit
			Hapke, Friedrich
FR1483596	FR	02/26/2003	Integrated circuit with test circuit

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			Hapke, Friedrich
GB1483596	GB	02/26/2003	Integrated circuit with test circuit
			Hapke, Friedrich
7528679	US	03/24/2003	Circuit arrangement for shifting the phase of an input signal and circuit arrangement for suppressing the mirror frequency
			Dick, Burkhard
CN2103806875.3	CN	03/24/2003	Circuit arrangement for shifting the phase of an input signal and circuit arrangement for suppressing the mirror frequency
			Dick, B.
JP4302531	JP	03/24/2003	Circuit arrangement for shifting the phase of an input signal and circuit arrangement for suppressing the mirror frequency
			Dick, Burkhard
CN200910005870.7	CN	03/24/2003	Circuit arrangement for shifting the phase of an input signal and circuit arrangement for suppressing the mirror frequency
			Dick, B.
DE10213423.5	DE	03/26/2002	Circuit arrangement for shifting the phase of an input signal and circuit arrangement for suppressing the mirror frequency
			Dick, Burkhard
7154735	US	05/13/2003	Decoupling module for decoupling high-frequency signals from a power supply line
			Matters-Kammerer, Marion Kornelia
CN2103811746.0	CN	05/13/2003	Decoupling module for decoupling high-frequency signals from a power supply line
			Matters-Kammerer, Marion Kornelia
DE60304172.8	DE	05/13/2003	Decoupling module for decoupling high-frequency signals from a power supply line
			Matters-Kammerer, Marion Kornelia
FR1512157	FR	05/13/2003	Decoupling module for decoupling high-frequency signals from a power supply line
			Matters-Kammerer, Marion Kornelia
GB1512157	GB	05/13/2003	Decoupling module for decoupling high-frequency signals from a power supply line
			Matters-Kammerer, Marion Kornelia
7391602	US	11/18/2003	Decoupling module for decoupling high-frequency signals from a voltage supply line
			Matters-Kammerer, Marion K.
CN2100380104317.X	CN	11/18/2003	Decoupling module for decoupling high-frequency signals from a voltage supply line
			Matters-Kammerer, M. K.
EP03811826.1	EP	11/18/2003	Decoupling module for decoupling high-frequency signals from a voltage supply line

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7498893	US	12/04/2003	Matters-Kammerer, M. K. Circuit for generating a high-frequency oscillation in a specified frequency band
CNZL200380105652.1	CN	12/04/2003	Knoellmann, Andreas An oscillator circuit for generating a high-frequency electromagnetic oscillation
EP03777069.0	EP	12/04/2003	Knoellmann, A. An oscillator circuit for generating a high-frequency electromagnetic oscillation
7779229	US	02/12/2003	Knoellmann, Andreas Method and arrangement for bringing together data on parallel data paths
DE50307765.8	DE	02/12/2003	Drescher, Wolfram Method and arrangement for bringing together data on parallel data paths
FR1476806	FR	02/12/2003	Drescher, Wolfram Method and arrangement for bringing together data on parallel data paths
GB1476806	GB	02/12/2003	Drescher, Wolfram Method and arrangement for bringing together data on parallel data paths
7471272	US	01/30/2004	Drescher, Wolfram Control method and device for a display device
CNZL200480004549.2	CN	01/30/2004	Oelhafen, Patrick Control method and device for a display device
EP04706761.6	EP	01/30/2004	Oelhafen, P. Control method and device for a display device
7352261	US	03/11/2004	Oelhafen, Patrick Philips Production method for chip-form film-forming component
CNZL200480007219.9	CN	03/11/2004	Tsilionkas, Efthimios Microstrip filter of short length
DE602004011489.8	DE	03/11/2004	Tsilionkas, E. Reduced length micro strip filter
FR1606876	FR	03/11/2004	Tsilionkas, Efthimios Microstrip filter of short length
GB1606876	GB	03/11/2004	Tsilionkas, Efthimios Micro strip filter of short length
JP4180635	JP	03/11/2004	Tsilionkas, Efthimios Microstrip filter of short length
			Tsilionkas, Efthimios

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7609127	US	03/11/2004	Electronically controllable RF switch Toh, Kong Lim
CNZL200480008314.0	CN	03/11/2004	Electron controlled RF converter Toh, Kong Lim
DE602004008246.5	DE	03/11/2004	Electronic rf switch and terrestrial applications in PC-TV Toh, Kong Lim
FR1611683	FR	03/11/2004	Electronically controllable RF switch Toh, Kong Lim
GB1611683	GB	03/11/2004	Electronically controllable RF switch Toh, Kong Lim
7471170	US	03/17/2004	Multilayer stack with compensated resonant circuit Matters-Kammerer, Marion Kornelia
CNZL200480007494.0	CN	03/17/2004	Multilayer stack with compensated resonant circuit Matters-Kammerer, M.
DE602004014126.7	DE	03/17/2004	Multilayer stack with compensated resonant circuit Matters-Kammerer, Marion
FR1618657	FR	03/17/2004	Multilayer stack with compensated resonant circuit Matters-Kammerer, Marion
GB1618657	GB	03/17/2004	Multilayer stack with compensated resonant circuit Matters-Kammerer, Marion
JP2006-506353	JP	03/17/2004	Multilayer stack with compensated resonant circuit Matters-Kammerer, Marion
7477883	US	03/22/2004	Receiver front-end sensitivity booster Lambrecht, Xavier Jean Francois
CNZL200480010235.3	CN	03/22/2004	Front-end sensitivity booster Lambrecht, X. J. F.
DE602004006987.6	DE	03/22/2004	Front-end sensitivity booster Lambrecht, Xavier
FR1618655	FR	03/22/2004	Front-end sensitivity booster Lambrecht, Xavier
GB1618655	GB	03/22/2004	Front-end sensitivity booster Lambrecht, Xavier
CNZL200480017699.7	CN	06/14/2004	Method and circuit arrangement for the self-testing of a reference voltage in electronic components Kadner, M.
DE602004011938.5	DE	06/14/2004	Reference built-in self-test (rbist) Kadner, Martin
FR1642143	FR	06/14/2004	Method and circuit arrangement for the self-testing of a reference

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			voltage in electronic components
GB1642143	GB	06/14/2004	Kadner, Martin Method and circuit arrangement for the self-testing of a reference voltage in electronic components
7592884	US	07/15/2004	Kadner, Martin High frequency component
CN21200480022054.2	CN	07/15/2004	Matters-Kammerer, Marion Kornelia High frequency component
7498888	US	12/10/2004	Matters-Kammerer, M. K. Method and arrangement for interference compensation in a voltage- controlled frequency generator
7498851	US	01/03/2005	Nitsche, Gunnar High speed comparator
CN21200580002302.1	CN	01/03/2005	Maone, Francesco High speed comparator
EP05702545.4	EP	01/03/2005	Maone, Francesco High speed comparator
7734261	US	12/09/2005	Maone, Francesco Calibrating amplitude and phase imbalance and DC offset of an analog I/Q modulator in a high-frequency transmitter
CN200580043136.X	CN	12/09/2005	Bury, Andreas Calibrating amplitude and phase imbalance and DC offset of an analog I/Q modulator in a high-frequency transmitter
DE602005027113.9	DE	12/09/2005	Bury, Andreas Calibration of amplitude and phase imbalance and DC offset of an analog I/Q modulator in a high-frequency transmitter
FR1829314	FR	12/09/2005	Bury, Andreas Calibrating amplitude and phase imbalance and DC offset of an analog I/Q modulator in a high frequency transmitter
GB1829314	GB	12/09/2005	Bury, Andreas Calibrating amplitude and phase imbalance and DC offset of an analog I/Q modulator in a high-frequency transmitter
JP4580991	JP	12/09/2005	Bury, Andreas Calibrating amplitude and phase imbalance and dc offset of an analog I/Q modulator in a high-frequency transmitter
5479621	US	08/04/1994	Bury, Andreas Data processor, for use with a chained language, with apparatus for distinguishing directly executable instructions from instructions requiring a sub-routine call
5675776	US	12/12/1994	Duranton, Marc Data processor using FIFO memories for routing operations to parallel

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			operational units
5444409	US	06/09/1993	Duranton, Marc Interface circuit for linking microprocessors
5896182	US	06/11/1996	Perraud, Jean-Claude Amplitude modulator
5900839	US	09/11/1997	Klaevie, Philippe Radio transmission apparatus comprising a retractable antenna and an antenna device for such apparatus
GB0834990	GB	09/26/1997	Duperray, David Digital programmable phase-shifter and analog-digital converter using such a phase shifter
JP3918046	JP	09/29/1997	Marie, Herve Digital programmable phase shifter and A/D converter using phase shifter
6215984	US	02/10/1998	Marie, Herve Luminous antenna and radio communication equipment comprising such an antenna
6172316	US	03/08/1999	Figueras, Eric Antenna switching device
6271791	US	10/28/1999	Jacob, Herve Radio-signal direction finding
6748198	US	12/03/1999	Bruzzong, Raul A. Tunable receiver
FR1053594	FR	11/24/1999	Salo, Jarmo Olavi Tunable receiver
GB1053594	GB	11/24/1999	Salo, Jarmo Olavi Tunable receiver
KR10-0693190	KR	11/24/1999	Salo, Jarmo Olavi Tunable receiver, controlling method thereof and method of operating a mobile phones system
6208302	US	01/18/2000	Salo, Jarmo Olavi Mobile telephone antenna system for a satellite and mobile telephone including this antenna system
FR1026775	FR	01/10/2000	Jacob, Herve Antenna set for a satellite mobile phone and a mobile equipped with this antenna system
GB1026775	GB	01/10/2000	Jacob, Herve Antenna set for a satellite mobile phone and a mobile equipped with this antenna system
			Jacob, Herve

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
6502151	US	05/27/1999	Data-processing arrangement including an interrupt generator Duranton, Marc
DE60040445.5	DE	05/17/2000	Data-processing arrangement including an interrupt generator Duranton, Marc
FR1099155	FR	05/17/2000	Data-processing arrangement including an interrupt generator Duranton, Marc
GB1099155	GB	05/17/2000	Data-processing arrangement including an interrupt generator Duranton, Marc
JP4090883	JP	12/12/2001	System integrating agents having different resource-accessing schemes Didier, Robin
TW1172236	TW	05/25/2000	Data-processing arrangement including an interrupt generator Duranton, Marc
6741281	US	05/16/2000	Low-noise memory device having a high sampling frequency Darthénay, Frederic
6459309	US	03/02/2001	Frequency converter enabling a non-integer division ratio to be programmed by means of a unique control word Canard, David
6720809	US	07/23/2002	Frequency converter enabling a non-integer division ratio to be programmed by means of a unique control word Canard, David
FR1133059	FR	03/05/2001	Frequency converter allowing the programming of a non-integer divider ratio by using a unique control word Canard, David
GB1133059	GB	03/05/2001	Frequency converter allowing the programming of a non-integer divider ratio by using a unique control word Canard, David
6407643	US	03/08/2001	Phase-locked loop enabling the generation of a reference signal having a high spectral purity Canard, David
6756843	US	10/22/2001	Signal transmitter with pulse-controlled amplification Charley, Sylvain
JP4037083	JP	10/24/2001	Signal transmitter with impulse control of amplification Charley, Sylvain
7038496	US	11/28/2001	Device for comparison of frequencies with low temporal inertia Canard, David
FR0015356	FR	11/28/2000	Improved phase/frequency demodulator Canard, David
JP4294243	JP	11/27/2001	Device for comparison of frequency with short time delay Charley, Sylvain

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
FR1220473	FR	12/18/2001	Space-diversity receiver and method for processing signals received from several paths Jacob, Herve
GB1220473	GB	12/18/2001	Space-diversity receiver and method for processing signals received from several paths Jacob, Herve
7020182	US	12/21/2001	Apparatus comprising a receiving device working with space diversity and processing method for signal received over various channels Herve, Jacob
6606691	US	12/20/2001	System integrating agents having different resource-accessing schemes Didier, Robin
6888893	US	02/26/2002	Method and device for encoding MPEG-4 video data Gentric, Philippe
CN202800255.5	CN	02/25/2002	Method and device for encoding MPEG-4 video data Gentric, Philippe
EP02712185.4	EP	02/25/2002	Method and device for encoding MPEG-4 video data Gentric, Philippe
KR10-0841896	KR	02/25/2002	Method and device for encoding MPEG-4 video data Gentric, Philippe
7038964	US	05/21/2002	Access to a common memory in which a priority access to a non-active bank is prepared during a non-priority access to a different bank Mutz, Stephane
CN202802468.0	CN	05/21/2002	Method and system for access to a common memory Mutz, S.
DE60226024.8	DE	05/21/2002	Method and system for access to a common memory Mutz, Stephane
FR1393909	FR	05/21/2002	Method and system for access to a common memory Mutz, Stephane
GB1393909	GB	05/21/2002	Method and system for access to a common memory Mutz, Stephane
JP4237503	JP	05/21/2002	Method and system for access to a common memory Mutz, Stephane
6710661	US	06/03/2002	Low input impedance with amplifier Braz, Fabio
DE60237336.0	DE	05/31/2002	Improved current driver Braz, Fabio
FR1265356	FR	05/31/2002	Amplifier with low input impedance Braz, Fabio
GB1265356	GB	05/31/2002	Amplifier with low input impedance

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
JP4180307	JP	06/05/2002	Braz, Fabio Low input impedance amplifier
6700443	US	07/03/2002	Braz, Fabio Reduction of sensitivity of an amplifier to an offset voltage
CNZL02127556.4	CN	06/29/2002	Desbonnets, Eric Bernard Marie Francois Reducing amplifier sensitivity to deflection voltage
JP4112295	JP	07/01/2002	Desbonnets, Eric Method and apparatus for amplifying input signal having dc component and ac component
DE60238237.8	DE	08/21/2002	Desbonnets, Eric Dynamic access control to a function in a distributed resource
FR1293909	FR	08/21/2002	de Perthuis, Hugues Dynamic access control of a function to shared resource
GB1293909	GB	08/21/2002	de Perthuis, Hugues Dynamic access control of a function to a shared resource
6959371	US	08/27/2002	de Perthuis, Hugues Dynamic access control of a function to a collective resource
7130179	US	10/23/2002	De Perthuis, Hugues Switching device provided with polarity-inverting means
JP4238008	JP	10/18/2002	Guiraud, Lionel Switching device provided with polarity-inverting means
6774727	US	12/17/2002	Guiraud, Lionel Device comprising a symmetrical amplifier
FR0116574	FR	12/20/2001	Bouvier, Stephane Dispositif comportant un amplificateur symAtrique
7319697	US	12/17/2002	Bouvier, Stephane Data communication system according to a packet-switching principle
7689781	US	02/24/2003	Duranton, Marc Access to a collective resource in which low priority functions are grouped, read accesses of the group being given higher priority than write accesses of the group
CNZL03106644.5	CN	02/21/2003	De Perthuis, Hugues Access public source
EP03075449.3	EP	02/17/2003	de Perthuis, H. Access to a shared resource
FR0202398	FR	02/26/2002	de Perthuis, Hugues Access to a resource group
			De Perthuis, Hugues

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			Bourge, Armand
7711343	US	04/26/2003	Flat tuner module with a standard connector
			Soh, Kim Leng
CNZL200420077189.6	CN	08/30/2004	Flat tuner module with standard connector
			Soh, K. L.
7660963	US	06/08/2005	Interface device for debugging and/or tracing a computer system comprising one or multiple masters and one or multiple slaves working together
			Bernasconi, Eric
CNZL200580019424.1	CN	06/08/2005	Interface device for debugging and/or tracing a computer system comprising one or multiple masters and one or multiple slaves working together
			Bernasconi, E.
DE602005017038.3	DE	06/08/2005	Interface device for debugging and/or of a computer system tracing with one or more master and one or multiple slaves to work together
			Bernasconi, Eric
FR1761851	FR	06/08/2005	Interface device for debugging and / or tracing a computer system or multiple masters comprising one and one or multiple slaves working together
			Bernasconi, Eric
GB1761851	GB	06/08/2005	Interface device for debugging and/or tracing a computer system comprising one or multiple masters and one or multiple slaves working together
			Bernasconi, Eric
7696840	US	08/22/2005	RF selection switch for multiple antenna input
			Lim, Kui Y.
CNZL200580028212.X	CN	08/22/2005	RF selection switch for multiple antenna input
			Lim, Kui Y.
6963625	US	08/01/2001	Dada decoding
			Redman-White, William
FR1346436	FR	11/29/2001	Antenna arrangement
			Boyle, Kevin
GB1346436	GB	11/29/2001	Antenna arrangement
			Boyle, Kevin
6624795	US	11/30/2001	Antenna arrangement
			Boyle, Kevin R.
6907559	US	12/20/2001	Method and apparatus for data reproduction
			Hall, Trevor G. R.
6877126	US	12/18/2001	Method and apparatus for data reproduction
			Hall, Trevor G. R.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
7295630	US	12/10/2001	Receiver having a variable threshold slicer stage and a method of updating the threshold levels of the slicer stage Payne, Adrian W.
CNZL01806086.2	CN	12/21/2001	Receiver with variable threshold slicer and method of DC offset correction Payne, A. W.
JP4228050	JP	12/21/2001	Receiver having a variable threshold slicer stage and a method of updating the threshold levels of the slicer stage Payne, Adrian W.
KR10-0856815	KR	12/21/2001	Receiver having a variable threshold slicer stage and a method of updating the threshold levels of the slicer stage Payne, Adrian W.
DE60133412.4	DE	12/21/2001	Variable threshold decision makers and method for correction of DC power shifts in a receiver Payne, Adrian
FR1350371	FR	12/21/2001	Receiver has a variable threshold slicer and method of revision level threshold slicer Payne, Adrian Weston
GB1350371	GB	12/21/2001	Receiver having a variable threshold slicer stage and a method of updating the threshold levels of the slicer stage Payne, Adrian W.
FR1360740	FR	01/15/2002	Diversity for capacitively back-coupled handset Boyle, Kevin
GB1360740	GB	01/15/2002	Wireless terminal with a plurality of antennas Boyle, Kevin
6791498	US	01/23/2002	Wireless terminal Boyle, Kevin R.
GB1310014	GB	07/20/2001	Wireless terminal Boyle, Kevin
7835776	US	07/25/2001	Wireless terminal Boyle, Kevin R.
7289784	US	09/10/2002	Active tunable filter circuit Nam, Sueng-il
JP4439912	JP	09/03/2002	Active tunable filter circuit Nam, Sueng-il
KR10-0880100	KR	09/03/2002	Active tunable filter circuit Nam, Sueng-il
DE60224545.1	DE	09/03/2002	Tunable active filter circuit Nam, Sueng-il
FR1430600	FR	09/03/2002	Tunable active filter circuit

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
GB1430600	GB	09/03/2002	Nam, Sueng-il Active tunable filter circuit
DE60323229.9	DE	06/13/2003	Nam, Sueng-il Pulse width modulation generator
FR1520345	FR	06/13/2003	Steele, Andrew Generator signal pulse width modulation
GB1520345	GB	06/13/2003	Steele, Andrew Pulse width modulation signal generator
7141896	US	06/13/2003	Steele, Andrew Pulse width modulation signal generator
JP4249130	JP	06/13/2003	Steele, Andrew Pulse width modulation signal generator
7671808	US	07/02/2004	Steele, Andrew Communication device and an antenna therefor
7457992	US	12/17/2004	Boyle, Kevin R. Delay fault test circuitry and related method
CNZL200480039163.5	CN	12/17/2004	Mittal, Aviral Delay fault test circuitry and related method
7693288	US	02/07/2005	Mittal, A. Remote control system and related method and apparatus
CNZL200580004573.X	CN	02/07/2005	Mergler, Iwo-Martin Remote control system and related method and apparatus
EP05702911.8	EP	02/07/2005	Mergler, Iwo-Martin Remote control system and related method and apparatus
DE602005006413.3	DE	06/06/2005	Mergler, Iwo-Martin Method and apparatus for writing data on an optical disc
FR1759385	FR	06/06/2005	Wu, Xinyan Method and apparatus for writing data on an optical disc
GB1759385	GB	06/06/2005	Wu, Xinyan Method and apparatus for writing data to an optical disc
7872954	US	06/06/2005	Wu, Xinyan Method and apparatus for writing data to an optical disc
CNZL200580019778.6	CN	06/06/2005	Wu, Xinyan Method and apparatus for writing data to an optical disc
DE602005006416.8	DE	06/06/2005	Wu, X. LPP signal processing
			Wu, Xinyan

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FR1761919	FR	06/06/2005	Land pre-pit signal processing method and related apparatus Wu, Xinyan
GB1761919	GB	06/06/2005	Land pre-pit signal processing method and related apparatus Wu, Xinyan
7796482	US	06/06/2005	Land Pre-Pit signal processing method and related apparatus Wu, Xinyan
CNZL200580020056.2	CN	06/06/2005	Land pre-pit signal processing method and related apparatus Wu, X.
7605641	US	07/26/2005	Apparatus comprising a charge pump and LCD driver comprising such an apparatus Negoi, Andy
CNZL200580025575.8	CN	07/26/2005	Apparatus comprising a charge pump and LCD driver comprising such an apparatus Negoi, A. C.
EP05776288.2	EP	07/26/2005	Apparatus comprising a charge pump and LCD driver comprising such an apparatus Negoi, Andy C.
JP2007-523224	JP	07/26/2005	Apparatus comprising a charge pump and LCD driver comprising such an apparatus Negoi, Andy C.
TW094125258	TW	07/26/2005	Apparatus comprising a charge pump and LCD driver comprising such an apparatus Negoi, Andy Catalin
DE60041856.1	DE	06/15/2000	An electric part Horie, Kenichi
FR1110312	FR	06/15/2000	An electronic part Horie, Kenichi
GB1110312	GB	06/15/2000	An electric part Horie, Kenichi
6388863	US	06/15/2000	Electronic part Horie, Kenichi
6621400	US	01/26/2001	Electronic part and a complex electronic device Horie, Kenichi
6683572	US	08/28/2001	Chip antenna device and method Abe, Masaaki
6778142	US	02/15/2002	Electronic device and a circuit arrangement Horie, Kenichi
FR0514974	FR	05/12/1992	Controllable amplifier circuit Uittenhogaard, Herman

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
GB0514974	GB	05/12/1992	Controllable amplifier circuit Uittenbogaard, Herman
5216383	US	04/23/1992	Controllable amplifier circuit Uittenbogaard, Teunis H.
HK0961709	HK	05/12/1992	Controllable amplifier circuit Uittenbogaard, Herman
5584000	US	05/22/1995	Addressing scheme for microcode store in a signal processor Benschop, Nico F.
5450621	US	08/17/1993	Radio receiver with digital control loop for coarse frequency acquisition and analog control loop for frequency lock-in Kianush, Kaveh
5430891	US	08/17/1993	Tuning detection circuit for a high-frequency receiver, and receiver including such detection circuit Kianush, Kaveh
5617303	US	12/03/1993	Motor control arrangement, and shaver comprising such a motor control arrangement Fronen, Robert J.
5438692	US	10/07/1993	Direct conversion receiver Mohindra, Rishi
FR0608012	FR	01/06/1994	Processing system with a ferroelectric memory Cuppens, Roger
GB0608012	GB	01/06/1994	Processing system with a ferroelectric memory Cuppens, Roger
5424976	US	01/11/1994	Processing system with a ferroelectric memory Cuppens, Roger
FR0605057	FR	12/24/1993	Switched-mode supply circuit and control circuit for use in such a power supply circuit Marinus, Antonius Adrianus Maria
GB0605057	GB	12/24/1993	Switched-mode supply circuit and control circuit for use in such a power supply circuit Marinus, Antonius Adrianus Maria
5412556	US	12/28/1993	Switched mode power supply circuit including a control circuit having two modes of operation each mode selected in dependence on the power to be supplied at the output Marinus, Amonius A. M.
6134688	US	11/26/1997	Electronic device selectably operable as a sequential logic circuit or a combinatorial logic circuit and circuit testing method Sachdev, Manoj
6097276	US	12/09/1994	Electric resistor having positive and negative TCR portions Van Den Broek, Jan J.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
DE69514168.6	DE	02/24/1995	Adjustable resistance device with control circuit Kasperkovitz, Wolfdietrich Georg
FR0697150	FR	02/24/1995	Adjustable resistance device with control circuit Kasperkovitz, Wolfdietrich Georg
GB0697150	GB	02/24/1995	Adjustable resistance device with control circuit Kasperkovitz, Wolfdietrich Georg
5493250	US	02/24/1995	Adjustable resistance device including first and second arrangements of a resistor and a positive-feedback transistor, and a control circuit for controlling the resistances of the first and second arrangements Kasperkovitz, Wolfdietrich G
DE69507924.7	DE	04/07/1995	An arrangement for manufacturing process, with a longitudinal carrier to head for electric railways of contacting semiconductor elements Nellissen, Antonius Johannes Maria
5750417	US	04/13/1995	Method of manufacturing a device having a support bar with conductor tracks for electrically contacting a semiconductor element Nellissen, Antonius J. M.
KR10-0335546	KR	04/07/1995	Method of manufacturing a device having a support bar with conductor tracks for electrically contacting a semiconductor element Nellissen, Antonius Johannes Maria
5552694	US	06/01/1995	Power supply with improved efficiency, transmitter comprising such a power supply Appeltans, Koen E. J.
GB0712547	GB	05/31/1995	Power supply with improved efficiency, transmitter comprising such a power supply Appeltans, Koen Emiel Jozef
KR10-0373195	KR	05/31/1995	Power supply with improved efficiency, transmitter comprising such a power supply Appeltans, Koen Emiel Jozef
5828099	US	09/22/1997	Semiconductor device having a nonvolatile memory cell in which the floating gate is charged with hot charge carriers at the source side Van Dort, Maarten J.
KR10-0350819	KR	10/20/1995	Field effect device Van Dort, Maarten Jeroen
5742203	US	10/16/1995	Gain-controllable amplifier, receiver Van De Plassche, Rudy J.
CNZL95191541.X	CN	10/06/1995	Gain controllable amplifier, receiver comprising gain controllable amplifier and method of controlling signal amplitudes van de Plassche, R. J.
DE69518737.6	DE	10/06/1995	A gain controllable amplifier, a receiver comprising a gain-controllable amplifier, and a method of controlling signal amplitudes

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
FR0737381	FR	10/06/1995	Van De Plassche, Rudy Johan A gain controllable amplifier, a receiver comprising a gain-controllable amplifier, and a method of controlling signal amplitudes
GB0737381	GB	10/06/1995	Van De Plassche, Rudy Johan A gain controllable amplifier, a receiver comprising a gain-controllable amplifier, and a method of controlling signal amplitudes
JP3924318	JP	10/06/1995	Van De Plassche, Rudy Johan A gain controllable amplifier, a receiver comprising a gain-controllable amplifier, and a method of controlling signal amplitudes
KR10-0388573	KR	10/06/1995	Van De Plassche, Rudy Johan A gain controllable amplifier, a receiver comprising a gain-controllable amplifier and method of controlling signal amplitudes
5742898	US	11/14/1995	Van De Plassche, Rudy Johan Tuning system with DC-DC converter
JP3650626	JP	10/25/1995	Brekelmans, Johannes H. A. Tuning system with DC-DC converter
KR10-0352203	KR	10/25/1995	Brekelmans, Johannes Hubertus Antonius Tuning system with DC-DC converter
5747841	US	12/19/1995	Brekelmans, Johannes Hubertus Antonius Circuit arrangement, and junction field effect transistor suitable for use in such a circuit arrangement
JP3983285	JP	11/16/1995	Ludikhuize, Adrianus W. Circuit arrangement, and junction field effect transistor suitable for use in such a circuit management
6445235	US	07/05/1995	Ludikhuize, Adrianus Willem IDDQ-testable uni-directional master-slave
KR10-0495456	KR	06/29/1995	Sachdev, Manoj A Method of testing and an electronic circuit comprising flipflop with a master and a slave
DE69616234.2	DE	05/07/1996	Sachdev, Manoj Preload with transformer
GB0772907	GB	05/07/1996	Van De Westerlo, Marcel Circuit bias via transformer
5731740	US	05/20/1996	Van De Westerlo, Marcel Henricus Wilhelmus Circuit bias via transformer
CNZL96190722.3	CN	05/07/1996	Van De Westerlo, Marcel H. W. Circuit bias via transformation
JP3778569	JP	05/07/1996	Van De Westerlo, M. H. W. Circuit bias via transformer
			Van De Westerlo, Marcel Henricus Wilhelmus

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6122751	US	12/09/1996	Pipelined data processing circuit Janssens, Mark A. E.
GB0777874	GB	05/31/1996	Pipelined data processing circuit Janssens, Jan Albert Maria
JP3776127	JP	05/31/1996	Pipelined data processing circuit Janssens, Jan Albert Maria
GB0795230	GB	09/20/1996	Switched-mode power supply with transformer and feedback via primary winding Schoofs, Franciscus Adrianus Cornelis Maria
5757625	US	09/30/1996	Switched mode power supply with transformer and feedback via primary winding Schoofs, Franciscus A. C. M.
JP3690814	JP	09/20/1996	Switched-mode power supply with transformer and feedback via primary winding Schoofs, Franciscus Adrianus Cornelis Maria
5828588	US	11/25/1996	Parametrizable control module comprising first and second loadable counter, an electronic circuit comprising a plurality of such parametrized control modules, and a method for synthesizing such circuit Grant, Douglas M.
5809407	US	12/11/1996	Receiver, an arrangement and a method for comparing two signals Kasperkovitz, Wolfdietrich G.
KR10-0477409	KR	12/03/1996	A receiver, an arrangement and a method for comparing two signals Kasperkovitz, Wolfdietrich Georg
6128043	US	01/31/1997	Multi-standard signal reception Tulder, Henricus B. J. M.
JP4183751	JP	01/17/1997	Multi-standard signal reception Tulder, Henricus Bernardus Josephus Maria
DE69707209.6	DE	01/17/1997	Reception of signals more standards Tulder, Henricus
FR0819355	FR	01/17/1997	Multi-standard signal reception Tulder, Henricus Bernardus Josephus Maria
GB0819355	GB	01/17/1997	Multi-standard signal reception Tulder, Henricus Bernardus Josephus
5808507	US	02/28/1997	Temperature compensated reference voltage source Melse, Abraham L.
JP4031043	JP	01/31/1997	Reference voltage source with temperature compensation Melse, Abraham Lodewijk
FR0834189	FR	03/13/1997	Manufacture of a semiconductor device with an epitaxial semiconductor zone

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
GB0834189	GB	03/13/1997	Manufacture of a semiconductor device with an epitaxial semiconductor zone Dekker, Ronald
6368946	US	03/24/1997	Manufacture of a semiconductor device with an epitaxial semiconductor zone Dekker, Ronald
JP4150076	JP	03/13/1997	Manufacture of a semiconductor device with an epitaxial semiconductor zone Dekker, Ronald
5760450	US	03/31/1997	Semiconductor resistor using back-to-back zener diodes Hurkx, Godefridus A. M.
JP4145353	JP	04/08/1997	Semiconductor device provided with a resistance element Hurkx, Godefridus, Adrianus, Maria
GB0846367	GB	05/29/1997	Bridge amplifier with feedback over the load Huijser, Jan Paulus Freerk
5903189	US	06/05/1997	High gain low distortion bridge amplifier with feedback Huijser, Jan-Paul
JP4027981	JP	05/29/1997	Bridge amplifier with feedback over the load Huijser, Jan Paulus Freerk
6046493	US	07/03/1997	Semiconductor device with special emitter connection Dekker, Ronald
KR10-0471520	KR	06/06/1997	Semiconductor device with special emitter connection Dekker, Ronald
5751236	US	09/04/1996	A/D Conversion with Folding and Interpolation Vorenkamp, Pieter
FR0855101	FR	06/18/1997	An amplifier and a method for detecting the presence of a load Huijser, Jan Paulus Freerk
JP3867278	JP	06/18/1997	Dimensionally stable flexographic printing plates Huijser, Jan Paulus Freerk
KR10-0457841	KR	06/18/1997	An amplifier and a method for detecting the presence of a load Huijser, Jan Paulus Freerk
JP3795923	JP	09/02/1996	Signal processing with sampling Van De Plassche, Rudy Johan
KR10-0436947	KR	09/02/1996	Signal processing with sampling Van De Plassche, Rudy, Johan
6009126	US	08/01/1997	Zero-IF receiver Van Bezooijen, Adrianus

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
KR10-0503531	KR	10/02/1997	Akhnoukh, Atef Semiconductor device with a high-frequency bipolar transistor on an insulating substrate
GB0870364	GB	09/26/1997	Akhnoukh, Atef Sample rate conversion
JP4356819	JP	09/26/1997	Van Dalssen, Age Jochem Sample rate conversion
KR10-0475201	KR	09/26/1997	Van Dalssen, Age Jochem Sample rate conversion
JP3942643	JP	12/08/1997	Van Dalssen, Age, Jochem Receiver with a tunable parallel resonant circuit
6011965	US	12/29/1997	Lim, Kui Yong Receiver with a tunable parallel resonant circuit
JP4014648	JP	03/02/1998	Lim, Kui Y. Digitally controlled switched-mode voltage converter
KR10-0580801	KR	03/02/1998	Appeltans, Koen Emiel Jozef Digitally controlled switched-mode voltage converter
FR0923323	FR	03/02/1998	Appeltans, Koen, Emiel, Jozef Digitally controlled switched-mode voltage converter
GB0922323	GB	03/02/1998	Appeltans, Koen Emiel Jozef Digitally controlled switched-mode voltage converter
6242762	US	05/13/1998	Appeltans, Koen Emiel Jozef Semiconductor device with a tunnel diode and method of manufacturing same
6436785	US	04/11/2001	Brown, Adam R. Method of manufacturing semiconductor device with a tunnel diode
DE69837030.9	DE	04/14/1998	Brown, Adam R. Tunnel diode with Si Ge
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GB0923792	GB	04/14/1998	Brown, Adam Richard Silicon germanium semiconductor device and a method of manufacturing the same
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			Tao, Guoqiao
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			Boezen, Hendrik
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			Boezen, H.
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7388439	US	05/26/2004	Collados Asensio, Manel Low pass filter and electronic device
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CNZL99801264.5	CN	05/31/1999	Opcode-driven buffer manager for use in data storage devices Kevin Ross
DE69938647.0	DE	05/31/1999	Opcode-driven buffer manager for use in data storage devices Kevin Ross
6286061	US	06/02/1998	System for applying a tag to a frame based on certain characteristics of the frame and transferring the frame as a function of the tag Ross, Kevin

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
JP2000-552663	JP	05/31/1999	Opcode-driven buffer manager for use in data storage devices Kevin Ross
TWI145156	TW	07/29/1999	Minimal frame buffer manager for use in data storage devices Ross, Kevin
6038194	US	12/28/1998	Memory decoder with zero static power Davies, Thomas J.
TWI150991	TW	03/29/2000	A memory decoder with zero static power Davies, Thomas James
TWI234109	TW	12/07/2000	Variable-instruction-length processing Saville, Winthrop L.
TWI172269	TW	03/07/2001	Circular address register Ostler, Farrell L.
5359337	US	05/14/1990	Automatic synthesis of integrated circuits employing controlled input dependency during a decomposition process Saucier, Gabriele
5291457	US	02/20/1992	Sequentially accessible non-volatile circuit for storing data Asato, Creighton S.
5233238	US	12/20/1991	High power buffer with increased current stability Matios, Derwin W.
5294295	US	10/31/1991	Method for moisture sealing integrated circuits using silicon nitride spacer protection of oxide passivation edges Gabriel, Calvin T.
5537572	US	03/31/1992	Cache controller and method for dumping contents of a cache directory and cache data random access memory (RAM) Michelsen, Jeff M.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
5271040	US	12/20/1991	Phase detector circuit Clark, Lawrence T.
5223751	US	10/29/1991	Logic level shifter for 3 volt CMOS to 5 volt CMOS or TTL Simmons, Laura E.
5339270	US	06/23/1993	AC drain voltage charging source for PROM devices Jiang, Chun
5857095	US	07/10/1995	Method and apparatus for synchronizing transfer of data between memory cells Buckenmaier, Karl C.
5483644	US	04/15/1993	Method for increasing cacheable address space in a second level cache Richardson, Nicholas J.
5473562	US	08/05/1994	Method and apparatus for minimizing power-up crowbar current in a retargetable SRAM memory system Liu, Pin-Wu
5603045	US	12/16/1994	Microprocessor system having instruction cache with reserved branch target section Dockser, Kenneth A.
5583894	US	03/20/1995	Slip buffer for synchronizing data transfer between two devices Linsley, Charles E.
5764357	US	04/12/1996	Zero-run-length encoder with shift register Dockser, Kenneth A.
5913228	US	03/12/1997	Method and apparatus for caching discontinuous address spaces with short cache tags Bedarida, Alessandro
5659496	US	12/28/1995	System and method for programming VPROM links Levy, Paul S.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
5752262	US	07/25/1996	System and method for enabling and disabling writeback cache Cassetti, David K.
5784625	US	03/19/1996	Method and apparatus for effecting a soft reset in a processor device without requiring a dedicated external pin Walker, Gary
5748744	US	06/03/1996	Secure mass storage system for computers Levy, Paul S.
5774135	US	11/05/1996	Non-contiguous memory location addressing scheme Letham, Lawrence
6016315	US	04/30/1997	Virtual contiguous FIFO for combining multiple data packets into a single contiguous stream Chambers, Peter
6226736	US	03/10/1997	Microprocessor configuration arrangement for selecting an external bus width Niot, Francois
6115367	US	08/05/1997	Methods of analyzing a radio signal and methods of analyzing a personal handy-phone system radio signal Archambaud, Denis
6768798	US	11/19/1997	Method of customizing HRTF to improve the audio experience through a series of test sounds Dempsey, Morgan James
6215879	US	11/19/1997	Method for introducing harmonics into an audio stream for improving three dimensional audio positioning Dempsey, Morgan James
6158018	US	11/25/1997	Integrated circuit including patching circuitry to bypass portions of an internally flawed read only memory and a method therefor Bernasconi, Eric
5973573	US	11/18/1997	VCO circuit having low gain variation over different processes and

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
			operating temperatures and having low power supply noise sensitivity Iravani, Kamran
5936880	US	11/13/1997	Bi-layer programmable resistor memory Payne, Robert L.
6069567	US	11/25/1997	Audio-recording remote control and method therefor Zawilski, Peter S.
6044432	US	11/12/1997	Method and system for latching an address for accessing synchronous random access memory using a single address status signal control line Anand, Vishal
6104226	US	01/20/1998	Low power undervoltage detector with power down mode Ciccone, John C.
6247095	US	06/24/1998	Digital reverberation processor and method for generating digital reverberation Knudsen, Carl John
6223232	US	02/22/1999	System and method to predict configuration of a bus target Adusumilli, Swaroop
6442671	US	03/03/1999	System for priming a latch between two memories and transferring data via the latch in successive clock cycle thereafter Faucon, Christelle
6317016	US	05/14/1999	Method and arrangement for gyration filtering with low power consumption Kuo, Timothy C.
DE69316037.3	DE	01/11/1993	SILICIDE GATE TUNER TETRODE Steve Battersby (Louis Praamsma)
TW1200822	TW	08/13/2002	Random-access memory devices comprising a dioded buffer Stojanov, Nikola

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
JP3442086	JP	09/20/1991	CIRCUIT FOR MAGNETORESISTIVE REVOLUTION SENSOR Juergen Kordts (Georg Kisters) (Michael Ramm)
DE59407254.9	DE	05/02/1994	SOUND CARRIER FROM IF IN MULTI-STANDARD TV Joachim Brilka (Wolfgang Weltersbach)
IT0624976	IT	05/02/1994	SOUND CARRIER FROM IF IN MULTI-STANDARD TV Joachim Brilka (Wolfgang Weltersbach)
CNZL97190157.0	CN	03/05/1997	STARTING CIRCUIT FOR HIGH PASS FILTERS Burkhard Dick (Manfred Biehl) (Winfried Jansen) (Bernhard Pille) (Norbert Wirges)
DE59707197.7	DE	07/21/1997	ANTENNA DIVERSITY IN PORTABLE TRANSCEIVER Peter Johan Massey
TWI116911	TW	05/20/1998	Reactor for the processing of wafers, with a protection device Wolfgang, Volie
MY123929	MY	04/23/1998	CIRCUIT FOR FILTERING A COLOR-UNDER-SIGNAL Herbert Heinemann (Shin Ichi Uchiyama)
TWI122891	TW	01/29/1999	Circuit arrangement for color signal processing Herbert, Heinemann
TWI171757	TW	10/04/1999	Circuit arrangement for bias adjustment of bus levels Mores, Robert
DE19943895.1	DE	09/14/1999	Unable to verify Unable to verify
TWI184755	TW	10/04/2000	Network coupler Wendt, Matthias
CNZL01119033.7	CN	04/03/2001	Medium insertion contention solving plan in network

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
DE10143999.7	DE	09/07/2001	Fuhrmann, P. Unable to verify Nicolai, Volker
JP3919895	JP	09/22/1997	ANTENNA FOR RADIO MOBILES David Duperray
JP4216392	JP	03/10/1999	PIEZO-ELECTRIC SWITCH IN GSM Herve Jacob
IT1053594	IT	11/24/1999	RECEIVER CHANNEL-SELECT CALIBRATION Jarmo Olavi Salo
DE60027491.8	DE	01/10/2000	LOG ANTENNA FOR MOBILE TELEPHONY BY SATELLITE Herve Jacob
CNZL00101106.5	CN	01/17/2000	LOG ANTENNA FOR MOBILE TELEPHONY BY SATELLITE Herve Jacob
KR10-0693932	KR	01/18/2000	LOG ANTENNA FOR MOBILE TELEPHONY BY SATELLITE Herve Jacob
DE60114909.2	DE	03/05/2001	INTERFACE BETWEEN CONVENTIONAL AND FRACTIONAL- N TUNERS David Canard Vincent Fillatre
CNZL01116890.0	CN	03/07/2001	INTERFACE BETWEEN CONVENTIONAL AND FRACTIONAL- N TUNERS David Canard Vincent Fillatre
KR10-0819217	KR	03/08/2001	INTERFACE BETWEEN CONVENTIONAL AND FRACTIONAL- N TUNERS David Canard Vincent Fillatre
KR10-0806506	KR	03/09/2001	IMPROVED FRACTIONAL-N PLL-TUNER David Canard Vincent Fillatre

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
DE60112632.7	DE	03/05/2001	IMPROVED FRACTIONAL-N PLL-TUNER David Canard Vincent Fillatre
JP4024038	JP	12/25/2001	2-D RAKE RECEIVER Herve Jacob
DE60124496.6	DE	12/18/2001	2-D RAKE RECEIVER Herve Jacob
IT1220473	IT	12/18/2001	2-D RAKE RECEIVER Herve Jacob
CNZL01144791.5	CN	12/22/2001	2-D RAKE RECEIVER Herve Jacob
KR10-0814245	KR	12/24/2001	2-D RAKE RECEIVER Herve Jacob
TWI236388	TW	02/22/2002	System integrating agents having different resource-accessing schemes Didier, Robin
IN947446	IN	02/25/2002	MAPPING OF MPEG-4 PARTITIONS Philippe Gentric Beatrice Nezot Yves Ramanzin
CNZL200480038306.0	CN	12/14/2004	UNIVERSAL DEROTATOR FOR UMTS MODES Eric Alliot Rodolfo Verzegnassi
IN3731/CHENP/2006	IN	03/22/2005	COMBINING SIGNAL-INTERFERENCE-RATIO(SIR) ESTIMATES FOR IMPROVED SIR ESTIMATION Giuseppe Montalbano
IN3951/CHENP/2006	IN	04/21/2005	LOW-COST RESIZING METHOD WITH SHARPNESS ENHANCEMENT Arnaud Pierre Hervé Bourge Luis Escobar

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
TWI281349	TW	04/27/2005	Flat tuner module with a standard connector Soh, Kim-Leng
EP01943508.0	EP	06/14/2001	MULTIPLE FEED ANTENNA WITH DISTRIBUTED MULTIPLEXER Kevin Boyle
KR10-0796828	KR	06/14/2001	MULTIPLE FEED ANTENNA WITH DISTRIBUTED MULTIPLEXER Kevin Boyle
CNZL01802505.6	CN	06/14/2001	MULTIPLE FEED ANTENNA WITH DISTRIBUTED MULTIPLEXER Kevin Boyle
CNZL01805076.X	CN	11/29/2001	MULTIPLE FEED (FREQUENCY) FOLDED ANTENNA Kevin Boyle
DE60121470.6	DE	11/29/2001	MULTIPLE FEED (FREQUENCY) FOLDED ANTENNA Kevin Boyle
JP3978136	JP	11/29/2001	MULTIPLE FEED (FREQUENCY) FOLDED ANTENNA Kevin Boyle
TWI172378	TW	01/08/2002	Receiver having a variable threshold slicer stage and a method of updating the threshold levels of the slicer stage Payne, Adrian Weston
TWI192229	TW	01/29/2002	DIVERSITY FOR CAPACITIVELY BACK-COUPLED HANDSET Kevin Boyle (Peter Johan Massey
CNZL02800225.3	CN	01/15/2002	DIVERSITY FOR CAPACITIVELY BACK-COUPLED HANDSET Kevin Boyle (Peter Johan Massey
CNZL01802312.6	CN	07/20/2001	WIRELESS TERMINAL Kevin Boyle (Peter Johan Massey (Roger Hill)

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
DE60128700.2	DE	07/20/2001	WIRELESS TERMINAL Kevin Boyle (Peter Johan Massey (Roger Hill
FR1310014	FR	07/20/2001	Wireless terminal Boyle, Kevin
KR10-0818041	KR	07/20/2001	WIRELESS TERMINAL Kevin Boyle (Peter Johan Massey (Roger Hill
TWI42023	TW	07/24/2000	An electronic part Horie, Kenichi
JP11-167988	JP	06/15/1999	Electronic component Horie, Kenichi
DE69204085.4	DE	02/04/1992	BROADBAND TRANSFORMER WITH COMPENSATION WINDING Robbert Carel Thuis
DE69207643.3	DE	05/12/1992	LOW BIAS VOLTAGE AGC AMPLIFIER Herman Uittenbogaard (Louis Praamsma
IT0514974	IT	05/12/1992	LOW BIAS VOLTAGE AGC AMPLIFIER Uittenbogaard, Herman
SG033187	SG	05/12/1992	LOW BIAS VOLTAGE AGC AMPLIFIER Uittenbogaard, Herman
KR10-0256379	KR	05/18/1992	LOW BIAS VOLTAGE AGC AMPLIFIER Herman Uittenbogaard (Louis Praamsma
DE69413567.4	DE	01/06/1994	WORD ORIENTED FERRO ELECTRIC MEMORY Roger Cuppens
DE69318123.0	DE	12/24/1993	SMPS HAVING TWO MODES, LOAD DEP. + DEMAG. DEP.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
MY115448	MY	11/13/1995	Antonius Adrianus Maria Marinus TUNING SYSTEM WITH DC-DC CONVERTER Johannes Brckelmans
MY97-000405	MY	02/03/1997	MULTI-STANDARD IF WITH SWITCHABLE PARALLEL FILTER Henricus Bernardus Josephus Maria Tulder
TWI095191	TW	03/14/1997	Semiconductor device Godefridus Adrianus Maria Hurk
MY96-003700	MY	09/06/1996	MULTIPLE FOLDING/INTERPOLATING ADC Pieter Vorenkamp Arnoldus Gerardus Wilhelmus Venes Rudy Johan Plassche
MY96-003701	MY	09/06/1996	SAMPLING OF AMPLITUDE SUB-RANGES Rudy Johan Plassche Arnoldus Gerardus Wilhelmus Venes
DE69809020.9	DE	03/02/1998	Increase of resolution of digital controlled power supply Appeltans, Koen
TWI151668	TW	03/03/2000	A method of manufacturing a semiconductor device Montree, Andreas Hubertus
6219270	US	05/24/1999	Integrated circuit having dynamic memory with boosted plateline Van Geloven, Johannes A. J.
TWI153304	TW	05/18/2000	Multifrequency low-power oscillator for telecommunication IC's Wang, Zhenhua
TWI177892	TW	04/19/2001	Method of manufacturing a semiconductor device Siolk, Peter A.
TWI177741	TW	06/15/2001	A semiconductor device, a memory cell structure comprising a plurality of memory cells, a method of controlling a memory cell and a method of controlling a memory cell structure

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
EP01200088.1	EP	12/01/2001	Tao, Guoqian Unit and method for memory address translation and image processing apparatus comprising such a unit
CNZL02823092.2	CN	11/21/2002	Jaspers, Egbert SCHOTTKY JUNCTION IN HBT WITH METAL EMITTER Raymond Josephus Engelbart Huesting (Jan Willem Slotboom) Leon Cornelis Maria Geever
DE602004017468.8	DE	06/21/2004	 FRAME FORMAT DECODER Arie Geert Cornelis Koppelaar
CNZL200480017672.8	CN	06/21/2004	 FRAME FORMAT DECODER Arie Geert Cornelis Koppelaar
DE602004025246.8	DE	07/12/2004	 ANTENNA SWITCH WITH ADAPTIVE FILTER Pim Jedeloo
JP4422724	JP	07/12/2004	 ANTENNA SWITCH WITH ADAPTIVE FILTER Pim Jedeloo
CN200480021028.8	CN	07/12/2004	 ANTENNA SWITCH WITH ADAPTIVE FILTER Pim Jedeloo
KR10-2006-7001324	KR	07/12/2004	 ANTENNA SWITCH WITH ADAPTIVE FILTER Pim Jedeloo
JP4040105	JP	01/15/1998	 TRANSMISSION-COIL ATTACHED TO SEAT BELT Konrad Baumann
CNZL98800048.6	CN	01/15/1998	 TRANSMISSION-COIL ATTACHED TO SEAT BELT Konrad Baumann
DE69834363.4	DE	01/15/1998	 TRANSMISSION-COIL ATTACHED TO SEAT BELT Konrad Baumann

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
KR10-0377625	KR	01/15/1998	TRANSMISSION-COIL ATTACHED TO SEAT BELT Konrad Baumann
TWI182341	TW	09/21/2000	Method of and device for determining deviations of a nominally planar surface of a thin circular disk-shaped object relative to a reference plane Schober, Joachim
KR10-0223725	KR	06/17/1991	BICMOS WITH GATE CONTACT FROM UNDERNEATH Wilhelmus Josquin
PCT/SG2000/000018	WO	02/08/2000	Unable to verify Unable to verify
TWI164086	TW	04/04/2001	Fast high voltage level shifter with gate oxide protection Goswick, Jeff
6477609	US	01/31/2000	Bridge state-machine progression for data transfers requested by a host bus and responded to by an external bus Reiss, Loren B.
6571308	US	01/31/2000	Bridging a host bus to an external bus using a host-bus-to-processor protocol translator Reiss, Loren B.
6851038	US	05/26/2000	Background fetching of translation lookaside buffer (TLB) entries Krolski, Duane F.
7117274	US	03/05/2004	Graphical user interface and approach therefor Avery, Daniel Lee
CNZL200480012513.9	CN	05/06/2004	WLAN Scheduler Parag Garg
KR10-1037850	KR	05/06/2004	WLAN Scheduler Parag Garg

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
EP04731413.3	EP	05/06/2004	WLAN Scheduler Parag Garg
CNZL200480014841.2	CN	05/28/2004	Closed loop I/Q imbalance detection Yifeng Zhang
EP04735278.6	EP	05/28/2004	Closed loop I/Q imbalance detection Yifeng Zhang
CNZL200480024338.5	CN	08/25/2004	WIRELESS DEVICE WITH DYNAMIC FRAGMENTATION THRESHOLD ADJUSTM Pen Li
EP04744837.8	EP	08/25/2004	WIRELESS DEVICE WITH DYNAMIC FRAGMENTATION THRESHOLD ADJUSTM Pen Li
KR10-2006-7003826	KR	08/25/2004	WIRELESS DEVICE WITH DYNAMIC FRAGMENTATION THRESHOLD ADJUSTM Pen Li
JP2006-524519	JP	08/25/2004	WIRELESS DEVICE WITH DYNAMIC FRAGMENTATION THRESHOLD ADJUSTM Pen Li
CNZL200480028602.2	CN	10/01/2004	DIRECT SPREAD MULTI-CARRIER OFDM UWB TRANSMISSION SYSTEM Charles Razzell
DE602004019442.5	DE	10/01/2004	DIRECT SPREAD MULTI-CARRIER OFDM UWB TRANSMISSION SYSTEM Charles Razzell
7,265,627	US	06/10/2005	SELF ADAPTABLE BIAS CIRCUIT FOR ENABLING DYNAMIC CONTROL OF QUIESCENT CURRENT IN A LINEAR POWER AMPLIFIER Christophe Joly

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority, and/or (iii) that were co-owned applications that incorporate by reference, or are incorporated by reference into, the Patents;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

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(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

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IN WITNESS WHEREOF this Assignment of Patent Rights is executed at New Canaan
NY on 26 SEPTEMBER, 2011

ASSIGNOR:

NXP B.V.

By:

Name:

Title:

(Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

The undersigned witnessed the signature of Kapal Gnanetilleke to the above Assignment of Patent Rights on behalf of NXP B.V. and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
2. Aruna Waxter is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on Sept 26, 2011 to execute the above Assignment of Patent Rights on behalf of NXP B.V.
3. Aruna Waxter subscribed to the above Assignment of Patent Rights on behalf of NXP B.V.

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on Sept 26, 2011 (date)

Print Name:

Kapal Gnanetilleke