PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Nanosys, Inc.	05/24/2012

RECEIVING PARTY DATA

Name:	SanDisk Corporation
Street Address:	601 McCarthy Boulevard
City:	Milpitas
State/Country:	CALIFORNIA
Postal Code:	95035

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	13098082

CORRESPONDENCE DATA

Fax Number: (914)579-2201
Phone: (914) 579-2200
Email: Duganemail@duganpatent.com
Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.
Correspondent Name: Brian M. Dugan, Dugan & Dugan, PC
Address Line 1: 245 Saw Mill River Road, Suite 309
Address Line 4: Hawthorne, NEW YORK 10532

OP $40.00 13098082

ATTORNEY DOCKET NUMBER:	SD-NSA-0003-2-I
NAME OF SUBMITTER:	James Trosino

Total Attachments: 7
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DEED OF PATENT ASSIGNMENT

This Patent Assignment ("Agreement") is entered into this 24th day of May, 2012 ("Effective Date"), by Nanosys, Inc., a Delaware corporation ("Assignor") in order to assign Assignor's entire right title and interest in and to the Assigned Patent Rights (defined below) to SanDisk Corporation, a Delaware corporation (together with any successors, legal representatives of assigns thereof, "Assignee").

WHEREAS, Assignor owns the entire right, title and interest in and to the Assigned Patent Rights (as defined below)

AND WHEREAS Assignee wants to acquire the entire right, title and interest in and to said Assigned Patent Rights;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, Assignor has sold, assigned and transferred, and does hereby sell, assign and transfer to Assignee the entire right, title and interest in and to the Assigned Patent Rights across all territories, provinces, or states of all countries, including of Australia, Canada, China, Europe, Japan, Korea, Malaysia, and Taiwan; Assignor empowers Assignee, its successors, assigns and nominees, to make applications for patent registration or protection anywhere in the world, to claim and receive the benefit of any applicable rights of priority in connection with such applications, to prosecute such applications to issue, and to have any and all registrations issued in the name of Assignee, and the right to sue or bring other actions for past, present and future infringement; Assignor hereby authorizes and requests the United States Commissioner of Patents and Trademarks, and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for said improvements to Assignee in accordance with the terms of this assignment; and Assignor hereby covenants and agrees that Assignor will communicate to Assignee any facts known to Assignor respecting the Assigned Patent Rights, and at the request and expense of Assignee will testify in any legal proceeding, sign all lawful papers, execute all documentation necessary to file, prosecute, or perfect the assignment or ownership of the Assigned Patent Rights, including, without limitation documentation for any inventor's certificates, or divisional, continuing, reissue, design, or model patents or patent applications in any country, and all extensions, renewals, reissues, and reexaminations thereof , make all rightful oaths, and generally do everything possible to aid Assignee at Assignee's request and expense to obtain and enforce proper protection for the Assigned Patent Rights in all countries.

All parties agree to the drawing up of this Deed Of Patent Assignment in another language, including French.

As used in this Agreement, "Assigned Patent Rights" means: (a) the patents and patent applications set forth on Schedule 1 hereto ("Listed Patents"); (b) any and all other patents and patent applications that claim priority from any of the Listed Patents, including, without limitation any and all reexaminations, extensions, reissues, divisionals, renewals, provisionals, substitutions, continuations and continuations-in-part patents or patent applications in all countries; (c) any and all other patents and patent applications that are within the patent family of any of the patents or patent applications set forth under the immediately preceding subsections (a) and (b) above, but excluding any Patent within such patent families that is a Non-Semiconductor Patent unless such Non-Semiconductor Patent is a Listed Patent; and (d) any and all inventions and discoveries in the patents and patent applications set forth under the immediately preceding subsections (a), (b) and (c) above.

As used in this Agreement, "Non-Semiconductor Patents" means any Patents owned, controlled or held in the name of Seller as of the Closing Date that are not principally related to Semiconductors. For clarity, Non-Semiconductor Patents includes U.S. Patent No. 8,088,483.

As used in this Agreement, "Semiconductors" means any semiconductor integrated circuits in any form (including wafer or die form), including memory and logic semiconductor integrated circuits.

IN TESTIMONY WHEREOF, Assignor, by its duly authorized representative, hereby executes this assignment this 24th day of May, 2012.

Assignor: NANOSYS, INC.

Name: JASON HARTLOVE

Title/Position: President & CEO

Date: May 24, 2012

Place: Palo Alto, CA

Witness 1:

Name: John Page

Date: 5/24/12

Place: Palo Alto, CA

State of California
County of Santa Clara

On May 24, 2012 before me, Mary B. Westberg, Notary Public, personally appeared Jason Hartlove and John Page who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that ~~he/she~~/they executed the same in ~~his/her~~/their authorized capacity~~(ies)~~, and that by ~~his/her/~~their signature(s) on the instrument the person~~(s)~~, or the entity upon behalf of which the person~~(s)~~ acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature Mary B Westberg

MARY B. WESTBERG
Commission # 1863869
Notary Public - California
Santa Clara County
My Comm. Expires Sep 5, 2013

IN TESTIMONY WHEREOF, Assignee, by its duly authorized representative, hereby executes this assignment this 24th day of May, 2012.

Assignee: SANDISK CORPORATION

Judy Bruner

Name: Judy Bruner

Title/Position: Executive Vice President, Administration
Chief Financial Officer

Date: May 24, 2012

Place: Milpitas, CA, USA

Witness 2:

Paul L Alpern

Name: Paul L. Alpern

Date: May 24, 2012

Place: Milpitas, CA USA

State of California
County of Santa Clara

On May 24, 2012 before me, Mary B. Westberg, Notary Public, personally appeared Judy Bruner and Paul Alpern who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that ~~he/she/~~they executed the same in ~~his/her/~~their authorized capacity(ies~~)~~, and that by ~~his/her/~~their signature(s) on the instrument the person(s)~~,~~ or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature Mary B. Westberg

MARY B. WESTBERG
Commission # 1863869
Notary Public - California
Santa Clara County
My Comm. Expires Sep 5, 2013

SCHEDULE 1

PATENTS AND APPLICATIONS					
Cntry	**Application No**	**Filing Date**	**Patent No**	**Issue Date**	**Title**
US	10/933827	9/2/2004	7422790	9/9/2008	Methods of Processing Nanocrystals, and Compositions, Devices and Systems Including Same
US	12/185444	8/4/2008	7731932	6/8/2010	Methods of Processing Nanocrystals, and Compositions, Devices and Systems Including Same
EP	5758741.2	3/9/2005			Nano-Enabled Memory Devices and Anisotropic Charge Carrying Arrays
JP	2007-502948	3/9/2005	4871255	11/25/2011	Nano-Enabled Memory Devices and Anisotropic Charge Carrying Arrays
US	11/018572	12/21/2004	7595528	9/29/2009	Nano-Enabled Memory Devices and Anisotropic Charge Carrying Arrays
US	11/695728	4/3/2007	7382017	6/3/2008	Nano-Enabled Memory Devices and Anisotropic Charge Carrying Arrays
AU	2005254490	6/7/2005	2005254490	9/8/2011	Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
CA	2567907	6/7/2005			Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
CN	200580018708.9	6/7/2005			Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
EP	5784268.4	6/7/2005			Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
JP	2007-527681	6/7/2005			Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
KR	10-2007-7000514	6/7/2005			Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
MY	PI20052518	6/7/2005			Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same

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Cntry	**Application No**	**Filing Date**	**Patent No**	**Issue Date**	**Title**
TW	94117823	5/31/2005			Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
US	11/147670	6/7/2005	7267875	9/11/2007	Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
CN	201010503565.3	6/7/2005			Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
US	11/706730	2/13/2007	7585564	9/8/2009	Post deposition encapsulation of nanostructures: compositions, devices and systems incorporating same
US	11/299299	12/9/2005			Compositions and Methods for Modulation of Nanostructure Energy Levels
AU	2005253604	6/7/2005	2005253604	12/22/2011	Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
CA	2567930	6/7/2005			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
CN	200580018709.3	6/7/2005	101076880B	9/15/2010	Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
EP	5786766.5	6/7/2005			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
JP	2007-527682	6/7/2005			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
KR	10-2007-7000502	6/7/2005			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
US	11/148001	6/7/2005	7501315	3/10/2009	Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
CN	201010245267.9	6/7/2005			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
US	12/291101	11/5/2008	8143703	3/27/2012	Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers

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Cntry	**Application No**	**Filing Date**	**Patent No**	**Issue Date**	**Title**
US	11/495188	7/28/2006	7776758	8/17/2010	Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
US	12/803568	6/29/2010			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
CN	200780028495.7	7/27/2007	101512754B	12/28/2011	Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
EP	7797046.5	7/27/2007			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
JP	2009-522811	7/27/2007			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
KR	10-2009-7003931	7/27/2007			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
US	11/881739	7/27/2007	7968273	6/28/2011	Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
US	13/098082	4/29/2011			Methods and Devices for Forming Nanostructure Monolayers and Devices Including Such Monolayers
US	11/641956	12/20/2006			Electron Blocking Layers for Electronic Devices
US	11/688087	3/19/2007			Electron Blocking Layers for Electronic Devices
US	11/743085	5/1/2007			Electron Blocking Layers for Electronic Devices
CN	200780046789.2	12/12/2007			Electron Blocking Layers for Electronic Devices
EP	7252410.1	6/14/2007			Electron Blocking Layers for Electronic Devices
JP	2009-543077	12/12/2007			Electron Blocking Layers for Electronic Devices
KR	10-2009-7012821	12/12/2007			Electron Blocking Layers for Electronic Devices
TW	96149083	12/20/2007	I361494	4/1/2012	Electron Blocking Layers for Electronic Devices
EP	11155070.3	6/14/2007			Electron Blocking Layers for Electronic Devices
US	12/247917	10/8/2008	7847341	12/7/2010	Electron Blocking Layers for Electronic Devices

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Cntry	**Application No**	**Filing Date**	**Patent No**	**Issue Date**	**Title**
CN	200980139673.2	9/24/2009			Electron Blocking Layers for Electronic Devices
EP	9819642.1	9/24/2009			Electron Blocking Layers for Electronic Devices
JP	2011-531058	9/24/2009			Electron Blocking Layers for Electronic Devices
KR	10-2011-7007472	9/24/2009			Electron Blocking Layers for Electronic Devices
TW	98134155	10/8/2009			Electron Blocking Layers for Electronic Devices
US	12/390275	2/20/2009			Electron Blocking Layers for Electronic Devices
US	12/003965	1/3/2008			Methods for Nanopatterning and Production of Nanostructures