

PATENT ASSIGNMENT

Electronic Version v1.1
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SUBMISSION TYPE:	NEW ASSIGNMENT				
NATURE OF CONVEYANCE:	ASSIGNMENT				
CONVEYING PARTY DATA					
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Marvell Semiconductor, Inc.</td> <td>05/31/2012</td> </tr> </tbody> </table>		Name	Execution Date	Marvell Semiconductor, Inc.	05/31/2012
Name	Execution Date				
Marvell Semiconductor, Inc.	05/31/2012				
RECEIVING PARTY DATA					
Name:	Marvell International Ltd.				
Street Address:	Canon's Court, 22 Victoria Street				
City:	Hamilton				
State/Country:	BERMUDA				
Postal Code:	Hm12				
PROPERTY NUMBERS Total: 1					
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>13485635</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	13485635
Property Type	Number				
Application Number:	13485635				
CORRESPONDENCE DATA					
Fax Number:	(408)222-2755				
Phone:	408-222-2500				
Email:	victoria@marvell.com				
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>					
Correspondent Name:	Horace Ng				
Address Line 1:	5488 Marvell Lane				
Address Line 4:	Santa Clara, CALIFORNIA 95054				
ATTORNEY DOCKET NUMBER:	MP4162				
NAME OF SUBMITTER:	HORACE NG				
Total Attachments: 2 source=MP4162_EXECUTED ASSIGNMENT MSI TO MIL#page1.tif source=MP4162_EXECUTED ASSIGNMENT MSI TO MIL#page2.tif					

OP \$40.00 13485635

ASSIGNMENT

Corporate

Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, CA 95054 (hereafter the "Assignor"), is the owner by respective Assignment of United States Patent Application(s) identified below (hereafter sometimes called the "Patent Application(s)").

Marvell International Ltd., a corporation of Bermuda, having a place of business at Canon's Court, 22 Victoria Street, Hamilton, Hm12, Bermuda (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s) and all inventions described and claimed therein or entitled to the benefit thereof.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) US or equivalent and other good and valuable consideration paid to it, Assignor hereby sells and assigns to the Assignee the entire right, title and interest in and to the Patent Application(s) identified below, and all inventions described and claimed therein or entitled to the benefit thereof, in any and all Letters Patent therefor, and in any and all reissues, extensions, renewals, reexaminations, divisions, continuations and foreign counterparts of such applications or Letters Patent to the full end of the term or terms for which such Letters Patent issue, such entire right, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this Assignment and sale not been made.

<u>Marvell Semiconductor, Inc. Case No.</u>	<u>U. S. Serial No.</u>	<u>U. S. Filing Date</u>	<u>Title</u>
MP4162	To Be Assigned	Herewith	METHOD AND APPARATUS FOR SUPPLYING POWER TO A SYSTEM ON A CHIP (SOC)
MP4162PR	61/491,682	May 31, 2011	3D IC TECHNOLOGY FOR POWER DISTRIBUTION

By its undersigned representative, the Assignor agrees

- a. to execute all papers necessary in connection with the Patent Application(s) and any continuing, divisional, reissue, reexamination or corresponding application thereof and also to execute separate Assignment in connection with such application as the Assignee may deem necessary or expedient;
- b. to execute all papers necessary in connection with any interference which may be declared concerning the Patent Application(s) or any continuation, division, reissue or reexamination thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference; and
- c. to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee on any of the Patent Application(s) and on any continuation, division, reissue or reexamination of any of the Patent Application.

The Assignor hereby covenants that it has the full right to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict therewith.

Marvell Semiconductor, Inc., a California corporation, certifies that it is the Assignee of the entire right, title and interest in the Patent Application(s) identified above by virtue of Assignment from the inventors of the Patent Application(s) identified above. Assignment to be recorded in the Patent and Trademark Office as identified below.

<u>Marvell Semiconductor, Inc.</u> <u>Case No.</u>	<u>U. S. Serial No.</u>	<u>Assignment Execution Date</u>
MP4162	To Be Assigned	<u>May 30, 2012</u>
MP4162PR	61/491,682	<u>May 30, 2012</u>

The undersigned has reviewed the documents in the Patent Application(s) identified above and, to the best of undersigned's knowledge and belief, title is in the Assignor identified above.

The undersigned is empowered to sign this certificate on behalf of the Assignor.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the undersigned's name.

Marvell Semiconductor, Inc.

By: _____

Horace Ng

Title: Managing IP Counsel and Director of Patent Procurement

Date: May 31, 2012