

PATENT ASSIGNMENT

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| NATURE OF CONVEYANCE: | ASSIGNMENT |
| CONVEYING PARTY DATA | |
| Name | Execution Date |
| NUMONYX B.V. | 11/22/2011 |
| RECEIVING PARTY DATA | |
| Name: | MICRON TECHNOLOGY, INC. |
| Street Address: | 8000 S. FEDERAL WAY |
| City: | BOISE |
| State/Country: | IDAHO |
| Postal Code: | 83707 |
| PROPERTY NUMBERS Total: 6 | |
| Property Type | Number |
| Patent Number: | 6828512 |
| Patent Number: | 6927346 |
| Patent Number: | 7173817 |
| Patent Number: | 7154175 |
| Patent Number: | 7642660 |
| Patent Number: | 7400041 |
| CORRESPONDENCE DATA | |
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| Address Line 4: | PORTLAND, OREGON 97204 |

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PATENT ASSIGNMENT AGREEMENT

WHEREAS, Numonyx B.V., a company organized under the laws of Netherlands, having a place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte. De l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignor") has rights in and to the following:

1. U.S. Patents and patent applications listed on Exhibit A, and
2. Foreign patents and patent applications listed on Exhibit B.

(said patents and patent applications listed in Exhibits A and B being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Micron Technology, Inc. is a corporation having a principal place of business at 8000 S. Federal Way, Boise, Idaho 83707, United States (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuation-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be executed by its duly authorized officer effective as of the 22 day of November, 2011.

Numonyx B.V.

By: [Signature]
Printed Name: Thomas L. Louis Jr
Title: Director

25
Micron Technology, Inc.

By: [Signature]
Printed Name: Russell Slifer
Title: Chief Patent Counsel

Intel originated

PATENT
REEL: 028362 FRAME: 0805

Exhibit A - US Applications and Issued Patents

| WIPO No. | App No. | App Status | Title | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|------------|--|------------|------------|------------|------------|---------|
| NMXI-0001.00/US | Utility - ORG | Expired | APPARATUS WITH A SINGLE MEMORY AND A PLURALITY OF QUEUE COUNTERS FOR QUEUING REQUESTS AND REPLIES ON A PIPELINED PACKET BUS | 10/14/1988 | 07/257,857 | 9/17/1991 | 5,050,066 | US |
| NMXI-0002.00/US | Utility - ORG | Expired | VOLTAGE MARGINING CIRCUIT FOR FLASH EPROM | 1/12/1988 | 07/144,567 | 10/17/1989 | 4,875,188 | US |
| NMXI-0003.00/US | Utility - ORG | Expired | LEAKAGE VERIFICATION FOR FLASH EPROM | 2/17/1988 | 07/157,364 | 6/20/1989 | 4,841,482 | US |
| NMXI-0004.00/US | Utility - ORG | Expired | A CIRCUIT FOR PROVIDING A LOAD FOR THE CHARGING OF AN EPROM CELL | 1/12/1988 | 07/144,569 | 8/15/1989 | 4,858,186 | US |
| NMXI-0005.00/US | Utility - CON | Expired | PROCESSOR CONTROLLED COMMAND PORT ARCHITECTURE FOR FLASH MEMORY | 10/19/1990 | 07/601,012 | 6/22/1993 | 5,222,046 | US |
| NMXI-0006.00/US | Utility - ORG | Abandoned | PROCESS FOR SIMULTANEOUSLY FABRICATING EPROM CELL AND FLASH EPROM CELL | 11/21/1988 | 07/274,430 | 9/18/1990 | 4,957,877 | US |
| NMXI-0007.00/US | Utility - CON | Issued | MICROCONTROLLER PERIPHERAL EXPANSION BUS FOR ACCESS TO INTERNAL SPECIFIC FUNCTION REGISTERS | 12/1/1992 | 07/984,280 | 5/31/1994 | 5,317,750 | US |
| NMXI-0008.00/US | Utility - ORG | Expired | MICROPROCESSOR BUS INTERFACE UNIT WHICH CHANGES SCHEDULED DATA TRANSFER INDICATIONS UPON SENSING CHANGE IN ENABLE SIGNALS BEFORE RECEIVING READY SIGNAL | 8/1/1988 | 07/227,078 | 12/17/1991 | 5,078,969 | US |
| NMXI-0009.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR CONDUCTING BUS TRANSACTIONS BETWEEN TWO CLOCK INDEPENDENT BUS AGENTS OF A COMPUTER SYSTEM USING A TRANSACTION BY TRANSACTION DETERMINISTIC REQUEST/RESPONSE PROTOCOL | 2/27/1995 | 08/394,560 | 10/3/1995 | 5,455,957 | US |
| NMXI-0010.00/US | Utility - CON | Expired | SELF-BIASED, HIGH-GAIN DIFFERENTIAL AMPLIFIER WITH FEEDBACK CIRCUIT INCLUDING A LATCHING UNIT FOR HOLDING INSTRUCTIONS DURING EXECUTION FOR RE-EXECUTION FOLLOWING EXCEPTION HANDLING | 1/2/1990 | 07/462,879 | 6/26/1990 | 4,937,476 | US |
| NMXI-0011.00/US | Utility - DIV | Issued | PORT EXPANDER ARCHITECTURE FOR MAPPING A FIRST SET OF ADDRESSES TO EXTERNAL MEMORY AND MAPPING A SECOND SET OF ADDRESSES TO AN I/O PORT | 4/14/1993 | 08/047,696 | 5/30/1995 | 5,421,021 | US |
| NMXI-0012.00/US | Utility - CON | Expired | TEST MODE ENABLE SCHEME FOR MEMORY | 6/12/1992 | 07/898,190 | 9/7/1993 | 5,243,700 | US |
| NMXI-0013.00/US | Utility - CON | Expired | CHEMICAL MODIFICATION OF SPIN-ON GLASS FOR IMPROVED PERFORMANCE IN IC FABRICATION | 5/22/1991 | 07/707,241 | 12/31/1991 | 5,077,738 | US |
| NMXI-0014.00/US | Utility - ORG | Expired | METHOD AND APPARATUS FOR AUTOMATICALLY CONFIGURING SYSTEM MEMORY ADDRESS SPACE OF A COMPUTER SYSTEM HAVING A MEMORY SUBSYSTEM WITH INDETERMINISTIC NUMBER OF MEMORY UNITS OF INDETERMINISTIC SIZES DURING SYSTEM RESET | 3/8/1989 | 07/320,763 | 12/5/1989 | 4,885,262 | US |
| NMXI-0015.00/US | Utility - CON | Abandoned | LEAKAGE VERIFICATION FOR FLASH EPROM | 3/29/1995 | 08/442,607 | 4/30/1996 | 5,513,331 | US |
| NMXI-0016.00/US | Utility - DIV | Expired | METHOD OF TRANSFERRING BURST DATA IN A MICROPROCESSOR | 3/14/1989 | 07/323,310 | 8/22/1989 | 4,860,261 | US |
| NMXI-0017.00/US | Utility - ORG | Expired | METHOD OF TRANSFERRING BURST DATA IN A MICROPROCESSOR | 4/5/1989 | 07/333,980 | 7/14/1992 | 5,131,083 | US |
| NMXI-0018.00/US | Utility - ORG | Expired | NOVEL ARCHITECTURE FOR VIRTUAL GROUND HIGH-DENSITY EPROMS | 11/24/1992 | 07/981,943 | 10/19/1993 | 5,255,378 | US |
| NMXI-0019.00/US | Utility - CON | Issued | MICROPROCESSOR PROVIDING ENCODED INFORMATION ON BYTE ENABLE LINES INDICATING WHETHER READING CODE OR DATA, LOCATION OF CODE/DATA ON DATA LINES, AND BIT WIDTH OF CODE/DATA | 8/7/1989 | 07/390,159 | 2/12/1991 | 4,992,980 | US |
| NMXI-0020.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR MINIMIZING DATA TRANSFER TO MAIN MEMORY FROM A WRITE CACHE DURING A CACHE MISS | 10/8/1993 | 08/133,771 | 1/3/1995 | 5,379,443 | US |

PATENT

REEL: 028362 FRAME: 0806

Exhibit A - US Applications and Issued Patents

| Patent No. | Applicant | Inventor | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|----------|---|------------|------------|------------|------------|---------|
| NMXI-0021.00/US | Utility - ORG | Expired | APPARATUS AND METHOD FOR ALIGNMENT OF ADJACENT SURFACES | 8/6/1990 | 07/563,560 | 9/17/1991 | 5,048,195 | US |
| NMXI-0022.00/US | Utility - ORG | Expired | SYSTEM FOR EXECUTING DIFFERENT CYCLE INSTRUCTIONS BY SELECTIVELY BYPASSING SCOREBOARD REGISTER AND CANCELING THE EXECUTION OF CONDITIONALLY ISSUED INSTRUCTION IF NEEDED RESOURCES ARE BUSY | 2/28/1990 | 07/486,407 | 2/9/1993 | 5,185,872 | US |
| NMXI-0023.00/US | Utility - ORG | Expired | ETCHBACK PROCESS FOR TUNGSTEN UTILIZING A NF3/AR CHEMISTRY | 4/17/1991 | 07/686,683 | 11/17/1992 | 5,164,330 | US |
| NMXI-0024.00/US | Utility - ORG | Expired | SIX-WAY ACCESS PORTED RAM ARRAY CELL | 2/28/1990 | 07/486,408 | 6/11/1991 | 5,023,844 | US |
| NMXI-0025.00/US | Utility - ORG | Expired | COMPUTER MEMORY WITH STATUS CELL | 9/26/1990 | 07/589,388 | 8/4/1992 | 5,136,544 | US |
| NMXI-0026.00/US | Utility - ORG | Expired | MEMORY DEVICE WITH A TEST MODE | 9/26/1990 | 07/589,391 | 1/5/1993 | 5,177,745 | US |
| NMXI-0027.00/US | Utility - CON | Issued | APPARATUS FOR ISSUING INSTRUCTIONS AND REISSUING A PREVIOUS INSTRUCTIONS BY RECIRCULATING USING THE DELAY CIRCUIT | 11/12/1993 | 08/150,784 | 4/23/2002 | 6,378,061 | US |
| NMXI-0028.00/US | Utility - ORG | Expired | METHOD FOR IMPROVING DEVICE SCALABILITY OF BURIED BIT LINE FLASH EPROM DEVICES HAVING SHORT REOXIDATION BEAKS AND SHALLOWER JUNCTIONS | 11/2/1990 | 07/609,192 | 4/7/1992 | 5,102,814 | US |
| NMXI-0029.00/US | Utility - CON | Issued | BRANCH LOOK AHEAD ADDER FOR USE IN AN INSTRUCTION PIPELINE SEQUENCER WITH MULTIPLE INSTRUCTION DECODING | 10/26/1993 | 08/141,685 | 9/26/1995 | 5,454,089 | US |
| NMXI-0030.00/US | Utility - ORG | Expired | METHOD OF MODIFYING A MICROINSTRUCTION WITH OPERANDS SPECIFIED BY AN INSTRUCTION HELD IN AN ALIAS REGISTER | 12/20/1990 | 07/630,497 | 6/22/1993 | 5,222,244 | US |
| NMXI-0031.00/US | Utility - CON | Expired | HIGH C4-FLOW-REACTIVE ION ETCH FOR ALUMINUM PATTERNING | 5/8/1992 | 07/888,067 | 4/13/1993 | 5,202,291 | US |
| NMXI-0032.00/US | Utility - ORG | Expired | HIGH BANDWIDTH OUTPUT HIERARCHICAL MEMORY STORE INCLUDING A CACHE, FETCH BUFFER AND ROM | 12/20/1990 | 07/630,534 | 5/17/1994 | 5,313,685 | US |
| NMXI-0033.00/US | Utility - ORG | Expired | HANDLE AND HANDLED IMPLEMENT | 12/27/1990 | 07/635,868 | 12/29/1992 | 5,175,126 | US |
| NMXI-0034.00/US | Utility - ORG | Expired | RADIATION SHIELD FOR EPROM CELLS | 11/13/1990 | 07/612,452 | 9/17/1991 | 5,030,123 | US |
| NMXI-0035.00/US | Utility - CON | Issued | HIGH VOLTAGE MOS TRANSISTOR FOR FLASH EEPROM APPLICATIONS HAVING A UNI-SIDED LIGHTLY DOPED DRAIN | 7/2/1993 | 08/087,140 | 10/3/2000 | 6,127,696 | US |
| NMXI-0036.00/US | Utility - ORG | Expired | TEOS INTERMETAL DIELECTRIC PRECLEAN FOR VIA FORMATION | 6/7/1991 | 07/712,119 | 6/15/1993 | 5,219,791 | US |
| NMXI-0037.00/US | Utility - ORG | Expired | ANNEAL TO DECREASE MOISTURE ABSORBANCE OF INTERMETAL DIELECTRICS | 6/7/1991 | 07/712,116 | 8/18/1992 | 5,139,971 | US |
| NMXI-0038.00/US | Utility - CON | Issued | SINGLE STEP SALICIDATION PROCESS | 9/28/1992 | 07/951,945 | 2/15/1994 | 5,286,678 | US |
| NMXI-0039.00/US | Utility - ORG | Issued | MICROPROCESSOR PLL CLOCK CIRCUIT WITH SELECTABLE DELAYED FEEDBACK | 5/29/1992 | 07/890,997 | 8/29/1995 | 5,446,867 | US |
| NMXI-0040.00/US | Utility - CON | Issued | NOVEL LITHOGRAPHIC TECHNIQUE FOR PATTERNING A SEMICONDUCTOR DEVICE | 4/19/1993 | 08/046,477 | 4/2/1996 | 5,503,959 | US |
| NMXI-0041.00/US | Utility - CON | Issued | CIRCUITRY AND METHOD FOR PROGRAMMING AND ERASING A NONVOLATILE SEMICONDUCTOR MEMORY | 2/24/1994 | 06/201,044 | 9/5/1995 | 5,448,712 | US |
| NMXI-0042.00/US | Utility - CON | Issued | COMMAND INTERFACE BETWEEN USER COMMANDS AND A MEMORY DEVICE | 1/21/1994 | 08/195,449 | 10/31/1995 | 5,463,757 | US |
| NMXI-0043.00/US | Utility - CON | Issued | ARCHITECTURE FOR READING INFORMATION FROM A MEMORY ARRAY | 5/9/1994 | 08/239,939 | 7/4/1995 | 5,490,677 | US |
| NMXI-0044.00/US | Utility - ORG | Expired | APPARATUS AND METHOD FOR ACHIEVING VIA STEP COVERAGE SYMMETRY | 12/27/1990 | 07/635,662 | 6/30/1992 | 5,126,029 | US |
| NMXI-0045.00/US | Utility - ORG | Expired | CIRCUITRY AND METHOD FOR DISCHARGING A DRAIN OF A CELL OF A NON-VOLATILE SEMICONDUCTOR MEMORY | 5/10/1991 | 07/698,547 | 11/23/1993 | 5,365,059 | US |

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Exhibit A - US Applications and Issued Patents

| NMXI-ExAppNo | MatterType | MaxStat | Title | FileDate | SerriNo | IssueDate | PatentNo | Country |
|-----------------|-----------------|-----------|--|------------|------------|------------|-----------|---------|
| NMXI-0045.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR CONDUCTING BUS TRANSACTIONS BETWEEN TWO CLOCK INDEPENDENT BUS AGENTS OF A COMPUTER SYSTEM USING A TRANSACTION BY TRANSACTION DETERMINISTIC REQUEST/RESPONSE PROTOCOL AND BURST TRANSFER | 5/26/1995 | 08/429,658 | 11/28/1995 | 5,471,637 | US |
| NMXI-0047.00/US | Utility - CON | Issued | HIGH-RESOLUTION SYNCHRONOUS DELAY LINE | 3/24/1993 | 08/036,219 | 11/15/1994 | 5,365,128 | US |
| NMXI-0048.00/US | Utility - ORG | Issued | METHOD OF REPAIRING OVERERASED CELLS IN A FLASH MEMORY | 10/9/1991 | 07/773,228 | 8/17/1993 | 5,237,535 | US |
| NMXI-0049.00/US | Utility - DIV | Expired | A METHOD FOR PROGRAMMING A VIRTUAL GROUND EPROM CELL INCLUDING SLOW RAMPING OF THE COLUMN LINE VOLTAGE | 2/20/1991 | 07/658,184 | 7/7/1992 | 5,128,895 | US |
| NMXI-0050.00/US | Utility - ORG | Expired | COMPOSITE POLISHING PAD FOR SEMICONDUCTOR PROCESS | 7/9/1991 | 07/727,829 | 5/25/1993 | 5,212,910 | US |
| NMXI-0051.00/US | Utility - CON | Issued | METHOD FOR TESTING ERASE CHARACTERISTICS OF A FLASH MEMORY ARRAY | 7/19/1994 | 08/277,369 | 5/2/1995 | 5,412,793 | US |
| NMXI-0052.00/US | Utility - CON | Abandoned | ADVANCE MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY | 8/18/1993 | 08/108,766 | | | US |
| NMXI-0052.00/US | Utility - NSPCT | Issued | ADVANCE MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY | 6/14/1994 | 08/140,070 | 9/17/1996 | 5,556,807 | US |
| NMXI-0052.02/US | Utility - CON | Issued | ADVANCE MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY | 2/6/1995 | 08/384,629 | 1/30/1996 | 5,488,257 | US |
| NMXI-0053.00/US | Utility - ORG | Expired | ASYMMETRIC FLOATING GATE OVERLAP FOR IMPROVED DEVICE CHARACTERISTICS IN BURIED BIT-LINE DEVICES | 8/14/1991 | 07/744,969 | 2/22/1994 | 5,289,026 | US |
| NMXI-0053.01/US | Utility - CON | Issued | A METHOD FOR FORMING AN ASYMMETRIC FLOATING GATE OVERLAP FOR IMPROVED DEVICE PERFORMANCE IN BURIED BIT-LINE DEVICES | 11/10/1994 | 08/337,131 | 9/26/2000 | 6,124,168 | US |
| NMXI-0054.00/RE | Utility - REIS | Issued | PROCESS FOR FORMING A THIN OXIDE LAYER | 9/14/1995 | 08/528,188 | 9/14/1993 | RE38,674 | US |
| NMXI-0054.00/US | Utility - ORG | Issued | NOVEL PROCESS FOR FORMING A THIN OXIDE LAYER | 12/17/1991 | 07/809,971 | 9/14/1993 | 5,244,843 | US |
| NMXI-0055.00/US | Utility - CIP | Expired | CHARGE DOMAIN DIFFERENTIAL CONDUCTANCE SYNAPSE CELL FOR NEURAL NETWORKS | 1/16/1992 | 07/821,634 | 10/13/1992 | 5,155,377 | US |
| NMXI-0056.00/US | Utility - ORG | Issued | BUFFER CIRCUITRY FOR TRANSFERRING SIGNALS FROM TTL CIRCUITRY TO DUAL RANGE CMOS CIRCUITRY | 12/23/1991 | 07/813,148 | 3/29/1994 | 5,298,807 | US |
| NMXI-0057.00/US | Utility - CIP | Issued | PROCESS FOR FILLING SUBMICRON SPACES WITH DIELECTRIC | 7/21/1992 | 07/917,465 | 12/14/1993 | 5,270,264 | US |
| NMXI-0058.00/US | Utility - ORG | Expired | MULTI-QUADRANT CHARGE DOMAIN SYNAPSE CELL | 8/20/1991 | 07/747,642 | 8/4/1992 | 5,136,177 | US |
| NMXI-0059.00/US | Utility - CON | Issued | LITHOGRAPHY USING A NEW PHASE-SHIFTING RETICLE | 3/1/1995 | 08/396,926 | 5/27/1997 | 5,653,102 | US |
| NMXI-0060.00/US | Utility - ORG | Issued | CLOCK SWITCHING APPARATUS AND METHOD FOR COMPUTER SYSTEMS | 12/30/1991 | 07/815,819 | 12/28/1993 | 5,274,678 | US |
| NMXI-0061.00/US | Utility - ORG | Expired | HIGH SPEED NORING INVERTING, MUX'ING AND LATCHING CIRCUIT WITH TEMPERATURE COMPENSATED OUTPUT NOISE CONTROL | 8/30/1991 | 07/752,780 | 12/1/1992 | 5,168,178 | US |
| NMXI-0062.00/US | Utility - ORG | Expired | HIGH THROUGHPUT INTERLEVEL DIELECTRIC GAP FILLING PROCESS | 6/28/1991 | 07/722,861 | 1/18/1994 | 5,279,865 | US |
| NMXI-0063.00/US | Utility - CIP | Expired | HIGH SPEED, LOW POWER OUTPUT CIRCUIT WITH TEMPERATURE COMPENSATED NOISE CONTROL | 5/12/1992 | 07/881,843 | 3/1/1994 | 5,291,071 | US |
| NMXI-0064.00/US | Utility - CON | Issued | SURFACE SWEEPING METHOD FOR SURFACE MOVEMENT IN THREE DIMENSIONAL TOPOGRAPHY SIMULATION | 11/5/1993 | 08/148,181 | 12/17/1996 | 5,586,230 | US |
| NMXI-0065.00/US | Utility - ORG | Issued | PARTICLE FLUX SHADOWING FOR THREE-DIMENSIONAL TOPOGRAPHY SIMULATION | 6/24/1992 | 07/904,001 | 1/25/1994 | 5,282,140 | US |
| NMXI-0066.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR TESTING THE CONTINUITY OF STATIC RANDOM ACCESS MEMORY CELLS | 12/31/1991 | 07/816,635 | 10/19/1993 | 5,255,230 | US |

Exhibit A - US Applications and Issued Patents

| App No | App Date | App Title | App Status | App Type | App Inventor | App Date | App No | App Date | App Inventor | App No | App Date | App Inventor | App No | App Date | App Inventor |
|-----------------|------------|--|------------|---------------|--------------|------------|------------|------------|--------------|-----------|------------|--------------|--------|----------|--------------|
| NMXI-0067.00/US | 2/26/1993 | DELAY LINE LOOP FOR ON-CHIP CLOCK SYNTHESIS WITH ZERO SKEW AND 50% DUTY CYCLE | Issued | Utility - ORG | | 2/26/1993 | 08/023,673 | 4/25/1995 | | 5,410,263 | 4/25/1995 | | | | US |
| NMXI-0058.00/US | 6/10/1992 | MULTI-STAGED CHARGE-PUMP WITH STAGGERED CLOCK PHASES FOR PROVIDING HIGH CURRENT CAPABILITY | Issued | Utility - ORG | | 6/10/1992 | 07/896,195 | 4/5/1994 | | 5,301,097 | 4/5/1994 | | | | US |
| NMXI-0069.00/US | 3/9/1994 | RANDOM ACCESS MEMORY WITH DIGITAL SIGNALS RUNNING OVER THE SMALL SIGNAL REGION OF THE ARRAY | Issued | Utility - CON | | 3/9/1994 | 08/208,800 | 10/10/1995 | | 5,457,648 | 10/10/1995 | | | | US |
| NMXI-0070.00/US | 4/29/1992 | STATIC PLA WITH STATE MACHINE CONTROLLED BY MODEL OF BOTH MINTERM AND AN OUTPUT LINE | Issued | Utility - ORG | | 4/29/1992 | 07/875,661 | 12/28/1993 | | 5,274,281 | 12/28/1993 | | | | US |
| NMXI-0071.00/US | 7/31/1992 | DETECTING VALID DATA FROM A TWISTED PAIR MEDIUM | Issued | Utility - ORG | | 7/31/1992 | 07/923,853 | 12/13/1994 | | 5,373,508 | 12/13/1994 | | | | US |
| NMXI-0072.00/US | 3/31/1992 | BIAS CIRCUITRY FOR CONTENT ADDRESSABLE MEMORY CELLS OF A FLOATING GATE NONVOLATILE MEMORY | Issued | Utility - ORG | | 3/31/1992 | 07/861,093 | 11/30/1993 | | 5,267,213 | 11/30/1993 | | | | US |
| NMXI-0073.00/US | 12/22/1994 | APPARATUS AND METHOD FOR MINIMIZING VERIFY TIME IN A SEMICONDUCTOR MEMORY BY CONSTANTLY CHARGING N-WELL CAPACITANCE | Issued | Utility - CON | | 12/22/1994 | 08/361,872 | 4/9/1996 | | 5,506,803 | 4/9/1996 | | | | US |
| NMXI-0074.00/US | 11/3/1992 | PROTECTED ERASE VOLTAGE DISCHARGE TRANSISTOR IN A NONVOLATILE SEMICONDUCTOR MEMORY | Issued | Utility - ORG | | 11/3/1992 | 07/971,074 | 5/3/1994 | | 5,309,612 | 5/3/1994 | | | | US |
| NMXI-0075.00/US | 6/5/1992 | POLYIMIDE PROCESS FOR PROTECTING INTEGRATED CIRCUITS | Issued | Utility - ORG | | 6/5/1992 | 07/893,765 | 9/7/1993 | | 5,242,864 | 9/7/1993 | | | | US |
| NMXI-0076.00/US | 9/8/1992 | METHOD FOR THE ANISOTROPIC ETCHING OF METAL FILMS IN THE FABRICATION OF INTERCONNECTS | Issued | Utility - ORG | | 9/8/1992 | 07/941,412 | 9/27/1994 | | 5,350,484 | 9/27/1994 | | | | US |
| NMXI-0077.00/US | 10/30/1992 | METHOD OF MANAGING DEFECTS IN FLASH DISK MEMORIES | Issued | Utility - ORG | | 10/30/1992 | 07/969,749 | 12/5/1995 | | 5,473,753 | 12/5/1995 | | | | US |
| NMXI-0077.00/US | 8/12/1996 | METHOD OF MANAGING DEFECTS IN FLASH DISK MEMORIES | Issued | Utility - DIV | | 8/12/1996 | 08/700,676 | 1/11/2000 | | 6,014,755 | 1/11/2000 | | | | US |
| NMXI-0078.00/US | 10/30/1992 | METHOD OF CONTROLLING CLEAN-UP OF A SOLID STATE MEMORY DISK STORING FLOATING SECTOR DATA | Issued | Utility - ORG | | 10/30/1992 | 07/969,763 | 12/26/1995 | | 5,479,633 | 12/26/1995 | | | | US |
| NMXI-0079.00/US | 2/19/1993 | POWER MANAGEMENT SYSTEM FOR COMPONENTS USED IN BATTERY POWERED APPLICATIONS | Issued | Utility - ORG | | 2/19/1993 | 08/019,617 | 2/11/1997 | | 5,603,036 | 2/11/1997 | | | | US |
| NMXI-0080.00/US | 6/12/1992 | PROCESS FOR ETCHING SILICON DIOXIDE LAYER WITHOUT MICRO MASKING EFFECT | Issued | Utility - ORG | | 6/12/1992 | 07/897,768 | 3/22/1994 | | 5,296,094 | 3/22/1994 | | | | US |
| NMXI-0081.00/US | 2/14/1995 | A METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING ACCESS TIME TO A RESOURCE SHARED BETWEEN A PERIPHERAL BUS AND A HOST BUS BY DYNAMICALLY CONTROLLING THE SIZE OF BURST DATA TRANSFERS ON THE PERIPHERAL BUS | Issued | Utility - CON | | 2/14/1995 | 08/389,078 | 11/26/1996 | | 5,579,530 | 11/26/1996 | | | | US |
| NMXI-0082.00/US | 9/30/1993 | GENERALLY-DIAGONAL MAPPING OF ADDRESS SPACE FOR ROW/COLUMN ORGANIZER MEMORIES | Issued | Utility - ORG | | 9/30/1993 | 08/130,023 | 2/6/1996 | | 5,490,264 | 2/6/1996 | | | | US |
| NMXI-0083.00/US | 1/30/1992 | APPARATUS UTILIZING DUAL COMPARE LOGIC FOR SELF CHECKING OF FUNCTIONAL REDUNDANCY CHECK (FRC) LOGIC | Issued | Utility - ORG | | 1/30/1992 | 07/830,209 | 1/4/1994 | | 5,276,690 | 1/4/1994 | | | | US |
| NMXI-0084.00/US | 4/14/1992 | METHOD FOR FORMING A TITANIUM NITRIDE BARRIER LAYER | Expired | Utility - DIV | | 4/14/1992 | 07/869,227 | 8/3/1993 | | 5,233,871 | 8/3/1993 | | | | US |
| NMXI-0085.00/US | 9/21/1992 | PROGRAMMABLE SECONDARY CLOCK GENERATOR | Issued | Utility - ORG | | 9/21/1992 | 07/948,215 | 10/26/1993 | | 5,256,994 | 10/26/1993 | | | | US |
| NMXI-0086.00/US | 6/12/1992 | PROGRAMMABLE DEDICATED TIMER OPERATED ON A CLOCK INDEPENDENT OF PROCESSOR TIMER | Issued | Utility - ORG | | 6/12/1992 | 07/897,897 | 7/25/1995 | | 5,437,021 | 7/25/1995 | | | | US |
| NMXI-0087.00/US | 6/19/1992 | GATE/SOURCE DISTURB PROTECTION FOR SIXTEEN-BIT FLASH EEPROM MEMORY ARRAYS | Issued | Utility - ORG | | 6/19/1992 | 07/901,110 | 5/31/1994 | | 5,317,535 | 5/31/1994 | | | | US |
| NMXI-0088.00/US | 6/19/1992 | HIGH-SPEED BIAS-STABILIZED CURRENT-MIRROR REFERENCING CIRCUIT FOR NON-VOLATILE MEMORIES | Issued | Utility - ORG | | 6/19/1992 | 07/901,395 | 2/22/1994 | | 5,289,412 | 2/22/1994 | | | | US |
| NMXI-0089.00/US | 10/30/1992 | METHOD AND APPARATUS FOR IMPROVING DATA FAILURE RATE TESTING FOR MEMORY ARRAYS | Issued | Utility - ORG | | 10/30/1992 | 07/969,465 | 5/16/1995 | | 5,416,782 | 5/16/1995 | | | | US |
| NMXI-0090.00/US | 10/30/1993 | METHOD FOR WRITING TO A FLASH MEMORY ARRAY DURING ERASE SUSPEND INTERVALS | Issued | Utility - CON | | 10/30/1993 | 08/145,732 | 8/23/1994 | | 5,341,330 | 8/23/1994 | | | | US |

PATENT

REEL: 028362 FRAME: 0809

Exhibit A - US Applications and Issued Patents

| MXI-App No. | Matter Type | Matter Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NMXI-0091.00/US | Utility - ORG | Issued | NONVOLATILE MEMORY WITH A PROGRAMMABLE OUTPUT OF SELECTABLE WIDTH AN A METHOD FOR CONTROLLING THE NONVOLATILE MEMORY TO SWITCH BETWEEN DIFFERENT OUT PUT WIDTHS | 3/17/1993 | 08/032,686 | 4/2/1996 | 5,504,875 | US |
| NMXI-0092.00/US | Utility - ORG | Issued | POLISHING PAD CONDITIONING APPARATUS FOR WAFER PLANARIZATION PROCESS | 9/24/1992 | 07/950,812 | 6/8/1993 | 5,216,843 | US |
| NMXI-0093.00/US | Utility - ORG | Issued | ELECTRICALLY CONDUCTIVE INTERPOSER AND ARRAY PACKAGE CONCEPT FOR INTERCONNECTING TO A CIRCUIT BOARD | 12/2/1992 | 07/984,335 | 6/14/1994 | 5,321,563 | US |
| NMXI-0094.00/US | Utility - CON | Issued | OPTIMIZED LOOKUP TABLE METHOD FOR CONVERTING YUV PIXEL VALUES TO RGB PIXEL VALUES | 4/17/1995 | 08/425,231 | 5/7/2002 | 6,384,838 | US |
| NMXI-0095.00/US | Utility - ORG | Issued | FAST PARITY GENERATOR USING COMPLEMENT PASS-TRANSISTOR LOGIC | 11/25/1993 | 08/156,427 | 3/4/1997 | 5,608,741 | US |
| NMXI-0096.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR UPDATING FLASH MEMORY RESIDENT FIRMWARE THROUGH A STANDARD DISK DRIVE INTERFACE | 11/9/1998 | 09/189,000 | 12/28/1999 | 6,009,497 | US |
| NMXI-0096.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR UPDATING FLASH MEMORY RESIDENT FIRMWARE THROUGH A STANDARD DISK DRIVE INTERFACE | 11/9/1998 | 09/189,000 | 12/28/1999 | 6,009,497 | US |
| NMXI-0097.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR DATA BUFFERING AND QUEUE MANAGEMENT OF DIGITAL MOTION VIDEO SIGNALS | 12/16/1994 | 08/357,544 | 9/12/1995 | 5,450,544 | US |
| NMXI-0098.00/US | Utility - ORG | Issued | SYSTEM FOR AUTOMATICALLY COMPRESSING AND DECOMPRESSING DATA FOR SENDER AND RECEIVER PROCESSES UPON DETERMINATION OF A COMMON COMPRESSION/ DECOMPRESSION METHOD UNDERSTOOD BY BOTH SENDER AND RECEIVER PROCESSES | 10/15/1992 | 07/961,475 | 9/17/1996 | 5,557,749 | US |
| NMXI-0099.00/US | Utility - CON | Issued | METHOD FOR ALLOCATING MEMORY IN A SOLID STATE MEMORY DISK | 6/7/1995 | 08/485,661 | 7/9/1996 | 5,535,369 | US |
| NMXI-0100.00/US | Utility - ORG | Issued | HIGH SPEED OR CIRCUIT CONFIGURATION | 9/1/1992 | 07/938,934 | 12/28/1993 | 5,274,777 | US |
| NMXI-0101.00/US | Utility - CON | Issued | MULTIPLE LAYER PROGRAMMABLE LAYOUT FOR VERSION IDENTIFICATION | 8/3/1994 | 08/285,495 | 10/17/1995 | 5,459,355 | US |
| NMXI-0102.00/US | Utility - ORG | Issued | IMPROVED SYSTEM OF RESOLVING CONFLICTING DATA PROCESSING MEMORY REQUESTS | 9/30/1992 | 07/954,743 | 8/16/1994 | 5,339,442 | US |
| NMXI-0103.00/US | Utility - ORG | Issued | METHOD FOR LOCATING SECTOR DATA IN A MEMORY DISK BY EXAMINING A PLURALITY OF HEADERS NEAR AN INITIAL POINTER | 10/30/1992 | 07/969,780 | 11/28/1995 | 5,471,604 | US |
| NMXI-0104.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROVIDING A CONTEXT SWITCH IN RESPONSE TO AN INTERRUPT IN A COMPUTER PROCESS | 6/30/1993 | 08/085,409 | 7/9/1996 | 5,535,397 | US |
| NMXI-0105.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR EXECUTION OF OPERATIONS IN A FLASH MEMORY ARRAY | 6/30/1993 | 08/086,186 | 4/16/1996 | 5,509,134 | US |
| NMXI-0106.00/US | Utility - ORG | Issued | CRITICAL PATH PREDICTION FOR DESIGN OF CIRCUITS | 12/29/1993 | 08/174,729 | 8/6/1996 | 5,544,071 | US |
| NMXI-0107.00/US | Utility - ORG | Issued | INSTRUCTION BUFFER FOR ALIGNING INSTRUCTION SETS USING BOUNDARY DETECTION | 6/30/1993 | 08/085,637 | 10/31/1995 | 5,463,748 | US |
| NMXI-0107.01/US | Utility - CON | Issued | BRANCH TARGET BUFFER WITH ADDRESS TAGS APPENDED TO STORED INSTRUCTIONS FOR FACILITATING BUFFER FLUSHING | 1/23/1997 | 08/787,983 | 6/26/2001 | 6,250,821 | US |
| NMXI-0107.02/US | Utility - CON | Issued | APPARATUS FOR CONCURRENT ISSUE OF INSTRUCTIONS IN PARALLEL FROM FIRST AND SECOND STORAGE AREA | 8/15/1997 | 08/912,048 | 6/12/2001 | 6,247,120 | US |

PATENT

REEL: 028362 FRAME: 0810

Exhibit A - US Applications and Issued Patents

| NY Doc No. | Matter Type | Market Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|------------|---------|
| NMXI-0108.00/US | Utility - CON | Issued | NONVOLATILE MEMORY WITH BLOCKS AND CIRCUITRY FOR SELECTIVELY PROTECTING THE BLOCKS FROM MEMORY OPERATIONS | 12/10/1994 | 08/358,978 | 4/30/1996 | 5,513,136 | US |
| NMXI-0109.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROVIDING PRECISE FAULT TRACING IN A SUPERSCALAR MICROPROCESSOR | 6/30/1993 | 08/085,508 | 5/12/1998 | 5,752,013 | US |
| NMXI-0110.00/US | Utility - ORG | Issued | NONVOLATILE MEMORY CARD WITH AN ADDRESS TABLE AND AN ADDRESS TRANSLATION LOGIC FOR MAPPING OUT DEFECTIVE BLOCKS WITHIN THE MEMORY CARD | 6/30/1993 | 08/086,043 | 6/4/1996 | 5,524,231 | US |
| NMXI-0111.00/US | Utility - CON | Issued | APPARATUS AND A METHOD FOR IMPROVING THE PROGRAM AND ERASE PERFORMANCE OF A FLASH EEPROM MEMORY ARRAY | 10/27/1994 | 08/329,773 | 10/3/1995 | 5,455,800 | US |
| NMXI-0112.00/US | Utility - ORG | Issued | METHOD AND CIRCUITRY FOR ENABLING AND PERMANENTLY DISABLING TEST MODE ACCESS IN A FLASH MEMORY DEVICE | 12/30/1993 | 08/175,599 | 6/11/1996 | 5,526,311 | US |
| NMXI-0113.00/US | Utility - ORG | Issued | PARALLEL MULTISTAGE SYNCHRONIZATION METHOD AND APPARATUS | 12/21/1993 | 08/171,554 | 1/30/1996 | 5,488,639 | US |
| NMXI-0114.00/US | Utility - ORG | Issued | A ROUND ROBIN SCHEDULER USING A SCHEDULER CARRY OPERATION FOR ARBITRATION | 12/30/1992 | 07/998,555 | 11/22/1994 | 5,367,679 | US |
| NMXI-0115.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR ANALYZING DIGITAL VIDEO IMAGES BY MERGING DISPLACEMENT VECTORS | 12/31/1992 | 07/999,148 | | | US |
| NMXI-0115.01/US | Utility - CON | Issued | METHOD AND APPARATUS FOR ANALYZING DIGITAL VIDEO IMAGES BY MERGING DISPLACEMENT VECTORS | 6/10/1994 | 08/258,613 | 10/22/1996 | 5,568,569 | US |
| NMXI-0116.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR INITIATING COMMUNICATION VIA PAGING A MOBILE COMPUTING DEVICE | 2/9/1995 | 08/599,032 | 12/3/1996 | 5,581,594 | US |
| NMXI-0117.00/US | Utility - DIV | Issued | THIN, HIGH LEAD COUNT PACKAGE | 2/1/1993 | 08/011,944 | 3/1/1994 | 5,290,735 | US |
| NMXI-0118.00/US | Utility - ORG | Issued | NOVEL SLURRIES FOR CHEMICAL MECHANICAL POLISHING | 11/3/1993 | 08/146,923 | 8/23/1994 | 5,340,370 | US |
| NMXI-0119.00/US | Utility - ORG | Issued | SPECIAL TEST MODES FOR A PAGE BUFFER SHARED RESOURCE IN A MEMORY DEVICE | 6/30/1993 | 08/085,542 | 4/22/1997 | 5,623,620 | US |
| NMXI-0119.01/US | Utility - CON | Issued | SPECIAL TEST MODES FOR A PAGE BUFFER SHARED RESOURCE IN A MEMORY DEVICE | 9/25/1996 | 08/719,583 | 11/10/1998 | 5,835,927 | US |
| NMXI-0120.00/US | Utility - CON | Issued | SENSING SCHEME FOR FLASH MEMORY WITH MULTILEVEL CELLS | 2/19/1997 | 08/801,004 | 10/27/1998 | 5,828,616 | US |
| NMXI-0120.01/US | Utility - CON | Issued | SENSING SCHEME FOR FLASH MEMORY WITH MULTILEVEL CELLS | 4/10/1997 | 08/827,670 | 5/5/1998 | 5,748,546 | US |
| NMXI-0121.00/US | Utility - ORG | Issued | WRITE VERIFY SCHEMES FOR FLASH MEMORY WITH MULTILEVEL CELLS | 6/2/1994 | 08/252,747 | 7/23/1996 | 5,539,690 | US |
| NMXI-0122.00/US | Utility - ORG | Issued | ADDRESSING MODES FOR A DYNAMIC SINGLE BIT PER CELL TO MULTIPLE BIT PER CELL MEMORY | 6/2/1994 | 08/252,920 | 5/7/1996 | 5,515,317 | US |
| NMXI-0123.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROGRAMMING AND ERASING FLASH EEPROM MEMORY ARRAYS UTILIZING A CHARGE PUMP CIRCUIT | 9/10/1993 | 08/119,719 | 5/9/1995 | 5,414,669 | US |
| NMXI-0124.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT VOLTAGE PROVIDED BY A CHARGE PUMP CIRCUIT | 6/19/1996 | 08/491,792 | 9/3/1996 | 5,553,030 | US |
| NMXI-0125.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR A TWO PHASE BOOTSTRAP CHARGE PUMP SYSTEM HAVING MULTIPLE PHASE BOOSTED CHARGE PUMP WITH A PLURALITY OF STAGES | 9/10/1993 | 08/119,423 | 6/6/1995 | 5,422,586 | US |
| NMXI-0125.01/US | Utility - DIV | Issued | METHOD AND APPARATUS FOR A TWO PHASE BOOTSTRAP CHARGE PUMP WITH A PLURALITY OF STAGES | 5/20/1994 | 08/246,779 | 6/4/1996 | 5,524,266 | US |
| NMXI-0125.02/US | Utility - DIV | Issued | METHOD AND APPARATUS FOR A TWO PHASE BOOTSTRAP CHARGE PUMP | 5/24/1994 | 08/248,419 | 7/11/1995 | 5,432,469 | US |
| NMXI-0126.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT CURRENT PROVIDED BY A CHARGE PUMP CIRCUIT | 9/10/1993 | 08/119,425 | 8/15/1995 | 5,442,586 | US |

PATENT

REEL: 028362 FRAME: 0811

Exhibit A - US Applications and Issued Patents

| NIM Docket No. | Matter Type | Market Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|------------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NIMXI-0126.00/US | Utility - DIV | Issued | METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT CURRENT PROVIDED BY A CHARGE PUMP CIRCUIT | 3/14/1995 | 08/403,633 | 10/3/1995 | 5,455,794 | US |
| NIMXI-0127.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR SEQUENTIAL PROGRAMMING OF THE BITS IN A WORD OF A FLASH EPROM MEMORY ARRAY | 9/10/1993 | 08/119,520 | 7/16/1996 | 5,537,350 | US |
| NIMXI-0128.00/US | Utility - ORG | Issued | TRENCH ISOLATION WITH PLANAR TOPOGRAPHY AND METHOD OF FABRICATION | 5/29/1993 | 08/084,795 | 10/23/1994 | 5,358,891 | US |
| NIMXI-0129.00/US | Utility - CON | Issued | ZERO STANDBY CURRENT POWER-UP RESET CIRCUIT | 1/24/1996 | 08/590,726 | 10/29/1996 | 5,570,050 | US |
| NIMXI-0130.00/US | Utility - CIP | Issued | METHOD AND APPARATUS FOR SEQUENTIAL PROGRAMMING OF A FLASH EPROM MEMORY ARRAY | 5/2/1994 | 08/753,850 | 7/4/1995 | 5,430,674 | US |
| NIMXI-0131.00/US | Utility - DIV | Issued | METHOD OF OPTIMIZING OPERATING PARAMETERS OF AN INTEGRATED CIRCUIT PACKAGE HAVING A VOLTAGE REGULATOR MOUNTED THEREON | 6/6/1995 | 08/468,972 | 9/17/1996 | 5,556,811 | US |
| NIMXI-0131.01/US | Utility - CON | Issued | APPARATUS FOR OPTIMIZING OPERATING PARAMETERS OF AN INTEGRATED CIRCUIT PACKAGE HAVING A VOLTAGE REGULATOR MOUNTED THEREON | 10/31/1996 | 08/741,526 | 4/15/1997 | 5,621,245 | US |
| NIMXI-0132.00/US | Utility - CON | Issued | METHOD AND CIRCUITRY FOR STORING DISCRETE AMOUNTS OF CHARGE IN A SINGLE MEMORY ELEMENT | 8/13/1997 | 08/910,866 | 4/6/1999 | 5,892,710 | US |
| NIMXI-0133.00/US | Utility - ORG | Issued | METHOD OF PIPELINING SEQUENTIAL WRITES IN A FLASH MEMORY | 5/30/1993 | 08/085,969 | 5/21/1996 | 5,519,847 | US |
| NIMXI-0134.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR PROGRAMMING EMBEDDED MEMORIES OF A VARIETY OF INTEGRATED CIRCUITS USING THE IEEE TEST ACCESS PORT | 12/19/1995 | 08/575,178 | 1/21/1997 | 5,596,734 | US |
| NIMXI-0135.00/US | Utility - ORG | Issued | CMOS VOLTAGE REFERENCE CIRCUIT | 11/29/1993 | 08/158,418 | 7/18/1995 | 5,434,534 | US |
| NIMXI-0136.00/US | Utility - ORG | Issued | CPU CLOCK CONTROL UNIT | 12/29/1993 | 08/176,944 | 8/13/1996 | 5,546,568 | US |
| NIMXI-0137.00/US | Utility - ORG | Issued | BINOMOS DRIVER CIRCUIT WITH INTEGRATED VOLTAGE SUPPLY CONVERSION | 5/16/1994 | 08/242,790 | 7/25/1995 | 5,436,585 | US |
| NIMXI-0138.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR REDIRECTING REGISTER ACCESS REQUESTS WHEREIN THE REGISTER SET IS SEPARATE FROM A CENTRAL PROCESSING UNIT | 9/19/1996 | 08/710,572 | 9/9/1997 | 5,666,556 | US |
| NIMXI-0139.00/US | Utility - ORG | Issued | ARCHITECTURE AND METHOD FOR PROVIDING GUARANTEED ACCESS FOR A RETRYING BUS MASTER TO A DATA TRANSFER BRIDGE CONNECTING TWO BUSES IN A COMPUTER SYSTEM | 2/24/1994 | 08/281,817 | 2/15/2000 | 6,026,455 | US |
| NIMXI-0140.00/US | Utility - DIV | Issued | CIRCUITRY AND METHOD FOR PROGRAMMING AND ERASING A NON-VOLATILE SEMICONDUCTOR MEMORY | 7/29/1993 | 08/100,508 | 4/30/1996 | 5,513,333 | US |
| NIMXI-0141.00/US | Utility - ORG | Issued | ANALOG CHROMA KEYING ON COLOR DATA | 11/19/1993 | 08/154,955 | 3/14/1995 | 5,398,075 | US |
| NIMXI-0142.00/US | Utility - CON | Issued | METHOD AND SYSTEM FOR UPDATING FILES OF A PLURALITY OF STORAGE DEVICES THROUGH PROPAGATION OF FILES OVER A NETWORK | 10/12/1995 | 08/542,039 | 8/13/1996 | 5,546,539 | US |
| NIMXI-0143.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR INITIALIZING A COMPUTER SYSTEM HAVING CENTRAL AND DISTRIBUTED ADDRESS DECODE MEMORY BUS RESOURCES | 1/31/1995 | 08/384,472 | 12/31/1996 | 5,590,289 | US |
| NIMXI-0144.00/US | Utility - CON | Issued | APPARATUS FOR ENCODING SIGNALS USING A CONFIGURABLE TRANSFORM CIRCUIT | 10/7/1994 | 08/319,779 | 6/20/1995 | 5,426,462 | US |
| NIMXI-0145.00/US | Utility - ORG | Issued | METHOD FOR CONTROLLING DATAFLOW BETWEEN A PLURALITY OF CIRCULAR BUFFERS | 5/26/1993 | 08/067,858 | 8/29/1995 | 5,446,839 | US |

PATENT

REEL: 028362 FRAME: 0812

Exhibit A - US Applications and Issued Patents

| Patent No. | Matr. Status | Matr. Type | Matr. Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|--------------|---------------|--------------|--|------------|------------|------------|------------|---------|
| NMXI-0146.00/US | Issued | Utility - ORG | Issued | AN ELECTRONIC PACKAGE THAT HAS A DIE COUPLED TO A LEAD FRAME BY A DIELECTRIC TAPE AND A HEAT SINK THAT PROVIDES BOTH AN ELECTRICAL AND A THERMAL PATH BETWEEN THE DIE AND THE LEAD FRAME | 2/25/1994 | 08/201,895 | 8/22/1995 | 5,444,602 | US |
| NMXI-0147.00/US | Issued | Utility - ORG | Issued | TAB TAPE | 12/14/1993 | 08/167,531 | 12/5/1995 | 5,473,190 | US |
| NMXI-0148.00/US | Issued | Utility - CON | Issued | METHOD AND APPARATUS FOR INTERFACING MEMORY DEVICES OPERATING AT DIFFERENT SPEEDS TO A COMPUTER SYSTEM BUS | 2/28/1997 | 08/807,212 | 2/24/1998 | 5,721,882 | US |
| NMXI-0149.00/US | Issued | Utility - CON | Issued | METHOD FOR SYNCHRONIZING CLOCKS UPON RESET | 7/11/1994 | 08/272,879 | 4/23/1996 | 5,510,740 | US |
| NMXI-0150.00/US | Issued | Utility - ORG | Issued | METHOD AND APPARATUS FOR SENSING THE STATE OF FLOATING GATE MEMORY CELLS BY APPLYING A VARIABLE GATE VOLTAGE | 9/29/1994 | 08/315,292 | 4/16/1996 | 5,508,958 | US |
| NMXI-0151.00/US | Issued | Utility - CON | Issued | SYSTEM UTILIZING MULTIPLE ADDRESS DECODE RESOURCES AND DECODER RECEIVING ADDRESS DETERMINES ADDRESS CORRESPONDING TO RESOURCES BASED ON SELECT AND READY SIGNALS BY THAT PARTICULAR RESOURCE | 10/19/1995 | 08/545,374 | 9/16/1997 | 5,668,949 | US |
| NMXI-0152.00/US | Issued | Utility - ORG | Issued | SINGLE STAGE SENSING APPARATUS FOR A CONTENT ADDRESSABLE MEMORY | 3/11/1994 | 08/212,350 | 6/27/1995 | 5,428,565 | US |
| NMXI-0153.00/US | Issued | Utility - ORG | Issued | THROTTLING CIRCUIT FOR A DATA TRANSFER SYSTEM DRIVER, COMPUTER-IMPLEMENTED PROCESS, AND COMPUTER SYSTEM FOR PROCESSING DATA USING LOADABLE MICROCODE RUNNING ON A PROGRAMMABLE PROCESSOR | 9/15/1994 | 08/307,502 | 7/18/1995 | 5,434,892 | US |
| NMXI-0154.00/US | Lapsed | Utility - ORG | Lapsed | DUAL ROW SELECTION USING MULTIPLEXED TRI-LEVEL DECODER | 11/12/1993 | 08/152,348 | 4/23/1996 | 5,511,195 | US |
| NMXI-0155.00/US | Issued | Utility - ORG | Issued | LOW IMPACT COLLISION DETECTION METHOD | 9/30/1994 | 08/316,546 | 5/14/1996 | 5,517,138 | US |
| NMXI-0156.00/US | Issued | Utility - ORG | Issued | ENCODING AND DECODING VIDEO SIGNALS USING ADAPTIVE FILTER SWITCHING CRITERIA | 3/23/1994 | 08/216,668 | 10/31/1995 | 5,463,538 | US |
| NMXI-0157.00/US | Issued | Utility - CON | Issued | METHOD AND APPARATUS FOR DECODING AND RECORDING OF ADDRESSES | 6/29/1994 | 08/268,270 | 1/30/1996 | 5,498,570 | US |
| NMXI-0158.00/US | Issued | Utility - CON | Issued | LEAD FRAME AND SEMICONDUCTOR DEVICE USING SAME | 12/5/1995 | 09/567,408 | 3/11/1997 | 5,611,054 | US |
| NMXI-0159.00/US | Issued | Utility - ORG | Issued | LEAD FRAME AND SEMICONDUCTOR DEVICE USING SAME | 12/9/1991 | 07/803,724 | 8/17/1993 | 5,237,202 | US |
| NMXI-0159.01/US | Issued | Utility - CON | Issued | CACHE MEMORY UTILIZING PSEUDO-STATIC FOUR TRANSISTOR MEMORY CELL | 12/9/1992 | 07/984,941 | 3/1/1994 | 5,291,060 | US |
| NMXI-0160.00/US | Issued | Utility - ORG | Issued | LOW CAPACITANCE CONTENT-ADDRESSABLE MEMORY CELL | 6/1/1994 | 08/252,320 | 12/12/1995 | 5,475,633 | US |
| NMXI-0161.00/US | Issued | Utility - ORG | Issued | BIT MAP ADDRESSING SCHEMES FOR FLASH MEMORY | 6/15/1994 | 08/260,044 | 2/6/1996 | 5,490,102 | US |
| NMXI-0162.00/US | Issued | Utility - ORG | Issued | FLASH MEMORY INCLUDING A MODE REGISTER FOR INDICATING SYNCHRONOUS OR ASYNCHRONOUS MODE OF OPERATION | 6/2/1994 | 08/253,902 | 3/5/1996 | 5,497,354 | US |
| NMXI-0163.00/US | Issued | Utility - CON | Issued | SYNCHRONOUS INTERFACE FOR A NONVOLATILE MEMORY | 6/18/1997 | 08/897,499 | 2/15/2000 | 6,026,465 | US |
| NMXI-0164.00/US | Issued | Utility - CON | Issued | PIPELINED READ ARCHITECTURE FOR MEMORY | 6/14/2000 | 09/595,327 | 5/13/2003 | 6,564,285 | US |
| NMXI-0164.01/US | Issued | Utility - DIV | Issued | PIPELINED READ ARCHITECTURE FOR MEMORY | 12/26/1995 | 08/575,339 | 1/7/1997 | 5,592,465 | US |
| NMXI-0165.00/US | Issued | Utility - ORG | Issued | SYNCHRONOUS ADDRESS LATCHING FOR MEMORY ARRAYS | 9/18/1996 | 08/174,270 | 11/4/1997 | 5,684,752 | US |
| NMXI-0166.00/US | Issued | Utility - ORG | Issued | METHOD FOR PRECONDITIONING A NONVOLATILE MEMORY ARRAY | 6/3/1994 | 08/253,842 | 3/5/1996 | 5,497,355 | US |
| NMXI-0167.00/US | Issued | Utility - CON | Issued | FAST INTERNAL REFERENCE CELL TRIMMING FOR FLASH EEPROM MEMORY | 6/27/1994 | 08/266,132 | 7/16/1996 | 5,537,357 | US |
| NMXI-0167.01/US | Issued | Utility - CON | Issued | FAST INTERNAL REFERENCE CELL TRIMMING FOR FLASH EEPROM MEMORY | 1/31/1996 | 08/594,415 | 3/4/1997 | 5,608,679 | US |
| NMXI-0168.00/US | Issued | Utility - CON | Issued | FAST INTERNAL REFERENCE CELL TRIMMING FOR FLASH EEPROM MEMORY | 2/9/1996 | 08/599,123 | 3/4/1997 | 5,608,669 | US |
| NMXI-0168.00/US | Issued | Utility - ORG | Issued | IC SOCKET PERMITTING CHECKING CONNECTED STATE BETWEEN IC SOCKET AND PRINTED WIRING BOARD | 11/18/1994 | 08/342,379 | 9/23/1997 | 5,669,783 | US |

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Exhibit A - US Applications and Issued Patents

| MTF Doc No | Matter Type | Matter Status | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|-----------|---------|
| NMXI-0168.00/US | Utility - DIV | Issued | IC SOCKET AND METHOD OF CHECKING CONNECTED STATE BETWEEN IC SOCKET AND PRINTED WIRING BOARD | 3/20/1996 | 08/618,686 | 12/23/1997 | 5,699,612 | US |
| NMXI-0169.00/US | Utility - ORG | Issued | DISPLAYING CALLER IDENTIFICATION INFORMATION IN A COMPUTER SYSTEM | 6/29/1994 | 08/268,265 | 8/13/1996 | 5,946,447 | US |
| NMXI-0170.00/US | Utility - ORG | Issued | PARALLEL FLEXIBLE TRANSMISSION CABLE | 6/27/1994 | 08/266,150 | 9/17/1996 | 5,557,075 | US |
| NMXI-0171.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR INTERACTIVE BUILT-IN-SELF-TESTING WITH USER-PROGRAMMABLE TEST PATTERNS | 8/15/1996 | 08/698,465 | 5/30/2000 | 6,070,252 | US |
| NMXI-0172.00/US | Utility - ORG | Issued | PROGRAMMABLE HIGH WATERMARK IN STACK FRAME CACHE USING SECOND REGION AS A STORAGE IF FIRST REGION IS FULL AND AN EVENT HAVING A PREDETERMINED MINIMUM PRIORITY | 9/28/1994 | 08/814,418 | 6/3/1997 | 5,696,362 | US |
| NMXI-0173.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROVIDING AN ULTRA LOW POWER REGULATED NEGATIVE CHARGE PUMP | 3/23/1994 | 08/217,095 | 7/2/1996 | 5,532,915 | US |
| NMXI-0175.00/US | Utility - ORG | Expired | HEAD DRUM GROUND SYSTEM FOR USE IN A VIDEO CASSETTE RECORDER | 1/5/1989 | 08/294,529 | 6/16/1992 | 5,123,097 | US |
| NMXI-0176.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR COOPERATIVE MESSAGING | 4/18/1995 | 08/423,306 | 4/15/1997 | 5,621,798 | US |
| NMXI-0177.00/US | Utility - CON | Issued | METHOD AND DATA STORAGE SYSTEM FOR STRING DATA IN BLOCKS WITHOUT FILE REALLOCATION BEFORE ERASURE | 3/17/1997 | 08/819,698 | 9/15/1998 | 5,809,558 | US |
| NMXI-0178.00/US | Utility - ORG | Issued | DRAIN BIAS MULTIPLEXING FOR MULTIPLE BIT FLASH CELL | 6/2/1994 | 08/252,684 | 1/16/1996 | 5,485,422 | US |
| NMXI-0179.00/US | Utility - ORG | Issued | I/O CONNECTOR FOR ADD IN PRINTED CIRCUIT CARDS FOR COMPUTER SYSTEMS | 7/15/1994 | 08/275,599 | 12/19/1995 | 5,477,418 | US |
| NMXI-0180.00/US | Utility - ORG | Issued | POWER VALIDATION TOOL FOR MICROPROCESSOR SYSTEMS | 10/19/1994 | 08/325,879 | 6/11/1996 | 5,535,913 | US |
| NMXI-0181.00/US | Utility - ORG | Issued | CLOCKING SCHEME FOR LATCHING OF A DOMINO OUTPUT | 1/4/1995 | 08/368,335 | 9/26/1995 | 5,453,708 | US |
| NMXI-0182.00/US | Utility - ORG | Issued | ERROR MANAGEMENT PROCESSES FOR FLASH EEPROM MEMORY ARRAYS | 12/27/1994 | 08/364,546 | 12/12/1995 | 5,475,693 | US |
| NMXI-0183.00/US | Utility - CON | Issued | POLYSILICON POLISH FOR PATTERNING IMPROVEMENT | 9/2/1997 | 08/994,041 | 6/9/1999 | 5,911,111 | US |
| NMXI-0184.00/US | Utility - CON | Issued | COMPUTER SYSTEM WITH A MEMORY IDENTIFICATION SCHEME | 5/7/1996 | 08/643,990 | 2/18/1997 | 5,604,880 | US |
| NMXI-0185.00/US | Utility - CIP | Issued | PROCESS, APPARATUS AND SYSTEM FOR SELECTING QUANTIZATION LEVELS FOR ENCODING VIDEO SIGNALS | 4/28/1994 | 08/234,978 | 7/2/1996 | 5,532,940 | US |
| NMXI-0186.00/US | Utility - CIP | Issued | PROCESS, APPARATUS AND SYSTEM FOR TRANSFORMING SIGNALS USING PSEUDO SIMD PROCESSING | 4/28/1994 | 08/234,976 | 9/24/1996 | 5,559,722 | US |
| NMXI-0187.00/US | Utility - ORG | Issued | TEST FLOW ASSURANCE USING MEMORY IMPRINTING | 9/27/1994 | 08/312,831 | 7/23/1996 | 5,538,141 | US |
| NMXI-0187.01/US | Utility - DIV | Issued | APPARATUS AND SEMICONDUCTOR COMPONENT FOR ASSURING TEST FLOW COMPLIANCE | 2/2/1996 | 08/595,796 | 2/18/1997 | 5,609,412 | US |
| NMXI-0188.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR ROUTING IN REDUCED SWITCH MATRICES TO PROVIDE ONE HUNDRED PERCENT COVERAGE | 7/25/1994 | 08/279,954 | 11/5/1996 | 5,572,196 | US |
| NMXI-0189.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PREVENTING OVER-ERASURE OF FLASH EEPROM MEMORY DEVICES | 6/28/1994 | 08/267,472 | 2/6/1996 | 5,490,109 | US |
| NMXI-0190.00/US | Utility - ORG | Issued | HI-JACK HINGED CONNECTION ADAPTER FOR INPUT/OUTPUT CARDS | 11/3/1994 | 08/333,139 | 6/10/1997 | 5,657,018 | US |
| NMXI-0190.01/US | Utility - CON | Issued | HI-JACK HINGED CONNECTION ADAPTER FOR INPUT/OUTPUT CARDS | 9/30/1996 | 08/724,625 | 7/7/1998 | 5,775,961 | US |
| NMXI-0191.00/US | Utility - ORG | Issued | RENDERING AN INTER VERSUS INTRA VIDEO ENCODING DECISION BASED UPON A VERTICAL GRADIENT MEASURE OF TARGET VIDEO FRAMES | 9/8/1994 | 08/302,595 | 2/4/1997 | 5,600,375 | US |
| NMXI-0192.00/US | Utility - CON | Issued | DATA CONSISTENCY ACROSS A BUS TRANSACTIONS THAT IMPOSE ORDERING CONSTRAINTS | 6/19/1997 | 08/878,660 | 11/3/1998 | 5,833,241 | US |

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Exhibit A - US Applications and Issued Patents

| WIPO Class No. | Mat. No. | Mat. Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|-------------|--|------------|------------|------------|------------|---------|
| NMXI-0193.00/US | Utility - ORG | Issued | PHOTOLITHOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL | 6/30/1995 | 08/497,321 | 9/1/1998 | 5,801,821 | US |
| NMXI-0193.01/US | Utility - DIV | Issued | PHOTOLITHOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL | 10/31/1997 | 08/962,138 | 8/31/1999 | 5,946,079 | US |
| NMXI-0193.02/US | Utility - DIV | Abandoned | PHOTOLITHOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL | 9/16/1998 | 09/156,272 | 4/14/1998 | 5,740,385 | US |
| NMXI-0194.00/US | Utility - ORG | Issued | LOW LOAD HOST/PCI BUS BRIDGE | 12/19/1994 | 08/356,359 | 7/1/1997 | 5,643,000 | US |
| NMXI-0195.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR PROVIDING PROCESSOR FIELD UPGRADABILITY TO A MOTHERBOARD | 7/31/1996 | 08/691,859 | 2/27/1996 | 5,495,453 | US |
| NMXI-0196.00/US | Utility - ORG | Issued | LOW POWER VOLTAGE DETECTOR CIRCUIT INCLUDING A FLASH MEMORY CELL | 11/28/1997 | 08/978,998 | 2/2/1999 | 5,857,721 | US |
| NMXI-0197.00/US | Utility - CON | Issued | SELECTING AN INTEGRATED CIRCUIT FROM DIFFERENT INTEGRATED CIRCUIT ARRAY CONFIGURATIONS | 6/3/1994 | 08/254,364 | 7/21/1998 | 5,784,046 | US |
| NMXI-0198.00/US | Utility - CON | Issued | HORIZONTALLY SCALING IMAGE SIGNALS USING DIGITAL DIFFERENTIAL ACCUMULATOR PROCESSING | 6/3/1994 | 08/253,737 | 12/2/1997 | 5,624,149 | US |
| NMXI-0200.00/US | Utility - CON | Issued | HORIZONTALLY SCALING A IMAGE SIGNALS USING SELECTED WEIGHT FACTORS | 6/3/1994 | 08/253,374 | 5/19/1998 | 5,754,162 | US |
| NMXI-0201.00/US | Utility - ORG | Issued | RUN-LENGTH ENCODING SEQUENCE FOR VIDEO SIGNALS | 10/26/1994 | 08/329,524 | 9/3/1996 | 5,653,832 | US |
| NMXI-0202.00/US | Utility - ORG | Issued | FOUR WIRE MODEM SIGNAL SWITCHING FOR VOICE AND DATA APPLICATIONS | 3/24/1995 | 08/410,212 | 2/11/1997 | 5,603,902 | US |
| NMXI-0203.01/US | Utility - CON | Issued | PORTABLE DISK DRIVE CARRIER AND CHASSIS | 12/29/1995 | 08/580,489 | 12/31/1996 | 5,588,728 | US |
| NMXI-0204.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR DETECTING AND SELECTING VOLTAGE SUPPLIES FOR FLASH MEMORY | 10/19/1994 | 08/326,702 | 9/2/1997 | 5,663,918 | US |
| NMXI-0204.01/US | Utility - CON | Issued | METHOD AND APPARATUS FOR DETECTING AND SELECTING VOLTAGE SUPPLIES FOR FLASH MEMORY | 12/19/1995 | 08/575,177 | 1/4/2000 | 6,012,082 | US |
| NMXI-0205.00/US | Utility - CON | Abandoned | CPU-CYCLE STEALING FOR MULTI-TASKING OPERATING SYSTEM | 9/23/1997 | 08/955,742 | 7/16/1996 | 5,537,069 | US |
| NMXI-0206.00/US | Utility - ORG | Issued | APPARATUS AND METHOD FOR SELECTING A TAP RANGE IN A DIGITAL DELAY LINE | 3/30/1995 | 08/413,951 | 8/24/1999 | 5,963,693 | US |
| NMXI-0207.00/US | Utility - ORG | Issued | ALGORITHMIC ARRAY MAPPING TO DECREASE DEFECT SENSITIVITY OF MEMORY DEVICES | 9/29/1995 | 08/537,233 | 2/11/1997 | 5,602,794 | US |
| NMXI-0208.00/US | Utility - ORG | Issued | VARIABLE STAGE CHARGE PUMP | 10/3/1996 | 08/720,943 | 6/16/1998 | 5,767,735 | US |
| NMXI-0208.01/US | Utility - DIV | Issued | VARIABLE STAGE CHARGE PUMP | 10/3/1996 | 08/720,876 | 3/24/1998 | 5,732,039 | US |
| NMXI-0208.02/US | Utility - DIV | Issued | VARIABLE STAGE CHARGE PUMP | 10/4/1996 | 08/720,944 | 7/14/1998 | 5,781,673 | US |
| NMXI-0209.00/US | Utility - ORG | Issued | POWER SUPPLY CONFIGURED SENSING SCHEME FOR FLASH EEPROM | 5/25/1995 | 08/461,037 | 11/21/1997 | 5,595,526 | US |
| NMXI-0210.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR ENDPOINT DETECTION IN A CHEMICAL/MECHANICAL PROCESS FOR POLISHING A SUBSTRATE | 11/30/1994 | 08/347,813 | 12/31/1996 | 5,589,829 | US |
| NMXI-0211.00/US | Utility - ORG | Issued | DECODING VARIABLE-LENGTH ENCODED SIGNALS | 10/26/1994 | 08/332,583 | 9/17/1996 | 5,557,330 | US |
| NMXI-0212.00/US | Utility - ORG | Issued | ENCODING VIDEO SIGNALS USING SELECTIVE PRE FILTERING | 10/31/1994 | 08/324,923 | 9/1/1998 | 5,803,213 | US |
| NMXI-0213.00/US | Utility - ORG | Issued | ENCODING VIDEO SIGNALS USING LOCAL QUANTIZATION LEVELS | 10/18/1994 | 08/607,521 | 1/14/1997 | 5,594,360 | US |
| NMXI-0214.00/US | Utility - CON | Issued | LOW CURRENT REDUCED AREA PROGRAMMING VOLTAGE DETECTOR FOR FLASH MEMORY | 2/27/1996 | 08/764,666 | 9/23/1997 | 5,671,179 | US |
| NMXI-0215.00/US | Utility - CON | Issued | LOW POWER PULSE GENERATOR FOR SMART VOLTAGE FLASH EEPROM | 12/11/1996 | | | | US |

PATENT

Exhibit A - US Applications and Issued Patents

| Pat. No. | App. No. | Pub. No. | Pub. Date | App. Date | Issue Date | Patent No. | Country |
|-----------------|---------------|-----------|-----------|------------|------------|------------|---------|
| NMXI-0216.00/US | Utility - ORG | Issued | | 12/15/1994 | 08/355,905 | 5,565,759 | US |
| NMXI-0217.00/US | Utility - CON | Abandoned | | 11/8/1996 | 08/748,551 | 5,889,968 | US |
| NMXI-0218.00/US | Utility - ORG | Issued | | 3/14/1995 | 08/403,407 | 5,517,452 | US |
| NMXI-0219.00/US | Utility - ORG | Issued | | 5/5/1995 | 08/437,090 | 5,627,788 | US |
| NMXI-0220.00/US | Utility - ORG | Issued | | 4/28/1995 | 08/431,016 | 5,687,360 | US |
| NMXI-0221.00/US | Utility - CON | Issued | | 8/25/1997 | 08/917,968 | 5,761,054 | US |
| NMXI-0221.01/US | Utility - CON | Issued | | 1/20/1998 | 09/008,968 | 6,438,825 | US |
| NMXI-0222.00/US | Utility - ORG | Issued | | 3/13/1995 | 08/402,933 | 5,719,436 | US |
| NMXI-0222.01/US | Utility - DIV | Issued | | 1/21/1997 | 08/781,358 | 5,793,101 | US |
| NMXI-0223.00/US | Utility - ORG | Issued | | 9/20/1995 | 08/530,076 | 5,848,288 | US |
| NMXI-0224.01/US | Utility - CON | Issued | | 9/30/1996 | 08/723,666 | 5,701,272 | US |
| NMXI-0225.00/US | Utility - CIP | Issued | | 3/31/1995 | 08/414,914 | 5,612,991 | US |
| NMXI-0225.01/US | Utility - CON | Issued | | 3/19/1997 | 08/820,129 | 6,073,030 | US |
| NMXI-0226.00/US | Utility - ORG | Issued | | 1/2/1996 | 08/582,200 | 5,777,624 | US |
| NMXI-0227.00/US | Utility - CON | Issued | | 5/12/1997 | 08/854,770 | 5,818,969 | US |
| NMXI-0228.00/US | Utility - ORG | Issued | | 9/6/1995 | 08/524,065 | 5,710,902 | US |
| NMXI-0229.00/US | Utility - DIV | Issued | | 6/13/1997 | 08/874,821 | 5,909,121 | US |
| NMXI-0229.01/US | Utility - DIV | Issued | | 6/13/1997 | 08/874,388 | 5,961,728 | US |
| NMXI-0230.00/US | Utility - ORG | Issued | | 6/2/1995 | 08/458,805 | 5,640,083 | US |
| NMXI-0231.00/US | Utility - CON | Issued | | 8/9/1997 | 08/907,754 | 5,844,852 | US |
| NMXI-0232.00/US | Utility - ORG | Issued | | 6/28/1996 | 08/672,676 | 5,771,183 | US |
| NMXI-0233.00/US | Utility - ORG | Issued | | 8/15/1995 | 08/515,248 | 5,576,636 | US |
| NMXI-0234.00/US | Utility - ORG | Abandoned | | 12/1/1995 | 08/565,953 | 6,006,282 | US |
| NMXI-0235.00/US | Utility - ORG | Issued | | 11/13/1995 | 08/558,032 | 5,602,719 | US |
| NMXI-0236.00/US | Utility - CON | Issued | | 7/18/1997 | 08/895,613 | 5,978,263 | US |
| NMXI-0237.00/US | Utility - ORG | Issued | | 9/29/1995 | 08/536,112 | 5,712,857 | US |
| NMXI-0238.00/US | Utility - CON | Issued | | 4/9/1997 | 08/831,576 | 5,768,351 | US |
| NMXI-0239.00/US | Utility - ORG | Issued | | 12/20/1995 | 08/575,295 | 5,824,571 | US |

Exhibit A - US Applications and Issued Patents

| Patent No. | Matter Type | Matter Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|------------|---------|
| NMXI-0240.00/US | Utility - ORG | Issued | CIRCUIT FOR DETECTING WHEN A SUPPLY OUTPUT VOLTAGE EXCEEDS A PREDETERMINED LEVEL | 9/29/1995 | 08/556,853 | 2/11/1997 | 5,602,502 | US |
| NMXI-0241.00/US | Utility - ORG | Issued | METHOD AND APPARATUS TO IMPROVE LATENCY EXPERIENCED BY AN AGENT UNDER A ROUND ROBIN ARBITRATION SCHEME | 9/15/1995 | 08/528,914 | 6/17/1997 | 5,640,519 | US |
| NMXI-0242.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR CONVERTING A FIVE WIRE ARBITRATION/BUFFER MANAGEMENT PROTOCOL INTO A TWO WIRE PROTOCOL | 12/31/1996 | 08/775,807 | 6/29/1999 | 5,918,025 | US |
| NMXI-0243.00/US | Utility - ORG | Issued | PLA ARCHITECTURE HAVING IMPROVED CLOCK SIGNAL TO OUTPUT TIMING USING A TYPE-I DOMINO AND PLANE | 12/27/1995 | 08/579,095 | 1/27/1998 | 5,712,578 | US |
| NMXI-0244.00/US | Utility - ORG | Issued | MULTIPLE INTAKE DUCT MICROPROCESSOR COOLING SYSTEM | 12/27/1995 | 08/578,984 | 11/25/1997 | 5,691,883 | US |
| NMXI-0245.00/US | Utility - ORG | Issued | FAST FLOATING-POINT TO INTEGER CONVERSION | 9/29/1995 | 08/557,260 | 6/9/1998 | 5,764,548 | US |
| NMXI-0246.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM | 7/1/1996 | 08/672,983 | 9/10/2002 | 6,449,735 | US |
| NMXI-0247.00/US | Utility - ORG | Issued | LOCKING POWER CABLE INTERFACE GUARD | 12/11/1995 | 08/570,239 | 4/7/1998 | 5,795,701 | US |
| NMXI-0248.00/US | Utility - ORG | Issued | SYSTEM AND METHOD FOR PROGRAMMING PROGRAMMABLE ELECTRONIC COMPONENTS USING BOARD-LEVEL AUTOMATED TEST EQUIPMENT | 11/3/1995 | 08/553,017 | 8/11/1998 | 5,794,007 | US |
| NMXI-0249.00/US | Utility - ORG | Issued | MULTIPLEXER HAVING A PLURALITY OF INTERNAL DATA PATHS THAT OPERATE AT DIFFERENT SPEEDS | 9/27/1995 | 08/534,485 | 4/29/1997 | 5,625,303 | US |
| NMXI-0250.00/US | Utility - ORG | Issued | CLEANING STEP WHICH IMPROVES ELECTROMIGRATION PERFORMANCE OF INTERLAYER CONNECTION IN INTEGRATED CIRCUITS | 3/29/1996 | 08/623,672 | 5/18/1999 | 5,904,560 | US |
| NMXI-0252.00/US | Utility - ORG | Issued | REORDER BUFFER WITH INTERLEAVING MECHANISM FOR ACCESSING A MULTI-PORTER CIRCULAR MEMORY ARRAY | 12/27/1995 | 08/578,964 | 7/28/1998 | 5,787,454 | US |
| NMXI-0253.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROTECTING DATA STORED IN FLASH MEMORY | 12/29/1995 | 08/578,175 | 1/14/1997 | 5,594,686 | US |
| NMXI-0254.00/US | Utility - CON | Issued | RECESSED OR RAISED CHARACTERS ON A CERAMIC LID | 4/7/1997 | 08/895,228 | 6/29/1999 | 5,917,239 | US |
| NMXI-0255.00/US | Utility - ORG | Issued | CONNECTOR WITH ATTACHABLE DAUGHTER CARD RETENTION SYSTEM | 1/8/1996 | 08/584,330 | 7/21/1998 | 5,784,263 | US |
| NMXI-0256.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR FAST SELF-DESTRUCTION OF A CMOS INTEGRATED CIRCUIT | 12/29/1995 | 08/581,436 | 4/7/1998 | 5,736,777 | US |
| NMXI-0257.00/US | Utility - ORG | Issued | ANCHOR PROVISIONS TO PREVENT MOLD DELAMINATION IN AN OVERMOLDED PLASTIC ARRAY PACKAGE | 4/3/1996 | 08/627,058 | 6/30/1998 | 5,773,895 | US |
| NMXI-0258.00/US | Utility - ORG | Issued | SLOW SPEED DRIVER WITH CONTROLLED SLEW RATE | 6/28/1996 | 08/672,412 | 5/26/1998 | 5,757,217 | US |
| NMXI-0259.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR TESTING A MEMORY WHERE DATA IS PASSED THROUGH THE MEMORY FOR COMPARISON WITH DATE READ FROM THE MEMORY | 12/29/1995 | 08/576,705 | 1/7/1997 | 5,592,425 | US |
| NMXI-0259.03/US | Utility - DIV | Issued | MEMORY BYPASS MODE | 9/10/1996 | 08/707,925 | 3/18/1997 | 5,612,916 | US |
| NMXI-0260.00/US | Utility - ORG | Issued | METHOD FOR VERIFYING CONTIGUITY OF A BINARY TRANSLATED BLOCK OF INSTRUCTIONS BY ATTACHING A COMPARE AND/OR BRANCH INSTRUCTION TO PREDECESSOR BLOCK OF INSTRUCTIONS | 8/7/1996 | 08/699,357 | 2/24/1998 | 5,721,927 | US |
| NMXI-0261.00/US | Utility - ORG | Issued | RANDOM NUMBER GENERATOR | 5/15/1996 | 08/648,553 | 1/6/1998 | 5,706,218 | US |
| NMXI-0262.00/US | Utility - ORG | Issued | TABLE-DRIVEN BI-DIRECTIONAL MOTION ESTIMATION USING SCRATCH AREA AND OFFSET VALUES | 3/7/1996 | 08/640,582 | 7/7/1998 | 5,778,097 | US |
| NMXI-0263.00/US | Utility - ORG | Issued | MULTIVOLTAGE KEYED ELECTRICAL CONNECTOR | 6/29/1996 | 08/672,643 | 6/27/2000 | 6,080,022 | US |
| NMXI-0264.00/US | Utility - ORG | Issued | ETCH PROCESS TO PRODUCE ROUNDED TOP CORNERS FOR SUB MICRON SILICON TRENCH APPLICATIONS | 12/31/1996 | 08/775,573 | 12/1/1998 | 5,843,846 | US |

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Exhibit A - US Applications and Issued Patents

| Patent No. | App No. | Pub No. | Pub Date | Pub Date | Serial No. | Issued Date | Patent No. | Country |
|-----------------|---------------|-----------|----------|------------|------------|-------------|------------|---------|
| NMXI-0265.00/US | Utility - ORG | Issued | | 6/18/1996 | 08/665,405 | 3/10/1998 | 5,726,865 | US |
| NMXI-0266.00/US | Utility - ORG | Issued | | 6/18/1996 | 08/668,045 | 12/8/1998 | 5,847,366 | US |
| NMXI-0267.00/US | Utility - ORG | Issued | | 6/28/1996 | 08/673,277 | 6/1/1999 | 5,909,635 | US |
| NMXI-0268.00/US | Utility - CON | Issued | | 2/23/1998 | 09/030,522 | 7/4/2000 | 6,085,341 | US |
| NMXI-0269.00/US | Utility - ORG | Issued | | 10/25/1996 | 08/738,086 | 8/31/1999 | 5,946,396 | US |
| NMXI-0270.00/US | Utility - ORG | Issued | | 12/26/1996 | 08/770,958 | 10/5/1999 | 5,963,970 | US |
| NMXI-0271.00/US | Utility - ORG | Issued | | 7/17/1996 | 08/682,108 | 3/9/1999 | 5,881,076 | US |
| NMXI-0272.00/US | Utility - ORG | Issued | | 7/12/1996 | 08/679,197 | 8/24/1999 | 5,943,487 | US |
| NMXI-0273.00/US | Utility - ORG | Abandoned | | 9/27/1996 | 08/721,685 | 3/30/1999 | 5,889,409 | US |
| NMXI-0274.00/US | Utility - ORG | Abandoned | | 6/27/1996 | 08/670,545 | 3/30/1999 | 5,889,408 | US |
| NMXI-0274.01/US | Utility - DIV | Issued | | 9/29/1998 | 09/162,912 | 5/29/2001 | 6,239,605 | US |
| NMXI-0274.02/US | Utility - DIV | Issued | | 9/29/1998 | 09/163,592 | 5/29/2001 | 6,239,606 | US |
| NMXI-0275.00/US | Utility - ORG | Issued | | 9/30/1996 | 08/723,270 | 10/13/1998 | 5,822,246 | US |
| NMXI-0276.00/US | Utility - ORG | Issued | | 12/20/1996 | 08/771,445 | 11/10/1998 | 5,835,413 | US |
| NMXI-0277.00/US | Utility - ORG | Issued | | 9/16/1997 | 08/931,247 | 10/26/1999 | 5,973,957 | US |
| NMXI-0278.00/US | Utility - ORG | Issued | | 2/26/1999 | 09/259,650 | 3/6/2001 | 6,198,421 | US |
| NMXI-0279.00/US | Utility - ORG | Issued | | 11/13/1996 | 08/746,547 | 10/12/1999 | 5,966,544 | US |
| NMXI-0280.00/US | Utility - ORG | Issued | | 6/28/1996 | 08/672,411 | 8/11/1998 | 5,793,069 | US |
| NMXI-0280.01/US | Utility - DIV | Issued | | 2/17/1998 | 09/024,830 | 9/5/2000 | 6,113,648 | US |
| NMXI-0281.00/US | Utility - ORG | Issued | | 8/12/1996 | 08/700,613 | 8/18/1998 | 5,796,282 | US |
| NMXI-0282.00/US | Utility - ORG | Issued | | 12/31/1996 | 08/775,796 | 4/27/1999 | 5,898,610 | US |
| NMXI-0283.00/US | Utility - ORG | Issued | | 9/26/1996 | 08/721,268 | 9/8/1998 | 5,804,771 | US |
| NMXI-0283.01/US | Utility - DIV | Issued | | 2/27/1998 | 09/032,628 | 1/16/2001 | 6,173,489 | US |
| NMXI-0284.00/US | Utility - ORG | Issued | | 10/31/1996 | 08/739,879 | 3/30/1999 | 5,888,897 | US |
| NMXI-0285.00/US | Utility - ORG | Issued | | 8/16/1996 | 08/689,698 | 6/24/1997 | 5,642,263 | US |

PATENT

REEL: 028362 FRAME: 0818

Exhibit A - US Applications and Issued Patents

| MTL Doc No | Matthr Type | Matthr Status | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|----------------|---------------|--|------------|------------|------------|-----------|---------|
| NMXI-0286.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR THE NON-INVASIVE TESTING OF PRINTED CIRCUIT BOARD ASSEMBLIES | 9/19/1996 | 08/715,857 | 9/1/1998 | 5,802,074 | US |
| NMXI-0287.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR DYNAMIC LOCATION AND CONTROL OF PROCESSOR RESOURCES TO INCREASE RESOLUTION OF DATA DEPENDENCY STALLS | 3/8/1997 | 08/780,675 | 6/29/1999 | 5,918,033 | US |
| NMXI-0288.00/US | Utility - ORG | Issued | PACKAGING MULTIPLE DIES ON A BALL GRID ARRAY SUBSTRATE | 3/25/1997 | 08/822,743 | 9/29/1998 | 5,815,372 | US |
| NMXI-0289.00/US | Utility - ORG | Issued | KEY MANAGEMENT SYSTEM FOR DVD COPYRIGHT MANAGEMENT | 11/5/1996 | 08/740,976 | 6/22/1999 | 5,915,018 | US |
| NMXI-0290.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR GUIDED TOURING OF INTERNET/INTRANET WEBSITES | 7/22/1996 | 08/685,952 | 9/15/1998 | 5,889,247 | US |
| NMXI-0291.00/US | Utility - ORG | Issued | INTEGRATED CIRCUIT PACKAGE | 9/9/1996 | 08/708,857 | 2/29/2000 | 6,031,283 | US |
| NMXI-0292.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR MOUNTING A VERY LARGE SCALE INTEGRATION (VLSI) CHIP TO A COMPUTER CHASSIS FOR COOLING | 12/31/1996 | 08/777,604 | 11/2/1999 | 5,978,228 | US |
| NMXI-0293.00/US | Utility - ORG | Issued | METHOD FOR PERFORMING COMMON SUBEXPRESSION ELIMINATION ON A RACK-N STATIC SINGLE ASSIGNMENT LANGUAGE | 4/1/1997 | 08/829,856 | 12/14/1999 | 6,002,879 | US |
| NMXI-0294.00/US | Utility - ORG | Issued | METHOD FOR USING STATIC SINGLE ASSIGNMENT TO COLOR OUT ARTIFICIAL REGISTER DEPENDENCIES | 4/1/1997 | 08/831,739 | 1/18/2000 | 6,016,398 | US |
| NMXI-0295.00/US | Utility - ORG | Issued | PICKUP CHUCK WITH AN INTEGRAL HEATSINK | 12/30/1997 | 09/000,981 | 2/1/2000 | 6,019,166 | US |
| NMXI-0296.00/US | Utility - ORG | Issued | PRODUCTION PRINTED CIRCUIT BOARD (PCB) EDGE CONNECTOR TEST CONNECTOR | 12/18/1996 | 08/768,961 | 8/18/1998 | 5,785,172 | US |
| NMXI-0297.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR PERFORMING BLOCK BASED FREQUENCY DOMAIN FILTERING | 12/19/1996 | 08/772,015 | | | US |
| NMXI-0298.00/US | Utility - CIP | Issued | METHOD AND APPARATUS FOR VERIFYING THE INSTALLATION OF STRAPPING DEVICES ON A CIRCUIT BOARD ASSEMBLY | 3/2/1997 | 08/778,305 | 8/18/1998 | 5,798,639 | US |
| NMXI-0299.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR FACILITATING DETECTION OF SOLDER OPENS OF SMT COMPONENTS | 10/30/1996 | 08/741,053 | 10/12/1999 | 5,966,020 | US |
| NMXI-0300.00/US | Utility - ORG | Issued | METHOD FOR CALIBRATING A DIGITAL CAMERA TO A PC MONITOR TO ENHANCE PICTURE QUALITY OF A PICTURE CAPTURED BY THE DIGITAL CAMERA AND DISPLAYED ON THE PC MONITOR | 3/23/1997 | 08/787,865 | 6/29/1999 | 5,918,192 | US |
| NMXI-0301.00/US | Utility - ORG | Issued | AN OUT-OF-ORDER SUPERSCALAR MICROPROCESSOR WITH A REMAINING DEVICE THAT MAPS INSTRUCTIONS FROM MEMORY TO REGISTERS | 12/30/1996 | 08/774,559 | 11/17/1998 | 5,888,941 | US |
| NMXI-0301.01/US | Utility - CON | Abandoned | ADVANCED REGISTER RENAMING SYSTEM FOR IMPROVED INSTRUCTION LEVEL PARALLELISM | 7/24/1998 | 09/122,270 | | | US |
| NMXI-0302.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR SCALING IMAGE DATA | 2/11/1997 | 08/799,554 | | | US |
| NMXI-0303.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR CONTROLLING A CHARGE PUMP FOR RAPID INITIALIZATION | 12/17/1996 | 08/768,297 | 3/9/1999 | 5,880,622 | US |
| NMXI-0304.00/US | Utility - ORG | Issued | APPARATUS FOR PERFORMING A USER REQUESTED RESET DURING ALGORITHM EXECUTION | 12/31/1996 | 08/778,015 | 4/25/2000 | 6,055,614 | US |
| NMXI-0305.00/RE | Utility - REIS | Issued | BLOCK LOCKING APPARATUS FOR FLASH MEMORY | 2/3/1997 | 10/094,056 | 3/7/2000 | RE42,551 | US |
| NMXI-0305.00/US | Utility - ORG | Issued | BLOCK LOCKING APPARATUS FOR FLASH MEMORY | 2/3/1997 | 08/784,283 | 3/7/2000 | 6,095,401 | US |
| NMXI-0306.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR DISTRIBUTION OF BROADCAST DATA | 12/29/1996 | 08/771,709 | 3/30/1999 | 5,889,950 | US |
| NMXI-0307.00/US | Utility - ORG | Issued | DYNAMIC COMMUNICATION PATH SELECTION FOR DATA TRANSMISSION BETWEEN COMPUTERS | 4/28/1997 | 08/848,554 | 8/15/2000 | 6,104,720 | US |
| NMXI-0308.00/US | Utility - ORG | Issued | FIDUCIAL FOR ALIGNING AN INTEGRATED CIRCUIT DIE | 12/29/1996 | 08/771,275 | 8/24/1999 | 5,942,805 | US |

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Exhibit A - US Applications and Issued Patents

| WI Docket No. | Matter Type | Matter Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NMXI-0308.00/US | Utility - DIV | Issued | A METHOD OF FORMING A FIDUCIAL FOR ALIGNING AN INTEGRATED CIRCUIT DIE | 11/26/1997 | 08/978,535 | 12/14/1999 | 6,001,703 | US |
| NMXI-0309.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR DELIVERING DATA | 12/31/1996 | 08/801,508 | 11/16/1999 | 5,987,504 | US |
| NMXI-0310.00/US | Utility - ORG | Issued | DYNAMICALLY CONTROLLING THE NUMBER OF BOUNDARY-SCAN CELLS IN A BOUNDARY-SCAN PATH | 5/22/1997 | 08/862,006 | 6/16/1998 | 5,768,289 | US |
| NMXI-0311.00/US | Utility - ORG | Issued | SELF-CONTAINING INPUT BUFFER ON FLASH MEMORIES | 4/11/1997 | 08/834,039 | 9/30/2003 | 6,628,552 | US |
| NMXI-0312.00/US | Utility - ORG | Issued | SELF-CONFIGURING OUTPUT BUFFER ON FLASH MEMORIES | 4/11/1997 | 08/834,032 | 5/11/1999 | 5,903,500 | US |
| NMXI-0313.00/US | Utility - ORG | Issued | SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES | 4/11/1997 | 08/834,026 | 8/3/1999 | 5,933,026 | US |
| NMXI-0314.00/US | Utility - ORG | Issued | VARIABLE LOADING APPARATUS FOR OUTPUT LOADING OF INTEGRATED CIRCUITS | 6/13/1997 | 08/874,496 | 5/4/1998 | 5,900,770 | US |
| NMXI-0315.00/US | Utility - ORG | Issued | COMPUTER PACKAGE WITH A POLYGONAL SHAPED MOTHERBOARD | 9/19/1997 | 08/933,405 | 5/11/1999 | 5,903,432 | US |
| NMXI-0316.00/US | Utility - ORG | Issued | MERGING DUMMY STRUCTURE REPRESENTATIONS FOR IMPROVED DISTRIBUTION OF ARTIFACTS IN A SEMICONDUCTOR LAYER | 9/30/1997 | 08/941,599 | 6/27/2000 | 6,081,272 | US |
| NMXI-0317.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR CONNECTING EXPANSION BUSES TO A PERIPHERAL COMPONENT INTERCONNECT BUS | 1/2/1997 | 08/778,192 | 9/14/1999 | 5,951,667 | US |
| NMXI-0318.00/US | Utility - ORG | Issued | EFFICIENT SELF-TIMED MARKING OF LENGTHY VARIABLE LENGTH INSTRUCTIONS | 12/23/1997 | 08/997,461 | 8/24/1999 | 5,941,982 | US |
| NMXI-0319.00/US | Utility - ORG | Issued | APPARATUS AND METHOD FOR SELF-TIMED MARKING OF VARIABLE LENGTH INSTRUCTIONS HAVING LENGTH-AFFECTING PREFIX BYTES | 12/23/1997 | 08/997,462 | 9/7/1999 | 5,948,095 | US |
| NMXI-0320.00/US | Utility - ORG | Issued | BRANCH INSTRUCTION HANDLING IN A SELF-TIMED MARKING SYSTEM | 12/23/1997 | 08/996,756 | 8/3/1999 | 5,931,944 | US |
| NMXI-0321.00/US | Utility - ORG | Issued | EMPLOYING A LOOK-UP SERVICE AND A CALLEE CONNECTION SERVICE TO ESTABLISH A NETWORK PHONE CALL BETWEEN A CALLER AND A CALLEE. | 3/31/1997 | 08/829,608 | 2/20/2001 | 6,192,044 | US |
| NMXI-0322.00/US | Utility - ORG | Issued | ANTI-THEFT MECHANISM FOR MOBILE COMPUTERS | 9/9/1997 | 08/927,370 | 11/21/2000 | 6,151,678 | US |
| NMXI-0323.00/US | Utility - ORG | Issued | PERIPHERAL DEVICE PREVENTING POST-SCAN MODIFICATION | 3/31/1997 | 08/829,594 | 7/20/1999 | 5,936,550 | US |
| NMXI-0324.00/US | Utility - ORG | Issued | UNIQUE CHEMICAL MECHANICAL PLANARIZATION APPROACH WHICH UTILIZES MAGNETIC SLURRY FOR POLISH AND MAGNETIC FIELDS FOR PROCESS CONTROL | 12/31/1997 | 09/001,509 | 7/4/2000 | 6,082,839 | US |
| NMXI-0324.01/US | Utility - DIV | Issued | UNIQUE CHEMICAL MECHANICAL PLANARIZATION APPROACH WHICH UTILIZES MAGNETIC SLURRY FOR POLISH AND MAGNETIC FIELDS FOR PROCESS CONTROL | 7/13/1999 | 09/352,634 | 9/4/2001 | 6,284,091 | US |
| NMXI-0325.00/US | Utility - ORG | Issued | MULTI-LAYER CODER/DECODER FOR PRODUCING QUANTIZATION ERROR SIGNAL SAMPLES | 11/5/1997 | 08/965,637 | 9/21/2004 | 6,795,501 | US |
| NMXI-0326.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR FINDING LOOP_LEVER PARALLELISM IN A POINTER BASED APPLICATION | 1/13/1998 | 09/006,276 | 8/7/2001 | 6,272,676 | US |
| NMXI-0327.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR USING FUNCTION CONTEXT TO IMPROVE BRANCH PREDICTION | 9/22/1997 | 08/934,964 | 10/12/1999 | 5,964,870 | US |
| NMXI-0328.00/US | Utility - ORG | Issued | FAST BIOMOS ACTIVE-PIXEL SENSOR CELL WITH FAST NPN EMITTER-FOLLOWER READOUT | 1/6/1998 | 09/003,477 | 10/2/2001 | 6,297,492 | US |
| NMXI-0329.00/US | Utility - CIP | Issued | STATIC, HIGH-SENSITIVITY, FUSE-BASED STORAGE CELL | 12/31/1997 | 09/002,174 | 9/28/1999 | 5,959,445 | US |
| NMXI-0330.00/US | Utility - CON | Issued | CIRCUIT AND METHOD FOR CONTROLLING POWER AND PERFORMANCE BASED ON OPERATING ENVIRONMENT | 5/7/1999 | 09/307,324 | 7/29/2003 | 6,601,179 | US |

PATENT

REEL: 028362 FRAME: 0820

Exhibit A - US Applications and Issued Patents

| MTI-DataBnk | MarketType | MarketStatus | Title | FileDate | SerialNo | IssueDate | PatentNo | Country |
|-----------------|---------------|--------------|---|------------|------------|------------|-----------|---------|
| NMXI-0331.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR SEARCHING A MASS STORAGE MEDIUM | 11/6/1997 | 08/965,592 | | | US |
| NMXI-0332.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR AUTOMATICALLY DETERMINING AND DYNAMICALLY UPDATING USER PREFERENCES IN AN ENTERTAINMENT SYSTEM | 5/30/1997 | 08/866,707 | 8/31/1999 | 5,945,988 | US |
| NMXI-0333.00/US | Utility - ORG | Issued | PLGA TEST HANDLER THROW MEASURING UNIT | 2/9/1998 | 09/020,661 | 9/7/1999 | 5,948,960 | US |
| NMXI-0334.00/US | Utility - ORG | Issued | TECHNIQUE FOR PERFORMING DMA INCLUDING ARBITRATION BETWEEN A CHAINED LOW PRIORITY DMA AND HIGH PRIORITY DMA OCCURRING BETWEEN TWO LINKS IN THE CHAINED LOW PRIORITY | 2/10/1998 | 09/021,688 | 8/22/2000 | 5,108,743 | US |
| NMXI-0335.00/US | Utility - ORG | Issued | THERMAL INTERFACE ATTACH MECHANISM FOR ELECTRICAL PACKAGES | 12/15/1997 | 08/990,856 | 10/12/1999 | 5,965,937 | US |
| NMXI-0336.00/US | Utility - ORG | Issued | METHOD AND APPARATUS EMPLOYING A DYNAMIC ENCRYPTION INTERFACE BETWEEN A PROCESSOR AND A MEMORY | 9/29/1997 | 08/937,874 | 11/16/1999 | 5,987,572 | US |
| NMXI-0337.00/US | Utility - ORG | Issued | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD | 1/14/1998 | 09/006,649 | 11/30/1999 | 5,994,939 | US |
| NMXI-0338.00/US | Utility - ORG | Issued | ADVANCED NEAR IDEAL MIXER | 12/14/1998 | 09/205,992 | 2/6/2001 | 6,184,739 | US |
| NMXI-0339.00/US | Utility - ORG | Issued | COMPUTER SYSTEM INCLUDING RIGHT ANGLE PROCESSOR AND ADD-ON CARD CONNECTORS | 9/24/1997 | 08/936,857 | 11/2/1999 | 5,978,873 | US |
| NMXI-0340.00/US | Utility - ORG | Issued | SUPPLYING STANDBY VOLTAGE TO MEMORY AND WAKEUP CIRCUITRY TO WAKE A COMPUTER FROM A LOW POWER MODE | 12/30/1997 | 08/001,102 | 10/23/2001 | 6,308,278 | US |
| NMXI-0342.00/US | Utility - ORG | Issued | SUPPLYING STANDBY VOLTAGE TO MEMORY AND WAKEUP CIRCUITRY TO WAKE A COMPUTER FROM A LOW POWER MODE | 1/15/1999 | 09/231,725 | 6/13/2000 | 6,075,751 | US |
| NMXI-0343.00/US | Utility - ORG | Issued | LOW VOLTAGE PROGRAMMABLE COMPLEMENTARY INPUT STAGE SENSE AMPLIFIER | 1/14/1998 | 09/006,770 | 4/18/2000 | 6,051,999 | US |
| NMXI-0344.00/US | Utility - ORG | Issued | SYSTEM FOR SPECULATIVE BRANCH TARGET PREDICTION HAVING A DYNAMIC PREDICTION HISTORY BUFFER AND A STATIC PREDICTION HISTORY BUFFER | 11/26/1997 | 08/979,579 | 11/2/1999 | 5,978,909 | US |
| NMXI-0345.00/US | Utility - ORG | Issued | CIRCUIT AND METHOD FOR CONFIGURING AND REGISTERING A CRYPTOGRAPHIC DEVICE | 9/30/1997 | 08/938,491 | 10/19/1999 | 5,970,147 | US |
| NMXI-0346.00/US | Utility - ORG | Issued | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE | 12/14/1997 | 08/984,766 | 2/2/1999 | 5,867,367 | US |
| NMXI-0346.01/US | Utility - CON | Issued | AD FLAT PACK INTEGRATED CIRCUIT PACKAGE | 10/26/1998 | 09/179,016 | 9/7/1999 | 5,949,651 | US |
| NMXI-0346.02/US | Utility - DIV | Issued | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE | 10/26/1998 | 09/179,125 | 11/2/1999 | 5,978,224 | US |
| NMXI-0346.03/US | Utility - DIV | Issued | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE | 10/26/1998 | 09/179,014 | 7/3/2001 | 6,255,155 | US |
| NMXI-0347.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR CHANGING THE FONT USED TO DISPLAY TEXT ENTERED INTO AN INFORMATION INPUT FIELD | 12/30/1997 | 09/000,712 | | | US |
| NMXI-0348.00/US | Utility - DIV | Issued | EFFICIENT COMPANDING ALGORITHM SUITABLE FOR COLOR IMAGING | 1/13/2000 | 09/482,551 | 2/26/2002 | 6,351,555 | US |
| NMXI-0349.00/US | Utility - ORG | Issued | A CHARGE PUMP AVOIDING GAIN DEGRADATION DUE TO THE BODY EFFECT | 1/12/1998 | 09/005,560 | 5/15/2001 | 6,232,826 | US |
| NMXI-0350.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR REDUCING THE POWER CONSUMPTION OF A VOLTAGE REGULATOR | 9/3/1998 | 09/148,033 | 10/23/2001 | 6,307,355 | US |
| NMXI-0351.00/US | Utility - ORG | Issued | OVERLAY COUNTER FOR ACCELERATED GRAPHICS PORT | 1/30/1998 | 09/016,790 | 3/6/2001 | 6,199,149 | US |
| NMXI-0352.00/US | Utility - ORG | Abandoned | PORTABLE DIGITAL PHOTOGRAPH VIEWER | 1/7/1996 | 09/004,052 | | | US |
| NMXI-0352.01/US | Utility - DIV | Abandoned | PORTABLE DIGITAL PHOTOGRAPH VIEWER | 12/19/2002 | 10/324,692 | | | US |
| NMXI-0353.00/US | Utility - ORG | Issued | PUMP SUPPLY SELF REGULATION FOR FLASH MEMORY CELL PAIR REFERENCE CIRCUIT | 3/16/1998 | 09/040,523 | 9/31/1999 | 5,946,258 | US |

PATENT

REEL: 028362 FRAME: 0821

Exhibit A - US Applications and Issued Patents

| App No. | Matter Type | Matter Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|------------------|---------------|---------------|--|------------|------------|------------|------------|---------|
| NIMX1-0382.00/US | Utility - ORG | Issued | METHOD AND APPARATUS OF RESOLVING A DEADLOCK BY COLLAPSING WRITEBACKS TO A MEMORY | 12/19/1997 | 08/987,278 | 10/17/2000 | 6,134,635 | US |
| NIMX1-0383.00/US | Utility - ORG | Issued | CORRECTING LENS DISTORTION | 7/6/1998 | 09/110,805 | 12/14/1999 | 6,002,525 | US |
| NIMX1-0384.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR DETERMINING A NEXT ADDRESS WITHIN A BINARY SEARCH ALGORITHM | 12/28/1998 | 09/221,626 | 11/16/2001 | 6,314,506 | US |
| NIMX1-0385.00/US | Utility - DIV | Abandoned | VOICE DRIVEN WIRELESS POINTER | 10/19/2004 | 10/962,688 | 3/11/2006 | 7,342,612 | US |
| NIMX1-0386.00/US | Utility - ORG | Issued | SYSTEM AND METHOD FOR CONTROLLING AN ELECTRONIC DEVICE | 2/18/1999 | 09/250,940 | 11/13/2007 | 7,296,060 | US |
| NIMX1-0387.00/US | Utility - ORG | Issued | SYSTEM AND METHOD FOR AUTOMATICALLY IDENTIFYING AND ATTACHING RELATED DOCUMENTS | 12/24/1998 | 09/220,910 | 11/13/2007 | 7,296,060 | US |
| NIMX1-0388.00/US | Utility - ORG | Issued | TACTILE TRACKING SYSTEMS AND METHODS | 12/18/1998 | 09/216,580 | 2/4/2003 | 6,515,586 | US |
| NIMX1-0389.00/US | Utility - ORG | Issued | IMPLEMENTING MICRO BGA ASSEMBLY TECHNIQUES FOR SMALL DIE | 9/24/1998 | 09/160,535 | 12/2/2003 | 6,655,022 | US |
| NIMX1-0389.01/US | Utility - DIV | Issued | IMPLEMENTING MICRO BGA ASSEMBLY TECHNIQUES FOR SMALL DIE | 8/30/1999 | 09/385,924 | 12/9/2002 | 6,489,557 | US |
| NIMX1-0390.00/US | Utility - GIP | Issued | FAN DUCT MODULE | 5/9/1998 | 09/074,951 | 5/16/2000 | 6,064,571 | US |
| NIMX1-0390.01/US | Utility - COM | Issued | FAN DUCT MODULE | 5/5/2000 | 09/566,185 | 4/22/2003 | 6,552,898 | US |
| NIMX1-0391.00/US | Utility - ORG | Issued | CHEMICAL-MECHANICAL POLISHING SLURRY | 12/1/1998 | 09/203,432 | 6/20/2000 | 6,077,337 | US |
| NIMX1-0391.01/US | Utility - CON | Issued | CHEMICAL-MECHANICAL POLISHING SLURRY | 2/13/2000 | 09/504,191 | 4/10/2001 | 6,214,098 | US |
| NIMX1-0391.02/US | Utility - CON | Issued | CHEMICAL-MECHANICAL POLISHING SLURRY | 11/27/2000 | 09/723,092 | 2/12/2002 | 6,348,144 | US |
| NIMX1-0392.00/US | Utility - CON | Issued | SYSTEM FOR DATA DEPENDENT VOLTAGE BIAS LEVEL | 3/3/2000 | 09/518,107 | 1/29/2002 | 6,342,848 | US |
| NIMX1-0393.00/US | Utility - ORG | Issued | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK | 12/31/1998 | 09/724,030 | 8/7/2001 | 6,272,540 | US |
| NIMX1-0394.00/US | Utility - ORG | Issued | CYCLIC MULTICASTING OR ASYNCHRONOUS BROADCASTING OF COMPUTER FILES | 12/17/1998 | 09/213,220 | 7/3/2001 | 6,356,673 | US |
| NIMX1-0395.00/US | Utility - ORG | Issued | HOT PLUG CARTRIDGE ASSEMBLY | 7/21/1999 | 09/358,373 | 2/27/2001 | 6,195,259 | US |
| NIMX1-0396.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR THE GENERATION OF STATISTICALLY RANDOM NUMBERS | 3/25/1999 | 09/276,966 | 4/16/2002 | 6,374,278 | US |
| NIMX1-0397.00/US | Utility - ORG | Issued | SETTING THE COMMON MODE LEVEL OF A DIFFERENTIAL CHARGE PUMP OUTPUT | 8/6/1999 | 09/370,622 | 2/6/2001 | 6,184,732 | US |
| NIMX1-0397.01/US | Utility - DIV | Issued | METHOD AND APPARATUS FOR SETTING THE COMMON MODE LEVEL OF A DIFFERENTIAL OUTPUT | 9/19/2000 | 09/665,339 | 7/3/2001 | 6,255,873 | US |
| NIMX1-0398.00/US | Utility - ORG | Issued | COLOR INTERPOLATION FOR A FOUR COLOR MOSAIC PATTERN | 11/24/1998 | 09/199,836 | 7/6/2004 | 6,759,646 | US |
| NIMX1-0399.00/US | Utility - ORG | Issued | SMALL OUTLINE RAMBUS IN-LINE MEMORY MODULE | 12/29/1998 | 09/221,804 | 5/9/2000 | 6,061,263 | US |
| NIMX1-0399.01/US | Utility - CON | Issued | SMALL OUTLINE MEMORY MODULE | 2/4/2000 | 09/499,084 | 10/24/2000 | 6,137,709 | US |
| NIMX1-0400.00/US | Utility - ORG | Issued | ENABLING SOFTWARE DESIGNED FOR ONE OPERATING SYSTEM TO OPERATE ON ANOTHER OPERATING SYSTEM | 10/8/1998 | 09/168,592 | 11/19/2002 | 6,484,309 | US |
| NIMX1-0401.00/US | Utility - ORG | Issued | QUANTUM MAGNETIC MEMORY | 12/23/1998 | 09/221,239 | 5/9/2000 | 6,061,265 | US |
| NIMX1-0402.00/US | Utility - ORG | Issued | HOT PLUG STRUCTURE AND METHOD FOR ENGAGING/DISENGAGING PCI COMPLIANT CARD | 12/31/1998 | 09/223,811 | 12/19/2000 | 6,162,073 | US |
| NIMX1-0403.00/US | Utility - ORG | Issued | MAGNETIC INK ENCODING PEN | 9/15/1999 | 09/396,407 | 5/27/2007 | 7,197,174 | US |
| NIMX1-0404.00/US | Utility - ORG | Abandoned | CALIBRATING PROJECTION DISPLAYS | 2/26/1999 | 09/258,060 | | | US |
| NIMX1-0405.00/US | Utility - ORG | Abandoned | CORRECTING NON-UNIFORMITY IN DISPLAYS | 2/26/1999 | 09/259,394 | | | US |
| NIMX1-0406.00/US | Utility - DIV | Issued | CARRIER FOR DISK DRIVE HOT SWAPPING | 12/3/2001 | 10/006,502 | 10/28/2003 | 6,637,719 | US |
| NIMX1-0407.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR DUAL MODE OUTPUT BUFFER IMPEDANCE COMPENSATION | 4/26/1999 | 09/299,771 | 12/26/2000 | 6,166,563 | US |
| NIMX1-0408.00/US | Utility - ORG | Issued | METHOD FOR DIRECTING THE ROUTE OF A CELL TRANSMITTING A NETWORK | 5/28/1999 | 09/322,152 | 11/25/2003 | 6,654,369 | US |

PATENT

REEL: 028362 FRAME: 0823

Exhibit A - US Applications and Issued Patents

| Patent No. | Issue Date | Serial No. | File Date | Title | Matter Status | Matter Type | Country |
|------------|------------|------------|------------|---|---------------|---------------|---------|
| 6,181,180 | 3/30/2001 | 09/340,417 | 6/28/1999 | FLIP-FLOP CIRCUIT | Issued | Utility - ORG | US |
| 7,519,947 | 1/15/2008 | 09/470,875 | 12/22/1999 | METHOD AND APPARATUS FOR PERFORMING DISTRIBUTED SIMULATION UTILIZING A SIMULATION BACKPLANE | Issued | Utility - ORG | US |
| 7,149,883 | 12/12/2006 | 09/539,734 | 3/30/2000 | METHOD AND APPARATUS TO SELECTIVELY ADVANCE A WRITE POINTER FOR A QUEUE BASED ON THE INDICATED VALIDITY OR INVALIDITY OF AN INSTRUCTION STORED WITHIN THE QUEUE | Issued | Utility - ORG | US |
| 6,072,723 | 6/6/2000 | 09/306,322 | 5/6/1999 | METHOD AND APPARATUS FOR PROVIDING REDUNDANCY IN NON-VOLATILE MEMORY DEVICES | Issued | Utility - ORG | US |
| 6,438,275 | 8/20/2002 | 09/296,451 | 4/21/1999 | METHOD FOR MOTION COMPENSATED FRAME RATE UPSAMPLING BASED ON PIECEWISE AFFINE WARPING | Issued | Utility - ORG | US |
| 6,700,209 | 3/2/2004 | 10/386,659 | 3/24/2003 | FLOURINE DOPED SiO ₂ FILM AND METHOD OF FABRICATION | Abandoned | Utility - DIV | US |
| 6,365,441 | 4/2/2002 | 09/474,746 | 12/29/1999 | PARTIAL UNDERFILL FOR FLIP-CHIP ELECTRONIC PACKAGES | Issued | Utility - ORG | US |
| 6,151,229 | 11/21/2000 | 09/694,802 | 10/23/2000 | CHARGE PUMP WITH GATED PUMPED OUTRUT DIODE AT INTERMEDIATE STAGE | Issued | Utility - DIV | US |
| 6,292,048 | 9/18/2001 | 09/346,483 | 6/30/1999 | GATE ENHANCEMENT CHARGE PUMP FOR LOW VOLTAGE POWER SUPPLY | Issued | Utility - ORG | US |
| 6,646,991 | 11/13/2003 | 09/493,185 | 11/11/1999 | MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT | Issued | Utility - ORG | US |
| 6,587,840 | 2/3/2004 | 09/470,189 | 12/22/1999 | MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT | Issued | Utility - ORG | US |
| 7,394,766 | 7/1/2008 | 09/557,099 | 4/21/2000 | MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT | Issued | Utility - CIP | US |
| 7,206,955 | 4/17/2007 | 10/654,252 | 9/2/2003 | BUNDLE SKEW MANAGEMENT AND CELL SYNCHRONIZATION | Issued | Utility - CON | US |
| 6,724,442 | 4/20/2004 | 10/745,903 | 12/23/2003 | OPTICALLY SWITCHABLE INFRARED DETECTOR | Issued | Utility - CON | US |
| | | 09/277,637 | 3/26/1999 | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | Abandoned | Utility - ORG | US |
| | | 09/261,849 | 3/3/1999 | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | Abandoned | Utility - ORG | US |
| 7,141,448 | 11/28/2006 | 09/874,666 | 6/5/2001 | TECHNIQUE FOR SYNCHRONIZING FAULTS IN A PROCESSOR HAVING A REPLAY SYSTEM | Issued | Utility - DIV | US |
| 5,629,271 | 9/30/2003 | 09/473,839 | 12/28/1999 | TECHNIQUE FOR SYNCHRONIZING FAULTS IN A PROCESSOR HAVING A REPLAY SYSTEM | Issued | Utility - ORG | US |
| 7,159,154 | 1/2/2007 | 10/614,215 | 7/8/2003 | ENHANCED VIRTUAL RENAMING SCHEME AND DEADLOCK PREVENTION THEREFOR | Issued | Utility - CON | US |
| 7,539,850 | 5/26/2009 | 10/351,444 | 1/27/2003 | METHOD AND APPARATUS FOR RECOVERY OF PHYSICAL REGISTERS DURING A MISPREDICTION CONDITION | Issued | Utility - CON | US |
| 7,155,599 | 12/26/2006 | 09/750,095 | 12/29/2000 | METHOD AND APPARATUS FOR PREDICTING BRANCHES USING A META PREDICTOR | Issued | Utility - ORG | US |
| 5,802,674 | 9/8/1998 | 09/749,405 | 12/28/2000 | FIBRE METERING ARRANGEMENT | Pending | Utility - ORG | US |
| 5,185,763 | 2/9/1993 | 08/849,031 | 11/13/1995 | DATA BIT TO CONSTELLATION SYMBOL MAPPER | Expired | Utility - ORG | US |
| 6,748,118 | 6/9/2004 | 07/683,359 | 4/9/1991 | METHOD OF QUANTIZING SIGNAL SAMPLES OF AN IMAGE DURING SAME CLOCK DOMAINS | Issued | Utility - ORG | US |
| 6,172,540 | 1/9/2001 | 09/507,399 | 2/18/2000 | APPARATUS FOR FAST LOGIC TRANSFER OF DATA ACROSS ASYNCHRONOUS CLOCK DOMAINS | Issued | Utility - ORG | US |
| 6,846,737 | 1/25/2005 | 09/374,798 | 6/16/1999 | PLASMA INDUCED DEPLETION OF FLUORINE FROM SURFACES OF FLUORINATED LOW-K DIELECTRIC MATERIALS | Issued | Utility - ORG | US |
| | | 09/659,625 | 8/15/2000 | | | Utility - ORG | US |

PATENT

REEL: 028362 FRAME: 0824

Exhibit A - US Applications and Issued Patents

| MTI-DeKRo | Matter Type | Matter Status | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|-----------|---------|
| NMXI-0429.00/US | Utility - DIV | Issued | PLASMA INDUCED DEPLETION OF FLUORINE FROM SURFACES OF FLUORINATED LOW-K DIELECTRIC MATERIALS | 1/15/2002 | 10/050,463 | 7/15/2003 | 5,593,550 | US |
| NMXI-0430.00/US | Utility - ORG | Abandoned | TRANSMITTING DATA AND COMMANDS OVER A BUS | 7/6/1999 | 09/350,269 | | | US |
| NMXI-0431.00/US | Utility - DIV | Issued | A SYSTEM PROCESSING DATA PACKETS RECEIVED FROM REMOTE HOST TO CONTROL SYSTEM OPERATION ACCORDING TO ADJUSTABLE TIMER INTERRUPTS BASED ON DATA FLOW RATE | 2/9/2006 | 11/351,991 | 4/10/2007 | 7,303,767 | US |
| NMXI-0431.00/US | Utility - DIV | Issued | A HOST COMPUTER USING BASIC INPUT AND OUTPUT SYSTEM TO PROCESS CONTROL COMMAND RECEIVED FROM A REMOTE COMPUTER ACCORDING TO TIMER INTERRUPTS | 2/9/2006 | 11/351,934 | 3/25/2008 | 7,349,991 | US |
| NMXI-0432.00/US | Utility - ORG | Issued | ELASTIC BUFFER | 11/1/1999 | 09/432,050 | 7/15/2003 | 6,594,329 | US |
| NMXI-0432.01/US | Utility - CIP | Issued | JABBER COUNTER MECHANISM FOR ELASTIC BUFFER OPERATION | 2/9/2000 | 09/500,524 | 2/21/2006 | 7,003,059 | US |
| NMXI-0433.00/US | Utility - ORG | Issued | MICROSCOPE WITH INFRARED IMAGING | 7/30/1999 | 09/364,142 | 4/25/2000 | 6,055,095 | US |
| NMXI-0433.01/US | Utility - CIP | Abandoned | MICROSCOPE WITH INFRARED IMAGING | 3/14/2000 | 09/524,701 | | | US |
| NMXI-0434.00/US | Utility - ORG | Pending | AUTOMATICALLY RECORDING VIDEO | 9/30/1999 | 09/409,522 | | | US |
| NMXI-0435.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR TRANSFERRING WAFER CASSETTES IN MICROELECTRONIC MANUFACTURING ENVIRONMENT | 10/1/1999 | 09/410,400 | 4/2/2002 | 6,364,331 | US |
| NMXI-0436.00/US | Utility - ORG | Issued | PIXEL SPAN DEPTH BUFFER | 11/18/1999 | 09/442,663 | 12/24/2002 | 6,498,605 | US |
| NMXI-0437.00/US | Utility - ORG | Issued | FIELD FRAME MOTION DESIGN FOR DIGITAL VIDEO DECODER | 12/29/1999 | 09/474,183 | 5/6/2003 | 6,560,286 | US |
| NMXI-0438.00/US | Utility - ORG | Issued | VOLTAGE TOLERANT HIGH DRIVE PULL-UP DRIVER FOR AN I/O BUFFER | 12/29/1999 | 09/474,566 | 5/14/2002 | 6,388,475 | US |
| NMXI-0439.00/US | Utility - ORG | Abandoned | OBSCURING VIDEO SIGNALS FOR CONDITIONAL ACCESS | 2/29/2000 | 09/515,556 | | | US |
| NMXI-0440.00/US | Utility - ORG | Issued | METHOD OF COMPRESSING A COLOR IMAGE | 10/1/1999 | 09/411,697 | 9/28/2004 | 6,798,901 | US |
| NMXI-0441.00/US | Utility - ORG | Issued | STEREOSCOPIC IMAGE SENSOR | 9/10/1999 | 09/393,757 | 8/22/2000 | 6,108,130 | US |
| NMXI-0442.00/US | Utility - ORG | Abandoned | CONTROLLING PROCESSOR-BASED SYSTEMS USING A DIGITAL CAMERA | 10/29/1999 | 09/450,282 | 6/12/2007 | 7,231,083 | US |
| NMXI-0443.00/US | Utility - ORG | Issued | EXECUTING A TRANSLATED BLOCK OF INSTRUCTIONS AND BRANCHING TO CORRECTION CODE WHEN EXPECTED TOP OF STACK DOES NOT MATCH ACTUAL TOP OF STACK TO ADJUST STACK AT EXECUTION TIME TO CONTINUE EXECUTING WITHOUT RESTARTING TRANSLATING | 9/29/2000 | 09/676,175 | 3/4/2008 | 7,340,592 | US |
| NMXI-0444.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR ADAPTIVE HIERARCHICAL VISIBILITY IN A TILED THREE-DIMENSIONAL GRAPHICS ARCHITECTURE | 11/2/1999 | 09/432,134 | 2/25/2003 | 6,525,726 | US |
| NMXI-0445.00/US | Utility - ORG | Issued | SELF-TUNING AMPLIFIER | 11/5/1999 | 09/434,250 | 7/31/2001 | 6,268,774 | US |
| NMXI-0446.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR RAPID INITIALIZATION OF CHARGE PUMP CIRCUITS | 9/27/1999 | 09/406,329 | 7/3/2001 | 6,255,896 | US |
| NMXI-0447.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR UNLOCKING A COMPUTER SYSTEM HARD DRIVE | 11/21/2001 | 10/002,541 | 2/14/2006 | 7,000,109 | US |
| NMXI-0448.00/US | Utility - ORG | Issued | REAL-TIME PERFORMANCE ASSESSMENT OF LARGE AREA NETWORK USER EXPERIENCE | 12/23/1999 | 09/471,964 | 4/21/2009 | 7,523,190 | US |
| NMXI-0449.00/US | Utility - ORG | Expired | METHOD AND APPARATUS FOR IDENTIFYING SPEECH IN TELEPHONE SIGNALS | 5/15/1999 | 07/351,785 | 12/18/1990 | 4,979,214 | US |
| NMXI-0450.00/US | Utility - ORG | Expired | METHOD AND APPARATUS FOR FREQUENCY ANALYSIS OF TELEPHONE SIGNALS | 5/15/1989 | 07/351,393 | 6/5/1990 | 4,992,062 | US |
| NMXI-0451.00/US | Utility - ORG | Expired | CONVERTER FOR IN-BAND ROUTING AND/OR ORIGINATION INFORMATION | 10/31/1989 | 07/429,849 | 9/3/1991 | 5,046,088 | US |
| NMXI-0452.00/US | Utility - ORG | Expired | AUTOMATIC CALL CENTER OVERFLOW RETRIEVAL SYSTEM | 11/13/1990 | 07/612,572 | 2/9/1993 | 5,185,786 | US |

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REEL: 028362 FRAME: 0825

Exhibit A - US Applications and Issued Patents

| Pub. No. | Pub. Date | Pub. No. | Pub. Date | Pub. No. | Pub. Date | Country |
|-----------------|------------|------------|------------|-----------|------------|---------|
| NMXI-0453.00/US | 3/7/1991 | 07/665,970 | 6/7/1993 | 5,218,636 | 6/7/1993 | US |
| NMXI-0454.00/US | 3/1/1993 | 08/024,402 | 4/4/1995 | 5,404,400 | 4/4/1995 | US |
| NMXI-0455.00/US | 4/12/1994 | 08/226,678 | 8/15/1995 | 5,442,694 | 8/15/1995 | US |
| NMXI-0456.00/US | 8/22/1995 | 08/524,111 | 6/10/1997 | 5,658,436 | 6/10/1997 | US |
| NMXI-0457.00/US | 1/27/1994 | 08/188,555 | 4/28/1998 | 5,745,786 | 4/28/1998 | US |
| NMXI-0458.00/US | 1/31/1996 | 08/594,411 | 9/15/1998 | 5,809,110 | 9/15/1998 | US |
| NMXI-0459.00/US | 10/6/1994 | 08/319,159 | 6/9/1998 | 5,764,759 | 6/9/1998 | US |
| NMXI-0460.00/US | 6/4/1997 | 08/828,000 | 3/9/1999 | 5,881,146 | 3/9/1999 | US |
| NMXI-0461.00/US | 1/30/1996 | 08/594,016 | 6/15/1999 | 5,912,950 | 6/15/1999 | US |
| NMXI-0462.00/PR | 6/2/1999 | 60/135,467 | | | | US |
| NMXI-0462.00/US | 3/16/2000 | 09/527,314 | 5/11/2004 | 6,735,418 | 5/11/2004 | US |
| NMXI-0463.00/US | 12/21/1999 | 09/468,477 | 12/30/2003 | 6,671,771 | 12/30/2003 | US |
| NMXI-0464.00/US | 12/21/1999 | 09/468,965 | 6/22/2004 | 6,754,764 | 6/22/2004 | US |
| NMXI-0465.00/US | 12/22/1999 | 09/470,574 | 11/18/2003 | 6,650,802 | 11/18/2003 | US |
| NMXI-0466.00/US | 4/2/2004 | 10/817,263 | 4/7/2009 | 7,516,312 | 4/7/2009 | US |
| NMXI-0467.00/US | 6/30/2000 | 09/609,496 | 4/29/2008 | 7,366,748 | 4/29/2008 | US |
| NMXI-0468.00/US | 12/29/1999 | 09/474,750 | 12/23/2003 | 6,668,298 | 12/23/2003 | US |
| NMXI-0468.01/US | 12/23/2003 | 10/743,397 | | | | US |
| NMXI-0469.00/US | 3/31/2000 | 09/539,343 | 2/20/2007 | 7,180,523 | 2/20/2007 | US |
| NMXI-0470.00/US | 4/12/2000 | 09/547,624 | 5/25/2004 | 6,743,978 | 5/25/2004 | US |
| NMXI-0471.00/US | 1/7/2000 | 09/478,994 | | | | US |
| NMXI-0472.00/US | 6/30/2000 | 09/608,616 | 7/3/2007 | 7,240,342 | 7/3/2007 | US |
| NMXI-0473.00/US | 3/4/1997 | 09/526,794 | 6/10/2003 | 6,577,695 | 6/10/2003 | US |
| NMXI-0474.00/PR | 9/10/1998 | 60/099,723 | | | | US |
| NMXI-0474.00/US | 11/20/2001 | 09/762,720 | 6/20/2006 | 7,065,327 | 6/20/2006 | US |
| NMXI-0475.00/US | 9/20/2000 | 09/665,826 | 2/28/2006 | 7,007,304 | 2/28/2006 | US |
| NMXI-0476.00/US | 1/31/2000 | 09/495,150 | 9/12/2006 | 7,107,302 | 9/12/2006 | US |
| NMXI-0477.00/US | 7/6/2005 | 11/174,662 | 2/5/2008 | 7,327,370 | 2/5/2008 | US |
| NMXI-0478.00/US | 6/30/2000 | 09/607,483 | 10/23/2002 | 6,469,482 | 10/23/2002 | US |
| NMXI-0478.01/US | 6/17/2002 | 10/174,193 | 6/1/2004 | 6,744,669 | 6/1/2004 | US |
| NMXI-0479.00/US | 12/13/1999 | 09/468,715 | 4/29/2003 | 6,556,128 | 4/29/2003 | US |

PATENT

REEL: 028362 FRAME: 0826

Exhibit A - US Applications and Issued Patents

| WIPO-Design | Matter Type | Matter Status | Title | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|------------|---------|
| NMXI-0480.00/US | Utility - ORG | Issued | METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS | 6/15/1998 | 09/103,683 | 4/4/2000 | 6,047,635 | US |
| NMXI-0481.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR BRANCH TRACE MESSAGE SCHEME DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK | 2/14/2000 | 09/503,879 | 11/11/2003 | 6,647,845 | US |
| NMXI-0482.00/US | Utility - ORG | Issued | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK | 6/26/2000 | 09/603,957 | 8/17/2004 | 6,778,548 | US |
| NMXI-0482.01/US | Utility - DIV | Issued | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK | 1/20/2004 | 10/761,394 | 4/1/2008 | 7,352,753 | US |
| NMXI-0482.02/US | Utility - DIV | Abandoned | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK | 1/20/2004 | 10/761,395 | | | US |
| NMXI-0482.03/US | Utility - CON | Issued | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK | 5/10/2004 | 10/842,752 | 11/11/2008 | 7,450,383 | US |
| NMXI-0483.00/US | Utility - ORG | Issued | METHOD OF INVERSE QUANTIZED SIGNAL SAMPLES OF AN IMAGE DURING IMAGE DECOMPRESSION | 2/18/2000 | 09/507,213 | 11/1/2005 | 6,961,472 | US |
| NMXI-0484.00/US | Utility - ORG | Issued | SHARED CREDIT ROUND ROBIN QUEUING | 3/21/2000 | 09/541,048 | 1/27/2004 | 6,683,884 | US |
| NMXI-0485.00/US | Utility - ORG | Issued | VITERBI ACS UNIT WITH RENORMALIZATION | 3/29/1993 | 08/098,323 | 9/20/1994 | 5,349,608 | US |
| NMXI-0486.00/US | Utility - ORG | Issued | CARRIER FREQUENCY ESTIMATOR FOR A SIGNAL RECEIVER | 7/20/1998 | 09/119,479 | 12/14/1999 | 6,002,721 | US |
| NMXI-0486.01/US | Utility - CON | Abandoned | CARRIER FREQUENCY ESTIMATOR FOR A SIGNAL RECEIVER | 9/30/1999 | 09/469,791 | | | US |
| NMXI-0487.00/US | Utility - ORG | Issued | PROCESS FOR FORMING MICROELECTRONIC PACKAGES AND INTERMEDIATE STRUCTURES FORMED THEREWITH | 3/1/2000 | 09/516,652 | 7/1/2003 | 6,586,836 | US |
| NMXI-0488.00/US | Utility - DIV | Abandoned | DIE-IN-HEAT SPREADER MICROELECTRONIC PACKAGE | 2/9/2004 | 10/774,952 | | | US |
| NMXI-0489.00/US | Utility - ORG | Issued | NOVEL SYSTEM AND METHOD OF HEAT EXTRACTION FROM AN INTEGRATED CIRCUIT DIE | 6/29/2000 | 09/606,893 | 2/26/2002 | 6,351,387 | US |
| NMXI-0489.01/US | Utility - DIV | Issued | METHODS OF HEAT EXTRACTION FROM AN INTEGRATED CIRCUIT DIE | 12/15/2001 | 10/025,434 | 6/22/2004 | 6,751,837 | US |
| NMXI-0490.00/US | Utility - ORG | Issued | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE | 5/1/1992 | 07/877,409 | 5/30/1995 | 5,421,601 | US |
| NMXI-0491.00/US | Utility - ORG | Issued | METHOD FOR IMPROVING ACCESSIBILITY OF A COMPUTER DISPLAY | 8/18/1997 | 09/912,390 | 7/6/1999 | 5,920,870 | US |
| NMXI-0492.00/US | Utility - ORG | Issued | EFFICIENT IMPLEMENTATION OF N-POINT DCT, N-POINT IDCT, SA-DCT AND SA-IDCT ALGORITHMS | 9/30/2000 | 09/676,555 | 5/8/2007 | 7,216,140 | US |
| NMXI-0493.00/US | Utility - ORG | Abandoned | COMBINED PROPAGATE/GENERATE/PARTIAL SUM CIRCUIT FOR SINGLE-RAIL ADDERS | 6/26/2000 | 09/603,800 | | | US |
| NMXI-0494.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROVIDING A SECURE PRIVATE PARTITION ON A HARD DISK DRIVE OF A COMPUTER SYSTEM VIA IDE CONTROLLER | 6/30/2000 | 09/608,117 | 12/26/2006 | 7,155,615 | US |
| NMXI-0495.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS TO PROVIDE CLIENT-SIDE FILTERING IN A BROADCAST SYSTEM | 3/22/2000 | 09/593,024 | | | US |
| NMXI-0496.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE | 6/30/2000 | 09/607,592 | 5/16/2006 | 7,047,495 | US |
| NMXI-0497.00/US | Utility - ORG | Issued | REMOVELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE | 5/31/2000 | 09/584,520 | 6/1/2010 | 7,729,943 | US |
| NMXI-0498.00/US | Utility - ORG | Issued | MEMORY SUBSYSTEM EMPLOYING POOL OF REFRESH CANDIDATES | 12/29/2000 | 09/753,005 | 4/2/2002 | 6,368,516 | US |
| NMXI-0499.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR ALLOWING CONTINUOUS APPLICATION OF HIGH VOLTAGE TO A FLASH MEMORY DEVICE POWER PIN | 12/29/2000 | 09/751,514 | 2/4/2003 | 6,515,901 | US |
| NMXI-0499.01/US | Utility - CON | Issued | METHOD AND APPARATUS FOR ALLOWING CONTINUOUS APPLICATION OF HIGH VOLTAGE TO A FLASH MEMORY DEVICE POWER PIN | 12/13/2002 | 10/518,464 | 4/6/2004 | 6,717,855 | US |

PATENT

Exhibit A - US Applications and Issued Patents

| MTL-DeckNo | MatterType | MatterStatus | Title | FileDate | SerialNo | IssueDate | PatentNo | Country |
|-----------------|---------------|--------------|---|------------|------------|------------|-----------|---------|
| NMXI-0526.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR INTERLACED IMAGE ENHANCEMENT | 9/13/1999 | 09/395,000 | 5/7/2002 | 6,384,872 | US |
| NMXI-0527.00/US | Utility - ORG | Issued | CLOCK SPLITTER CIRCUIT TO GENERATE SYNCHRONIZED CLOCK AND INVERTED CLOCK | 9/29/2000 | 09/676,313 | 5/7/2002 | 6,384,658 | US |
| NMXI-0528.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR A LOW LATENCY SOURCE SYNCHRONOUS ADDRESS RECEIVER FOR A HOST SYSTEM BUS IN A MEMORY CONTROLLER | 9/20/2000 | 09/665,922 | 6/8/2004 | 6,748,513 | US |
| NMXI-0529.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR A LOW LATENCY SOURCE SYNCHRONOUS ADDRESS RECEIVER FOR A HOST SYSTEM BUS IN A MEMORY CONTROLLER | 3/30/2004 | 10/813,145 | 7/5/2005 | 6,915,407 | US |
| NMXI-0529.00/US | Utility - ORG | Issued | RAY INTERSECTION REDUCTION USING DIRECTIONALLY CLASSIFIED TARGET LISTS | 6/7/1999 | 09/327,015 | 12/3/2002 | 6,489,555 | US |
| NMXI-0530.00/US | Utility - ORG | Issued | STOCHASTIC SAMPLING WITH CONSTANT DENSITY IN OBJECT SPACE FOR ANISOTROPIC TEXTURE MAPPING | 9/10/1999 | 09/393,477 | 6/4/2002 | 6,400,370 | US |
| NMXI-0531.00/US | Utility - ORG | Issued | 3D RENDERING TEXTURE CACHING SCHEME | 2/11/2000 | 09/502,994 | 5/23/2006 | 7,050,063 | US |
| NMXI-0532.00/US | Utility - ORG | Issued | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM | 10/15/1996 | 08/731,635 | 12/29/1998 | 5,854,632 | US |
| NMXI-0533.00/US | Utility - ORG | Issued | METHOD AND SYSTEM FOR SERVICING CACHE LINE IN RESPONSE TO PARTIAL CACHE LINE REQUEST | 12/29/2000 | 09/752,846 | 12/24/2002 | 6,499,085 | US |
| NMXI-0534.00/US | Utility - ORG | Abandoned | METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN | 6/30/2000 | 09/608,637 | | | US |
| NMXI-0534.01/US | Utility - CON | Abandoned | METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN | 5/5/2005 | 11/124,315 | | | US |
| NMXI-0534.02/US | Utility - DIV | Abandoned | ANTECEDENT STRENGTHENING TO PERFORM GENERALIZED TRAJECTORY EVALUATION | 10/27/2006 | 11/553,505 | | | US |
| NMXI-0535.00/US | Utility - ORG | Issued | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS | 9/27/2000 | 09/670,850 | 3/12/2002 | 6,356,062 | US |
| NMXI-0536.00/US | Utility - ORG | Issued | EXTENSIBLE BIOS ERROR LOG | 12/13/2000 | 09/737,140 | 10/17/2006 | 7,124,412 | US |
| NMXI-0537.00/US | Utility - ORG | Issued | DIFFERENTIAL SIGNAL PATH FOR HIGH SPEED DATA TRANSMISSION IN FLASH MEMORY | 12/29/2000 | 09/752,245 | 8/27/2002 | 6,442,069 | US |
| NMXI-0537.01/US | Utility - CIP | Issued | DIFFERENTIAL REDUNDANCY MULTIPLEXOR FOR FLASH MEMORY DEVICES | 10/16/2001 | 09/982,246 | 6/3/2003 | 6,574,141 | US |
| NMXI-0538.00/US | Utility - ORG | Issued | GATE ENHANCED TRI-CHANNEL POSITIVE CHARGE PUMP | 12/29/2000 | 09/755,045 | 12/17/2002 | 6,496,055 | US |
| NMXI-0538.01/US | Utility - CON | Issued | GATE ENHANCED TRI-CHANNEL POSITIVE CHARGE PUMP | 10/18/2002 | 10/273,689 | 2/3/2004 | 6,686,793 | US |
| NMXI-0539.00/US | Utility - ORG | Issued | TRIPLE WELL NO BODY EFFECT NEGATIVE CHARGE PUMP | 12/28/2000 | 09/751,695 | 9/17/2002 | 6,452,438 | US |
| NMXI-0540.00/US | Utility - ORG | Issued | NOVEL BI-VOLTAGE LEVELS SWITCHES | 12/21/2000 | 09/745,970 | 2/18/2003 | 6,522,180 | US |
| NMXI-0541.00/US | Utility - ORG | Issued | SELF INITIALIZATION FOR CHARGE PUMPS | 12/27/2000 | 09/752,247 | 4/2/2002 | 6,366,358 | US |
| NMXI-0541.01/US | Utility - CON | Issued | SELF INITIALIZATION FOR CHARGE PUMPS | 1/17/2002 | 10/051,696 | 8/27/2002 | 6,441,678 | US |
| NMXI-0542.00/US | Utility - ORG | Issued | OUTPUT SWITCH FOR CHARGE PUMP RECONFIGURATION | 12/26/2000 | 09/748,611 | 4/9/2002 | 6,369,642 | US |
| NMXI-0543.00/US | Utility - ORG | Issued | NEGATIVE OUTPUT VOLTAGE CHARGE PUMP AND METHOD THEREFOR | 12/29/2000 | 09/753,351 | 3/19/2002 | 6,359,814 | US |
| NMXI-0544.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR MATCHED-REFERENCE SENSING ARCHITECTURE FOR NON-VOLATILE MEMORIES | 12/28/2000 | 09/752,714 | 2/4/2003 | 6,515,906 | US |
| NMXI-0544.01/US | Utility - CON | Issued | METHOD AND APPARATUS FOR MATCHED-REFERENCE SENSING ARCHITECTURE FOR NON-VOLATILE MEMORIES | 12/26/2002 | 10/330,469 | 12/14/2004 | 6,831,852 | US |
| NMXI-0545.00/US | Utility - ORG | Issued | CHARGING A CAPACITANCE OF A MEMORY CELL AND CHARGER | 6/23/2001 | 09/828,252 | 1/7/2003 | 6,504,760 | US |
| NMXI-0545.01/US | Utility - DIV | Issued | CHARGING A CAPACITANCE OF A MEMORY CELL AND CHARGER | 5/23/2002 | 10/154,019 | 7/22/2003 | 6,597,606 | US |

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REEL: 028362 FRAME: 0829

Exhibit A - US Applications and Issued Patents

| Patent No. | Issue Date | Serial No. | Issue Date | Title | Matter Type | Matter Status | Issue Date | Serial No. | Issue Date | Country |
|------------|------------|------------|------------|--|---------------|---------------|------------|------------|------------|---------|
| 6,092,154 | 7/18/2000 | 08/966,771 | 11/7/1997 | THREAD LIST EXPLICIT CACHING SYSTEM | Utility - CON | Issued | | | | US |
| 7,168,074 | 1/23/2007 | 09/675,286 | 9/29/2000 | RUNTIME PREDICTION FRAMEWORK FOR CPU INTENSIVE APPLICATIONS | Utility - ORG | Issued | | | | US |
| 6,477,091 | 11/5/2002 | 09/823,463 | 3/30/2001 | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING | Utility - ORG | Issued | | | | US |
| 6,788,584 | 9/7/2004 | 10/078,106 | 2/19/2002 | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING | Utility - DIV | Issued | | | | US |
| 6,944,065 | 9/13/2005 | 10/877,634 | 6/25/2004 | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING | Utility - DIV | Issued | | | | US |
| 6,868,071 | 3/15/2005 | 09/714,382 | 11/15/2000 | SYSTEM AND METHOD OF TRANSMITTING DATA FRAMES IN A MESH OF DATA SWITCHES | Utility - ORG | Issued | | | | US |
| 5,999,593 | 12/7/1999 | 09/089,561 | 1/20/1998 | SYSTEM AND METHOD FOR AUTOMATIC PEX DETECTION | Utility - ORG | Issued | | | | US |
| 7,530,066 | 5/5/2009 | 10/179,790 | 6/24/2002 | CONTROLLING SNOOP ACTIVITIES USING TASK TABLE IN MULTIPROCESSOR SYSTEM | Utility - ORG | Issued | | | | US |
| 7,246,135 | 7/17/2007 | 09/756,579 | 1/8/2001 | SHARING CLASSES BETWEEN PROGRAMS PROVIDING A VALID INITIAL FRAME | Utility - ORG | Abandoned | | | | US |
| 5,469,164 | 11/23/1995 | 08/129,899 | 11/27/2000 | CIRCUIT AND METHOD FOR DIGITAL TO ANALOG SIGNAL CONVERSION | Utility - ORG | Issued | | | | US |
| 6,456,110 | 9/24/2002 | 09/750,110 | 9/30/1993 | VOLTAGE LEVEL SHIFTER HAVING ZERO DC CURRENT AND STATE RETENTION IN DROWSY MODE | Utility - ORG | Issued | | | | US |
| 7,123,265 | 10/17/2006 | 11/321,632 | 12/28/2005 | MECHANISM FOR HANDLING EXPLICIT WRITEBACK IN A CACHE COHERENT MULTI-NODE ARCHITECTURE | Utility - CON | Abandoned | | | | US |
| 7,212,497 | 5/1/2007 | 09/741,599 | 12/19/2000 | METHOD & APPARATUS FOR ADDING REAL-TIME PRIMITIVES | Utility - ORG | Issued | | | | US |
| 7,275,254 | 9/25/2007 | 09/742,405 | 12/19/2000 | METHOD AND DRIVER FOR DETECTING GLITCHES | Utility - ORG | Issued | | | | US |
| 6,594,344 | 7/15/2003 | 09/711,579 | 11/21/2000 | METHOD AND APPARATUS FOR DETERMINING AND DISPLAYING THE SERVICE LEVEL OF A DIGITAL TELEVISION BROADCAST SIGNAL | Utility - ORG | Issued | | | | US |
| 7,017,062 | 3/21/2006 | 09/749,610 | 12/28/2000 | AUTO LATENCY TEST TOOL | Utility - ORG | Issued | | | | US |
| 6,617,593 | 9/9/2003 | 09/751,601 | 12/29/2000 | METHOD AND APPARATUS FOR RECOVERING FROM AN OVERHEATED MICROPROCESSOR | Utility - ORG | Issued | | | | US |
| 6,548,399 | 4/15/2003 | 09/730,206 | 12/4/2000 | ION IMPLANTATION SYSTEM | Utility - ORG | Issued | | | | US |
| 6,630,390 | 10/7/2003 | 09/996,215 | 11/20/2001 | METHOD OF FORMING A SEMICONDUCTOR DEVICE USING A CARBON DOPED OXIDE LAYER TO CONTROL THE CHEMICAL MECHANICAL POLISHING OF A DIELECTRIC LAYER | Utility - ORG | Issued | | | | US |
| 7,215,383 | 5/8/2007 | 10/354,361 | 1/29/2003 | METHOD OF FORMING A SEMICONDUCTOR DEVICE USING A CARBON DOPED OXIDE LAYER TO CONTROL THE CHEMICAL MECHANICAL POLISHING OF A DIELECTRIC LAYER | Utility - DIV | Issued | | | | US |
| 6,671,847 | 12/30/2003 | 10/767,324 | 1/29/2004 | COHERENT LIGHT DESPECKLING | Utility - CON | Abandoned | | | | US |
| 6,723,167 | 4/20/2004 | 09/732,115 | 12/7/2000 | APPARATUS FOR SHIELDING TRANSMISSION LINE EFFECTS ON A PRINTED CIRCUIT BOARD | Utility - ORG | Abandoned | | | | US |
| 6,784,004 | 8/31/2004 | 09/709,000 | 11/8/2000 | I/O DEVICE TESTING METHOD AND APPARATUS | Utility - ORG | Issued | | | | US |
| 6,710,845 | 3/23/2004 | 09/746,435 | 12/21/2000 | SPINDLE SLEEVE FOR COATER/DEVELOPER | Utility - ORG | Issued | | | | US |
| 7,256,872 | 8/14/2007 | 10/662,021 | 9/11/2003 | EXPOSED AND EMBEDDED OVERLAY STRUCTURE | Utility - DIV | Abandoned | | | | US |
| | | 09/752,938 | 12/29/2000 | PURGING GAS FROM A PHOTOLITHOGRAPHY ENCLOSURE BETWEEN A MASK PROTECTIVE DEVICE AND A PATTERNED MASK | Utility - ORG | Issued | | | | US |
| | | 10/759,641 | 1/16/2004 | PURGING GAS FROM A PHOTOLITHOGRAPHY ENCLOSURE BETWEEN A MASK PROTECTIVE DEVICE AND A PATTERNED MASK | Utility - DIV | Issued | | | | US |

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Exhibit A - US Applications and Issued Patents

| App No | Matter Type | Matter Status | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|-----------|---------|
| NMXI-0570.00/US | Utility - ORG | Issued | POST ETCH CLEAN SEQUENCE FOR MAKING A SEMICONDUCTOR DEVICE | 10/16/2000 | 09/684,550 | 10/15/2002 | 6,465,358 | US |
| NMXI-0571.00/US | Utility - ORG | Issued | INTERCONNECT MECHANICS FOR ELECTROMAGNETIC COUPLER | 11/15/2000 | 09/713,702 | 12/24/2002 | 6,498,305 | US |
| NMXI-0572.00/US | Utility - ORG | Issued | CLAMP TO SECURE CARRIER TO DEVICE FOR ELECTROMAGNETIC COUPLER | 12/29/2000 | 09/751,442 | 6/10/2003 | 6,576,847 | US |
| NMXI-0573.00/US | Utility - ORG | Issued | ELECTROMAGNETIC COUPLER SOCKET | 12/29/2000 | 09/751,527 | 3/18/2003 | 6,533,586 | US |
| NMXI-0574.00/US | Utility - ORG | Issued | SYSTEM AND METHOD FOR SPLIT AUTOMATIC GAIN CONTROL | 9/14/2001 | 09/951,904 | 3/4/2008 | 7,340,069 | US |
| NMXI-0575.00/US | Utility - ORG | Issued | PROGRAMMABLE DIGITAL PHASE LOCK LOOP | 12/29/2000 | 09/750,671 | 9/3/2002 | 6,445,230 | US |
| NMXI-0576.00/US | Utility - ORG | Abandoned | TIMING MULTIPLE EVENTS WITH A SINGLE TIMER | 4/13/2001 | 09/835,034 | | | US |
| NMXI-0577.00/US | Utility - ORG | Issued | DATA PATH EVALUATION SYSTEM AND METHOD | 4/12/2001 | 09/834,016 | 10/7/2003 | 6,631,339 | US |
| NMXI-0578.00/PR | Prov - ORG | Expired | UNIVERSAL TELEPHONY INTERFACE POLARITY DETECTOR | 8/29/2001 | 60/315,797 | | | US |
| NMXI-0578.00/US | Utility - ORG | Issued | UNIVERSAL TELEPHONY INTERFACE POLARITY DETECTOR | 11/28/2001 | 09/996,255 | 1/9/2007 | 7,163,028 | US |
| NMXI-0579.00/US | Utility - ORG | Issued | KEYBOARD HOLDER | 3/12/2001 | 09/804,489 | 10/29/2003 | 6,471,170 | US |
| NMXI-0580.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR SYNCHRONIZING NETWORK POLICIES | 11/17/2000 | 09/715,529 | | | US |
| NMXI-0581.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR MAPPING IN ONE STAGE AND MERGING AND REMAPPING IN A SECOND STAGE A PLURALITY OF INSTRUCTION GROUPS TO A PLURALITY OF FUNCTIONAL UNITS | 12/28/2000 | 09/749,725 | | | US |
| NMXI-0582.00/US | Utility - ORG | Issued | METHOD FOR READING A STRUCTURAL PHASE-CHANGE MEMORY | 6/2/2001 | 09/921,853 | 7/8/2003 | 6,590,807 | US |
| NMXI-0582.01/US | Utility - CON | Issued | METHOD FOR READING A STRUCTURAL PHASE-CHANGE MEMORY | 6/10/2003 | 10/458,828 | 3/16/2004 | 6,707,712 | US |
| NMXI-0584.00/PR | Prov - ORG | Expired | SINGLE-CHIP NETWORK PROCESSOR AND INTERFACE SYSTEM | 6/27/1998 | 60/090,939 | | | US |
| NMXI-0585.00/US | Utility - ORG | Abandoned | NETWORK ACCELERATOR SUBSYSTEM BASED ON SINGLE-CHIP NETWORK | 6/25/1999 | 09/344,453 | | | US |
| NMXI-0586.00/US | Utility - ORG | Issued | TWO-DIMENSIONAL QUEUING/DE-QUEUING METHODS AND SYSTEMS FOR IMPLEMENTING THE SAME | 3/16/1999 | 09/271,061 | 4/20/2004 | 6,724,767 | US |
| NMXI-0587.00/US | Utility - ORG | Issued | SYSTEMS AND METHODS FOR IMPLEMENTING ABR WITH GUARANTEED INCR | 6/17/1999 | 09/335,223 | 12/2/2003 | 6,657,959 | US |
| NMXI-0588.00/US | Utility - ORG | Issued | SYSTEMS AND METHODS FOR ON-CHIP STORAGE OF VIRTUAL CONNECTION DESCRIPTORS | 3/16/1999 | 09/270,287 | 10/30/2001 | 6,311,212 | US |
| NMXI-0589.00/US | Utility - ORG | Issued | SYSTEM AND METHOD FOR PERFORMING CUT-THROUGH FORWARDING IN AN ATM NETWORK SUPPORTING LAN EMULATION | 6/25/1999 | 09/344,608 | 4/27/2004 | 6,728,249 | US |
| NMXI-0590.00/US | Utility - ORG | Issued | CBR/VBR TRAFFIC SCHEDULER | 6/25/1999 | 09/344,820 | 12/31/2002 | 6,501,731 | US |
| NMXI-0591.00/US | Utility - ORG | Issued | SYSTEM FOR MULTI-LAYER BROADBAND PROVISIONING IN COMPUTER NETWORKS | 6/25/1999 | 09/340,068 | 9/23/2003 | 6,625,650 | US |
| NMXI-0592.00/US | Utility - ORG | Issued | MULTI-PROTOCOL CONVERSION ASSISTANCE METHOD AND SYSTEM FOR A NETWORK ACCELERATOR | 6/25/1999 | 09/344,672 | 8/5/2003 | 6,603,768 | US |
| NMXI-0593.00/US | Utility - ORG | Issued | SYSTEMS AND METHODS FOR IMPLEMENTING POINTER MANAGEMENT | 6/25/1999 | 09/340,282 | 7/23/2002 | 6,425,067 | US |
| NMXI-0594.00/US | Utility - ORG | Issued | APPLICATION PROGRAMMING INTERFACES AND METHODS ENABLING A HOST TO INTERFACE WITH A NETWORK PROCESSOR | 6/25/1999 | 09/337,025 | 8/5/2003 | 6,604,136 | US |
| NMXI-0595.00/US | Utility - ORG | Issued | MATCHING ROUTED CALL TO AGENT WORKSTATION | 12/29/2000 | 09/750,030 | 9/14/2004 | 6,799,102 | US |
| NMXI-0596.00/US | Utility - ORG | Issued | AUTOMATIC DETECTING WHEN AN AGENT IS AVAILABLE | 12/29/2000 | 09/750,025 | 7/10/2007 | 7,242,760 | US |
| NMXI-0597.00/US | Utility - CON | Abandoned | RECOGNIZING SIGNALS IN DESIGN SIMULATION | 11/21/2005 | 11/285,565 | | | US |
| NMXI-0598.00/US | Utility - ORG | Issued | SYSTEM AND METHOD FOR PROTECTED MESSAGING | 12/28/2000 | 09/752,862 | 12/13/2005 | 6,976,172 | US |
| NMXI-0599.00/US | Utility - ORG | Issued | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL | 12/28/2000 | 09/749,745 | 3/30/2007 | 7,194,071 | US |

PATENT

Exhibit A - US Applications and Issued Patents

| App No. | MatrType | MatrStatus | Title | FileDate | SerialNo | IssueDate | PatentNo | Country |
|-----------------|---------------|------------|--|------------|------------|------------|-----------|---------|
| NMXI-0600.00/US | Utility - ORG | Issued | DEVICE AND METHOD OF INTERFACING VOICE DATA BETWEEN A SWITCH AND A COMPUTER SYSTEM. | 4/13/2001 | 09/894,713 | 3/1/2005 | 6,862,341 | US |
| NMXI-0601.00/US | Utility - ORG | Issued | COLOR FILTER ARRAY AND COLOR INTERPOLATION ALGORITHM | 11/30/2000 | 09/727,038 | 7/12/2005 | 6,917,361 | US |
| NMXI-0602.00/US | Utility - DIV | Abandoned | PHYSICALLY INTERACTING WITH A PROCESSOR-BASED DISPLAY | 12/7/2004 | 11/005,894 | | | US |
| NMXI-0603.00/US | Utility - ORG | Abandoned | LOCATING A POSITION ON A DISPLAY SCREEN | 4/18/2001 | 09/836,978 | | | US |
| NMXI-0604.00/US | Utility - ORG | Issued | METAL STRUCTURE FOR A PHASE-CHANGE MEMORY DEVICE | 12/21/2000 | 09/745,835 | 5/27/2003 | 6,569,705 | US |
| NMXI-0604.01/US | Utility - DIV | Issued | METAL STRUCTURE FOR A PHASE-CHANGE MEMORY DEVICE | 4/17/2003 | 10/418,530 | 9/28/2004 | 6,797,979 | US |
| NMXI-0605.00/US | Utility - ORG | Issued | METHOD FOR SWITCHING DATA STREAMS | 11/15/2000 | 09/712,185 | 1/11/2005 | 6,842,452 | US |
| NMXI-0605.00/US | Utility - ORG | Issued | REMOVING REDUNDANT INFORMATION IN HYBRID BRANCH PREDICTION | 10/3/2001 | 09/970,485 | 4/10/2007 | 7,203,825 | US |
| NMXI-0607.00/US | Utility - ORG | Issued | METHOD OF CREATING A PHOTONIC VIA USING FIBER OPTIC | 12/19/2000 | 09/741,520 | 11/18/2003 | 6,650,823 | US |
| NMXI-0608.00/US | Utility - ORG | Issued | METHOD OF CREATING AN ANGLED WAVEGUIDE USING LITHOGRAPHIC TECHNIQUES | 12/19/2000 | 09/741,237 | 2/21/2005 | 7,000,434 | US |
| NMXI-0609.00/US | Utility - ORG | Issued | MARKINGS FOR ALIGNING FIBER OPTIC BUNDLE | 12/19/2000 | 09/741,642 | 10/21/2003 | 6,636,671 | US |
| NMXI-0609.01/US | Utility - DIV | Issued | MARKINGS FOR ALIGNING FIBER OPTIC BUNDLE | 9/26/2003 | 10/672,329 | 8/9/2005 | 6,928,216 | US |
| NMXI-0610.00/US | Utility - ORG | Issued | ALIGNMENT OF OPTICAL FIBERS TO AN ETCHED ARRAY WAVEGUIDE | 12/15/2000 | 09/738,800 | 9/30/2003 | 6,628,665 | US |
| NMXI-0611.00/US | Utility - ORG | Issued | ALIGNMENT OF FIBER OPTIC BUNDLE TO ARRAY WAVEGUIDE USING AN EPOXY | 12/15/2000 | 09/738,799 | 3/16/2004 | 6,707,970 | US |
| NMXI-0612.00/US | Utility - ORG | Issued | ALIGNMENT OF FIBER OPTIC BUNDLE TO ARRAY WAVEGUIDE USING PINS | 12/15/2000 | 09/738,686 | 5/3/2005 | 6,896,989 | US |
| NMXI-0613.00/US | Utility - ORG | Issued | METHOD FOR CENTERING A CORE OF A WAVE GUIDE AMPUFIER | 3/30/2001 | 09/822,380 | 2/17/2004 | 6,694,076 | US |
| NMXI-0614.00/US | Utility - ORG | Issued | METHOD AND SYSTEM FOR DYNAMIC AND INTERACTIVE ROUTE FINDING | 3/28/2001 | 09/836,993 | 11/19/2002 | 6,484,092 | US |
| NMXI-0615.00/US | Utility - ORG | Issued | CLOCK NETWORK | 12/12/2000 | 09/735,358 | 12/16/2008 | 7,466,180 | US |
| NMXI-0616.00/US | Utility - ORG | Issued | PACKAGING AND ASSEMBLY METHOD FOR OPTICAL COUPLING | 6/26/2001 | 09/892,921 | 1/28/2003 | 6,512,861 | US |
| NMXI-0617.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR MULTIPLY-ACCUMULATE TWO-DIMENSIONAL SEPARABLE SYMMETRIC FILTERING | 11/20/2000 | 09/718,877 | 6/15/2004 | 6,751,640 | US |
| NMXI-0618.00/US | Utility - ORG | Issued | HIGH-SPEED DIGITAL DISTRIBUTION SYSTEM | 5/25/1999 | 09/918,287 | 9/10/2002 | 6,449,308 | US |
| NMXI-0619.00/US | Utility - ORG | Issued | DYNAMIC DELAYED TRANSACTION BUFFER CONFIGURATION BASED ON BUS FREQUENCY | 8/20/2001 | 09/953,547 | 4/5/2005 | 6,877,060 | US |
| NMXI-0620.00/US | Utility - ORG | Issued | INITIALIZATION OF NEGATIVE CHARGE PUMP | 3/15/2001 | 09/809,623 | 4/23/2003 | 6,552,600 | US |
| NMXI-0621.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR SWITCHING AN OPTICAL BEAM IN A SEMICONDUCTOR SUBSTRATE | 3/27/2001 | 09/819,160 | 8/5/2003 | 6,603,893 | US |
| NMXI-0622.00/US | Utility - ORG | Issued | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (AWG) AND OPTICAL AMPLIFIER(S) | 10/17/2001 | 09/982,227 | 5/23/2006 | 7,050,663 | US |
| NMXI-0623.00/US | Utility - DIV | Issued | USE OF MEMBRANE PROPERTIES TO REDUCE RESIDUAL STRESS IN AN INTERLAYER REGION | 9/10/2002 | 10/238,081 | 9/2/2003 | 6,614,065 | US |
| NMXI-0623.00/US | Utility - CON | Issued | USE OF MEMBRANE PROPERTIES TO REDUCE RESIDUAL STRESS IN AN INTERLAYER REGION | 9/18/2002 | 10/246,273 | 7/22/2003 | 6,596,614 | US |
| NMXI-0624.00/US | Utility - ORG | Issued | APPARATUS AND METHOD FOR REMOVING PHOTOMASK CONTAMINATION AND CONTROLLING ELECTROSTATIC DISCHARGE | 5/8/2001 | 09/850,766 | 5/11/2004 | 6,734,443 | US |
| NMXI-0625.00/US | Utility - ORG | Issued | APPARATUS FOR ENHANCED RATE CHEMICAL MECHANICAL POLISHING WITH ADJUSTABLE SELECTIVITY | 12/22/2000 | 09/746,470 | 12/3/2002 | 6,468,571 | US |
| NMXI-0625.01/US | Utility - DIV | Issued | APPARATUS FOR ENHANCED RATE CHEMICAL MECHANICAL POLISHING WITH ADJUSTABLE SELECTIVITY | 8/6/2002 | 10/233,776 | 6/14/2005 | 6,905,397 | US |
| NMXI-0626.00/US | Utility - ORG | Issued | VIA-IN-PAD WITH OFF-CENTER GEOMETRY | 12/29/2000 | 09/751,614 | 12/21/2004 | 6,833,615 | US |

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Exhibit A - US Applications and Issued Patents

| MTL DocNo | Matter Type | Matter Status | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|-----------|---------|
| NMXI-0636.01/US | Utility - DIV | Issued | METHODS OF FABRICATING A VIA-IN-PAD WITH OFF-CENTER GEOMETRY | 11/22/2004 | 10/994,995 | 4/24/2007 | 7,208,348 | US |
| NMXI-0637.00/US | Utility - ORG | Issued | CONNECTOR ASSEMBLY WITH DECOUPLING CAPACITORS | 5/15/2001 | 09/858,224 | 9/16/2003 | 6,621,287 | US |
| NMXI-0637.01/US | Utility - DIV | Issued | CONNECTOR ASSEMBLY WITH DECOUPLING CAPACITORS | 8/25/2003 | 10/647,396 | 5/31/2005 | 6,898,852 | US |
| NMXI-0628.00/US | Utility - ORG | Issued | PORTABLE DOWNLOAD UNIT INCLUDING A MEMORY CHIP-TO-CD BURNER | 10/23/2001 | 09/999,784 | 9/9/2008 | 7,424,208 | US |
| NMXI-0629.00/US | Utility - ORG | Issued | INTERCONNECTING CIRCUIT MODULES TO A MOTHERBOARD USING AN EDGE CONNECTOR WITH CONDUCTIVE POLYMER CONTACTS | 5/4/2001 | 09/849,506 | 10/9/2002 | 6,461,169 | US |
| NMXI-0630.00/PR | Prov - ORG | Expired | TIME VARYING FILTER WITH ZERO | 9/7/2001 | 60/317,802 | | | US |
| NMXI-0631.00/US | Utility - ORG | Abandoned | METHOD OF SCHEDULING MODULES ON A CAROUSEL | 6/29/2001 | 09/895,113 | | | US |
| NMXI-0632.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR DETECTING AC REMOVAL | 9/9/2001 | 09/802,451 | 8/9/2004 | 6,772,366 | US |
| NMXI-0633.00/US | Utility - ORG | Issued | MULTIPLIERLESS PYRAMID FILTER | 1/3/2001 | 09/754,684 | 12/9/2003 | 6,662,200 | US |
| NMXI-0634.00/US | Utility - ORG | Issued | LOAD FOR NON-VOLATILE MEMORY DRAIN BIAS | 12/29/2000 | 09/752,535 | 5/27/2003 | 6,570,799 | US |
| NMXI-0625.00/US | Utility - ORG | Issued | KICKER FOR NON-VOLATILE MEMORY DRAIN BIAS | 12/29/2000 | 09/752,550 | 6/3/2004 | 6,744,671 | US |
| NMXI-0636.00/US | Utility - ORG | Issued | DUAL-SWIPING INTERCONNECTION CLIP, AND HOOK AND SLOT ARRANGEMENT FOR PRINTED CIRCUIT BOARD (PCB) ATTACHMENT | 3/30/2001 | 09/821,114 | 4/8/2003 | 6,544,047 | US |
| NMXI-0637.00/US | Utility - ORG | Issued | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION | 1/2/2002 | 10/039,035 | 1/13/2004 | 6,677,233 | US |
| NMXI-0638.00/US | Utility - ORG | Abandoned | LARGE FORMAT EMISSIVE DISPLAY | 5/2/2001 | 09/847,447 | | | US |
| NMXI-0638.01/US | Utility - DIV | Issued | LARGE FORMAT EMISSIVE DISPLAY | 1/5/2006 | 11/325,961 | 2/2/2010 | 7,654,878 | US |
| NMXI-0639.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR INTERSYSTEM CUT/COPY AND PASTE | 3/30/2001 | 09/823,215 | 11/21/2006 | 7,139,842 | US |
| NMXI-0640.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR COMPRESSING CALORIE BURN | 1/30/2001 | 09/773,174 | 2/10/2004 | 6,689,057 | US |
| NMXI-0641.00/US | Utility - ORG | Issued | CALCULATION DATA USING POLYNOMIAL COEFFICIENTS | 6/27/2001 | 09/893,779 | 11/11/2003 | 6,647,455 | US |
| NMXI-0642.00/US | Utility - ORG | Issued | ON-DIE CACHE MEMORY WITH REPEATERS | 4/19/2001 | 09/838,730 | 8/17/2004 | 6,777,970 | US |
| NMXI-0642.01/US | Utility - DIV | Issued | AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT | 7/12/2004 | 10/889,417 | 11/22/2005 | 6,967,496 | US |
| NMXI-0642.02/US | Utility - DIV | Issued | AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT | 10/19/2005 | 11/253,377 | 3/25/2008 | 7,348,790 | US |
| NMXI-0643.00/US | Utility - ORG | Issued | FASTENER INSTALLATION TOOL | 3/29/2001 | 09/821,247 | 9/23/2003 | 6,622,802 | US |
| NMXI-0643.01/US | Utility - CON | Issued | FASTENER INSTALLATION TOOL | 7/28/2003 | 10/628,597 | 8/5/2008 | 7,437,070 | US |
| NMXI-0643.02/US | Utility - DIV | Issued | FASTENER INSTALLATION SYSTEMS | 9/5/2003 | 10/655,822 | 5/23/2006 | 7,048,073 | US |
| NMXI-0643.03/US | Utility - DIV | Abandoned | FASTENERS INSTALLATION TOOLS, SYSTEMS, AND METHODS OF USE | 4/9/2006 | 11/396,769 | | | US |
| NMXI-0644.00/US | Utility - ORG | Issued | INTEGRATED CIRCUIT ANNEALING METHODS AND APPARATUS | 4/25/2001 | 09/842,389 | 3/6/2004 | 6,675,057 | US |
| NMXI-0645.00/US | Utility - ORG | Issued | DIGITAL LEAKAGE COMPENSATION CIRCUIT | 3/29/2001 | 09/823,191 | 5/28/2002 | 6,396,305 | US |
| NMXI-0646.00/US | Utility - ORG | Issued | A METHOD OF REDUCING RESIDUE DEPOSITION ONTO ASH CHAMBER BASE SURFACES | 9/26/2001 | 09/818,177 | 5/20/2003 | 6,564,611 | US |
| NMXI-0647.00/US | Utility - ORG | Abandoned | NETWORK COMMUNICATION CASHING | 9/28/2001 | 09/964,502 | | | US |
| NMXI-0648.00/US | Utility - ORG | Issued | DYNAMIC LOCAL DRIVE AND PRINTER SHARING | 7/27/2001 | 09/917,320 | 1/15/2005 | 7,320,033 | US |
| NMXI-0649.00/US | Utility - ORG | Issued | CACHE ARCHITECTURE FOR PIPELINED OPERATION WITH ON-DIE PROCESSOR | 6/27/2001 | 09/894,513 | 10/7/2003 | 6,631,444 | US |
| NMXI-0650.00/US | Utility - ORG | Issued | CACHE ARCHITECTURE WITH REDUNDANT SUB ARRAY | 6/27/2001 | 09/894,658 | 12/9/2003 | 6,682,271 | US |
| NMXI-0651.00/US | Utility - ORG | Issued | METHOD AND SYSTEM FOR SOFTWARE MODULARIZATION AND AUTOMATIC CODE GENERATION FOR EMBEDDED SYSTEMS | 9/17/2001 | 09/953,277 | 3/7/2006 | 7,010,780 | US |

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REEL: 028362 FRAME: 0833

Exhibit A - US Applications and Issued Patents

| App No. | Matter Type | Matter Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|------------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NIMXI-0652.00/US | Utility - ORG | Issued | APPARATUS HAVING A FIRST CIRCUIT SUPPLYING A POWER POTENTIAL TO A SECOND CIRCUIT UNDER A FIRST OPERATING MODE OTHERWISE DECOUPLING THE POWER POTENTIAL. | 4/13/2001 | 09/834,408 | 3/7/2006 | 7,010,706 | US |
| NIMXI-0653.00/US | Utility - ORG | Issued | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY | 11/29/2001 | 09/997,809 | 11/18/2003 | 6,650,589 | US |
| NIMXI-0654.00/US | Utility - ORG | Issued | CHOPPER STABILIZED BANDGAP REFERENCE CIRCUIT TO CANCEL OFFSET VARIATION | 6/28/2001 | 09/894,151 | 10/8/2002 | 6,462,612 | US |
| NIMXI-0655.00/US | Utility - ORG | Issued | NOISE DEPENDENT FILTER | 6/22/2001 | 09/887,595 | 1/10/2006 | 6,985,709 | US |
| NIMXI-0656.00/US | Utility - ORG | Issued | RELEASING FUNCTIONAL BLOCKS IN RESPONSE TO A DETERMINATION OF A SUPPLY VOLTAGE PREDETERMINED LEVEL AND A LOGIC PREDETERMINED INITIAL STATE | 9/28/2001 | 09/967,180 | 12/9/2003 | 6,561,264 | US |
| NIMXI-0657.00/US | Utility - ORG | Issued | GENERATING PULSES FOR RESETTING INTEGRATED CIRCUITS | 9/28/2001 | 09/966,481 | 10/4/2005 | 6,952,122 | US |
| NIMXI-0658.00/US | Utility - ORG | Issued | PYRAMID FILTER | 3/28/2001 | 09/820,108 | 7/20/2004 | 6,786,286 | US |
| NIMXI-0659.00/US | Utility - ORG | Abandoned | ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED | 3/26/2001 | 09/817,719 | | | US |
| NIMXI-0660.00/US | Utility - DIV | Issued | VOLTAGE-LEVEL CONVERTER | 4/26/2006 | 11/411,647 | 4/1/2008 | 7,352,209 | US |
| NIMXI-0661.00/PR | Prov - ORG | Expired | PARALLEL CACHELETS | 5/3/2001 | 60/287,707 | | | US |
| NIMXI-0661.00/US | Utility - ORG | Issued | PARALLEL CACHELETS | 6/27/2001 | 09/891,523 | 9/9/2008 | 7,424,576 | US |
| NIMXI-0661.01/US | Utility - CON | Issued | PARALLEL CACHELETS | 1/9/2006 | 11/327,454 | 5/8/2007 | 7,216,201 | US |
| NIMXI-0662.00/US | Utility - ORG | Issued | LOOK-AHEAD CARRY ADDER CIRCUIT | 12/18/2001 | 10/020,447 | 1/29/2008 | 7,325,025 | US |
| NIMXI-0663.00/US | Utility - ORG | Issued | SYMMETRIC CASCADED DOMING CARRY GENERATE CIRCUIT | 9/21/2001 | 09/956,903 | 9/23/2008 | 7,428,568 | US |
| NIMXI-0664.00/US | Utility - ORG | Issued | CASCADED DOMING FOUR-TO-TWO REDUCER CIRCUIT AND METHOD | 6/29/2001 | 09/893,868 | 6/24/2008 | 7,392,277 | US |
| NIMXI-0665.00/US | Utility - ORG | Abandoned | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE | 3/26/2001 | 09/817,711 | | | US |
| NIMXI-0666.00/US | Utility - ORG | Issued | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE | 3/30/2001 | 09/823,212 | 5/3/2005 | 6,889,237 | US |
| NIMXI-0667.00/US | Utility - ORG | Issued | ELECTRONIC ASSEMBLIES WITH HIGH CAPACITY CURVED FIN HEAT SINKS | 9/10/2001 | 09/950,100 | 12/30/2003 | 6,671,172 | US |
| NIMXI-0668.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR UTILIZING STATIC QUEUES IN PROCESSOR STAGING | 6/7/2001 | 09/878,062 | 10/14/2003 | 6,633,972 | US |
| NIMXI-0669.00/US | Utility - ORG | Issued | DUAL-TARGET BLOCK REGISTER ALLOCATION | 9/28/2001 | 09/968,278 | 12/11/2007 | 7,308,563 | US |
| NIMXI-0670.00/US | Utility - ORG | Issued | DATA-FLOW METHOD OF ANALYZING DEFINITIONS AND USES OF L VALUES IN PROGRAMS | 1/6/1999 | 09/226,804 | 4/9/2002 | 6,370,685 | US |
| NIMXI-0671.00/US | Utility - ORG | Issued | DATA-FLOW METHOD FOR OPTIMIZING EXCEPTION-HANDLING INSTRUCTIONS IN PROGRAMS | 4/19/2000 | 09/952,292 | 10/10/2006 | 7,120,904 | US |
| NIMXI-0672.00/US | Utility - ORG | Issued | TRENCH SIDEWALL PROFILE FOR DEVICE ISOLATION | 6/30/2001 | 09/896,532 | 2/4/2003 | 6,514,805 | US |
| NIMXI-0674.00/US | Utility - ORG | Issued | BARRIER MATERIAL ENCAPSULATION OF PROGRAMMABLE MATERIAL | 6/30/2001 | 09/896,530 | 11/4/2003 | 6,642,102 | US |
| NIMXI-0674.01/US | Utility - DIV | Issued | BARRIER MATERIAL ENCAPSULATION OF PROGRAMMABLE MATERIAL | 4/17/2003 | 10/418,548 | 6/20/2006 | 7,054,344 | US |
| NIMXI-0675.00/US | Utility - ORG | Issued | ADHESIVE MATERIAL FOR PROGRAMMABLE DEVICE | 6/30/2001 | 09/896,008 | 8/3/2004 | 6,770,531 | US |
| NIMXI-0676.00/US | Utility - ORG | Issued | REDUCED AREA INTERSECTION BETWEEN ELECTRODE AND PROGRAMMING ELEMENT | 6/30/2001 | 09/896,531 | 8/12/2003 | 6,605,527 | US |
| NIMXI-0676.01/US | Utility - CON | Issued | REDUCED AREA INTERSECTION BETWEEN ELECTRODE AND PROGRAMMING ELEMENT | 5/13/2003 | 10/438,146 | 8/11/2009 | 7,572,666 | US |
| NIMXI-0677.00/US | Utility - ORG | Abandoned | ASSEMBLING DISPLAY MODULES | 7/12/2001 | 09/904,269 | | | US |
| NIMXI-0678.00/US | Utility - ORG | Issued | PROVIDING OPTICAL ELEMENTS OVER EMISSIVE DISPLAYS | 7/12/2001 | 09/904,268 | 3/29/2005 | 6,873,380 | US |
| NIMXI-0678.01/US | Utility - DIV | Abandoned | PROVIDING OPTICAL ELEMENTS OVER EMISSIVE DISPLAYS | 1/11/2005 | 11/032,828 | | | US |
| NIMXI-0679.00/US | Utility - ORG | Issued | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE | 4/30/2001 | 09/846,609 | 4/20/2004 | 6,725,247 | US |

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REEL: 028362 FRAME: 0834

Exhibit A - US Applications and Issued Patents

| MTL Docket No. | Matter Type | Matter Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|------------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NIMXI-0680.00/US | Utility - ORG | Issued | SYSTEM FOR SHARING CHANNELS BY INTERLEAVING FLITS | 6/29/2001 | 09/894,136 | 2/3/2009 | 7,486,685 | US |
| NIMXI-0681.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR REORDERING MEMORY REQUESTS FOR PAGE COHERENCY | 5/24/2005 | 11/137,700 | 4/1/2008 | 7,353,349 | US |
| NIMXI-0682.00/US | Utility - CON | Issued | METHOD AND SYSTEM FOR USING INTERNAL FIFO RAM TO IMPROVE SYSTEM BOOT TIMES | 7/29/2005 | 11/192,025 | 7/8/2008 | 7,398,383 | US |
| NIMXI-0683.00/US | Utility - ORG | Issued | PULSE POSITION MODULATION WITH SPREAD SPECTRUM | 11/1/1993 | 08/146,490 | 8/5/1997 | 5,654,978 | US |
| NIMXI-0683.01/US | Utility - CIP | Issued | BEST-OF-M PULSE POSITION MODULATION DETECTOR | 6/6/1995 | 08/468,802 | 9/9/1997 | 5,666,379 | US |
| NIMXI-0684.00/US | Utility - CON | Issued | APPARATUS FOR RECEIVING AND CORRELATING A SPREAD SPECTRUM SIGNAL | 5/7/1999 | 09/307,646 | 11/13/2001 | 6,317,452 | US |
| NIMXI-0685.00/US | Utility - ORG | Issued | MOBILE STATION LOCATING SYSTEM AND METHOD | 7/15/1997 | 08/893,938 | 5/4/1999 | 5,901,358 | US |
| NIMXI-0685.01/US | Utility - CON | Issued | MOBILE STATION LOCATING SYSTEM AND METHOD | 3/31/1999 | 09/282,281 | 10/23/2001 | 6,308,073 | US |
| NIMXI-0686.00/US | Utility - ORG | Issued | COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER | 3/20/1997 | 08/823,234 | 10/26/1999 | 5,974,310 | US |
| NIMXI-0686.01/US | Utility - CON | Issued | COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER | 7/15/1999 | 09/354,926 | 7/3/2001 | 6,256,492 | US |
| NIMXI-0686.02/US | Utility - CON | Issued | COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER | 3/27/2001 | 09/819,460 | 6/15/2004 | 6,751,456 | US |
| NIMXI-0687.00/US | Utility - ORG | Issued | SPREAD SPECTRUM CODES FOR USE IN COMMUNICATION | 3/20/1997 | 08/821,611 | 8/28/2001 | 6,282,228 | US |
| NIMXI-0688.00/US | Utility - ORG | Issued | BYPASSABLE AMPLIFIER | 5/18/1998 | 09/080,812 | 10/24/2000 | 6,137,354 | US |
| NIMXI-0689.00/US | Utility - ORG | Issued | MOUNTING BRACKET FOR PCS AND OTHER ANTENNAS | 1/14/2000 | 09/485,215 | 3/26/2002 | 6,361,007 | US |
| NIMXI-0690.00/US | Utility - ORG | Issued | COMMON BOOT ENVIRONMENT FOR A MODULAR SERVER SYSTEM | 11/20/2001 | 10/086,410 | 6/7/2005 | 6,904,482 | US |
| NIMXI-0691.00/US | Utility - ORG | Issued | SUCCESSIVE TEMPLATE GENERATION USING MINIMAL RANDOM ACCESS MEMORY BANDWIDTH | 6/28/2001 | 09/894,755 | 7/22/2003 | 6,598,133 | US |
| NIMXI-0692.00/US | Utility - ORG | Issued | METHOD AND APPARATUS TO IMPROVE THE PROTECTION OF INFORMATION PRESENTED BY A COMPUTER | 6/29/2001 | 09/895,738 | 7/4/2006 | 7,073,070 | US |
| NIMXI-0693.00/US | Utility - ORG | Issued | MECHANISM TO IMPROVE AUTHENTICATION FOR REMOTE MANAGEMENT OF A COMPUTER SYSTEM | 10/30/2001 | 10/002,337 | 8/15/2006 | 7,093,124 | US |
| NIMXI-0693.01/US | Utility - DIV | Abandoned | MECHANISM TO IMPROVE AUTHENTICATION FOR REMOTE MANAGEMENT OF A COMPUTER SYSTEM | 7/25/2005 | 11/188,420 | | | US |
| NIMXI-0694.00/US | Utility - ORG | Issued | VARIABLE LEVEL MEMORY | 9/18/2001 | 09/955,282 | 11/4/2003 | 6,643,169 | US |
| NIMXI-0694.01/US | Utility - CON | Issued | VARIABLE LEVEL MEMORY | 9/18/2003 | 10/666,988 | 3/22/2005 | 6,870,767 | US |
| NIMXI-0695.00/US | Utility - ORG | Issued | MEMBRANE-ACTUATED CHARGE CONTROLLED MIRROR (CCM) PROJECTION DISPLAY | 12/9/1998 | 09/208,041 | 2/29/2000 | 6,031,657 | US |
| NIMXI-0696.00/US | Utility - ORG | Issued | BEAM-ADDRESSED MICROMIRROR DIRECT VIEW DISPLAY | 10/28/1998 | 09/179,933 | 2/29/2000 | 6,031,656 | US |
| NIMXI-0697.00/US | Utility - ORG | Issued | CHARGE CONTROLLED MIRROR WITH IMPROVED FRAME TIME UTILIZATION AND METHOD OF ADDRESSING THE SAME | 10/15/1998 | 09/172,615 | 2/22/2000 | 6,028,696 | US |
| NIMXI-0698.00/US | Utility - CON | Issued | SAWC PHASE DETECTION METHOD AND APPARATUS | 11/4/1992 | 07/971,689 | 1/4/1994 | 5,276,704 | US |
| NIMXI-0699.00/US | Utility - ORG | Issued | PCB-TO-CHASSIS MOUNTING SCHEMES | 8/21/2001 | 09/934,871 | 12/10/2002 | 6,493,233 | US |
| NIMXI-0700.00/US | Utility - CIP | Abandoned | METHOD AND APPARATUS TO EMULATE EXTERNAL I/O INTERCONNECTION | 10/16/2001 | 09/982,242 | | | US |
| NIMXI-0701.00/US | Utility - DIV | Abandoned | METHOD AND APPARATUS FOR ACCESSING THREAD-PRIVATE GLOBAL STORAGE OBJECTS | 5/19/2006 | 11/437,352 | | | US |
| NIMXI-0702.00/US | Utility - ORG | Issued | REPAIRABLE TILED DISPLAYS | 7/16/2001 | 09/906,342 | 10/2/2007 | 7,277,066 | US |
| NIMXI-0702.01/US | Utility - CON | Abandoned | REPAIRABLE TILED DISPLAYS | 1/18/2006 | 11/334,729 | | | US |
| NIMXI-0703.00/US | Utility - ORG | Abandoned | PARAMETRIC REPRESENTATION METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN | 12/3/2002 | 10/309,472 | | | US |

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REEL: 028362 FRAME: 0835

Exhibit A - US Applications and Issued Patents

| NIM Patent No. | Matter Type | Market Status | Title | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|------------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NIMX1-0704.00/US | Utility - ORG | Issued | AUTOMATIC SYMBOLIC INDEXING METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN | 12/3/2002 | 10/309,529 | 12/18/2007 | 7,310,790 | US |
| NIMX1-0705.00/US | Utility - ORG | Issued | DIGITAL UPDATE SCHEME FOR ADAPTIVE IMPEDANCE CONTROL OF ON-DIE INPUT/OUTPUT CIRCUITS | 9/28/2001 | 09/965,627 | 6/3/2003 | 6,573,747 | US |
| NIMX1-0705.00/US | Utility - ORG | Issued | STOPPING REPLAY TORNADOES | 12/31/2001 | 10/089,588 | 10/4/2005 | 6,952,764 | US |
| NIMX1-0707.00/US | Utility - ORG | Issued | WRITE-ONCE POLYMER MEMORY WITH E-BEAM WRITING AND READING | 9/27/2001 | 09/966,014 | 9/23/2003 | 6,525,052 | US |
| NIMX1-0707.01/US | Utility - DIV | Issued | WRITE-ONCE POLYMER MEMORY WITH E-BEAM WRITING AND READING | 10/28/2002 | 10/282,534 | 11/4/2003 | 6,643,161 | US |
| NIMX1-0708.00/US | Utility - ORG | Issued | GLOBAL I/O TIMING ADJUSTMENT USING CALIBRATED DELAY ELEMENTS | 9/28/2001 | 09/965,223 | 3/27/2007 | 7,197,659 | US |
| NIMX1-0709.00/US | Utility - ORG | Issued | SLURRY FOR POLISHING A BARRIER LAYER | 11/30/2001 | 10/002,855 | 4/13/2004 | 6,719,920 | US |
| NIMX1-0710.00/US | Utility - ORG | Issued | ELECTRONIC ASSEMBLIES WITH FILLED NO-FLOW UNDERFILL | 10/26/2001 | 10/003,238 | 1/29/2008 | 7,325,360 | US |
| NIMX1-0711.00/US | Utility - ORG | Issued | A ZERO INSERTION FORCE CONNECTOR FOR SUBSTRATES WITH EDGE CONTACTS | 9/28/2001 | 09/967,060 | 1/6/2004 | 6,672,891 | US |
| NIMX1-0712.00/US | Utility - ORG | Issued | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS | 2/24/1998 | 09/028,367 | 11/2/1999 | 5,978,591 | US |
| NIMX1-0712.01/US | Utility - CIP | Issued | A WALLET DETACHABLY RETAINING A PERSONAL INFORMATION DEVICE WHICH SUPPLIES A WAKE SIGNAL TO THE WALLET TO TURN ON AN INPUT DEVICE OF THE WALLET | 11/11/1998 | 09/189,572 | 4/24/2001 | 6,223,233 | US |
| NIMX1-0712.02/US | Utility - CIP | Issued | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS | 8/5/1999 | 09/968,878 | 3/13/2001 | 6,202,209 | US |
| NIMX1-0712.03/US | Utility - CON | Issued | WALLET DETACHABLY RETAINING A PERSONAL INFORMATION DEVICE AND RECEIVING A WAKE SIGNAL AND A STAY AWAKE SIGNAL FROM THE PERSONAL INFORMATION DEVICE | 4/12/2001 | 09/834,167 | 1/18/2005 | 6,845,455 | US |
| NIMX1-0713.00/US | Utility - CON | Issued | RECOVERY OF BIT-ROTATED FRAMES DURING FACSIMILE TRANSMISSIONS IN A GLOBAL SYSTEM FOR MOBILE COMMUNICATIONS (GSM) NETWORK | 7/19/2002 | 10/199,382 | 5/17/2005 | 6,894,801 | US |
| NIMX1-0714.00/US | Utility - ORG | Abandoned | PROTOCOL DRIVER APPLICATION PROGRAMMING INTERFACE FOR OPERATING SYSTEMS | 1/31/2002 | 10/062,863 | | | US |
| NIMX1-0715.00/US | Utility - ORG | Issued | LOCKED CONTENT ADDRESSABLE MEMORY FOR EFFICIENT ACCESS | 1/9/2002 | 10/042,905 | 8/17/2004 | 6,779,089 | US |
| NIMX1-0716.00/US | Utility - ORG | Issued | METHODS AND APPARATUS USING A FIBER-FLEXURE INDUCTION SOLDERING STATION | 11/25/2002 | 10/304,195 | 3/20/2007 | 7,194,182 | US |
| NIMX1-0717.00/US | Utility - ORG | Issued | PROPERTY MANAGEMENT SYSTEM PROTOCOL AUTO-DETECTION | 1/3/2002 | 10/038,917 | 5/18/2007 | 7,272,211 | US |
| NIMX1-0718.00/US | Utility - ORG | Issued | INTELLIGENT FORWARDED TELEPHONE CALL HANDLING WITH A CALL ANSWERING SYSTEM | 9/28/2001 | 09/967,108 | 2/27/2007 | 7,184,536 | US |
| NIMX1-0719.00/US | Utility - ORG | Issued | DIGITAL FRAME DETERMINATION METHOD AND APPARATUS | 1/4/2002 | 10/040,639 | 10/12/2004 | 6,804,317 | US |
| NIMX1-0720.00/US | Utility - ORG | Issued | SOFTWARE CALL CONTROL AGENT | 9/28/2001 | 09/967,367 | 2/8/2005 | 6,853,720 | US |
| NIMX1-0721.00/US | Utility - ORG | Issued | PROBABALISTIC NETWORKS FOR DETECTING SIGNAL CONTENT | 9/25/2001 | 09/963,177 | 11/14/2006 | 7,136,813 | US |
| NIMX1-0722.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR MODIFYING GRAPHICS CONTENT PRIOR TO DISPLAY FOR COLOR BLIND USE | 11/21/2001 | 09/991,629 | 8/16/2005 | 6,991,151 | US |

PATENT

Exhibit A - US Applications and Issued Patents

| App No | Matter Type | Matter Status | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|-----------|---------|
| NMXI-0723.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR SOFTWARE SELECTION OF PROTECTED REGISTER SETTINGS | 11/14/2001 | 09/993,128 | 5/7/2005 | 5,904,504 | US |
| NMXI-0724.00/US | Utility - ORG | Issued | SYNCHRONIZING DATA OR SIGNAL TRANSFER ACROSS Clocked LOGIC DOMAINS | 1/2/2002 | 10/038,956 | 1/9/2007 | 7,161,999 | US |
| NMXI-0725.00/US | Utility - ORG | Issued | FLASH MEMORY PROGRAM AND ERASE OPERATIONS | 11/13/2001 | 09/993,336 | 8/30/2005 | 6,997,948 | US |
| NMXI-0726.00/US | Utility - ORG | Issued | CHARGE PUMP RIPPLE REDUCTION | 1/2/2002 | 10/038,499 | 8/12/2003 | 6,605,984 | US |
| NMXI-0726.01/US | Utility - CIP | Issued | CHARGE PUMP RIPPLE REDUCTION | 6/27/2002 | 10/185,419 | 12/28/2004 | 6,836,176 | US |
| NMXI-0727.00/US | Utility - ORG | Issued | VOLTAGE DRIVER FOR A MEMORY | 8/31/2001 | 09/945,021 | 9/10/2002 | 6,449,211 | US |
| NMXI-0728.00/US | Utility - ORG | Issued | PHASE CHANGE MATERIAL MEMORY DEVICE | 9/7/2001 | 09/948,890 | 7/1/2003 | 6,586,761 | US |
| NMXI-0728.01/US | Utility - DIV | Issued | PHASE CHANGE MATERIAL MEMORY DEVICE | 4/29/2003 | 10/426,380 | 5/21/2005 | 6,908,812 | US |
| NMXI-0729.00/US | Utility - ORG | Issued | REDUCING SHUNTS IN MEMORIES WITH PHASE-CHANGE MATERIAL | 9/17/2001 | 09/953,833 | 3/1/2005 | 6,861,267 | US |
| NMXI-0729.01/US | Utility - DIV | Issued | REDUCING SHUNTS IN MEMORIES WITH PHASE-CHANGE MATERIAL | 1/19/2005 | 11/038,336 | 1/9/2007 | 7,161,225 | US |
| NMXI-0730.00/US | Utility - ORG | Expired | DOUBLE DENSITY BACKWARD AND FORWARD COMPATIBLE CARD EDGE CONNECTOR SYSTEM | 8/14/1990 | 07/567,123 | 10/29/1991 | 5,061,190 | US |
| NMXI-0731.00/US | Utility - ORG | Issued | MULTIPLE LAYER PHASE-CHANGE MEMORY | 8/31/2001 | 09/945,331 | 1/14/2003 | 6,507,061 | US |
| NMXI-0731.01/US | Utility - DIV | Issued | MULTIPLE LAYER PHASE-CHANGE MEMORY | 11/21/2002 | 10/302,421 | 2/14/2006 | 6,988,269 | US |
| NMXI-0731.02/US | Utility - CON | Issued | MULTIPLE LAYER PHASE-CHANGE MEMORY | 12/4/2002 | 10/309,633 | 1/6/2004 | 6,674,115 | US |
| NMXI-0732.00/US | Utility - ORG | Issued | SIGNAL TO NOISE RATIO OPTIMIZATION FOR VIDEO COMPRESSION BIT-RATE CONTROL | 9/5/2001 | 09/947,331 | 5/10/2005 | 6,891,889 | US |
| NMXI-0733.00/US | Utility - ORG | Issued | EMBEDDED PCB IDENTIFICATION | 9/11/2001 | 09/954,402 | 1/8/2003 | 7,317,310 | US |
| NMXI-0734.00/US | Utility - ORG | Issued | OPTOELECTRICAL PACKAGE | 3/28/2002 | 10/109,313 | 8/26/2008 | 7,418,163 | US |
| NMXI-0735.00/US | Utility - ORG | Issued | ULTRA-THIN POLARIZATION MODE CONVERTERS BASED ON LIQUID CRYSTAL MATERIALS | 10/7/2002 | 10/265,873 | 8/9/2005 | 6,928,200 | US |
| NMXI-0735.01/US | Utility - CON | Issued | ULTRA-THIN POLARIZATION MODE CONVERTERS BASED ON LIQUID CRYSTAL MATERIALS | 7/26/2005 | 11/189,543 | 2/6/2007 | 7,174,060 | US |
| NMXI-0736.00/US | Utility - CIP | Issued | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING | 3/23/2002 | 10/109,811 | 11/23/2004 | 6,822,535 | US |
| NMXI-0736.01/US | Utility - DIV | Issued | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING | 9/16/2004 | 10/942,147 | 12/26/2006 | 7,154,358 | US |
| NMXI-0737.00/US | Utility - ORG | Issued | 1 X N FANOUT WAVEGUIDE PHOTODETECTOR | 12/7/2001 | 10/008,922 | 2/15/2005 | 6,856,733 | US |
| NMXI-0737.01/US | Utility - DIV | Issued | 1 X N FANOUT WAVEGUIDE PHOTODETECTOR | 1/25/2005 | 11/042,537 | 6/19/2007 | 7,233,725 | US |
| NMXI-0738.00/US | Utility - DIV | Issued | METHOD FOR REDUCING SOCKET WARPAGE | 1/15/2004 | 10/756,055 | 4/1/2008 | 7,350,299 | US |
| NMXI-0738.01/US | Utility - DIV | Abandoned | SOCKET WARPAGE REDUCTION APPARATUS | 1/31/2008 | 12/023,521 | | | US |
| NMXI-0739.00/US | Utility - ORG | Issued | EXTENSION MECHANISM AND METHOD FOR ASSEMBLING OVERHANGING COMPONENTS | 9/28/2001 | 09/964,747 | 10/5/2004 | 6,801,436 | US |
| NMXI-0739.01/US | Utility - DIV | Issued | EXTENSION MECHANISM AND METHOD FOR ASSEMBLING OVERHANGING COMPONENTS | 8/12/2004 | 10/916,061 | 7/12/2005 | 6,917,524 | US |
| NMXI-0740.00/US | Utility - ORG | Issued | METHOD AND STRUCTURE FOR IDENTIFYING LEAD-FREE SOLDER | 9/28/2001 | 09/964,746 | 8/7/2007 | 7,251,880 | US |
| NMXI-0742.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR SYSTEM MANAGEMENT APPLICATIONS USING A LOCAL CONTROLLER | 3/22/2002 | 10/104,341 | 9/19/2006 | 7,111,103 | US |
| NMXI-0742.01/US | Utility - DIV | Issued | METHOD AND APPARATUS FOR GENERATING TRAFFIC IN AN ELECTRONIC BRIDGE VIA A LOCAL CONTROLLER | 8/3/2006 | 11/462,264 | 3/18/2008 | 7,346,725 | US |
| NMXI-0743.00/US | Utility - ORG | Issued | SYSTEM AND METHOD TO SYNCHRONIZE TWO OR MORE OBJECT MANAGEMENT SYSTEMS | 1/23/2002 | 10/055,529 | 7/5/2005 | 6,915,522 | US |
| NMXI-0744.00/US | Utility - ORG | Issued | DIELECTRIC MATERIAL TREATMENT | 9/23/2002 | 10/252,295 | 5/13/2004 | 6,734,118 | US |
| NMXI-0745.00/US | Utility - ORG | Issued | MEMORY SYSTEM ORGANIZED INTO BLOCKS OF DIFFERENT SIZES AND ALLOCATION METHOD THEREFOR | 9/17/2001 | 09/953,356 | 2/1/2005 | 6,851,017 | US |

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Exhibit A - US Applications and Issued Patents

| WIPO Class | Market Type | Market Status | TIBS | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|-----------|---------|
| NMXI-0746.00/US | Utility - ORG | Issued | SCALABLE RECEIVER STRUCTURE FOR EFFICIENT BIT SEQUENCE DECODING | 6/30/1997 | 08/885,803 | 5/30/2000 | 6,069,928 | US |
| NMXI-0747.00/PR | Prov - ORG | Expired | DIGITAL IMPAIRMENT LEARNING SEQUENCE | 6/24/1999 | 60/140,705 | | | US |
| NMXI-0748.00/US | Utility - ORG | Issued | PAID AND CODEC DETECTION | 6/22/2000 | 09/598,994 | 4/20/2004 | 6,724,814 | US |
| NMXI-0749.00/US | Utility - DIV | Issued | DEVICE AND METHOD FOR MAXIMIZING PERFORMANCE ON A MEMORY INTERFACE WITH A VARIABLE NUMBER OF CHANNELS | 5/24/2005 | 11/137,314 | 4/21/2009 | 7,523,230 | US |
| NMXI-0750.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR HEADER UPDATING | 1/7/2002 | 10/041,543 | 9/22/2009 | 7,583,318 | US |
| NMXI-0751.00/US | Utility - COM | Issued | METHOD AND APPARATUS FOR ANALOG COMPENSATION OF DRIVER OUTPUT SIGNAL SLEW RATE AGAINST DEVICE IMPEDANCE VARIATION | 3/31/2004 | 10/814,398 | 4/22/2008 | 7,363,595 | US |
| NMXI-0752.00/US | Utility - ORG | Issued | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER | 4/16/2002 | 10/126,274 | 2/14/2006 | 7,000,126 | US |
| NMXI-0753.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES | 12/19/2001 | 10/028,472 | 6/10/2003 | 6,577,274 | US |
| NMXI-0753.01/US | Utility - DIV | Issued | METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES | 4/16/2003 | 10/417,540 | 6/8/2004 | 6,747,598 | US |
| NMXI-0754.00/US | Utility - ORG | Issued | PRACTICAL CODING AND METRIC CALCULATION FOR THE LATTICE INTERFERED CHANNEL | 8/7/2002 | 10/214,630 | 4/25/2006 | 7,036,071 | US |
| NMXI-0755.00/US | Utility - ORG | Issued | SEARCH RECEIVER USING ADAPTIVE DETECTION THRESHOLDS | 5/23/2002 | 10/154,948 | 9/16/2008 | 7,436,392 | US |
| NMXI-0756.00/US | Utility - ORG | Issued | RECEIVING CALLER IDENTIFICATION INFORMATION WITH A TELECOMMUNICATIONS DEVICE FOR THE DEAF | 1/3/2002 | 10/098,411 | 5/6/2003 | 6,560,317 | US |
| NMXI-0757.00/US | Utility - ORG | Issued | PROVIDING A CHAIN OF TOKENIZED ERROR AND STATE INFORMATION FOR A CALL STACK | 4/29/2002 | 10/133,625 | 7/3/2007 | 7,240,247 | US |
| NMXI-0758.00/US | Utility - ORG | Issued | PROGRAMMABLE FRAME SPLITTER | 1/10/2002 | 10/044,766 | 3/3/2006 | 6,982,988 | US |
| NMXI-0759.00/US | Utility - ORG | Issued | INVERSE MULTIPLEXING OF UNMANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK | 2/21/2002 | 10/090,093 | 7/4/2006 | 7,072,352 | US |
| NMXI-0760.00/US | Utility - ORG | Issued | METHOD FOR PACKAGING A TAPE SUBSTRATE | 8/26/2002 | 10/228,617 | 3/17/2009 | 7,503,155 | US |
| NMXI-0760.01/US | Utility - DIV | Abandoned | METHOD FOR PACKAGING A TAPE SUBSTRATE | 1/29/2009 | 12/362,445 | | | US |
| NMXI-0761.00/US | Utility - ORG | Issued | COAXIAL RADIO FREQUENCY ADAPTER AND METHOD | 12/10/2001 | 10/007,672 | 1/18/2005 | 6,844,738 | US |
| NMXI-0761.01/US | Utility - DIV | Issued | COAXIAL RADIO FREQUENCY ADAPTER AND METHOD | 8/20/2003 | 10/645,229 | 3/8/2005 | 6,863,564 | US |
| NMXI-0761.02/US | Utility - DIV | Issued | COAXIAL RADIO FREQUENCY ADAPTER AND METHOD | 4/7/2004 | 10/820,693 | 9/26/2006 | 7,112,970 | US |
| NMXI-0762.00/US | Utility - ORG | Issued | DEFERRED PROCEDURE CALL IN INTERFACE DESCRIPTION LANGUAGE | 11/28/2001 | 09/997,669 | 8/23/2005 | 6,934,953 | US |
| NMXI-0763.00/US | Utility - ORG | Issued | MANUFACTURING INTEGRATED CIRCUITS AND TESTING ON-DIE POWER SUPPLIES USING DISTRIBUTED PROGRAMMABLE DIGITAL CURRENT SINKS | 3/12/2002 | 10/097,136 | 5/1/2007 | 7,212,021 | US |
| NMXI-0764.00/US | Utility - ORG | Issued | UNDERFILL PROCESS FOR FLIP-CHIP DEVICE | 12/21/2001 | 10/032,115 | 3/9/2004 | 6,703,299 | US |
| NMXI-0764.01/US | Utility - DIV | Abandoned | UNDERFILL PROCESS FOR FLIP-CHIP DEVICE | 2/9/2004 | 10/774,869 | | | US |
| NMXI-0765.00/US | Utility - ORG | Issued | ELECTRONIC PACKAGES AND COMPONENTS THEREOF FORMED BY SUBSTRATE-IMPRESSING | 12/18/2002 | 10/322,502 | 5/13/2008 | 7,371,975 | US |
| NMXI-0765.00/US | Utility - ORG | Issued | MULTI-DIE SEMICONDUCTOR PACKAGE | 8/22/2002 | 10/226,070 | 8/9/2005 | 6,927,497 | US |
| NMXI-0765.01/US | Utility - DIV | Issued | METHOD OF FORMING A MULTI-DIE SEMICONDUCTOR PACKAGE | 5/27/2005 | 11/167,495 | 3/3/2009 | 7,498,201 | US |
| NMXI-0767.00/US | Utility - ORG | Issued | ELECTROSTATIC DISCHARGE PROTECTION UNIT INCLUDING EQUALIZATION | 12/23/2002 | 10/329,058 | 5/15/2007 | 7,218,491 | US |
| NMXI-0768.00/US | Utility - ORG | Issued | TEST INTERFACE, SYSTEM, AND METHOD FOR TESTING COMMUNICATIONS DEVICES WITH NON-DETERMINISTIC LATENCY | 7/22/2002 | 10/202,624 | 8/26/2008 | 7,418,639 | US |
| NMXI-0769.00/US | Utility - ORG | Abandoned | USING MULTIPLE SIMULATION ENVIRONMENTS | 5/23/2003 | 10/445,272 | | | US |

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Exhibit A - US Applications and Issued Patents

| App No | MatType | MatStat | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|-----------|---|------------|------------|------------|-----------|---------|
| NMXI-0770.00/US | Utility - ORG | Issued | REFRESHING MEMORY CELLS OF A PHASE CHANGE MATERIAL MEMORY DEVICE | 8/5/2002 | 10/232,630 | 7/27/2004 | 6,768,665 | US |
| NMXI-0771.00/US | Utility - ORG | Issued | TUNABLE OPTICAL DISPERSION COMPENSATOR | 4/17/2002 | 10/426,106 | 8/17/2004 | 6,776,493 | US |
| NMXI-0772.00/US | Utility - CON | Issued | METHOD TO REALIZE FAST SILICON-ON-INSULATOR (SOI) OPTICAL DEVICE | 4/25/2006 | 11/411,059 | 11/7/2006 | 7,133,586 | US |
| NMXI-0773.00/US | Utility - DIV | Issued | METHOD TO REALIZE FAST SILICON-ON-INSULATOR (SOI) OPTICAL DEVICE | 4/25/2006 | 11/410,591 | 11/7/2006 | 7,133,585 | US |
| NMXI-0773.00/US | Utility - CON | Issued | METHOD AND APPARATUS FOR WAFER LEVEL TESTING OF INTEGRATED OPTICAL WAVEGUIDE CIRCUITS | 11/19/2004 | 10/994,021 | 6/10/2008 | 7,386,197 | US |
| NMXI-0774.00/US | Utility - ORG | Abandoned | BIT ERROR RATE TEST SYSTEM FOR MULTI-SOURCE AGREEMENT COMPLIANT TRANSCEIVERS | 12/20/2001 | 10/027,053 | | | US |
| NMXI-0775.00/US | Utility - ORG | Issued | ON-CHIP JITTER TESTING | 12/26/2002 | 10/331,112 | 8/5/2008 | 7,409,621 | US |
| NMXI-0775.00/US | Utility - ORG | Issued | TESTING METHODOLOGY AND APPARATUS FOR INTERCONNECTS | 12/16/2002 | 10/319,517 | 5/16/2006 | 7,047,453 | US |
| NMXI-0777.00/US | Utility - ORG | Abandoned | LCD CONTROLLER ARCHITECTURE FOR HANDLING FLUCTUATING BANDWIDTH CONDITIONS | 1/31/2002 | 10/062,268 | | | US |
| NMXI-0778.00/US | Utility - ORG | Issued | INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK | 2/21/2002 | 10/080,329 | 10/30/2007 | 7,289,525 | US |
| NMXI-0779.00/US | Utility - ORG | Issued | WAFER-LEVEL TEST STRUCTURE FOR EDGE-EMITTING SEMICONDUCTOR LASERS | 4/18/2002 | 10/126,348 | 2/20/2007 | 7,180,929 | US |
| NMXI-0779.00/US | Utility - DIV | Issued | WAFER-LEVEL TEST STRUCTURE FOR EDGE-EMITTING SEMICONDUCTOR LASERS | 9/11/2003 | 10/659,896 | 10/17/2006 | 7,122,391 | US |
| NMXI-0780.00/US | Utility - ORG | Issued | SMALLER AND LOWER POWER STATIC MUX CIRCUITRY IN GENERATING MULTIPLIER PARTIAL PRODUCT SIGNALS | 12/5/2003 | 10/728,395 | 12/11/2007 | 7,308,470 | US |
| NMXI-0781.00/US | Utility - ORG | Issued | ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMER CIRCUIT | 4/24/2002 | 10/128,979 | 3/4/2008 | 7,359,770 | US |
| NMXI-0782.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR GENERATING GRAY CODE FOR ANY EVEN COUNT VALUE TO ENABLE EFFICIENT POINTER EXCHANGE MECHANISMS IN ASYNCHRONOUS FIFO'S | 6/28/2002 | 10/186,564 | 10/5/2004 | 6,801,143 | US |
| NMXI-0783.00/US | Utility - ORG | Issued | RATE POLICING ALGORITHM FOR PACKET FLOWS | 5/7/2002 | 10/137,793 | 12/26/2006 | 7,154,853 | US |
| NMXI-0784.00/US | Utility - ORG | Issued | FLIP-CHIP PACKAGE INTEGRATING OPTICAL AND ELECTRICAL DEVICES AND COUPLING TO A WAVEGUIDE ON A BOARD | 5/30/2002 | 10/158,250 | 6/22/2004 | 6,754,407 | US |
| NMXI-0784.00/US | Utility - CON | Issued | FLIP-CHIP PACKAGE INTEGRATING OPTICAL AND ELECTRICAL DEVICES AND COUPLING TO A WAVEGUIDE ON A BOARD | 5/6/2004 | 10/840,017 | 5/23/2006 | 7,049,704 | US |
| NMXI-0785.00/US | Utility - ORG | Issued | APPARATUS AND METHODS FOR INTERCONNECTING COMPONENTS TO VIA-IN-PAD INTERCONNECTS | 10/8/2002 | 10/267,158 | 12/7/2004 | 6,828,512 | US |
| NMXI-0787.00/US | Utility - ORG | Issued | DYNAMIC COMPUTATION OF CHIPSET-SUPPORTED ACCELERATED GRAPHICS PORT APERTURE SIZES | 3/21/2002 | 10/104,042 | 8/23/2005 | 6,933,944 | US |
| NMXI-0787.00/US | Utility - CON | Issued | DYNAMIC COMPUTATION OF CHIPSET-SUPPORTED ACCELERATED GRAPHICS PORT APERTURE SIZES | 6/29/2005 | 11/172,342 | 2/7/2007 | 7,184,050 | US |
| NMXI-0788.00/US | Utility - ORG | Issued | SLAVE I/O DRIVER CALIBRATION USING ERROR-NULLING MASTER REFERENCE | 8/29/2002 | 10/231,963 | 3/20/2007 | 7,194,559 | US |
| NMXI-0789.00/US | Utility - ORG | Abandoned | LOW LATENCY MEMORY ACCESS METHOD USING UNIFIED QUEUE MECHANISM | 9/23/2002 | 10/253,229 | | | US |
| NMXI-0790.00/US | Utility - ORG | Issued | UPDATING STACK POINTER BASED ON INSTRUCTION BIT INDICATOR WITHOUT EXECUTING AN UPDATE MICROINSTRUCTION | 9/30/2002 | 10/262,214 | 7/18/2006 | 7,080,236 | US |
| NMXI-0791.00/US | Utility - ORG | Issued | CODE-BASED OPTICAL NETWORKS, METHODS, AND APPARATUS | 11/17/1999 | 09/454,164 | 6/20/2006 | 7,065,298 | US |

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REEL: 028362 FRAME: 0839

Exhibit A - US Applications and Issued Patents

| MTL-DeskNo | MatterType | MatterStatus | Title | FileDate | SerialNo | IssueDate | PatentNo | Country |
|-----------------|---------------|--------------|---|------------|------------|------------|-----------|---------|
| NMXI-0792.00/US | Utility - ORG | Issued | METHOD, SYSTEM, AND PROGRAM FOR MEMORY BASED DATA TRANSFER | 7/24/2002 | 10/205,546 | 10/19/2004 | 6,807,600 | US |
| NMXI-0793.00/PR | Prov - ORG | Expired | OPTICAL PHASE AND AMPLITUDE MEASUREMENT DEVICE | 4/10/2001 | 60/282,961 | | | US |
| NMXI-0794.00/US | Utility - DIV | Abandoned | SEGMENTED COMPLEX DIFFRACTION GRATINGS | 6/3/2005 | 11/145,251 | | | US |
| NMXI-0794.01/US | Utility - DIV | Abandoned | SEGMENTED COMPLEX DIFFRACTION GRATINGS | 6/3/2005 | 11/144,583 | | | US |
| NMXI-0794.02/US | Utility - DIV | Abandoned | SEGMENTED COMPLEX DIFFRACTION GRATINGS | 6/3/2005 | 11/145,291 | | | US |
| NMXI-0795.00/US | Utility - ORG | Issued | PHASE-CONTROLLED FIBER BRAGG GRATINGS AND MANUFACTURING METHODS | 5/9/2002 | 10/142,329 | 6/7/2005 | 6,904,201 | US |
| NMXI-0796.00/US | Utility - ORG | Issued | CODE-SWITCHED OPTICAL NETWORK | 11/13/2000 | 09/712,822 | 3/8/2005 | 6,885,344 | US |
| NMXI-0797.00/US | Utility - ORG | Issued | DYNAMIC POWER LEVEL CONTROL BASED ON A BOARD LATCH STATE | 8/1/2002 | 10/211,465 | 9/27/2005 | 6,950,952 | US |
| NMXI-0798.00/US | Utility - ORG | Issued | CONFIGURABLE MULTI-PORT MULTI-PROTOCOL NETWORK INTERFACE TO SUPPORT PACKET PROCESSING | 7/9/2002 | 10/190,851 | 1/9/2007 | 7,162,564 | US |
| NMXI-0799.00/US | Utility - ORG | Abandoned | TELEPHONE CONFERENCE BRIDGE PROVIDED VIA A PLURALITY OF COMPUTER TELEPHONY RESOURCE ALGORITHMS | 3/29/2002 | 10/112,194 | | | US |
| NMXI-0800.00/US | Utility - ORG | Issued | DIGITAL CAMERA WITH ISO PICKUP SENSITIVITY ADJUSTMENT | 2/1/2002 | 10/061,907 | 11/25/2008 | 7,456,868 | US |
| NMXI-0801.00/US | Utility - ORG | Issued | MEASURING OPTICAL PHASE | 4/10/2002 | 10/119,501 | 7/20/2004 | 6,765,681 | US |
| NMXI-0802.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR INDEPENDENT CONTROL OF DEVICES UNDER TEST CONNECTED IN PARALLEL | 10/24/2002 | 10/280,458 | 6/12/2007 | 7,231,552 | US |
| NMXI-0803.00/US | Utility - ORG | Issued | SLOW MOTION DETECTION SYSTEM | 2/14/2002 | 10/076,817 | 2/20/2007 | 7,180,429 | US |
| NMXI-0804.00/US | Utility - ORG | Issued | ACCURATELY TUNING RESISTORS | 4/19/2002 | 10/126,402 | 3/1/2005 | 6,882,714 | US |
| NMXI-0805.00/US | Utility - ORG | Abandoned | COMPACTING CIRCUIT RESPONSES | 3/27/2002 | 10/107,628 | 2/27/2007 | 7,185,253 | US |
| NMXI-0805.01/US | Utility - CON | Issued | COMPACTING CIRCUIT RESPONSES | 10/17/2005 | 11/251,664 | 10/12/2010 | 7,814,385 | US |
| NMXI-0806.00/US | Utility - ORG | Issued | METHODS AND APPARATUS FOR DETERMINING A FLOATING-POINT EXPONENT ASSOCIATED WITH AN UNDERFLOW CONDITION OR AN OVERFLOW CONDITION | 4/8/2002 | 10/118,349 | 9/18/2007 | 7,272,623 | US |
| NMXI-0807.00/US | Utility - ORG | Issued | CLUSTER BASED REDUNDANCY SCHEME FOR SEMICONDUCTOR MEMORIES | 7/16/2002 | 10/196,401 | 6/21/2005 | 6,909,645 | US |
| NMXI-0808.00/US | Utility - ORG | Issued | EXECUTING APPLICATIONS FROM A SEMICONDUCTOR NONVOLATILE MEMORY | 8/8/2002 | 10/215,549 | 3/8/2011 | 7,904,897 | US |
| NMXI-0808.01/US | Utility - CON | Pending | EXECUTING APPLICATIONS FROM A SEMICONDUCTOR NONVOLATILE MEMORY | 3/7/2011 | 13/041,541 | | | US |
| NMXI-0809.00/US | Utility - ORG | Issued | FORMING FERROELECTRIC POLYMER MEMORIES | 5/31/2002 | 10/160,641 | 6/1/2010 | 7,727,777 | US |
| NMXI-0810.00/US | Utility - ORG | Issued | IN-SITU SEQUENTIAL HIGH DENSITY PLASMA DEPOSITION AND ETCH PROCESSING FOR GAP FILL | 9/16/2002 | 10/236,203 | 5/29/2007 | 7,223,701 | US |
| NMXI-0811.00/US | Utility - ORG | Issued | PROMOTING ADHESION OF FLUOROPOLYMER FILMS TO SEMICONDUCTOR SUBSTRATES | 4/24/2002 | 10/131,017 | 3/2/2004 | 6,699,798 | US |
| NMXI-0812.00/US | Utility - ORG | Abandoned | DATA LINK ANALYZER | 9/10/2002 | 10/238,669 | | | US |
| NMXI-0813.00/US | Utility - ORG | Issued | SYSTEM AND METHOD TO ANALYZE VLSI DESIGNS | 12/31/2002 | 10/334,113 | 4/10/2007 | 7,203,631 | US |
| NMXI-0814.00/US | Utility - ORG | Issued | ADAPTIVE EQUALIZATION USING A CONDITIONAL UPDATE SIGN-SIGN LEAST MEAN SQUARE ALGORITHM | 9/10/2003 | 10/660,228 | 10/30/2007 | 7,289,557 | US |
| NMXI-0815.00/US | Utility - ORG | Issued | FUTURE ACTIVITY LIST FOR PERIPHERAL BUS HOST CONTROLLER | 6/5/2003 | 10/456,185 | 6/12/2007 | 7,231,468 | US |
| NMXI-0816.00/US | Utility - ORG | Issued | SPEED CONTROL PLAYBACK OF PARAMETRIC SPEECH ENCODED DIGITAL AUDIO | 7/23/2002 | 10/200,328 | 7/3/2007 | 7,239,999 | US |
| NMXI-0817.00/US | Utility - ORG | Issued | DATA PACKET HEADER CONVERSION | 8/14/2002 | 10/217,644 | 1/29/2008 | 7,324,516 | US |
| NMXI-0818.00/US | Utility - ORG | Issued | DETERMINISTIC BANDWIDTH THROTTLING TO FACILITATE OPERATION OF A CONTROL UNIT | 9/27/2002 | 10/256,475 | 1/9/2007 | 7,161,908 | US |

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Exhibit A - US Applications and Issued Patents

| App No | Matter Type | Matter Status | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|-----------|---------|
| NMXI-0819.00/US | Utility - ORG | Issued | OPTICAL THUMBTRACK | 12/31/2002 | 10/334,749 | 9/15/2004 | 6,792,179 | US |
| NMXI-0820.00/US | Utility - ORG | Issued | ISOLATING PHASE CHANGE MEMORY DEVICES | 12/13/2002 | 10/319,183 | 9/18/2007 | 7,271,403 | US |
| NMXI-0821.00/US | Utility - ORG | Issued | EFFICIENT ECHO CANCELLATION TECHNIQUES | 10/30/2002 | 10/284,724 | 1/2/2007 | 7,158,631 | US |
| NMXI-0822.00/US | Utility - ORG | Issued | PHOTOACTIVE ADHESION PROMOTER | 11/4/2002 | 10/288,021 | 3/10/2009 | 7,501,250 | US |
| NMXI-0822.01/US | Utility - DIV | Abandoned | PHOTOACTIVE ADHESION PROMOTER | 8/12/2004 | 10/917,679 | | | US |
| NMXI-0822.02/US | Utility - CON | Issued | PHOTOACTIVE ADHESION PROMOTER | 11/24/2008 | 12/277,114 | 11/3/2009 | 7,611,828 | US |
| NMXI-0823.00/US | Utility - ORG | Issued | FLASH ASSISTED ANNEALING | 7/31/2002 | 10/210,866 | 5/29/2007 | 7,223,660 | US |
| NMXI-0824.00/US | Utility - DIV | Abandoned | FORMING PLANARIZED SEMICONDUCTOR STRUCTURES | 12/16/2005 | 11/308,417 | | | US |
| NMXI-0825.00/US | Utility - ORG | Abandoned | DIGITAL PHASE DETECTION WITH JITTER FILTER | 12/31/2002 | 10/335,012 | | | US |
| NMXI-0826.00/US | Utility - ORG | Issued | OPTICAL STORAGE, TRANSFER PERFORMANCE | 12/31/2002 | 10/335,049 | 10/26/2004 | 6,810,443 | US |
| NMXI-0827.00/US | Utility - ORG | Issued | METHODS AND APPARATUS FOR CONFIGURING HARDWARE RESOURCES IN A PRE-BOOT ENVIRONMENT WITHOUT REQUIRING A SYSTEM RESET | 10/30/2002 | 10/283,920 | 6/26/2007 | 7,337,102 | US |
| NMXI-0828.00/US | Utility - ORG | Issued | GENERATING ANIMATION FROM VISUAL AND AUDIO INPUT | 10/7/2002 | 10/266,226 | 8/14/2007 | 7,257,538 | US |
| NMXI-0829.00/US | Utility - ORG | Issued | PHASE CHANGE MEMORY AND METHOD THEREFOR | 12/13/2002 | 10/318,984 | 4/17/2007 | 7,305,562 | US |
| NMXI-0830.00/US | Utility - ORG | Issued | SURFACE MOUNT TECHNOLOGY TO VIA-IN-PAD INTERCONNECTIONS | 12/20/2002 | 10/326,901 | 8/9/2005 | 6,927,346 | US |
| NMXI-0831.00/US | Utility - DIV | Issued | METHOD AND APPARATUS FOR REDUCING ELECTRICAL INTERCONNECTION FATIGUE | 12/26/2006 | 11/616,164 | 1/5/2010 | 7,642,660 | US |
| NMXI-0832.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR INTEGRATED CIRCUIT DATAPATH LAYOUT USING A VECTOR EDITOR | 9/30/2003 | 10/676,893 | 5/20/2008 | 7,376,922 | US |
| NMXI-0833.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR INTEGRATED CIRCUIT SELF-DESCRIPTION | 6/30/2003 | 10/630,713 | 3/24/2009 | 7,509,548 | US |
| NMXI-0834.00/US | Utility - ORG | Abandoned | COMPACT INPUT/OUTPUT SIGNAL DRIVER FOR ELECTROSTATIC DISCHARGE PROTECTION | 10/25/2002 | 10/280,926 | | | US |
| NMXI-0835.00/US | Utility - ORG | Abandoned | CODING OF SOURCE DRAINS USING CARBON OR FLUORINE ION IMPLANTS TO IMPROVE POLYSILICON DEPLETION | 11/27/2002 | 10/306,320 | | | US |
| NMXI-0836.00/US | Utility - DIV | Abandoned | USING BENZOCYCLOBUTENE BASED POLYMERS AS UNDERFILL MATERIALS | 9/10/2004 | 10/939,226 | | | US |
| NMXI-0837.00/US | Utility - ORG | Issued | ANALYZING INTERCONNECT STRUCTURES | 10/28/2002 | 10/281,857 | 10/30/2007 | 7,289,945 | US |
| NMXI-0838.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR LASER ANNEALING | 12/12/2002 | 10/319,310 | 5/1/2007 | 7,211,501 | US |
| NMXI-0839.00/US | Utility - ORG | Abandoned | PRONUNCIATION NETWORK | 12/30/2002 | 10/930,537 | | | US |
| NMXI-0840.00/US | Utility - ORG | Issued | PORTABLE COMPUTING DEVICE ADAPTED TO UPDATE DISPLAY INFORMATION WHILE IN A LOW POWER MODE | 11/5/2002 | 10/289,081 | 7/17/2007 | 7,245,945 | US |
| NMXI-0840.01/US | Utility - CON | Abandoned | PORTABLE COMPUTING DEVICE ADAPTED TO UPDATE DISPLAY INFORMATION WHILE IN A LOW POWER MODE | 1/24/2006 | 11/338,932 | | | US |
| NMXI-0841.00/US | Utility - DIV | Abandoned | HEATING OPTICAL DEVICES | 6/13/2005 | 11/451,010 | 2/13/2007 | 7,177,502 | US |
| NMXI-0842.00/US | Utility - ORG | Issued | USING A TRANSVERSAL FILTER TO COMPENSATE FOR DISPERSION | 4/8/2003 | 10/409,237 | 8/30/2005 | 6,937,794 | US |
| NMXI-0843.00/US | Utility - ORG | Issued | MECHANISM TO GUARANTEE FORWARD PROGRESS FOR INCOMING COHERENT INPUT/OUTPUT (I/O) TRANSACTIONS FOR CACHING I/O AGENT ON ADDRESS CONFLICT WITH PROCESSOR TRANSACTIONS | 12/19/2002 | 10/324,863 | 12/14/2004 | 6,832,268 | US |
| NMXI-0843.01/US | Utility - CON | Issued | MECHANISM TO GUARANTEE FORWARD PROGRESS FOR INCOMING COHERENT INPUT/OUTPUT (I/O) TRANSACTIONS FOR CACHING I/O AGENT ON ADDRESS CONFLICT WITH PROCESSOR TRANSACTIONS | 10/21/2004 | 10/970,015 | 6/10/2008 | 7,386,643 | US |

PATENT

REEL: 028362 FRAME: 0841

Exhibit A - US Applications and Issued Patents

| MTL No. | Matter Type | Matter Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|------------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NMXXI-0844.00/US | Utility - ORG | Issued | SYSTEM AND METHOD FOR EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE | 2/18/2003 | 10/369,993 | 11/18/2008 | 7,453,950 | US |
| NMXXI-0845.00/US | Utility - ORG | Abandoned | REMOVING FLUORINE-BASED PLASMA ETCH RESIDUES | 10/23/2002 | 10/278,535 | | | US |
| NMXXI-0846.00/US | Utility - ORG | Issued | APPARATUS AND METHOD FOR TIME SYNCHRONIZATION OF A PLURALITY OF MULTIMEDIA STREAMS | 11/14/2002 | 10/294,271 | 4/4/2006 | 7,024,575 | US |
| NMXXI-0847.00/US | Utility - ORG | Issued | STIMULUS GENERATION | 12/11/2002 | 10/317,605 | 7/3/2007 | 7,240,280 | US |
| NMXXI-0848.00/US | Utility - ORG | Abandoned | SYSTEM AND PROCESS TO CONTROL ELECTROPLATING A METAL ONTO A SUBSTRATE | 6/20/2003 | 10/465,666 | | | US |
| NMXXI-0849.00/US | Utility - ORG | Issued | METHODS TO PATTERN CONTACTS USING CHROMELESS PHASE SHIFT MASKS | 11/25/2002 | 10/304,303 | 5/20/2008 | 7,374,865 | US |
| NMXXI-0849.01/US | Utility - DIV | Abandoned | METHODS TO PATTERN CONTACTS USING CHROMELESS PHASE SHIFT MASKS | 4/18/2008 | 12/106,211 | | | US |
| NMXXI-0850.00/US | Utility - ORG | Abandoned | THERMAL MANAGEMENT FOR TELECOMMUNICATION DEVICES | 1/3/2003 | 10/336,629 | | | US |
| NMXXI-0851.00/US | Utility - CIP | Issued | INTERCONNECTION DESIGNS AND MATERIALS HAVING IMPROVED STRENGTH AND FATIGUE LIFE | 4/30/2003 | 10/427,168 | 6/17/2008 | 7,387,827 | US |
| NMXXI-0851.01/US | Utility - DIV | Pending | INTERCONNECTION DESIGNS AND MATERIALS HAVING IMPROVED STRENGTH AND FATIGUE LIFE | 4/9/2008 | 12/100,380 | | | US |
| NMXXI-0852.00/US | Utility - ORG | Abandoned | LOW POWER ADDER CIRCUIT UTILIZING BOTH STATIC AND DYNAMIC LOGIC | 4/30/2003 | 10/426,044 | | | US |
| NMXXI-0853.00/US | Utility - ORG | Abandoned | LOW POWER ADDER | 4/30/2003 | 10/425,987 | | | US |
| NMXXI-0854.00/US | Utility - ORG | Abandoned | APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING | 5/9/2003 | 10/435,347 | | | US |
| NMXXI-0855.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR PREFETCHING BASED UPON TYPE IDENTIFIER TAGS | 6/2/2003 | 10/453,115 | | | US |
| NMXXI-0856.00/US | Utility - DIV | Issued | METHODS OF FORMING A HIGH CONDUCTIVITY DIAMOND FILM AND STRUCTURES FORMED THEREBY | 10/25/2004 | 10/973,161 | 8/26/2008 | 7,417,255 | US |
| NMXXI-0856.01/US | Utility - DIV | Issued | METHODS OF FORMING A HIGH CONDUCTIVITY DIAMOND FILM AND STRUCTURES FORMED THEREBY | 4/4/2005 | 11/100,885 | 6/30/2009 | 7,553,694 | US |
| NMXXI-0857.00/US | Utility - ORG | Issued | SYSTEM AND METHOD TO INITIALIZE REGISTERS WITH AN EEPROM STORED BOOT SEQUENCE | 4/22/2003 | 10/421,287 | 5/1/2007 | 7,213,142 | US |
| NMXXI-0858.00/US | Utility - ORG | Issued | MEMORY DRIVER ARCHITECTURE AND ASSOCIATED METHODS | 7/15/2003 | 10/520,469 | 4/3/2007 | 7,200,060 | US |
| NMXXI-0859.00/US | Utility - ORG | Abandoned | SMART CONTROL POINTS | 5/9/2003 | 10/427,377 | | | US |
| NMXXI-0860.00/US | Utility - ORG | Issued | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR SIDE DIE AND METHOD | 3/5/2003 | 10/382,680 | 9/11/2007 | 7,268,425 | US |
| NMXXI-0861.00/US | Utility - ORG | Abandoned | POST-DEPOSITION MODIFICATION OF INTERLAYER DIELECTRICS | 6/30/2003 | 10/609,963 | | | US |
| NMXXI-0862.00/US | Utility - ORG | Issued | VENTING OF PELLUC CAVITY FOR A MASK | 12/31/2002 | 10/334,841 | 4/17/2007 | 7,205,074 | US |
| NMXXI-0863.00/US | Utility - ORG | Issued | DIELECTRIC WITH SIDEWALL PASSIVATING LAYER | 3/4/2003 | 10/379,061 | 2/13/2007 | 7,176,122 | US |
| NMXXI-0864.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS TO CHARACTERIZE AN ELECTRONIC DEVICE | 6/13/2003 | 10/451,217 | | | US |
| NMXXI-0865.00/US | Utility - ORG | Issued | METHOD AND APPARATUS TO CREATE BYPASS LOGIC IN A DIGITAL CIRCUIT DESIGN | 6/26/2003 | 10/606,868 | 11/20/2007 | 7,289,429 | US |
| NMXXI-0866.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR TRUSTED KEYBOARD SCANNING | 9/30/2003 | 10/676,888 | 12/5/2006 | 7,145,481 | US |
| NMXXI-0867.00/US | Utility - ORG | Issued | DYNAMIC BIOS EXECUTION AND CONCURRENT UPDATE FOR A BLADE SERVER | 5/29/2003 | 10/448,696 | 11/28/2006 | 7,143,279 | US |
| NMXXI-0868.00/US | Utility - ORG | Issued | ELIMINATION OF POTENTIAL REMAINING STALLS DUE TO USE OF PARTIAL REGISTERS | 6/30/2003 | 10/608,121 | 1/9/2007 | 7,162,624 | US |
| NMXXI-0869.00/US | Utility - ORG | Issued | PCB DESIGN AND METHOD FOR PROVIDING VENTED BLIND VIAS | 5/20/2003 | 10/442,834 | 9/7/2004 | 6,787,443 | US |

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REEL: 028362 FRAME: 0842

Exhibit A - US Applications and Issued Patents

| MTL-Booking | Matter Type | Matter Status | Title | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NMXI-0869.01/US | Utility - DIV | Abandoned | PCB DESIGN AND METHOD FOR PROVIDING VENTED VIAS | 6/17/2004 | 10/870,506 | | | US |
| NMXI-0870.00/US | Utility - ORG | Issued | BOND FINGER ON VIA SUBSTRATE, PROCESS OF MAKING SAME, PACKAGE MADE THEREBY, AND METHOD OF ASSEMBLING SAME | 6/30/2003 | 10/612,281 | 12/4/2007 | 7,302,796 | US |
| NMXI-0871.00/US | Utility - ORG | Issued | CONNECTION APPARATUS, SYSTEMS, AND METHODS | 3/31/2003 | 10/405,088 | 10/25/2005 | 6,958,450 | US |
| NMXI-0872.00/US | Utility - ORG | Issued | APPARATUS AND METHOD FOR COMBINING WRITES TO I/O | 3/28/2003 | 10/402,125 | 4/17/2007 | 7,206,865 | US |
| NMXI-0873.00/US | Utility - ORG | Issued | TIMING ANALYSIS APPARATUS, SYSTEMS, AND METHODS | 6/9/2003 | 10/458,537 | 11/20/2007 | 7,299,433 | US |
| NMXI-0874.00/US | Utility - ORG | Issued | SELECTIVE CONTROL OF TEST-ACCESS PORTS IN INTEGRATED CIRCUITS | 6/30/2003 | 10/612,293 | 7/29/2008 | 7,406,641 | US |
| NMXI-0875.00/US | Utility - ORG | Issued | TEST SCAN CELLS | 5/13/2003 | 10/436,775 | 10/14/2008 | 7,437,634 | US |
| NMXI-0876.00/US | Utility - ORG | Issued | LEAKAGE CONTROL IN INTEGRATED CIRCUITS | 3/31/2003 | 10/404,937 | 11/27/2007 | 7,302,652 | US |
| NMXI-0877.00/US | Utility - ORG | Abandoned | MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A HIGH-ACID ELECTROLYTE SOLUTION | 10/8/2003 | 10/682,275 | | | US |
| NMXI-0878.00/US | Utility - ORG | Issued | MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A LOW-ACID ELECTROLYTE SOLUTION | 10/8/2003 | 10/682,276 | 5/13/2008 | 7,373,311 | US |
| NMXI-0878.01/US | Utility - DIV | Abandoned | MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A LOW-ACID ELECTROLYTE SOLUTION | 8/19/2005 | 11/207,305 | | | US |
| NMXI-0879.00/US | Utility - ORG | Issued | VARIABLE LATCH | 3/18/2003 | 10/391,893 | 1/9/2007 | 7,160,127 | US |
| NMXI-0879.01/US | Utility - DIV | Abandoned | VARIABLE LATCH | 11/20/2006 | 11/561,684 | | | US |
| NMXI-0880.00/US | Utility - ORG | Abandoned | METHOD, APPARATUS AND SYSTEM FOR CREATING EFFICIENT UPNP CONTROL POINTS | 6/30/2003 | 10/611,617 | | | US |
| NMXI-0881.00/US | Utility - ORG | Issued | INTEGRATED CIRCUIT WITH IMPROVED CHANNEL STRESS PROPERTIES AND A METHOD FOR MAKING IT | 5/21/2003 | 10/443,152 | 5/16/2006 | 7,045,408 | US |
| NMXI-0882.00/US | Utility - ORG | Issued | OSCILLATOR SYSTEM WITH SWITCHED-CAPACITOR NETWORK AND METHOD FOR GENERATING A PRECISION TIME REFERENCE | 5/12/2003 | 10/444,311 | 3/1/2005 | 6,881,917 | US |
| NMXI-0883.00/US | Utility - ORG | Abandoned | TOPOLOGY BASED REPLACEMENT ROUTING OF SIGNAL PATHS | 6/30/2003 | 10/610,895 | | | US |
| NMXI-0884.00/US | Utility - ORG | Issued | REUSABLE, BUILT-IN SELF-TEST METHODOLOGY FOR COMPUTER SYSTEMS | 3/20/2003 | 10/393,223 | 12/26/2006 | 7,155,370 | US |
| NMXI-0885.00/US | Utility - ORG | Issued | PUSH BUTTON MODE AUTOMATIC PATTERN SWITCHING FOR INTERCONNECT BUILT-IN SELF TEST | 3/31/2003 | 10/404,405 | 11/30/2004 | 6,826,100 | US |
| NMXI-0886.00/US | Utility - ORG | Issued | METHOD AND PROCESS FOR DETERMINING A QUOTIENT | 6/3/2003 | 10/454,790 | 7/1/2008 | 7,395,301 | US |
| NMXI-0887.00/US | Utility - ORG | Abandoned | TECHNIQUES TO PERFORM LINE QUALIFICATION | 6/6/2003 | 10/457,092 | | | US |
| NMXI-0888.00/PR | Prov - ORG | Expired | ENHANCED GPRS MOBILITY MANAGEMENT | 2/14/2003 | 60/447,665 | | | US |
| NMXI-0888.00/US | Utility - ORG | Abandoned | ENHANCED GENERAL PACKET RADIO SERVICE (GPRS) MOBILITY MANAGEMENT | 9/15/2003 | 10/663,165 | | | US |
| NMXI-0889.00/US | Utility - ORG | Issued | CONTROLLING THE LOCATION OF CONDUCTION BREAKDOWN IN PHASE CHANGE MEMORIES | 8/4/2003 | 10/633,869 | 4/18/2006 | 7,029,978 | US |
| NMXI-0890.00/US | Utility - ORG | Abandoned | DEVICE, SYSTEM AND METHOD FOR CHANNEL SCANNING | 6/26/2003 | 10/603,959 | | | US |
| NMXI-0891.00/US | Utility - ORG | Issued | STROBE THROUGH DIFFERENTIAL SIGNALING | 4/30/2003 | 10/427,163 | 1/11/2005 | 6,842,133 | US |
| NMXI-0892.00/US | Utility - ORG | Issued | APPARATUS AND METHOD FOR ADAPTING A LEVEL SENSITIVE DEVICE TO PRODUCE EDGE-TRIGGERED BEHAVIOR | 5/30/2003 | 10/449,332 | 4/18/2006 | 7,032,149 | US |
| NMXI-0892.01/US | Utility - CON | Issued | APPARATUS AND METHOD FOR ADAPTING A LEVEL SENSITIVE DEVICE TO PRODUCE EDGE-TRIGGERED BEHAVIOR | 12/28/2005 | 11/321,012 | 4/24/2007 | 7,210,079 | US |
| NMXI-0893.00/US | Utility - DIV | Issued | METHOD AND AN APPARATUS FOR A HARD-CODED BIT VALUE CHANGEABLE IN ANY LAYER OF METAL | 2/1/2005 | 11/049,435 | 2/5/2008 | 7,326,973 | US |
| NMXI-0893.01/US | Utility - DIV | Issued | METHOD AND AN APPARATUS FOR A HARD-CODED BIT VALUE CHANGEABLE IN ANY LAYER OF METAL | 2/1/2005 | 11/049,524 | 12/11/2007 | 7,307,295 | US |
| NMXI-0894.00/US | Utility - DIV | Abandoned | COMMUNICATION REGISTERS FOR PROCESSING ELEMENTS | 8/31/2006 | 11/514,802 | | | US |

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Exhibit A - US Applications and Issued Patents

| Pub. No. | Mat. No. | Mat. Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|-------------|--|------------|------------|------------|------------|---------|
| NMXI-0895.00/US | Utility - ORG | Abandoned | MANAGING A CACHE WITH PINNED DATA | 7/28/2003 | 10/629,093 | | | US |
| NMXI-0896.00/US | Utility - ORG | Abandoned | MULTI-CONFERENCE STREAM MIXING | 11/26/2003 | 10/723,413 | | | US |
| NMXI-0897.00/US | Utility - ORG | Issued | APPARATUS, SYSTEM AND METHOD FOR UPDATING A SORTED LIST | 6/30/2003 | 10/609,954 | 3/27/2007 | 7,197,498 | US |
| NMXI-0898.00/US | Utility - ORG | Abandoned | MEDIA CENTER STORAGE DEVICE PROXY | 5/2/2003 | 10/428,286 | | | US |
| NMXI-0899.00/US | Utility - ORG | Abandoned | PASS THROUGH MODE FOR A PERSONAL VIDEO RECORDER | 6/17/2003 | 10/463,651 | | | US |
| NMXI-0900.00/US | Utility - ORG | Abandoned | APPARATUS, SYSTEM AND METHOD FOR PERFORMING TABLE MAINTENANCE | 6/30/2003 | 10/610,104 | | | US |
| NMXI-0901.00/US | Utility - ORG | Abandoned | SECURE COMPUTER TELEPHONY INTEGRATION ACCESS | 9/12/2003 | 10/660,876 | | | US |
| NMXI-0902.00/US | Utility - ORG | Issued | METHOD AND APPARATUS TO PERFORM TASK SCHEDULING | 9/23/2003 | 10/669,309 | 9/22/2009 | 7,594,228 | US |
| NMXI-0903.00/US | Utility - ORG | Issued | TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA | 6/6/2003 | 10/456,076 | 7/18/2006 | 7,078,912 | US |
| NMXI-0904.00/US | Utility - ORG | Abandoned | MODULAR CONTENT ADDRESSABLE MEMORY | 6/30/2003 | 10/609,714 | | | US |
| NMXI-0905.00/US | Utility - ORG | Issued | NETWORK STATISTICS | 7/28/2003 | 10/628,997 | 10/30/2007 | 7,289,455 | US |
| NMXI-0906.00/US | Utility - ORG | Issued | SYSTEMS AND METHODS FOR USING HDLC CHANNEL CONTEXT TO SIMULTANEOUSLY PROCESS MULTIPLE HDLC CHANNELS | 9/4/2003 | 10/655,565 | 1/8/2008 | 7,317,737 | US |
| NMXI-0907.00/US | Utility - ORG | Issued | SYMBOLIC BUFFER ALLOCATION IN LOCAL CACHE AT A NETWORK PROCESSING ELEMENT | 10/31/2003 | 10/699,638 | 11/21/2006 | 7,140,023 | US |
| NMXI-0908.00/US | Utility - DIV | Issued | WIRE-BONDED PACKAGE WITH ELECTRICALLY INSULATING WIRE ENCAPSULANT AND THERMALLY CONDUCTIVE OVERMOLD | 12/9/2003 | 10/731,177 | 11/28/2006 | 7,141,454 | US |
| NMXI-0909.00/US | Utility - ORG | Issued | APPARATUS FOR PRECISE ALIGNMENT OF PACKAGING CAPS ON A SUBSTRATE | 6/30/2003 | 10/611,215 | 10/2/2007 | 7,275,312 | US |
| NMXI-0910.00/US | Utility - CON | Issued | I/O ARCHITECTURE FOR INTEGRATED CIRCUIT PACKAGE | 2/23/2005 | 11/063,963 | 2/12/2008 | 7,329,946 | US |
| NMXI-0911.00/US | Utility - ORG | Issued | ELECTRICAL DEVICE CONNECTOR | 6/20/2003 | 10/600,668 | 12/14/2004 | 6,830,470 | US |
| NMXI-0912.00/US | Utility - ORG | Abandoned | HIERARCHICAL REORDER BUFFERS FOR CONTROLLING SPECULATIVE EXECUTION IN A MULTI-CLUSTER SYSTEM | 6/30/2003 | 10/611,380 | | | US |
| NMXI-0913.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR ENABLING AN ADAPTIVE REPLAY LOOP IN A PROCESSOR | 12/30/2003 | 10/749,271 | | | US |
| NMXI-0914.00/US | Utility - ORG | Issued | MEMORY ARRAY WITH STAGED OUTPUT | 12/19/2003 | 10/739,268 | 2/21/2006 | 7,002,873 | US |
| NMXI-0915.00/US | Utility - ORG | Issued | METHODS AND APPARUSES FOR MANUFACTURING ULTRA THIN DEVICE LAYERS FOR INTEGRATED CIRCUIT DEVICES | 9/11/2003 | 10/661,738 | 8/15/2006 | 7,091,108 | US |
| NMXI-0916.00/US | Utility - ORG | Abandoned | SUBSTRATE CARRIER FOR SURFACE PLANARIZATION | 6/30/2004 | 10/883,404 | | | US |
| NMXI-0917.00/US | Utility - ORG | Abandoned | SYSTEM AND METHOD FOR MULTIPLE ENGINE PROCESSING INSTRUCTIONS | 8/15/2003 | 10/640,614 | | | US |
| NMXI-0918.00/US | Utility - ORG | Issued | PASSING PARAMETERS BY IMPLICIT REFERENCE | 9/30/2003 | 10/677,081 | 4/8/2008 | 7,356,812 | US |
| NMXI-0919.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR LIMITING FORTS IN A REGISTER ALIAS TABLE HAVING HIGH-BANDWIDTH AND LOW-BANDWIDTH STRUCTURES | 10/22/2003 | 10/692,436 | 9/18/2007 | 7,272,701 | US |
| NMXI-0920.00/US | Utility - ORG | Issued | PRIORITIZED ADDRESS DECODER | 9/19/2003 | 10/666,077 | 10/27/2009 | 7,610,611 | US |
| NMXI-0921.00/US | Utility - DIV | Abandoned | ACTIVE PHASE CANCELLATION FOR INDUCTOR / CAPACITOR NETWORKS | 8/4/2005 | 11/197,806 | | | US |
| NMXI-0922.00/US | Utility - ORG | Issued | DYNAMICALLY RECONFIGURABLE OPTICAL SWITCHING SYSTEM | 4/20/2001 | 09/839,848 | 6/3/2003 | 6,574,306 | US |
| NMXI-0923.00/US | Utility - ORG | Issued | CONTROLLED BREAKDOWN PHASE CHANGE MEMORY DEVICE | 9/10/2004 | 10/959,237 | 3/25/2008 | 7,348,268 | US |
| NMXI-0925.00/US | Utility - ORG | Issued | METHOD, SYSTEM, AND ARTICLE OF MANUFACTURE FOR RUNNING DIAGNOSTICS RELATED TO A DEVICE | 12/19/2003 | 10/742,218 | 4/18/2006 | 7,031,881 | US |
| NMXI-0926.00/US | Utility - ORG | Issued | SECURE BOOTING AND PROVISIONING | 12/24/2003 | 10/746,975 | 4/17/2007 | 7,207,039 | US |
| NMXI-0927.00/US | Utility - ORG | Abandoned | USE OF ALTERNATIVE POLYMER MATERIALS FOR "SOFT" POLYMER PELLICLES | 3/12/2004 | 10/799,926 | | | US |
| NMXI-0928.00/US | Utility - ORG | Issued | PREDICTIVE FILTERING OF REGISTER CACHE ENTRY | 12/30/2003 | 10/787,625 | 1/30/2007 | 7,171,545 | US |

PATENT

REEL: 028362 FRAME: 0844

Exhibit A - US Applications and Issued Patents

| MTL Doc No | Matter Type | Matter Status | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|-----------|---------|
| NMXI-0929.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PREDICTION FOR FORK AND JOIN INSTRUCTIONS IN SPECULATIVE EXECUTION | 12/8/2003 | 10/731,557 | 2/30/2007 | 7,181,601 | US |
| NMXI-0930.00/US | Utility - ORG | Issued | FRONT SIDE HOT-SWAP CHASSIS MANAGEMENT MODULE | 12/29/2003 | 10/748,385 | 2/6/2007 | 7,173,817 | US |
| NMXI-0931.00/US | Utility - DRG | Issued | FORMING PHASE CHANGE MEMORY ARRAYS | 9/10/2004 | 10/939,145 | 8/21/2007 | 7,259,023 | US |
| NMXI-0932.00/US | Utility - DRG | Issued | MEMORY REUSE FOR MULTIPLE ENDPOINTS IN USB DEVICE | 11/12/2004 | 10/988,417 | 10/20/2009 | 7,606,951 | US |
| NMXI-0933.00/US | Utility - ORG | Abandoned | USB ON-THE-GO IMPLEMENTATION | 11/17/2004 | 10/992,250 | | | US |
| NMXI-0934.00/US | Utility - DRG | Issued | REWRITABLE FUSE MEMORY | 12/23/2003 | 10/744,664 | 10/30/2007 | 7,289,382 | US |
| NMXI-0935.00/US | Utility - ORG | Abandoned | HYBRID BRANCH PREDICTION | 3/22/2004 | 10/805,947 | | | US |
| NMXI-0936.00/US | Utility - ORG | Issued | PRIORITIZATION OF NETWORK TRAFFIC | 7/7/2004 | 10/885,821 | 7/27/2010 | 7,764,709 | US |
| NMXI-0937.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR DEDICATING CACHE ENTRIES TO CERTAIN STREAMS FOR PERFORMANCE OPTIMIZATION | 2/20/2004 | 10/783,621 | 9/14/2010 | 7,797,492 | US |
| NMXI-0938.00/US | Utility - ORG | Issued | METHOD AND APPARATUS TO MANAGE EXCEPTIONS IN NETWORK PROCESSORS | 6/8/2004 | 10/863,661 | 10/21/2008 | 7,443,269 | US |
| NMXI-0939.00/US | Utility - ORG | Abandoned | METHOD, APPARATUS AND SYSTEM FOR DATA INTEGRITY OF STATE RETENTIVE ELEMENTS UNDER LOW POWER MODES | 9/30/2004 | 10/956,994 | | | US |
| NMXI-0940.00/US | Utility - ORG | Issued | PRINT STRIPPER FOR ESD CONTROL | 8/31/2004 | 10/930,251 | 9/18/2007 | 7,270,956 | US |
| NMXI-0941.00/US | Utility - DRG | Issued | GROUND PLANE FOR INTEGRATED CIRCUIT PACKAGE | 6/21/2004 | 10/873,817 | 12/26/2006 | 7,154,175 | US |
| NMXI-0942.00/US | Utility - ORG | Issued | COMPLIANT MULTI-COMPOSITION INTERCONNECTS | 4/26/2004 | 10/832,178 | 7/15/2008 | 7,400,041 | US |
| NMXI-0943.00/US | Utility - ORG | Abandoned | TECHNIQUES FOR TRANSDUCTION OF A MATRIX ARRANGED IN A MEMORY AS MULTIPLE ITEMS PER WORD | 2/2/2005 | 11/050,369 | | | US |
| NMXI-0944.00/US | Utility - DRG | Issued | METHODS AND APPARATUSES FOR PROVIDING STACKED-DIE DEVICES | 8/11/2004 | 10/917,142 | 3/6/2007 | 7,187,088 | US |
| NMXI-0944.01/US | Utility - DIV | Issued | METHODS AND APPARATUSES FOR PROVIDING STACKED-DIE DEVICES | 8/24/2005 | 11/212,015 | 1/11/2011 | 7,967,818 | US |
| NMXI-0945.00/US | Utility - ORG | Issued | METHOD AND APPARATUS TO WRITE BACK DATA | 3/10/2004 | 10/799,555 | 8/19/2008 | 7,415,577 | US |
| NMXI-0946.00/US | Utility - ORG | Abandoned | APPARATUS AND METHOD OF DYNAMIC USAGE PROFILE ACQUISITION AND UTILIZATION IN A WIRELESS MOBILE DEVICE | 6/15/2004 | 10/868,703 | | | US |
| NMXI-0947.00/US | Utility - ORG | Abandoned | A METHOD AND APPARATUS FOR MULTIPROCESSOR EMBEDDED DEBUGGING SUPPORT VIA ATTACHMENT OF BREAKPOINT BIT FIELDS TO INSTRUCTIONS AND DEBUG REGISTER FIELD MANIPULATION | 3/31/2004 | 10/815,904 | | | US |
| NMXI-0948.00/US | Utility - ORG | Issued | ELECTROMAGNETIC INTERFERENCE (EMI) FILTER WITH PASSIVE NOISE CANCELLATION | 6/30/2004 | 10/883,610 | 2/27/2007 | 7,183,500 | US |
| NMXI-0949.00/US | Utility - ORG | Issued | QUEUE STRUCTURE WITH VALIDITY VECTOR AND ORDER ARRAY | 6/23/2004 | 10/874,998 | 7/24/2007 | 7,249,230 | US |
| NMXI-0950.00/US | Utility - ORG | Abandoned | METHOD, SYSTEM, AND PROGRAM FOR OPTIMIZING CODE | 3/19/2004 | 10/805,106 | | | US |
| NMXI-0951.00/US | Utility - ORG | Abandoned | PROGRAMMABLE CURRENT LOAD SYSTEMS AND METHODS | 3/31/2005 | 11/095,950 | | | US |
| NMXI-0952.00/US | Utility - ORG | Abandoned | NON STROBE SENSING CIRCUIT | 2/9/2005 | 11/053,786 | | | US |
| NMXI-0953.00/US | Utility - ORG | Abandoned | FLIP-CHIPS ON FLEX SUBSTRATES, FLIP-CHIP AND WIRE-BONDED CHIP STACKS, AND METHODS OF ASSEMBLING SAME | 8/10/2004 | 10/915,299 | | | US |
| NMXI-0954.00/US | Utility - ORG | Issued | METHOD, SYSTEM, AND APPARATUS FOR TRACKING DEFECTIVE CACHE LINES | 7/16/2004 | 10/893,015 | 5/27/2008 | 7,380,180 | US |
| NMXI-0955.00/US | Utility - ORG | Abandoned | DETERMINING AVAILABLE BANDWIDTH IN A NETWORK | 6/30/2004 | 10/883,107 | | | US |
| NMXI-0956.00/US | Utility - ORG | Issued | INTERCONNECT STRUCTURE IN INTEGRATED CIRCUITS | 6/30/2004 | 10/880,988 | 2/5/2008 | 7,326,972 | US |
| NMXI-0957.00/US | Utility - ORG | Issued | INITIALIZING PHASE CHANGE MEMORIES | 6/30/2004 | 10/851,664 | 1/29/2008 | 7,323,707 | US |
| NMXI-0958.00/US | Utility - ORG | Abandoned | OVERHEAD PROCESSING AND GENERATION TECHNIQUES | 9/29/2004 | 10/964,947 | | | US |
| NMXI-0959.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR CHANNEL HEALTH ENCODING | 11/27/2004 | 11/015,965 | | | US |

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| App No. | Matter Type | Matter Status | Title | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NMXI-0960.00/US | Utility - ORG | Issued | CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM | 12/17/2004 | 11/016,197 | 10/13/2009 | 7,602,777 | US |
| NMXI-0961.00/US | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROVIDING A CONNECTION MATRIX | 12/17/2004 | 11/015,967 | 3/31/2009 | 7,512,060 | US |
| NMXI-0962.00/US | Utility - ORG | Abandoned | REMOTE STATUS FORWARDING IN A DISTRIBUTED CROSS-CONNECTION SYSTEM | 12/17/2004 | 11/015,964 | | | US |
| NMXI-0963.00/US | Utility - ORG | Issued | DEVICE AND METHODS FOR INCREASING WIRELESS CONNECTION SPEEDS | 9/21/2004 | 10/945,797 | 7/1/2008 | 7,394,783 | US |
| NMXI-0963.01/US | Utility - COV | Issued | DEVICE AND METHODS FOR INCREASING WIRELESS CONNECTION SPEEDS | 6/25/2008 | 12/145,773 | 2/8/2011 | 7,885,227 | US |
| NMXI-0964.00/US | Utility - ORG | Abandoned | CALIBRATION METHODS FOR TUNABLE LASERS | 9/30/2004 | 10/955,394 | | | US |
| NMXI-0965.00/US | Utility - ORG | Issued | PROCESSING REPLIES TO REQUEST PACKETS IN AN ADVANCED SWITCHING CONTEXT | 9/3/2004 | 10/934,640 | 8/21/2007 | 7,260,661 | US |
| NMXI-0965.00/US | Utility - ORG | Issued | PROVIDING CURRENT FOR PHASE CHANGE MEMORIES | 6/30/2004 | 10/890,692 | 4/15/2008 | 7,359,231 | US |
| NMXI-0967.00/US | Utility - ORG | Abandoned | REMOTE USB NETWORK DEVICE CONTROL | 12/29/2004 | 11/025,116 | | | US |
| NMXI-0968.00/US | Utility - ORG | Issued | PHASE CHANGE MEMORY BITS RESET THROUGH A SERIES OF PULSES OF INCREASING AMPLITUDE | 2/15/2005 | 11/058,797 | 8/29/2006 | 7,099,180 | US |
| NMXI-0968.01/US | Utility - DIV | Issued | METHOD OF RESETTING PHASE CHANGE MEMORY BITS THROUGH A SERIES OF PULSES OF INCREASING AMPLITUDE | 7/14/2006 | 11/486,872 | 12/25/2007 | 7,313,016 | US |
| NMXI-0969.00/US | Utility - ORG | Issued | PHASE CHANGE MEMORY WITH DAMASCENE MEMORY ELEMENT | 9/24/2004 | 10/949,090 | 11/14/2006 | 7,135,696 | US |
| NMXI-0969.01/US | Utility - DIV | Issued | PHASE CHANGE MEMORY WITH DAMASCENE MEMORY ELEMENT | 10/4/2006 | 11/542,712 | 5/19/2009 | 7,594,625 | US |
| NMXI-0970.00/US | Utility - ORG | Issued | PHASE CHANGE MEMORY WITH A SELECT DEVICE HAVING A BREAKDOWN LAYER | 9/24/2004 | 10/948,884 | 9/12/2006 | 7,105,408 | US |
| NMXI-0971.00/US | Utility - ORG | Pending | DEPOSITING TITANIUM SILICON NITRIDE FILMS FOR FORMING PHASE CHANGE MEMORIES | 10/28/2004 | 10/977,186 | | | US |
| NMXI-0972.00/US | Utility - ORG | Issued | COMPENSATING THE WORKFUNCTION OF A METAL GATE TRANSISTOR FOR ABSTRACTION BY THE GATE DIELECTRIC LAYER | 7/28/2004 | 10/900,666 | 10/24/2006 | 7,125,762 | US |
| NMXI-0973.00/US | Utility - ORG | Issued | MULTI-WIRELESS NETWORK CONFIGURABLE BEHAVIOR | 5/4/2000 | 09/565,215 | 3/20/2007 | 7,199,965 | US |
| NMXI-0974.00/US | Utility - ORG | Issued | WALKING WEAVER IMAGE REJECT MIXER FOR RADIO | 1/8/2002 | 10/043,746 | 3/14/2006 | 7,013,120 | US |
| NMXI-0975.00/US | Utility - ORG | Issued | LOW-SWING LEVEL SHIFTER | 1/31/2005 | 11/047,442 | 5/8/2007 | 7,215,173 | US |
| NMXI-0976.00/US | Utility - ORG | Pending | DC-DC CONVERTER SWITCHING TRANSISTORS CURRENT MEASUREMENT TECHNIQUE | 6/30/2005 | 11/173,760 | | | US |
| NMXI-0977.00/US | Utility - ORG | Issued | ADAPTIVE COMMUNICATION INTERFACE | 10/29/2004 | 10/976,572 | 3/3/2009 | 7,500,129 | US |
| NMXI-0977.01/US | Utility - DIV | Pending | ADAPTIVE COMMUNICATION INTERFACE | 1/12/2009 | 12/352,489 | | | US |
| NMXI-0978.00/US | Utility - ORG | Issued | TECHNIQUES FOR TRANSMITTING AND RECEIVING TRAFFIC OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS | 12/28/2004 | 11/025,420 | 9/1/2009 | 7,583,664 | US |
| NMXI-0979.00/US | Utility - ORG | Abandoned | TECHNIQUES FOR PROCESSING TRAFFIC TRANSMITTED OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS | 12/28/2004 | 11/024,928 | | | US |
| NMXI-0981.00/US | Utility - ORG | Issued | POWER DISTRIBUTION WITHIN A FOLDED FLEX PACKAGE METHOD AND APPARATUS | 10/28/2004 | 10/977,968 | 12/4/2007 | 7,304,373 | US |
| NMXI-0982.00/US | Utility - ORG | Abandoned | SORTING OF CARBON NANOTUBES THROUGH SELECTIVE DNA DELAMINATION OF DNA/CARBON NANOTUBE HYBRID STRUCTURES | 3/30/2005 | 11/095,414 | | | US |
| NMXI-0983.00/US | Utility - ORG | Issued | FLASH MEMORY CELL HAVING REDUCED FLOATING GATE TO FLOATING GATE COUPLING | 3/30/2005 | 11/095,330 | 3/25/2008 | 7,348,618 | US |
| NMXI-0983.01/US | Utility - DIV | Issued | FLASH MEMORY CELL HAVING REDUCED FLOATING GATE TO FLOATING GATE COUPLING | 10/17/2005 | 11/592,881 | 12/16/2008 | 7,465,625 | US |

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Exhibit A - US Applications and Issued Patents

| MTL DocNo | Matter Type | Matter Status | Title | File Date | SerialNo | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|-----------|---------|
| NMXI-0985.00/US | Utility - ORG | Issued | TRAINING PATTERN BASED DE-SKEW MECHANISM AND FRAME ALIGNMENT | 9/7/2004 | 10/935,902 | 3/3/2009 | 7,560,131 | US |
| NMXI-0986.00/US | Utility - ORG | Pending | HEATING PHASE CHANGE MATERIAL FORMING A CARBON LAYER BETWEEN PHASE CHANGE LAYERS OF A PHASE CHANGE MEMORY | 4/11/2005 | 11/103,188 | | | US |
| NMXI-0987.00/US | Utility - ORG | Issued | READING PHASE CHANGE MEMORIES TO REDUCE READ DISTURBS | 1/15/2005 | 11/097,850 | 10/16/2007 | 7,282,730 | US |
| NMXI-0988.00/US | Utility - ORG | Issued | PROGRAMMABLE POWER TRANSITION COUNTER | 1/5/2005 | 11/029,981 | 8/21/2007 | 7,259,982 | US |
| NMXI-0989.00/US | Utility - ORG | Issued | TECHNIQUES TO MANAGE PARTITION PHYSICAL MEMORY | 11/15/2004 | 10/989,948 | 5/5/2009 | 7,529,958 | US |
| NMXI-0990.00/US | Utility - ORG | Abandoned | SCALABLE TRACEBACK TECHNIQUE FOR CHANNEL DECODER | 12/23/2004 | 11/021,323 | | | US |
| NMXI-0991.00/US | Utility - ORG | Issued | PROTECTED PROCESSING APPARATUS, SYSTEMS, AND METHODS | 12/23/2004 | 11/022,212 | 3/17/2009 | 7,506,239 | US |
| NMXI-0992.00/US | Utility - ORG | Abandoned | SYSTEMS AND METHODS FOR EFFICIENT OPERATIONS OF COMPONENTS IN A WIRELESS COMMUNICATIONS DEVICE | 12/24/2004 | 11/018,958 | | | US |
| NMXI-0993.00/US | Utility - ORG | Pending | PIXEL DATA COMPRESSION FROM CONTROLLER TO DISPLAY | 12/29/2004 | 11/024,956 | | | US |
| NMXI-0994.00/US | Utility - ORG | Abandoned | SECURE MEMORY CONTROLLER | 6/29/2005 | 11/169,537 | | | US |
| NMXI-0995.00/US | Utility - ORG | Issued | MEMORY WITH MODIFIABLE ADDRESS MAP | 12/30/2004 | 11/027,784 | 8/12/2008 | 7,412,579 | US |
| NMXI-0996.00/US | Utility - ORG | Abandoned | SECURE CONTROLLER FOR BLOCK ORIENTED STORAGE | 12/30/2005 | 11/333,473 | | | US |
| NMXI-0997.00/US | Utility - ORG | Issued | SECURE CONTROLLER FOR BLOCK ORIENTED STORAGE | 12/28/2004 | 11/027,913 | 3/8/2011 | 7,904,943 | US |
| NMXI-0998.00/US | Utility - ORG | Pending | COMMUNICATING AN ADDRESS TO A MEMORY DEVICE | 3/5/2011 | 13/041,379 | | | US |
| NMXI-0999.00/US | Utility - ORG | Abandoned | METHOD FOR FORMING A MEMORY DEVICE | 12/15/2004 | 11/012,316 | | | US |
| NMXI-0998.00/US | Utility - ORG | Abandoned | MEMORY CELL WITH NANOCRYSTAL AS DISCRETE STORAGE ELEMENT | 11/23/2004 | 10/997,609 | | | US |
| NMXI-0998.00/US | Utility - DIV | Abandoned | MECHANISM FOR PERFORMING RUNAHEAD EXECUTION IN A CENTRAL PROCESSING UNIT | 12/23/2005 | 11/615,862 | | | US |
| NMXI-0999.00/US | Utility - ORG | Abandoned | ADAPTIVE WIRELESS NETWORKS AND METHODS FOR COMMUNICATING MULTIMEDIA IN A PROACTIVE ENTERPRISE | 12/28/2004 | 11/024,164 | | | US |
| NMXI-1000.00/US | Utility - ORG | Abandoned | POWER LOSS RECOVERY IN NON-VOLATILE MEMORY | 11/16/2004 | 10/989,539 | | | US |
| NMXI-1002.00/US | Utility - ORG | Issued | WIRELESS DEVICE ENCLOSURE USING PIEZOELECTRIC COOLING STRUCTURES | 9/2/2005 | 11/218,371 | 11/5/2009 | 7,613,894 | US |
| NMXI-1003.00/US | Utility - ORG | Issued | ERROR PROTECTION TECHNIQUES FOR FRAMES ON A WIRELESS NETWORK | 5/31/2005 | 11/140,769 | 7/24/2007 | 7,348,475 | US |
| NMXI-1004.00/US | Utility - ORG | Abandoned | PULSED DC AND RF PHYSICAL VAPOR DEPOSITION CLUSTER TOOL | 9/30/2005 | 11/241,339 | | | US |
| NMXI-1005.00/US | Utility - ORG | Pending | METHOD AND APPARATUS FOR MONITORING PATH STATISTICS | 4/26/2005 | 11/114,261 | | | US |
| NMXI-1006.00/US | Utility - ORG | Abandoned | FREQUENCY-DEPENDENT VOLTAGE CONTROL IN DIGITAL LOGIC | 3/25/2005 | 11/090,571 | | | US |
| NMXI-1007.00/US | Utility - ORG | Issued | POWER MODE CHANGE VOLTAGE CONTROL IN COMPUTERIZED SYSTEM | 6/30/2005 | 11/171,218 | 10/13/2009 | 7,603,575 | US |
| NMXI-1008.00/US | Utility - ORG | Abandoned | LOCKING ENTRIES INTO TRANSLATION LOOKASIDE BUFFERS | 3/1/2005 | 11/069,924 | | | US |
| NMXI-1009.00/US | Utility - ORG | Pending | LOCKING ENTRIES INTO TRANSLATION LOOKASIDE BUFFERS | 3/29/2005 | 11/092,482 | | | US |
| NMXI-1009.00/US | Utility - CON | Pending | METHODS TO PERFORM CACHE COHERENCY IN MULTIPROCESSOR SYSTEM USING RESERVE SIGNALS AND CONTROL BITS | 8/3/2011 | 13/197,221 | | | US |
| NMXI-1010.00/US | Utility - ORG | Issued | LOW LOCALITY-OF-REFERENCE SUPPORT IN A MULTI-LEVEL CACHE HIERARCHY | 3/30/2005 | 11/094,687 | 2/5/2008 | 7,328,313 | US |
| NMXI-1011.00/US | Utility - ORG | Issued | MOVEABLE LOCKED LINES IN A MULTI-LEVEL CACHE | 4/7/2005 | 11/101,785 | 8/25/2009 | 7,591,065 | US |
| NMXI-1012.00/US | Utility - ORG | Pending | DEVICE, SYSTEM AND METHOD FOR WIRELESS COMMUNICATION AND CURSOR POINTING | 2/24/2006 | 11/361,228 | | | US |
| NMXI-1013.00/US | Utility - ORG | Abandoned | DIFFERENTIAL DELAY COMPENSATION | 6/30/2005 | 11/170,110 | | | US |
| NMXI-1014.00/US | Utility - ORG | Issued | DIFFERENTIAL DELAY COMPENSATION | 3/30/2005 | 11/093,907 | 9/13/2011 | 8,018,926 | US |
| NMXI-1014.00/US | Utility - CON | Pending | VOLTAGE CONTROLLED OSCILLATOR (VCO) TUNING | 9/9/2011 | 13/229,455 | | | US |
| NMXI-1015.00/US | Utility - ORG | Issued | | 6/30/2005 | 11/171,860 | 9/18/2007 | 7,271,673 | US |

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| App No | App Type | Matr Status | Title | File Date | Serial No | Issue Date | Patent No | Country |
|-----------------|---------------|-------------|---|------------|------------|------------|-----------|---------|
| NMXI-1016.00/US | Utility - CRG | Abandoned | METHOD AND SYSTEM FOR TRANSFERRING MESSAGES TO A MOBILE STATION ACCORDING TO SPECIFIC PARAMETERS | 6/27/2005 | 11/166,143 | | | US |
| NMXI-1017.00/US | Utility - CRG | Issued | PHASE CHANGE MEMORY WITH U-SHAPED CHALCOGENIDE CELL | 7/20/2005 | 11/185,488 | 2/2/2010 | 7,655,938 | US |
| NMXI-1018.00/US | Utility - CRG | Abandoned | FABRICATING SUB-LITHOGRAPHIC CONTACTS | 7/29/2005 | 11/193,952 | | | US |
| NMXI-1018.01/US | Utility - DIV | Issued | FABRICATING SUB-LITHOGRAPHIC CONTACTS | 10/6/2008 | 12/287,073 | 7/13/2010 | 7,754,516 | US |
| NMXI-1018.02/US | Utility - DIV | Allowed | FABRICATING SUB-LITHOGRAPHIC CONTACTS | 6/3/2010 | 12/792,992 | | | US |
| NMXI-1019.00/US | Utility - CRG | Issued | PERIPHERAL INTERFACE ALERT MESSAGE FOR DOWNSTREAM DEVICE | 6/20/2005 | 11/156,898 | 6/28/2011 | 7,970,959 | US |
| NMXI-1019.01/US | Utility - CON | Pending | PERIPHERAL INTERFACE ALERT MESSAGE FOR DOWNSTREAM DEVICE | 5/19/2011 | 13/111,673 | | | US |
| NMXI-1020.00/US | Utility - CRG | Abandoned | REDUCING OXIDATION OF PHASE CHANGE MEMORY ELECTRODES | 6/28/2005 | 11/168,780 | | | US |
| NMXI-1021.00/US | Utility - CRG | Issued | UNIVERSAL NONVOLATILE MEMORY BOOT MODE | 7/26/2005 | 11/189,448 | 3/24/2009 | 7,496,719 | US |
| NMXI-1022.00/US | Utility - CRG | Issued | APPARATUS, SYSTEM, AND METHOD FOR OSCILLATOR NETWORK WITH MULTIPLE PARALLEL OSCILLATOR CIRCUITS | 6/29/2005 | 11/172,133 | 6/26/2007 | 7,236,059 | US |
| NMXI-1023.00/US | Utility - CRG | Issued | STRUCTURED, ELECTRICALLY-FORMED FLOATING GATE FOR FLASH MEMORIES | 11/14/2005 | 11/274,622 | 5/20/2008 | 7,374,996 | US |
| NMXI-1023.01/US | Utility - DIV | Issued | STRUCTURED, ELECTRICALLY-FORMED FLOATING GATE FOR FLASH MEMORIES | 9/25/2008 | 12/055,216 | 12/7/2010 | 7,847,333 | US |
| NMXI-1024.00/US | Utility - CRG | Issued | METHOD AND APPARATUS FOR OPTIMIZING FLASH DEVICE ERASE DISTRIBUTION | 9/30/2005 | 11/240,004 | 9/16/2006 | 7,426,605 | US |
| NMXI-1025.00/US | Utility - CRG | Abandoned | ALTERNATING BETWEEN UPLINK AND DOWNLINK WITHIN EACH CHANNEL OF A FREQUENCY DIVISION MULTIPLEXING SYSTEM | 9/12/2005 | 11/225,303 | | | US |
| NMXI-1026.00/US | Utility - CRG | Abandoned | COMPOSITE ORGANIC INORGANIC NANOCLEUSTERS AS CARRIERS AND IDENTIFIERS OF TESTER MOLECULES | 9/26/2006 | 11/527,895 | | | US |
| NMXI-1027.00/US | Utility - CRG | Abandoned | FORMING HEATERS FOR PHASE CHANGE MEMORIES | 10/12/2005 | 11/248,488 | | | US |
| NMXI-1027.01/US | Utility - DIV | Abandoned | FORMING HEATERS FOR PHASE CHANGE MEMORIES | 3/6/2008 | 12/074,813 | | | US |
| NMXI-1028.00/US | Utility - CRG | Abandoned | METHOD AND APPARATUS FOR DISPLAYING ROTATED IMAGES | 12/16/2005 | 11/303,117 | | | US |
| NMXI-1029.00/US | Utility - CRG | Abandoned | LOW-JITTER CLOCK DISTRIBUTION | 11/30/2005 | 11/291,204 | | | US |
| NMXI-1030.00/US | Utility - CRG | Issued | PROCESSOR MULTI-PARTITION SECURITY ARCHITECTURE | 11/10/2005 | 11/271,778 | 11/24/2009 | 7,624,249 | US |
| NMXI-1031.00/US | Utility - CRG | Issued | WIRELESS COMMUNICATIONS WITH AUXILIARY ANTENNAS | 3/30/2006 | 11/394,994 | 7/21/2009 | 7,564,919 | US |
| NMXI-1032.00/US | Utility - CRG | Abandoned | FORMING OVONIC THRESHOLD SWITCHES WITH REDUCED DEPOSITION CHAMBER GAS PRESSURE | 3/29/2006 | 11/392,135 | | | US |
| NMXI-1033.00/US | Utility - CRG | Abandoned | ENCODING TECHNIQUES EMPLOYING NOISE-BASED ADAPTATION | 3/31/2006 | 11/394,929 | | | US |
| NMXI-1034.00/US | Utility - CRG | Abandoned | LOW DELAY AND SMALL MEMORY FOOTPRINT PICTURE BUFFERING | 12/2/2005 | 11/292,569 | | | US |
| NMXI-1035.00/US | Utility - CRG | Abandoned | SECURITY MODULE HAVING ACCESS LIMITED BASED UPON SECURITY LEVEL OF CODE SEEKING ACCESS | 2/15/2006 | 11/354,876 | | | US |
| NMXI-1035.00/US | Utility - CRG | Abandoned | METHOD AND APPARATUS TO EFFICIENTLY CONFIGURE MULTI-ANTENNA EQUALIZERS | 3/30/2006 | 11/394,533 | | | US |
| NMXI-1037.00/US | Utility - CRG | Issued | RADIO FREQUENCY AMPLIFIER | 7/6/2006 | 09/611,720 | 6/18/2002 | 6,407,632 | US |
| NMXI-1038.00/US | Utility - CRG | Pending | ARCHITECTURE FOR VIRTUAL SECURITY MODULE | 9/29/2006 | 11/529,987 | | | US |
| NMXI-1039.00/US | Utility - CRG | Issued | CURRENT MIRROR DRIVE CIRCUIT WITH HIGH BREAKDOWN VOLTAGE | 11/2/1994 | 08/333,264 | 11/21/1995 | 5,469,093 | US |
| NMXI-1040.00/US | Utility - CRG | Abandoned | SYSTEM AND METHOD FOR REDUCING FALSE ALARM IN THE PRESENCE OF RANDOM SIGNALS | 3/31/2006 | 11/396,329 | | | US |
| NMXI-1041.00/US | Utility - CRG | Issued | LOW VOLTAGE DOUBLE BALANCED MIXER | 1/7/1998 | 09/003,908 | 4/3/2001 | 6,211,718 | US |
| NMXI-1042.00/US | Utility - CRG | Issued | FREQUENCY SYNTHESIZER | 4/22/1998 | 09/054,275 | 5/9/2000 | 6,060,917 | US |
| NMXI-1043.00/US | Utility - CRG | Issued | TRANSDUCTANCE AMPLIFIER ARRANGEMENT | 11/2/2000 | 09/705,499 | 11/12/2002 | 6,480,066 | US |

PATENT

REEL: 028362 FRAME: 0848

Exhibit A - US Applications and Issued Patents

| MTI-BookNo | MatterType | MatterStatus | Title | FileDate | SerialNo | IssueDate | PatentNo | Country |
|-----------------|-----------------|--------------|---|------------|------------|-----------|-----------|---------|
| NMXI-1044.00/US | Utility - ORG | Issued | RADIO FREQUENCY AMPLIFIER WITH IMPROVED INTERMODULATION PERFORMANCE | 3/24/2002 | 10/056,155 | 4/5/2005 | 6,876,843 | US |
| NMXI-1045.00/US | Utility - ORG | Issued | SELF-ALIGNED BIPOLAR JUNCTION TRANSISTORS | 4/26/2006 | 11/411,311 | 1/25/2011 | 7,875,513 | US |
| NMXI-1045.01/US | Utility - DIV | Pending | SELF-ALIGNED BIPOLAR JUNCTION TRANSISTORS | 12/16/2010 | 12/969,652 | | | US |
| NMXI-1045.00/US | Utility - ORG | Issued | VOLTAGE CONTROLLED OSCILLATORS | 8/20/2002 | 10/223,869 | 1/27/2004 | 6,683,509 | US |
| NMXI-1047.00/US | Utility - ORG | Issued | SIGNAL PROCESSING STAGE AND RADIO FREQUENCY TUNER | 4/28/2003 | 10/424,899 | 9/14/2004 | 6,791,414 | US |
| NMXI-1048.00/US | Utility - ORG | Abandoned | ASYMMETRIC CHALCOGENIDE DEVICE | 10/10/2006 | 11/545,234 | | | US |
| NMXI-1049.00/US | Utility - ORG | Pending | READ WINDOW IN CHALCOGENIDE SEMICONDUCTOR MEMORIES | 11/9/2006 | 11/595,055 | | | US |
| NMXI-1050.00/US | Utility - ORG | Abandoned | AUTHENTICATING DATA RETURNED FROM NON-VOLATILE MEMORY COMMANDS | 9/28/2006 | 11/529,022 | | | US |
| NMXI-1051.00/US | Utility - ORG | Abandoned | A PHASE CHANGE MEMORY CELL HAVING A TAPERED MICROTRENCH | 11/30/2006 | 11/606,800 | | | US |
| NMXI-1052.00/US | Utility - ORG | Issued | HIGH DENSITY NOR FLASH ARRAY ARCHITECTURE | 6/28/2007 | 11/823,518 | 3/22/2011 | 7,910,976 | US |
| NMXI-1052.01/US | Utility - CON | Pending | HIGH DENSITY NOR FLASH ARRAY ARCHITECTURE | 3/9/2011 | 13/043,677 | | | US |
| NMXI-1053.00/US | Utility - NSPCT | Expired | OSCILLATOR NETWORK FOR RADIO RECEIVER | 5/17/1989 | 07/335,087 | 8/7/1990 | 4,947,141 | US |
| NMXI-1054.00/US | Utility - ORG | Issued | DIGITAL TELEPHONE SYSTEM WITH AUTOMATIC VOICE MAIL REDIRECTION | 8/31/1998 | 09/444,186 | 4/1/2003 | 6,542,594 | US |

Exhibit B - Foreign Applications and Patents

| NIMSI-DefNo | Matter Type | Matter Status | Title | PubDate | SerialNo | IssueDate | PatentNo | Country |
|------------------|-----------------|---------------|--|------------|-------------|------------|--------------|---------|
| NIMXI-0490.00/AU | Utility - NSPCT | Abandoned | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE | 1/27/1993 | 35952/93 | 8/20/1996 | 668379 | AU |
| NIMXI-0546.00/AU | Utility - ORG | Lapsed | METHOD OF PRE-CACHING DATA UTILIZING THREAD LISTS AND MULTIMEDIA EDITING SYSTEM USING SUCH PRE-CACHING | 9/12/1995 | 37233/95 | | | AU |
| NIMXI-0341.00/BE | Utility - ORG | Expired | PORTABLE COMMUNICATION TERMINAL FOR REMOTE DATABASE QUERY | 6/30/1987 | 87109409.0 | 12/15/1993 | 0251296 | BE |
| NIMXI-0209.00/BR | Utility - ORG | Abandoned | UNIVERSAL CARRIER FOR HOT SWAPABLE DRIVER | 11/28/1995 | P19509921-2 | 8/19/2003 | P19509921-2 | BR |
| NIMXI-0173.00/CA | Utility - ORG | Abandoned | LOW POWER CHARGE PUMP FOR FLASH MEMORY | 3/16/1995 | 2,185,333 | 6/1/2004 | 2,185,333 | CA |
| NIMXI-0399.00/CA | Utility - ORG | Abandoned | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK | 6/18/2001 | 2,355,881 | | | CA |
| NIMXI-0463.00/CA | Utility - ORG | Abandoned | ANTENNA INTERFACE | 11/26/2001 | 2,371,945 | | | CA |
| NIMXI-0490.00/CA | Utility - NSPCT | Abandoned | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE | 1/27/1993 | 2,132,108 | 3/30/1999 | 2,132,108 | CA |
| NIMXI-0497.00/CA | Utility - NSPCT | Abandoned | REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE | 10/31/2002 | 2,408,276 | | | CA |
| NIMXI-0550.00/CA | Utility - ORG | Abandoned | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION | 1/20/1999 | 2,318,450 | 6/5/2007 | 2,318,450 | CA |
| NIMXI-0599.00/CA | Utility - NSPCT | Abandoned | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL | 8/27/2003 | 2,432,906 | | | CA |
| NIMXI-0622.00/CA | Utility - NSPCT | Abandoned | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACWG) AND OPTICAL AMPLIFIER(S) | 10/17/2002 | 2,463,627 | | | CA |
| NIMXI-0689.00/CL | Utility - ORG | Abandoned | BEST-OF-M PULSE POSITION MODULATION DETECTOR | 6/5/1996 | 1000-96 | | | CL |
| NIMXI-0204.00/CN | Utility - NSPCT | Issued | ACCURATE VPP DETECTOR FOR FLASH MEMORY | 10/18/1995 | 95196684.7 | 5/21/2003 | Z195196684.7 | CN |
| NIMXI-0239.00/CN | Utility - NSPCT | Issued | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS | 12/12/1996 | 96199936.5 | 1/14/2004 | Z196199936.5 | CN |
| NIMXI-0251.00/CN | Utility - NSPCT | Abandoned | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER | 9/29/1997 | 97181414.7 | 5/28/2003 | Z197181414.7 | CN |
| NIMXI-0377.00/CN | Utility - NSPCT | Issued | METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING | 10/25/2000 | 00819034.8 | 1/11/2006 | Z100819034.8 | CN |
| NIMXI-0392.00/CN | Utility - ORG | Issued | CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL | 11/5/1999 | 99815589.6 | 8/4/2004 | Z199815589.6 | CN |
| NIMXI-0414.00/CN | Utility - NSPCT | Issued | IMPROVED FLOURINE DOPED SiO2 FILM AND METHOD OF FABRICATION | 10/11/2000 | 00816502.5 | 9/28/2005 | Z100816502.5 | CN |
| NIMXI-0420.00/CN | Utility - ORG | Issued | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | 2/8/2000 | 00804578.X | 3/2/2005 | Z100804578.X | CN |
| NIMXI-0478.00/CN | Utility - NSPCT | Issued | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS | 6/1/2001 | 01814712.7 | 8/5/2009 | Z101814712.7 | CN |
| NIMXI-0502.00/CN | Utility - NSPCT | Abandoned | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS | 6/10/2003 | 1820255.1 | | | CN |
| NIMXI-0504.00/CN | Utility - NSPCT | Issued | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS | 11/20/2001 | 01819255.6 | 10/22/2008 | Z101819255.6 | CN |
| NIMXI-0516.00/CN | Utility - NSPCT | Abandoned | CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS | 1/24/2001 | 01806003.X | | | CN |
| NIMXI-0535.00/CN | Utility - NSPCT | Issued | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS | 9/25/2001 | 01816419.6 | 12/7/2005 | Z101816419.6 | CN |
| NIMXI-0550.00/CN | Utility - ORG | Issued | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION | 12/27/2000 | 99804084.3 | 2/25/2004 | Z199804084.3 | CN |
| NIMXI-0589.00/CN | Utility - NSPCT | Abandoned | SYSTEM AND METHOD FOR PROVIDING AUTHENTICATION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY | 8/29/2003 | 01822961.1 | 8/27/2008 | Z101822961.1 | CN |
| NIMXI-0599.00/CN | Utility - NSPCT | Issued | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL | 12/21/2001 | 01822925.5 | 11/12/2008 | Z101822925.5 | CN |

PATENT

REEL: 028362 FRAME: 0850

Exhibit B - Foreign Applications and Patents

| WIPO No. | Priority No. | Pub. No. | Title | Pub. Date | Pub. No. | Pub. Date | Country |
|-----------------|-----------------|-----------|--|------------|-------------|------------|---------|
| NMXI-0490.00/EP | Utility - NSPCT | Abandoned | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE | 1/27/1993 | 59904673.6 | 12/20/1995 | EP |
| NMXI-0495.00/EP | Utility - NSPCT | Abandoned | METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE | 12/27/2002 | 01948405.4 | 9/17/2008 | EP |
| NMXI-0497.00/EP | Utility - NSPCT | Abandoned | REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE | 5/17/2001 | 01935694.8 | | EP |
| NMXI-0502.00/EP | Utility - NSPCT | Abandoned | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS | 6/9/2003 | 1992286.3 | | EP |
| NMXI-0504.00/EP | Utility - NSPCT | Issued | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS | 11/20/2001 | 01995179.7 | 4/13/2011 | EP |
| NMXI-0532.00/EP | Utility - ORG | Issued | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM | 10/15/1997 | 97308183.9 | 7/30/2003 | EP |
| NMXI-0535.00/EP | Utility - NSPCT | Issued | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS | 9/25/2001 | 01975434.0 | 5/6/2009 | EP |
| NMXI-0546.00/EP | Utility - NSPCT | Issued | THREAD LIST EXPLICIT CACHING SYSTEM | 9/12/1995 | 95935071.1 | 12/6/2006 | EP |
| NMXI-0550.00/EP | Utility - NSPCT | Issued | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION | 1/20/1999 | 99908159.3 | 1/14/2009 | EP |
| NMXI-0583.00/EP | Utility - NSPCT | Abandoned | SYSTEM AND METHOD FOR PROVIDING AUTHENTICATION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY | 8/29/2003 | 1273026.3 | | EP |
| NMXI-0622.00/EP | Utility - NSPCT | Issued | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRANGED WAVEGUIDE GRATING (ACWG) AND OPTICAL AMPLIFIER(S) | 10/17/2002 | 02795533.5 | 12/21/2005 | EP |
| NMXI-0626.00/EP | Utility - NSPCT | Issued | WGA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE | 11/28/2001 | 011273027.1 | 5/11/2011 | EP |
| NMXI-0637.00/EP | Utility - NSPCT | Allowed | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION | 12/30/2002 | 02792542.9 | | EP |
| NMXI-0679.00/EP | Utility - NSPCT | Issued | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE | 11/27/2003 | 02725662.7 | 6/22/2005 | EP |
| NMXI-0694.00/EP | Utility - NSPCT | Issued | VARIABLE LEVEL MEMORY | 8/5/2002 | 02757016.7 | 6/27/2007 | EP |
| NMXI-0710.00/EP | Utility - NSPCT | Abandoned | ELECTRONIC ASSEMBLY WITH FILLED NO-FLOW UNDERFILL METHODS OF MANUFACTURE | 4/8/2004 | 02802225.9 | | EP |
| NMXI-0719.00/EP | Utility - NSPCT | Abandoned | DIGITAL FRAME DETERMINATION AND APPARATUS | 6/24/2004 | 02798607.4 | 12/2/2009 | EP |
| NMXI-0721.00/EP | Utility - NSPCT | Abandoned | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT | 4/22/2004 | 02757625.5 | | EP |
| NMXI-0736.00/EP | Utility - NSPCT | Pending | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF PRODUCING IT | 12/17/2002 | 02805206.6 | | EP |
| NMXI-0741.00/EP | Utility - NSPCT | Pending | SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES | 6/30/2005 | 05768522.4 | | EP |
| NMXI-0778.00/EP | Utility - NSPCT | Abandoned | INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK | 2/5/2003 | 03717887.8 | 4/11/2007 | EP |
| NMXI-0786.00/EP | Utility - NSPCT | Issued | ESTABLISHING AN AD HOC NETWORK | 4/24/2003 | 03724240.1 | 7/8/2009 | EP |
| NMXI-0844.00/EP | Utility - NSPCT | Issued | SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE - EPO | 1/8/2004 | 04700900.6 | 12/12/2007 | EP |
| NMXI-0860.00/EP | Utility - NSPCT | Pending | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD | 2/9/2004 | 04709509.6 | | EP |
| NMXI-0924.00/EP | Utility - NSPCT | Abandoned | ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION | 10/27/2004 | 04796692.4 | | EP |
| NMXI-0993.00/FR | Utility - EPPAT | Abandoned | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK | 12/30/1999 | 99967807.1 | 8/10/2005 | FI |
| NMXI-0018.00/FR | Utility - ORG | Abandoned | A NOVEL ARCHITECTURE FOR FLASH VIRTUAL-GROUND MEMORY ARRAY | 5/30/1990 | 9006713 | 1/27/1995 | FR |
| NMXI-0050.00/FR | Utility - ORG | Abandoned | CONFORMING POLISHING PAD | 7/7/1992 | 9208358 | 4/29/1994 | FR |
| NMXI-0224.00/FR | Utility - EPPAT | Abandoned | NEGATIVE VOLTAGE SWITCHING CIRCUIT | 6/7/1996 | 96018382.1 | 9/10/2003 | FR |

Exhibit B - Foreign Applications and Patents

| App. No. | Pub. No. | Pub. Date | Pub. No. | Pub. Date | Title | Market Status | Priority Type | Priority No. | Priority Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|----------|------------|-------------|------------|---|---------------|-----------------|--------------|---------------|------------|------------|-------------|---------|
| NMXI-0392.00/FR | | 12/30/1999 | 99967807.1 | 8/10/2005 | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK | Abandoned | Utility - EPPAT | | | | | 1142202 | FR |
| NMXI-0532.00/FR | | 10/15/1997 | 97308183.9 | 7/30/2003 | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM | Lapsed | Utility - EPPAT | | | | | 0837429 | FR |
| NMXI-0622.00/FR | | 10/17/2002 | 02795533.5 | 12/21/2005 | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACWG) AND OPTICAL AMPLIFIER(S) | Abandoned | Utility - EPPAT | | | | | 1436648 | FR |
| NMXI-0679.00/FR | | 11/27/2003 | 02725662.7 | 6/22/2005 | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE | Abandoned | Utility - EPPAT | | | | | 1396081 | FR |
| NMXI-0004.00/GB | | 8/24/1988 | 8820080.3 | 11/13/1991 | LOAD LINE FOR EPROM | Abandoned | Utility - ORG | | | | | 2214380 | GB |
| NMXI-0012.00/GB | | 11/2/1989 | 8924745.6 | 11/4/1992 | PORT EXPANDER ARCHITECTURE FOR EPROM | Abandoned | Utility - ORG | | | | | 2227582 | GB |
| NMXI-0018.00/GB | | 3/5/1990 | 9314259.4 | 1/26/1994 | METHOD OF ACCESSING AN ELECTRICALLY PROGRAMMABLE MEMORY DEVICE | Abandoned | Utility - DIV | | | | | 2266796 | GB |
| NMXI-0018.01/GB | | 3/5/1990 | 9004859.6 | 1/5/1994 | A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY | Abandoned | Utility - ORG | | | | | 2234834 | GB |
| NMXI-0050.00/GB | | 2/19/1992 | 9203649.0 | 11/30/1994 | CONFORMING POLISHING PAD | Abandoned | Utility - ORG | | | | | 2257382 | GB |
| NMXI-0174.00/GB | | 5/11/1988 | 881077610.3 | 11/30/1994 | M&A FOR BACKING OUT OF A SOFTWARE INSTRUCT. AFTER EXEC. HAS BEGUN | Abandoned | Utility - ORG | | | | | 292791 | GB |
| NMXI-0203.00/GB | | 11/28/1995 | 9594294.2 | 9/8/2004 | PORTABLE DISK DRIVE CARRIER AND CHASSIS | Abandoned | Utility - EPPAT | | | | | 0794717 | GB |
| NMXI-0222.00/GB | | 3/12/1996 | 969087568 | 6/30/2004 | PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES | Abandoned | Utility - EPPAT | | | | | 0815615 | GB |
| NMXI-0239.00/GB | | 12/13/1996 | 9811429.1 | 12/13/2000 | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS | Abandoned | Utility - ORG | | | | | 2323212 | GB |
| NMXI-0251.00/GB | | 9/29/1997 | 9911695.6 | 9/12/2001 | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER | Abandoned | Utility - NSPCT | | | | | 2340973 | GB |
| NMXI-0291.00/GB | | 7/9/1997 | 9903399.3 | 8/15/2001 | INTEGRATED CIRCUIT PACKAGE | Abandoned | Utility - ORG | | | | | 2333182 | GB |
| NMXI-0341.00/GB | | 6/30/1997 | 87109409.0 | 12/15/1993 | PORTABLE COMMUNICATION TERMINAL FOR REMOTE DATABASE QUERY | Abandoned | Utility - ORG | | | | | 0251296 | GB |
| NMXI-0376.00/GB | | 11/1/2000 | 0035721.1 | 12/4/2002 | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE | Lapsed | Utility - ORG | | | | | 2353912 | GB |
| NMXI-0414.00/GB | | 10/11/2000 | 0212404.8 | 4/28/2004 | IMPROVED FLOURINE DOPED SIO2 FILM AND METHOD OF FABRICATION | Abandoned | Utility - NSPCT | | | | | 2373372 | GB |
| NMXI-0490.00/GB | | 1/27/1993 | 99904673.6 | 12/20/1995 | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE | Abandoned | Utility - NSPCT | | | | | 638188 | GB |
| NMXI-0532.00/GB | | 10/15/1997 | 97308183.9 | 7/30/2003 | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM | Issued | Utility - EPPAT | | | | | 0837429 | GB |
| NMXI-0546.00/GB | | 9/12/1995 | 95935071.1 | 12/6/2006 | THREAD LIST EXPLICIT CACHING SYSTEM | Lapsed | Utility - EPPAT | | | | | 0781432 | GB |
| NMXI-0622.00/GB | | 10/17/2002 | 02795533.5 | 12/21/2005 | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACWG) AND OPTICAL AMPLIFIER(S) | Abandoned | Utility - EPPAT | | | | | 1436648 | GB |
| NMXI-0679.00/GB | | 11/27/2003 | 02725662.7 | 6/22/2005 | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE | Abandoned | Utility - EPPAT | | | | | 1396081 | GB |
| NMXI-0694.00/GB | | 8/6/2002 | 03757016.7 | 6/27/2007 | VARIABLE LEVEL MEMORY | Issued | Utility - EPPAT | | | | | 1428221 | GB |
| NMXI-0712.00/GB | | 11/16/1999 | 0202551.8 | 3/5/1/2004 | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER | Abandoned | Utility - ORG | | | | | 2369702 | GB |
| NMXI-0012.00/HK | | 11/2/1989 | 97102060.5 | 3/27/1998 | PORT EXPANDER ARCHITECTURE FOR EPROM | Abandoned | Utility - ORG | | | | | 1000475 | HK |
| NMXI-0203.00/HK | | 3/17/1998 | 98102243.4 | 5/13/2005 | PORTABLE DISK DRIVE CARRIER AND CHASSIS | Issued | Utility - ORG | | | | | HK1003252 | HK |
| NMXI-0204.00/HK | | 2/26/1998 | 981015020.2 | 7/19/2002 | METHOD AND APPARATUS FOR DETECTING AND SELECTING VOLTAGE SUPPLIES FOR FLASH MEMORY | Issued | Utility - ORG | | | | | HK1002790 | HK |
| NMXI-0208.00/HK | | 1/15/1999 | 99100215.1 | 2/14/2003 | VARIABLE STAGE CHARGE PUMP | Issued | Utility - ORG | | | | | HK1015520 | HK |
| NMXI-0224.00/HK | | 9/21/1998 | 98110789.7 | 7/30/2004 | NEGATIVE VOLTAGE SWITCHING CIRCUIT | Issued | Utility - ORG | | | | | HK 10098978 | HK |
| NMXI-0239.00/HK | | 12/12/1996 | 99101098.1 | 3/17/2001 | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS | Issued | Utility - ORG | | | | | 1016341 | HK |

PATENT

Exhibit B - Foreign Applications and Patents

| WIPO No. | Match Type | Match Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|-----------------|--------------|--|------------|---------------------|------------|------------|---------|
| NMXI-0246.00/HK | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM | 5/7/1999 | 99102071 | 5/6/2005 | HK1017451 | HK |
| NMXI-0251.00/HK | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER | 9/29/1997 | 00105429.0 | 5/31/2002 | 1027403 | HK |
| NMXI-0376.00/HK | Utility - ORG | Issued | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE | 4/4/2001 | 01102410.7 | 5/2/2003 | HK1051968 | HK |
| NMXI-0377.00/HK | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING | 5/31/2002 | 02108033.0 | | | HK |
| NMXI-0414.00/HK | Utility - ORG | Issued | IMPROVED FLOURINE DOPED SiO ₂ FILM AND METHOD OF FABRICATION | 10/11/2000 | 02107701.3 | 12/10/2004 | HK1046331 | HK |
| NMXI-0478.00/HK | Utility - ORG | Abandoned | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS | 12/23/2002 | 03104815.2 | | | HK |
| NMXI-0502.00/HK | Utility - ORG | Abandoned | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS | 11/5/2003 | 03107859.2 | | | HK |
| NMXI-0504.00/HK | Utility - ORG | Abandoned | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS | 11/20/2001 | 03106719.4 | | | HK |
| NMXI-0516.00/HK | Utility - ORG | Abandoned | CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS | 5/23/2003 | 03103974.1 | | | HK |
| NMXI-0532.00/HK | Utility - ORG | Issued | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM | 5/23/1998 | 98194475.9 | 2/20/2004 | 1005524 | HK |
| NMXI-0550.00/HK | Utility - ORG | Issued | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION | 5/22/2001 | 01103480.8 | 9/11/2009 | 1084154 | HK |
| NMXI-0626.00/HK | Utility - ORG | Abandoned | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE | 11/28/2001 | 3109116.7 | | | HK |
| NMXI-0719.00/HK | Utility - ORG | Abandoned | DIGITAL FRAME DETERMINATION AND APPARATUS | 10/27/2004 | 04108409.4 | | | HK |
| NMXI-0721.00/HK | Utility - ORG | Abandoned | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT | 8/6/2004 | 04105864.8 | | | HK |
| NMXI-0736.00/HK | Utility - ORG | Abandoned | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING | 11/3/2004 | 04108659.1 | | | HK |
| NMXI-0786.00/HK | Utility - ORG | Abandoned | ESTABLISHING AN AD-HOC NETWORK | 4/24/2003 | 05102495.1 | 7/8/2009 | 1069269 | HK |
| NMXI-0844.00/HK | Utility - ORG | Abandoned | SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE | 12/30/2005 | 05112185.5 | | | HK |
| NMXI-0860.00/HK | Utility - ORG | Abandoned | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD | 12/19/2005 | 05111716.5 | | | HK |
| NMXI-0950.00/IE | Utility - ORG | Abandoned | CONFORMING POLISHING PAD | 4/7/1992 | 921103 | 11/23/1995 | 66126 | IE |
| NMXI-0282.00/IL | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN | 12/11/1997 | 130563 | 2/1/2004 | 130563 | IL |
| NMXI-0420.00/IN | Utility - ORG | Abandoned | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | 8/10/2001 | IN/PCT/200101042/MU | 4/17/2006 | 300061 | IN |
| NMXI-0497.00/IN | Utility - NSPCT | Abandoned | A METHOD AND SYSTEM FOR PROVIDING INTERNET SERVICES TO A CONSUMER THROUGH REMOTE MANAGEMENT AND CONTROL | 5/17/2001 | IN/PCT/2002/01359-C | 6/9/2006 | 199364 | IN |
| NMXI-0502.00/IN | Utility - NSPCT | Abandoned | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS | 4/29/2003 | 540/KOLNP/2003 | | | IN |
| NMXI-0636.00/IN | Utility - NSPCT | Abandoned | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE | 5/27/2003 | 675/KOLNP/2003 | 10/14/2005 | 195131 | IN |
| NMXI-0018.00/IT | Utility - ORG | Abandoned | A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY | 7/23/1990 | 21015 A/90 | 5/16/1994 | 1242497 | IT |
| NMXI-0174.00/IT | Utility - ORG | Abandoned | M2A FOR BACKING OUT OF A SOFTWARE INSTRUCTION AFTER EXECUTION HAS BEGUN | 5/11/1988 | 88107610.3 | 11/30/1994 | 292791 | IT |

PATENT

REEL: 028362 FRAME: 0855

Exhibit B - Foreign Applications and Patents

| App No | App Type | App Status | Title | Filed | Serial No | Issued Date | Patent No | Country |
|-----------------|-----------------|------------|--|------------|-------------|-------------|-----------|---------|
| NMXI-0532.00/JP | Utility - EPPAT | Lapsed | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM | 10/27/2003 | 97308183.9 | | | JP |
| NMXI-0550.00/IT | Utility - EPPAT | Issued | SYSTEM AND METHOD FOR AUTOMATIC PEB DETECTION | 1/20/1999 | 999041593 | 1/14/2009 | 1053621 | IT |
| NMXI-0023.00/JP | Utility - ORG | Issued | TUNGSTEN ETCH PROCESS | 4/17/1992 | 122983/92 | 7/27/2001 | 3213920 | JP |
| NMXI-0034.00/JP | Utility - ORG | Issued | LIPROM DEVICE | 11/13/1991 | 324106/91 | 4/6/2001 | 317963 | JP |
| NMXI-0050.00/JP | Utility - ORG | Abandoned | CONFORMING POLISHING PAD | 5/22/1992 | 154196/92 | 8/18/2000 | 3099209 | JP |
| NMXI-0052.00/JP | Utility - DIW | Abandoned | ADVANCED MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY | 2/18/1993 | 514358/93 | | | JP |
| NMXI-0064.00/JP | Utility - ORG | Issued | SURFACE SWEETING METHOD FOR 3-D TOPOGRAPHY SIMULATION | 5/24/1993 | 121723/1993 | 4/11/2003 | 3416894 | JP |
| NMXI-0065.00/JP | Utility - ORG | Issued | CALCULATION METHOD FOR PARTICLE FLUX SHADOWING | 5/24/1993 | 121605/1993 | 11/1/2002 | 3365555 | JP |
| NMXI-0163.00/JP | Utility - ORG | Issued | BUILD UP TYPE SOCKETABLE BOH | 3/17/1994 | 71270/94 | 4/15/2005 | 3667786 | JP |
| NMXI-0173.00/JP | Utility - ORG | Issued | METHOD AND APPARATUS FOR PROVIDING AN ULTRA LOW POWER REGULATED NEGATIVE CHARGE PUMP | 3/16/1995 | 524718/95 | 6/17/2005 | 3688710 | JP |
| NMXI-0174.00/JP | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR BACKING OUT OF A SOFTWARE INSTRUCTION AFTER EXECUTION HAS BEGUN | 5/18/1988 | 121665/88 | 8/26/1994 | 1865189 | JP |
| NMXI-0204.00/JP | Utility - NSPCT | Pending | ACCURATE VPP DETECTOR FOR FLASH MEMORY | 10/18/1995 | 514005/96 | | | JP |
| NMXI-0208.00/JP | Utility - NSPCT | Issued | VARIABLE STAGE CHARGE PUMP | 9/30/1996 | 513766/97 | 2/3/2006 | 3766101 | JP |
| NMXI-0222.00/JP | Utility - NSPCT | Issued | DUAL DIE PACKAGE FOR SOCIAL APPLICATIONS | 3/12/1996 | 527776/96 | 9/9/2005 | 3717937 | JP |
| NMXI-0224.00/JP | Utility - NSPCT | Abandoned | NEGATIVE VOLTAGE SWITCHING CIRCUIT | 6/7/1996 | 501941/97 | | | JP |
| NMXI-0251.00/JP | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER | 9/29/1997 | 523636/98 | 2/26/2010 | 4463879 | JP |
| NMXI-0282.00/JP | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN | 12/11/1997 | 530079/98 | | | JP |
| NMXI-0323.00/JP | Utility - ORG | Abandoned | PERIPHERAL DEVICE PREVENTING POST-SCAN MODIFICATION | 3/12/1998 | 541666/98 | | | JP |
| NMXI-0325.00/JP | Utility - ORG | Abandoned | MULTI-LAYER CODER/DECODER FOR PRODUCING QUANTIZATION ERROR SIGNAL SAMPLES | 9/21/1998 | 2000-539556 | | | JP |
| NMXI-0337.00/JP | Utility - NSPCT | Issued | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD | 1/5/1999 | 2000-540619 | 9/19/2008 | 4188562 | JP |
| NMXI-0376.00/JP | Utility - NSPCT | Abandoned | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE | 3/12/1999 | 2000-547785 | | | JP |
| NMXI-0392.00/JP | Utility - ORG | Issued | METHOD AND CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL | 4/19/2001 | 2000-583167 | 7/24/2009 | 4548015 | JP |
| NMXI-0420.00/JP | Utility - ORG | Abandoned | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | 6/31/2001 | 2000-603092 | | | JP |
| NMXI-0490.00/JP | Utility - NSPCT | Issued | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE | 1/27/1993 | 519231/93 | 3/31/2006 | 3786419 | JP |
| NMXI-0502.00/JP | Utility - NSPCT | Abandoned | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS | 6/9/2003 | 2002-548782 | | | JP |
| NMXI-0516.00/JP | Utility - NSPCT | Issued | CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS | 5/23/2003 | 2001-565629 | 6/9/2006 | 3813873 | JP |
| NMXI-0532.00/JP | Utility - ORG | Abandoned | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM | 7/4/1997 | 195011/1997 | | | JP |
| NMXI-0583.00/JP | Utility - NSPCT | Abandoned | SYSTEM AND METHOD FOR PROVIDING AUTHENTICATION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY | 6/27/2003 | 2002-554833 | 3/6/2009 | 4272429 | JP |
| NMXI-0599.00/JP | Utility - NSPCT | Abandoned | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL | 8/27/2003 | 2002-555069 | | | JP |
| NMXI-0599.01/JP | Utility - NSPCT | Abandoned | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL | 12/21/2001 | 2007-290121 | | | JP |
| NMXI-0622.00/JP | Utility - NSPCT | Issued | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYS WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIER(S) | 10/17/2002 | 2003-536786 | 3/13/2009 | 4276946 | JP |

PATENT

REEL: 028362 FRAME: 0856

Exhibit B - Foreign Applications and Patents

| WIPO Class | Market Status | Market Status | Title | PubDate | PubNo | IssueDate | PatentNo | Country |
|-----------------|---------------|---------------|--|------------|-----------------|------------|----------|---------|
| NMXI-0625.00/JP | Abandoned | Abandoned | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE | 6/30/2003 | 2002-555593 | | | JP |
| NMXI-0653.00/JP | Abandoned | Abandoned | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY | 11/20/2002 | P2003-550231 | | | JP |
| NMXI-0659.00/JP | Abandoned | Abandoned | ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED | 9/26/2003 | 2002-575882 | | | JP |
| NMXI-0679.00/JP | Issued | Issued | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE | 4/12/2002 | 2002-586286 | 3/28/2008 | 4102198 | JP |
| NMXI-0712.00/JP | Abandoned | Abandoned | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS. | 1/30/2002 | 2001-516051 | | | JP |
| NMXI-0736.00/JP | Issued | Issued | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING | 12/17/2002 | 2002-553708 | 7/11/2008 | 4152892 | JP |
| NMXI-0752.00/JP | Abandoned | Abandoned | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER | 10/14/2004 | 586733/2003 | | | JP |
| NMXI-0752.00/JP | Abandoned | Abandoned | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER | 12/23/2008 | 2008-329618 | | | JP |
| NMXI-0786.00/JP | Abandoned | Abandoned | ESTABLISHING AN AD HOC NETWORK | 4/24/2003 | 2004-500440 | | | JP |
| NMXI-0854.00/JP | Abandoned | Abandoned | APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING | 4/16/2004 | 2004-501283 | | | JP |
| NMXI-0924.00/JP | Abandoned | Abandoned | ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION | 10/27/2004 | 2006-538272 | | | JP |
| NMXI-0050.00/KR | Issued | Issued | CONFORMING POLISHING PAD | 3/17/1999 | 92-4336 | 5/16/1999 | 214163 | KR |
| NMXI-0173.00/KR | Issued | Issued | LOW POWER CHARGE PUMP FOR FLASH MEMORY | 9/23/1996 | 1996-705254 | 5/15/2000 | 263275 | KR |
| NMXI-0224.00/KR | Issued | Issued | NEGATIVE VOLTAGE SWITCHING CIRCUIT | 6/11/1996 | 1997-709025 | 3/27/2001 | 282882 | KR |
| NMXI-0239.00/KR | Issued | Issued | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS | 6/20/1998 | 1998-704709 | 8/24/2001 | 307895 | KR |
| NMXI-0251.00/KR | Issued | Issued | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER | 9/29/1997 | 1999-7004461 | 5/30/2003 | 387336 | KR |
| NMXI-0282.00/KR | Issued | Issued | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN | 6/29/1999 | 1999-7005947 | 6/21/2002 | 343039 | KR |
| NMXI-0291.00/KR | Issued | Issued | INTEGRATED CIRCUIT PACKAGE | 3/9/1999 | 1999-7001953 | 4/29/2005 | 488324 | KR |
| NMXI-0313.00/KR | Issued | Issued | SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES | 10/9/1999 | 1999-7009316 | 5/30/2003 | 387938 | KR |
| NMXI-0337.00/KR | Issued | Issued | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD | 7/13/2000 | 10-2000-7007725 | 12/20/2002 | 0367110 | KR |
| NMXI-0370.00/KR | Abandoned | Abandoned | POSITION SENSITIVE DISPLAY CONTROLLER | 11/7/2000 | 2000-7012436 | 4/22/2004 | 430139 | KR |
| NMXI-0392.00/KR | Issued | Issued | METHOD AND CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL | 4/19/2001 | 10-2001-7006012 | 4/4/2006 | 0569749 | KR |
| NMXI-0420.00/KR | Issued | Issued | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | 9/3/2001 | 2001-7011227 | 10/11/2005 | 0522383 | KR |
| NMXI-0420.01/KR | Issued | Issued | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | 1/21/2005 | 2005-7001212 | 6/22/2005 | 0498675 | KR |
| NMXI-0502.00/KR | Abandoned | Abandoned | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS | 6/5/2003 | 10-2003-7007506 | | | KR |
| NMXI-0535.00/KR | Issued | Issued | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS | 3/5/2003 | 2003-7004397 | 1/25/2006 | 0546910 | KR |

PATENT

REEL: 028362 FRAME: 0857

Exhibit B - Foreign Applications and Patents

| App No | Matter Type | Matter Status | Title | Pub Date | Serial No | Issue Date | Patent No | Country |
|-----------------|-----------------|---------------|---|------------|-------------|------------|-----------|---------|
| NMXI-0246.00/SG | Utility - NSPCT | Issued | METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM | 6/27/1997 | 9806364-7 | 3/20/2001 | 61079 | SG |
| NMXI-0251.00/SG | Utility - NSPCT | Abandoned | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER | 9/29/1997 | 9901175-0 | 10/16/2001 | 65368 | SG |
| NMXI-0282.00/SG | Utility - ORG | Issued | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN | 12/11/1997 | 9902870-6 | 2/15/2002 | 86110 | SG |
| NMXI-0291.00/SG | Utility - ORG | Issued | INTEGRATED CIRCUIT PACKAGE | 7/9/1997 | 9900858-3 | 4/17/2001 | 64145 | SG |
| NMXI-0414.00/SG | Utility - NSPCT | Issued | IMPROVED FLOURINE DOPED SiO2 FILM AND METHOD OF FABRICATION | 10/11/2000 | 200202938-7 | 4/29/2005 | 89071 | SG |
| NMXI-0462.00/SG | Utility - ORG | Abandoned | ANTENNA INTERFACE | 11/25/2001 | 200107090-3 | 7/29/2005 | 84929 | SG |
| NMXI-0473.00/SG | Utility - NSPCT | Issued | EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP | 3/4/1998 | 9904271-5 | 10/31/2007 | 67712 | SG |
| NMXI-0478.00/SG | Utility - NSPCT | Issued | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS | 6/1/2001 | 200207831-9 | 10/31/2005 | 94050 | SG |
| NMXI-0502.00/SG | Utility - NSPCT | Abandoned | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS | 5/29/2003 | 200305042-6 | 8/31/2006 | 97371 | SG |
| NMXI-0532.00/SG | Utility - ORG | Issued | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM | 10/13/1997 | 9703726-1 | 4/21/2001 | 74026 | SG |
| NMXI-0535.00/SG | Utility - NSPCT | Issued | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS | 9/25/2001 | 200301211-9 | 10/31/2005 | 95375 | SG |
| NMXI-0626.00/SG | Utility - NSPCT | Issued | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE | 11/28/2001 | 200302874-1 | 11/30/2005 | 96977 | SG |
| NMXI-0637.00/SG | Utility - NSPCT | Issued | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION | 12/30/2002 | 200307522-3 | 8/31/2006 | 101681 | SG |
| NMXI-0653.00/SG | Utility - NSPCT | Issued | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY | 11/20/2002 | 200402809-8 | 8/31/2006 | 104206 | SG |
| NMXI-0694.00/SG | Utility - NSPCT | Issued | VARIABLE LEVEL MEMORY | 8/5/2002 | 200400973-4 | 5/31/2006 | 103418 | SG |
| NMXI-0719.00/SG | Utility - NSPCT | Abandoned | DIGITAL FRAME DETERMINATION AND APPARATUS | 6/28/2004 | 200403602-6 | 7/31/2006 | 105264 | SG |
| NMXI-0721.00/SG | Utility - NSPCT | Abandoned | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT | 3/10/2004 | 200401398-3 | 5/31/2006 | 103034 | SG |
| NMXI-0786.00/SG | Utility - NSPCT | Issued | ESTABLISHING AN AD HOC NETWORK | 4/24/2003 | 200406094-3 | 10/31/2006 | 117627 | SG |
| NMXI-0844.00/SG | Utility - NSPCT | Pending | SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE | 1/8/2004 | 200504905-1 | | | SG |
| NMXI-0502.00/TH | Utility - ORG | Abandoned | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS | 12/9/2001 | 70155 | | | TH |
| NMXI-0626.00/TH | Utility - ORG | Abandoned | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE | 12/27/2001 | 70821 | | | TH |
| NMXI-0710.00/TH | Utility - ORG | Abandoned | ELECTRONIC ASSEMBLY WITH FILLED NO-FLOW UNDERFILL METHODS OF MANUFACTURE | 10/24/2002 | 77522 | | | TH |
| NMXI-0050.00/TW | Utility - ORG | Issued | CONFORMING POLISHING PAD- UTILITY | 4/1/1992 | 81102503 | 2/1/1994 | 065102 | TW |
| NMXI-0168.00/TW | Utility - ORG | Issued | BUILD UP TYPE SOCKETABLE BGA | 2/28/1996 | 85102352 | 10/1/1999 | 107586 | TW |
| NMXI-0221.00/TW | Utility - ORG | Issued | A METHOD TO PREVENT INTRUSION INTO ELECTRONIC CIRCUITRY | 12/28/1995 | 84114085 | 4/23/2002 | NI 147696 | TW |
| NMXI-0239.00/TW | Utility - ORG | Issued | HIC BASED VBI DECODER | 1/30/1997 | 86101070 | 4/1/1998 | 093868 | TW |
| NMXI-0251.00/TW | Utility - ORG | Issued | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER | 10/15/1997 | 86115173 | 7/11/1999 | 105201 | TW |
| NMXI-0282.00/TW | Utility - ORG | Issued | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN | 12/26/1997 | 86119800 | 2/21/1999 | NI-101313 | TW |
| NMXI-0330.00/TW | Utility - ORG | Issued | CIRCUIT AND METHOD FOR CONTROLLING POWER AND PERFORMANCE BASED ON OPERATING ENVIRONMENT | 5/7/1998 | 87106806 | 8/21/2001 | NI-138833 | TW |
| NMXI-0337.00/TW | Utility - ORG | Issued | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD | 1/13/1999 | 88100466 | 5/28/2001 | NI-132638 | TW |
| NMXI-0376.00/TW | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE | 8/4/1999 | 88107420 | 8/1/2001 | NI-137397 | TW |

PATENT

REEL: 028362 FRAME: 0859

Exhibit B - Foreign Applications and Patents

| App No. | App Type | App Status | Title | File Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|------------|--|------------|------------|------------|------------|---------|
| NMXI-0377.00/TW | Utility - ORG | Issued | METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING | 12/5/2000 | 089125982 | 12/27/2002 | NI-162716 | TW |
| NMXI-0411.00/TW | Utility - ORG | Issued | METHOD AND APPARATUS FOR WRITING UDPS INTO A BUFFER QUEUE INCLUDING OVERWRITING ENTRIES THAT ARE INDICATED TO CONTAIN INVALID UDPS | 2/21/2001 | 090103910 | 7/1/2003 | NI-172931 | TW |
| NMXI-0414.00/TW | Utility - ORG | Issued | IMPROVED FLOURINE DOPED SiO2 FILM AND METHOD OF FABRICATION | 11/16/2000 | 089124269 | 1/1/2005 | 1226100 | TW |
| NMXI-0462.00/TW | Utility - ORG | Issued | ANTENNA INTERFACE | 5/23/2000 | 89109929 | 4/17/2002 | 147310 | TW |
| NMXI-0474.00/TW | Utility - ORG | Issued | SINGLE-CHIP CMOS DIRECT-CONVERSION TRANSDUCER | 12/7/1999 | 88115667 | 12/3/2001 | 138262 | TW |
| NMXI-0478.00/TW | Utility - ORG | Issued | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS | 5/24/2001 | 90112533 | 2/1/2003 | 519657 | TW |
| NMXI-0497.00/TW | Utility - ORG | Abandoned | REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE | 5/30/2001 | 90113072 | 11/1/2005 | 1242733 | TW |
| NMXI-0504.00/TW | Utility - ORG | Issued | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS | 11/21/2001 | 090128857 | 11/11/2003 | NI-189923 | TW |
| NMXI-0532.00/TW | Utility - ORG | Abandoned | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM | 6/27/1997 | 86109133 | 12/16/1998 | 096578 | TW |
| NMXI-0535.00/TW | Utility - ORG | Abandoned | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS | 9/27/2001 | 090125932 | 10/16/2003 | NI-179952 | TW |
| NMXI-0548.00/TW | Utility - ORG | Issued | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING | 3/5/2002 | 091104054 | 7/27/2004 | NI-199496 | TW |
| NMXI-0637.00/TW | Utility - ORG | Issued | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION | 12/9/2002 | 911138032 | 10/11/2005 | 1241355 | TW |
| NMXI-0645.00/TW | Utility - ORG | Issued | DIGITAL LEAKAGE COMPENSATION CIRCUIT | 3/26/2002 | 091105913 | 10/27/2003 | NI-180769 | TW |
| NMXI-0652.00/TW | Utility - ORG | Issued | APPARATUS HAVING A FIRST CIRCUIT SUPPLYING A POWER POTENTIAL TO A SECOND CIRCUIT UNDER A FIRST OPERATING MODE OTHERWISE DECOUPLING THE POWER POTENTIAL | 3/7/2002 | 91104263 | 7/11/2005 | 1235909 | TW |
| NMXI-0653.00/TW | Utility - ORG | Issued | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY | 11/20/2002 | 091132868 | 3/21/2007 | 1277100 | TW |
| NMXI-0659.00/TW | Utility - ORG | Abandoned | ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED | 1/15/2002 | 91100508 | 9/29/2004 | 203147 | TW |
| NMXI-0679.00/TW | Utility - ORG | Issued | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE | 3/12/2002 | 091104602 | 10/1/2003 | NI-187516 | TW |
| NMXI-0694.00/TW | Utility - ORG | Issued | VARIABLE LEVEL MEMORY | 8/28/2002 | 91119529 | 12/11/2006 | 1268511 | TW |
| NMXI-0719.00/TW | Utility - ORG | Abandoned | DIGITAL FRAME DETERMINATION AND APPARATUS | 1/3/2003 | 92100083 | 11/11/2006 | 1266510 | TW |
| NMXI-0721.00/TW | Utility - ORG | Abandoned | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT | 9/23/2002 | 091121741 | 1/21/2008 | 1292902 | TW |
| NMXI-0734.00/TW | Utility - ORG | Abandoned | INTEGRATED OPTOELECTRICAL CIRCUIT PACKAGE WITH OPTICAL WAVEGUIDE INTERCONNECTS | 3/11/2003 | 92105182 | | | TW |
| NMXI-0736.00/TW | Utility - ORG | Issued | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING | 12/13/2002 | 91136074 | 6/4/2004 | 136797 | TW |
| NMXI-0741.00/TW | Utility - ORG | Issued | SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES | 7/4/2005 | 94122531 | 12/1/2009 | 1317868 | TW |
| NMXI-0752.00/TW | Utility - ORG | Abandoned | METHOD, MACHINE-READABLE MEDIUM, APPARATUS AND SYSTEM FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER | 4/17/2003 | 92108929 | | | TW |
| NMXI-0778.00/TW | Utility - ORG | Abandoned | INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK | 2/21/2003 | 92103629 | 12/1/2009 | 1318056 | TW |
| NMXI-0781.00/TW | Utility - ORG | Abandoned | ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMER CIRCUIT | 2/24/2003 | 092103764 | | | TW |
| NMXI-0814.00/TW | Utility - ORG | Issued | ADAPTIVE EQUALIZATION USING A CONDITIONAL UPDATE SIGN-SIGN LEAST MEAN SQUARE ALGORITHM | 9/9/2004 | 95127272 | 12/1/2005 | 1244835 | TW |

PATENT

REEL: 028362 FRAME: 0860

Exhibit B - Foreign Applications and Patents

| App No | App Type | Market Status | Title | Filing Date | Serial No | Issue Date | Patent No | Country |
|------------------|---------------|---------------|--|-------------|----------------|------------|-----------|---------|
| NMXI-0860.00/TW | Utility - ORG | Issued | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD | 2/11/2004 | 93103221 | 8/11/2009 | I313504 | TW |
| NMXI-0887.00/TW | Utility - ORG | Issued | TECHNIQUES TO PERFORM LINE QUALIFICATION | 5/27/2004 | 93115087 | 12/1/2005 | I244854 | TW |
| NMXI-0891.00/TW | Utility - ORG | Issued | STROBE THROUGH DIFFERENTIAL SIGNALING IN MSL | 4/19/2004 | 93110830 | 1/21/2006 | I248255 | TW |
| NMXI-0903.00/TW | Utility - ORG | Issued | TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA | 5/27/2004 | 93115086 | 12/11/2005 | I245503 | TW |
| NMXI-0924.00/TW | Utility - ORG | Abandoned | ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION | 10/28/2004 | 93132733 | 11/1/2009 | I316670 | TW |
| NMXI-0944.00/TW | Utility - ORG | Issued | MOVEL STACKED PACKAGE THROUGH SOLDER BALL WITH STENCIL-PRINTED ENCAPSULANT | 7/27/2005 | 94125491 | 4/21/2008 | I296151 | TW |
| NMXI-0960.00/TW | Utility - ORG | Abandoned | CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM | 12/16/2005 | 94144718 | | | TW |
| NMXI-0962.00/TW | Utility - ORG | Abandoned | REMOTE STATUS FORWARDING IN A DISTRIBUTED CROSS-CONNECTION SYSTEM | 12/16/2005 | 94144719 | | | TW |
| NMXI-0978.00/TW | Utility - ORG | Issued | TECHNIQUES FOR TRANSMITTING AND RECEIVING TRAFFIC OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS | 12/28/2005 | 94147137 | 3/11/2010 | I321931 | TW |
| NMXI-0984.00/TW | Utility - ORG | Issued | RESONANT TUNNELING TRANSISTOR USING CONVENTIONAL CMOS PROCESSING | 10/28/2005 | 094137898 | 12/1/2010 | I334224 | TW |
| NMXI-1028.00/TW | Utility - ORG | Pending | METHOD AND APPARATUS FOR EFFICIENT AND ARTIFACTS FREE DISPLAYING OF ROTATING IMAGES | 12/12/2006 | 95146490 | | | TW |
| NMXI-1034.00/TW | Utility - ORG | Pending | LOW DELAY AND SMALL MEMORY FOOTPRINT PICTURE BUFFERING | 12/1/2006 | 95144673 | | | TW |
| NMXI-10037.00/PC | Utility - ORG | Expired | ANNEAL TO DECREASE MOISTURE ABSORBANCE OF INTERMETAL DIELECTRICS | 5/18/1992 | PCT/US92/04145 | | | WO |
| NMXI-0203.00/PC | Utility - ORG | Completed | PORTABLE DISK DRIVE CARRIER AND CHASSIS | 11/28/1995 | PCT/US95/15443 | | | WO |
| NMXI-0222.00/PC | Utility - ORG | Completed | PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES | 3/12/1996 | PCT/US96/03331 | | | WO |
| NMXI-0239.00/PC | Utility - ORG | Completed | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS | 12/12/1996 | PCT/US96/19808 | | | WO |
| NMXI-0251.00/PC | Utility - ORG | Completed | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER | 9/29/1997 | PCT/US97/17491 | | | WO |
| NMXI-0256.00/PC | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR FAST SELF DESTRUCTION OF A CMOS INTEGRATED CIRCUIT | 12/27/1996 | PCT/US96/20808 | | | WO |
| NMXI-0276.00/PC | Utility - ORG | Expired | METHOD FOR IMPROVED DATA RETENTION IN A NONVOLATILE WRITEABLE MEMORY BY SENSING AND REPROGRAMMING CELL VOLTAGE LEVELS | 10/10/1997 | PCT/US97/18694 | | | WO |
| NMXI-0291.00/PC | Utility - ORG | Expired | INTEGRATED CIRCUIT PACKAGE | 7/9/1997 | PCT/US97/11890 | | | WO |
| NMXI-0308.00/PC | Utility - ORG | Expired | FIDUCIAL FOR ALIGNING AN INTEGRATED CIRCUIT DIE | 12/11/1997 | PCT/US97/22690 | | | WO |
| NMXI-0323.00/PC | Utility - ORG | Expired | PERIPHERAL DEVICE PREVENTING POST-SCAN MODIFICATION | 3/12/1998 | PCT/US98/05010 | | | WO |
| NMXI-0325.00/PC | Utility - ORG | Expired | MULTI-LAYER CODER/DECODER | 9/21/1998 | PCT/US98/19536 | | | WO |
| NMXI-0330.01/PC | Utility - ORG | Expired | CIRCUIT AND METHOD FOR CONTROLLING POWER AND PERFORMANCE BASED ON OPERATING ENVIRONMENT | 4/17/1998 | PCT/US98/07759 | | | WO |
| NMXI-0370.00/PC | Utility - ORG | Expired | POSITION SENSITIVE DISPLAY CONTROLLER | 4/13/1999 | PCT/US99/08034 | | | WO |
| NMXI-0376.00/PC | Utility - ORG | Expired | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE | 3/12/1999 | PCT/US99/05436 | | | WO |
| NMXI-0411.00/PC | Utility - ORG | Expired | METHOD AND APPARATUS FOR WRITING UOPS INTO A BUFFER QUEUE INCLUDING OVERWRITING ENTRIES THAT ARE INDICATED TO CONTAIN INVALID UOPS | 3/13/2001 | PCT/US01/08104 | | | WO |
| NMXI-0414.00/PC | Utility - ORG | Completed | IMPROVED FLOURINE DOPED SiO2 FILM AND METHOD OF FABRICATION | 10/11/2000 | PCT/US00/28164 | | | WO |

PATENT

REEL: 028362 FRAME: 0861

Exhibit B - Foreign Applications and Patents

| Office No. | Matter No. | Matter Status | Title | Filed Date | Serial No. | Issued Date | Patent No. | Country |
|-----------------|---------------|---------------|--|------------|-----------------|-------------|------------|---------|
| NMXI-0430.00/PC | Utility - ORG | Expired | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | 2/8/2000 | PCT/US00/03242 | | | WO |
| NMXI-0462.00/PC | Utility - ORG | Expired | ANTENNA INTERFACE | 5/18/2000 | PCT/US00/13606 | | | WO |
| NMXI-0473.00/PC | Utility - ORG | Completed | EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP | 3/4/1998 | PCT/US98/04175 | | | WO |
| NMXI-0474.00/PC | Utility - ORG | Expired | SINGLE-CHIP CMOS DIRECT-CONVERSION TRANSCIVER | 9/10/1999 | PCT/US99/20792 | | | WO |
| NMXI-0485.00/PC | Utility - ORG | Expired | VITERBI ACS UNIT WITH RENORMALIZATION | 7/15/1994 | PCT/US94/07957 | | | WO |
| NMXI-0486.00/PC | Utility - ORG | Expired | CARRIER FREQUENCY ESTIMATOR FOR A SIGNAL RECEIVER | 5/25/1999 | PCT/US99/10464 | | | WO |
| NMXI-0490.00/PC | Utility - ORG | Expired | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE | 1/27/1993 | PCT/US93/00732 | | | WO |
| NMXI-0495.00/PC | Utility - ORG | Completed | METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE | 6/15/2001 | PCT/US01/19280 | | | WO |
| NMXI-0497.00/PC | Utility - ORG | Expired | REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE | 5/17/2001 | PCT/US01/16135 | | | WO |
| NMXI-0502.00/PC | Utility - ORG | Expired | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS | 11/9/2001 | PCT/US01/49898 | | | WO |
| NMXI-0504.00/PC | Utility - ORG | Completed | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS | 11/20/2001 | PCT/US01/495615 | | | WO |
| NMXI-0516.00/PC | Utility - ORG | Completed | CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORM | 1/24/2001 | PCT/US01/02323 | | | WO |
| NMXI-0548.00/PC | Utility - ORG | Expired | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING | 3/21/2002 | PCT/US02/06751 | | | WO |
| NMXI-0550.00/PC | Utility - ORG | Completed | SYSTEM AND METHOD FOR AUTOMATIC PIR DETECTION | 1/20/1999 | PCT/US99/01202 | | | WO |
| NMXI-0582.00/PC | Utility - ORG | Expired | SYSTEM AND METHOD FOR PROVIDING AUTHENTICATION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY | 11/29/2001 | PCT/US01/44575 | | | WO |
| NMXI-0587.00/PC | Utility - ORG | Expired | SYSTEMS AND METHODS FOR IMPLEMENTING WITH GUARANTEED QoS | 6/25/1999 | PCT/US99/14500 | | | WO |
| NMXI-0598.00/PC | Utility - ORG | Expired | CBR/VBR TRAFFIC SCHEDULER | 6/25/1999 | PCT/US99/14368 | | | WO |
| NMXI-0599.00/PC | Utility - ORG | Completed | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL | 12/21/2001 | PCT/US01/49779 | | | WO |
| NMXI-0526.00/PC | Utility - ORG | Completed | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE | 11/28/2001 | PCT/US01/44721 | | | WO |
| NMXI-0637.00/PC | Utility - ORG | Completed | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION | 12/30/2002 | PCT/US02/41705 | | | WO |
| NMXI-0645.00/PC | Utility - ORG | Expired | DIGITAL LEAKAGE COMPENSATION CIRCUIT | 2/7/2002 | PCT/US02/03633 | | | WO |
| NMXI-0652.00/PC | Utility - ORG | Expired | APPARATUS HAVING A FIRST CIRCUIT SUPPLYING A POWER POTENTIAL TO A SECOND CIRCUIT UNDER A FIRST OPERATING MODE OTHERWISE DECOUPLING THE POWER POTENTIAL | 4/5/2002 | PCT/US02/10816 | | | WO |
| NMXI-0659.00/PC | Utility - ORG | Expired | ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED | 1/25/2002 | PCT/US02/02110 | | | WO |
| NMXI-0663.00/PC | Utility - ORG | Completed | BEST-OF-M PULSE POSITION MODULATION DETECTOR | 5/13/1996 | PCT/US96/06811 | | | WO |
| NMXI-0686.00/PC | Utility - ORG | Abandoned | COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER | 3/13/1998 | PCT/US98/04864 | | | WO |
| NMXI-0694.00/PC | Utility - ORG | Completed | VARIABLE LEVEL MEMORY | 8/6/2002 | PCT/US02/25092 | | | WO |
| NMXI-0710.00/PC | Utility - ORG | Completed | ELECTRONIC ASSEMBLY WITH FILLED NO-FLOW UNDERFILL METHODS OF MANUFACTURE | 10/28/2002 | PCT/US02/34516 | | | WO |
| NMXI-0712.00/PC | Utility - ORG | Abandoned | WALLET FOR PERSONAL INFORMATION DEVICE | 11/11/1999 | PCT/US99/26626 | | | WO |

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REEL: 028362 FRAME: 0862

Exhibit B - Foreign Applications and Patents

| WIPO Class. | Matter No. | Matter Type | Mar. Status | Title | Pub. Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|-----------------|-------------|---|------------|-------------------|------------|------------|------------|---------|
| NMXI-0712.01/PC | Utility - ORG | Expired | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS | 11/16/1999 | PCT/US99/271132 | | | | WO |
| NMXI-0719.00/PC | Utility - ORG | Expired | DIGITAL FRAME DETERMINATION AND APPARATUS | 12/27/2002 | PCT/US02/41548 | | | | WO |
| NMXI-0721.00/PC | Utility - ORG | Expired | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT | 9/5/2002 | PCT/US02/28358 | | | | WO |
| NMXI-0734.00/PC | Utility - ORG | Expired | INTEGRATED OPTOELECTRICAL CIRCUIT PACKAGE WITH OPTICAL WAVEGUIDE INTERCONNECTS | 2/13/2003 | PCT/US03/04584 | | | | WO |
| NMXI-0741.00/PC | Utility - ORG | Completed | SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES | 6/30/2005 | PCT/US05/23733 | | | | WO |
| NMXI-0752.00/PC | Utility - ORG | Expired | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER | 4/10/2003 | PCT/US03/11345 | | | | WO |
| NMXI-0778.00/PC | Utility - ORG | Completed | INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK | 2/5/2003 | PCT/US03/09891 | | | | WO |
| NMXI-0781.00/PC | Utility - ORG | Expired | ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMER CIRCUIT | 3/28/2003 | PCT/US03/09535 | | | | WO |
| NMXI-0786.00/PC | Utility - ORG | Completed | ESTABLISHING AN AD HOC NETWORK | 4/24/2003 | PCT/US03/12902 | | | | WO |
| NMXI-0814.00/PC | Utility - ORG | Expired | ADAPTIVE EQUALIZATION USING A CONDITIONAL UPDATE SIGN-SIGN LEAST MEAN SQUARE ALGORITHM | 9/8/2004 | PCT/US04/29175 | | | | WO |
| NMXI-0839.00/PC | Utility - ORG | Expired | PRONUNCIATION NETWORK | 12/22/2003 | PCT/US03/39108 | | | | WO |
| NMXI-0854.00/PC | Utility - ORG | Expired | APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING | 4/16/2004 | PCT/US2004/012020 | | | | WO |
| NMXI-0860.00/PC | Utility - ORG | Completed | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD | 2/9/2004 | PCT/US04/03817 | | | | WO |
| NMXI-0887.00/PC | Utility - ORG | Expired | TECHNIQUES TO PERFORM LINE QUALIFICATION | 5/26/2004 | PCT/US2004/016583 | | | | WO |
| NMXI-0891.00/PC | Utility - ORG | Expired | STROBE THROUGH DIFFERENTIAL SIGNALING | 4/14/2004 | PCT/US2004/011645 | | | | WO |
| NMXI-0903.00/PC | Utility - ORG | Expired | TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA | 5/26/2004 | PCT/US2004/016587 | | | | WO |
| NMXI-0944.00/PC | Utility - ORG | Abandoned | METHODS AND APPARATUS FOR PROVIDING STACKED-DIE DEVICES | 7/29/2005 | PCT/US2005/027103 | | | | WO |
| NMXI-0960.00/PC | Utility - ORG | Expired | CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM | 12/15/2005 | PCT/US05/46571 | | | | WO |
| NMXI-0962.00/PC | Utility - ORG | Pending | REMOTE STATUS FORWARDING IN A DISTRIBUTED CROSS-CONNECTION SYSTEM | 12/15/2005 | PCT/US05/46580 | | | | WO |
| NMXI-0978.00/PC | Utility - ORG | Abandoned | TECHNIQUES FOR TRANSMITTING AND RECEIVING TRAFFIC OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS | 12/28/2005 | PCT/US2005/047581 | | | | WO |
| NMXI-0980.00/PC | Utility - ORG | Abandoned | AIRFLOW CONTROL SYSTEM | 11/17/2005 | PCT/US05/42254 | | | | WO |
| NMXI-0984.00/PC | Utility - ORG | Expired | RESONANT TUNNELING DEVICE USING METAL OXIDE SEMICONDUCTOR PROCESSING | 10/27/2005 | PCT/US2005/039310 | | | | WO |
| NMXI-1001.00/PC | Utility - ORG | Abandoned | ADVANCED TCA CHASSIS WITH A MOVABLE FAN TRAY | 1/17/2006 | PCT/US06/01918 | | | | WO |
| NMXI-1028.00/PC | Utility - ORG | Pending | METHOD AND APPARATUS FOR EFFICIENT AND ARTIFACTS FREE DISPLAYING OF ROTATING IMAGES | 12/16/2006 | PCT/US2006/046778 | | | | WO |
| NMXI-1034.00/PC | Utility - ORG | Pending | LOW DELAY AND SMALL MEMORY FOOTPRINT PICTURE BUFFERING | 11/30/2006 | PCT/US2006/046360 | | | | WO |
| NMXI-1038.00/PC | Utility - ORG | Pending | ARCHITECTURE FOR VIRTUAL SECURITY MODULE | 9/26/2007 | PCT/US2007/030797 | | | | WO |
| NMXI-1003.00/ZA | Utility - NSPCT | Abandoned | UNIVERSAL CARRIER FOR HOT SWAPPABLE DRIVER | 11/29/1995 | 95/10128 | | 8/28/1996 | 95/10128 | ZA |

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