

## PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT										
NATURE OF CONVEYANCE:	ASSIGNMENT										
CONVEYING PARTY DATA											
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Jung-ho LEE</td> <td>07/12/2011</td> </tr> <tr> <td>Hyun-soo BAE</td> <td>07/12/2011</td> </tr> <tr> <td>Won-hi OH</td> <td>07/12/2011</td> </tr> <tr> <td>Jong-mu LEE</td> <td>07/12/2011</td> </tr> </tbody> </table>	Name	Execution Date	Jung-ho LEE	07/12/2011	Hyun-soo BAE	07/12/2011	Won-hi OH	07/12/2011	Jong-mu LEE	07/12/2011	
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RECEIVING PARTY DATA											
Name:	FAIRCHILD KOREA SEMICONDUCTOR LTD.										
Street Address:	82-3 Dodang-dong, Wonmi-ku										
City:	Bucheon-si, Kyungki-do										
State/Country:	REPUBLIC OF KOREA										
Postal Code:	420-711										
PROPERTY NUMBERS Total: 1											
Property Type	Number										
Application Number:	13324843										
CORRESPONDENCE DATA											
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Email:	jina@OBLLP.com										
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Address Line 1:	P.O. Box 641330										
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ATTORNEY DOCKET NUMBER:	10053.000300										
NAME OF SUBMITTER:	PATRICK D. BENEDICTO										
Total Attachments: 2 source=53-300_Assignment#page1.tif source=53-300_Assignment#page2.tif											

OP \$40.00 13324843

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the person(s) named below (referred herein as "INVENTOR" whether singular or plural) has sold, assigned, and transferred and does hereby sell, assign, and transfer to **FAIRCHILD KOREA SEMICONDUCTOR LTD.**, a Republic of Korea corporation, having a place of business at **82-3, Dodang-dong, Wonmi-gu, Bucheon-si, Gyeonggi-do 420-711, Korea**, ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. The entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the provisional application filed under 35 U.S.C. §111(b) or non-provisional application filed under 35 U.S.C. §111(a) ("APPLICATION"), entitled "**SEMICONDUCTOR DEVICE INCLUDING POWER-ON RESET CIRCUIT**" and with the following particulars:

Filing Date: December 13, 2011

Attorney Docket No.: 10053.000300

Application No.: 13/324,843

2. The entire worldwide right, title, and interest in and to:  
(a) the APPLICATION; (b) all applications claiming priority from the APPLICATION;  
(c) all divisional, provisional, continuation, substitute, renewal, reissue, and other related applications thereto which have been or may be filed in the United States or elsewhere in the world; (d) all patents (including reissues and re-examinations) which may be granted or have been granted on the applications set forth in (a), (b), and (c) above; and (e) all right of priority in and to the APPLICATION, together with all rights to recover damages for infringement of provisional rights.

INVENTOR agrees that ASSIGNEE may apply for and receive patents for SUBJECT MATTER in ASSIGNEE's own name.

INVENTOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all assignments, oaths, powers of attorney, applications, and other papers (and/or documents) necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests herein conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the SUBJECT MATTER and for vesting in ASSIGNEE the rights, titles, and interests herein conveyed. INVENTOR further agrees to provide any successor, assign, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

INVENTOR represents that INVENTOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the INVENTOR has not made and will not hereafter make any assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

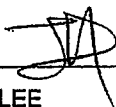
INVENTOR further authorizes and directs the attorneys of record to insert in this document the serial number and filing date of the APPLICATION, identified by the attorney docket number and title set forth above, as soon as the same shall have been made known to them by the United States Patent and Trademark Office.


PATENT

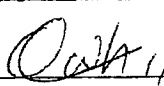
Title of Document: ASSIGNMENT

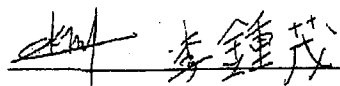
Re:  
 Title: SEMICONDUCTOR DEVICE INCLUDING POWER-ON RESET CIRCUIT  
 Serial No.: 13/324,843  
 Filing Date: December 13, 2011  
 Atty. Docket No.: 10053.000300

This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

<u>Name and Signature</u>	<u>Date of Signature</u>	<u>Date Declaration Executed By This Person</u>
 Jung-ho LEE	July 12, 2011	July 12, 2011

<u>Name and Signature</u>	<u>Date of Signature</u>	<u>Date Declaration Executed By This Person</u>
 Hyun-soo BAE	July 12, 2011	July 12, 2011

<u>Name and Signature</u>	<u>Date of Signature</u>	<u>Date Declaration Executed By This Person</u>
 Won-hi OH	July 12, 2011	July 12, 2011

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