501993527 07/18/2012

PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Micron Technology, Inc.	09/26/2008

RECEIVING PARTY DATA

Name:	Aptina Imaging Corporation	
Street Address:	Walker House, 87 Mary Street	
City:	George Town, Grand Cayman	
State/Country:	CAYMAN ISLANDS	
Postal Code:	KY1-9002	

PROPERTY NUMBERS Total: 1

Property Type	Number
Patent Number:	6707410

CORRESPONDENCE DATA

Fax Number: (415)837-0651

Email: tishihara@treyzlawgroup.com

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

via US Mail.

Correspondent Name: Tiffany Ishihara
Address Line 1: 870 Market Street

Address Line 2: Suite 984

Address Line 4: San Francisco, CALIFORNIA 94102

NAME OF SUBMITTER: Tiffany Ishihara

Total Attachments: 11

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PATENT ASSIGNMENT AGREEMENT

1. **Definitions**

- (a) "Agreement" shall mean this Patent Assignment Agreement.
- (b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.
- (c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.
 - (d) "Effective Date" shall mean October 3, 2008.



- (g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

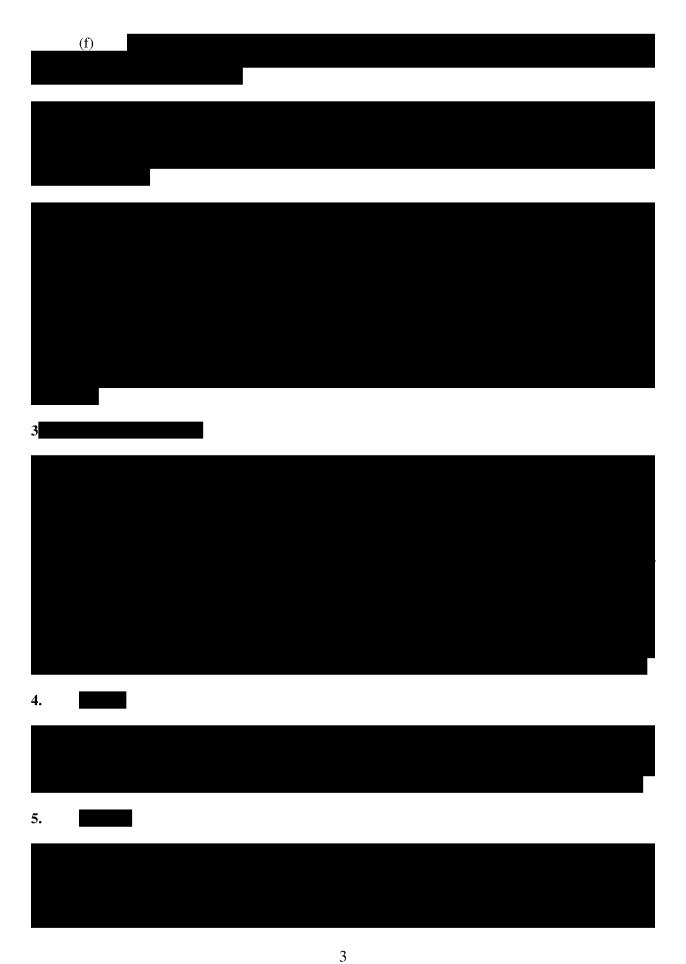


- (a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.
- (b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.



- (d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.
- (e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any,

 Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.





7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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	Micron Technology, Inc.
	Signed:
	Date: 3/20/42 , 2008
Notarization State of Idaho County of Ada	
On 9/26/08, before me, Javia D. Song	_, personally appeared the above-named Skun R. Appleton
who executed this Assignment in my presence and ack authorized capacity for the purposes set forth herein.	nowledged to me that he did so of his own free will and in his Signed:
REVIEWED MTI LEGAL	My commission expires: 8/12/11
R. Sommand	Agreed to by: Aptina Imaging Corporation Signed: Jour J. Date: 9/26/09 , 2008
County of Ada	, personally appeared the above-named Thomas L Lour, Jr.
(doint) dame)	nowledged to me that he did so of his own free will and in his Signed: My commission expires:
OF DATIONAL OF THE PROPERTY OF	

Agreed to by:

[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"

Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE CONTRACTOR ASSESSMENT	COUNTRY NAME	ISSUE DATE
		STAGGERED SUCCESSIVE APPROXIMATION CELLS		
6649899	AVGO-0050.00/US	SYSTEMS AND METHODS OF SAMPLING A PHOTODETECTOR AND PHOTOCELL CIRCUITS INCORPORATING THE SAME	United States of America	Nov 18, 2003
6649993	AVGO-0024.00/US	SIMPLIFIED UPPER ELECTRODE CONTACT STRUCTURE FOR PIN DIODE ACTIVE PIXEL SENSOR	United States of America	Nov 18, 2003
6654054	AVGO-0038.00/US	METHOD AND APPARATUS FOR CANCELING THE EFFECTS OF NOISE IN AN ELECTRONIC SIGNAL	United States of America	Nov 25, 2003
6657755	PBIT-0069.00/US	DIAGONALIZED IMAGE SENSOR PIXELS	United States of America	Dec 2, 2003
6661047	1998-1078.02/US	CMOS IMAGER AND METHOD OF FORMATION	United States of America	Dec 9, 2003
6667768	PBIT-0022.00/US	PHOTODIODE-TYPE PIXEL FOR GLOBAL ELECTONIC SHUTTER AND REDUCED LAG	United States of America	Dec 23, 2003
6677628	PBIT-0047.01/US	PINNED FLOATING PHOTORECEPTOR WITH ACTIVE PIXEL SENSOR	United States of America	Jan 13, 2004
6677872	PBIT-0021.02/US	DIFFERENTIAL NON-LINEARITY CORRECTION SCHEME	United States of America	Jan 13, 2004
6677873	PBIT-0046.01/US	OVERSAMPLED CENTROID A TO D CONVERTER	United States of America	Jan 13, 2004
6686220	1998-1076.01/US	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	United States of America	Feb 3, 2004
6690076	PBIT-0063.00/US	STITCHED CIRCUITS LARGER THAN THE MAXIMUM RETICLE SIZE IN SUB-MICRON PROCESS	United States of America	Feb 10, 2004
6696314	1998-1078.04/US	CMOS IMAGER AND METHOD OF FORMATION	United States of America	Feb 24, 2004
6696997	PBIT-0039.02/US	USING SINGLE LOOKUP TABLE TO CORRECT DIFFERENTIAL NON-LINEARITY ERRORS IN AN ARRAY OF A/D CONVERTERS	United States of America	Feb 24, 2004
6704049	PBIT-0004.00/US	INTERPOLATOR FOR A CMOS IMAGE SENSOR USING A DIGITAL REGISTER	United States of America	Mar 9, 2004
6707410	2002-0329.00/US	DIGITAL PIXEL SENSOR WITH A	United States of America	Mar 16, 2004

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ATTACHMENT "B"

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ATTACHMENT "C"

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ATTACHMENT "D"

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ATTACHMENT "E"

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