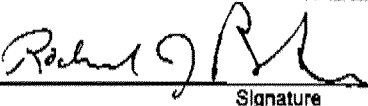


Form PTO-1595 (Rev. 03-11)
OMB No. 0851-0027 (exp. 03/31/2015)

U.S. DEPARTMENT OF COMMERCE
United States Patent and Trademark Office

RECORDATION FORM COVER SHEET PATENTS ONLY	
To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.	
1. Name of conveying party(ies): MICRON TECHNOLOGY, INC. Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	2. Name and address of receiving party(ies) Name: <u>Round Rock Research, LLC</u> Internal Address: _____ Street Address: _____ P. O. Box 1042 City: <u>Mount Kisco</u> State: <u>New York</u> Country: <u>United States of America</u> Zip: <u>10549</u> Additional name(s) & address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No
3. Nature of conveyance/Execution Date(s): Execution Date(s): <u>December 30, 2009</u> <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Change of Name <input type="checkbox"/> Security Agreement <input type="checkbox"/> Joint Research Agreement <input type="checkbox"/> Government Interest Assignment <input type="checkbox"/> Executive Order 9424, Confirmatory License <input type="checkbox"/> Other _____	4. Application or patent number(s): <input type="checkbox"/> This document is being filed together with a new application. A. Patent Application No.(s) _____ B. Patent No.(s) <u>8,234,479</u> Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No
5. Name and address to whom correspondence concerning document should be mailed: Name: <u>Richard J. Botos</u> <u>LERNER, DAVID, LITTENBERG,</u> <u>KRUMHOLZ & MENTLIK, LLP</u> Internal Address: Atty. Dkt.: _____ Street Address: <u>600 South Avenue West</u> City: <u>Westfield</u> State: <u>NJ</u> Zip: <u>07090</u> Phone Number: <u>908-654-5000</u> Fax Number: <u>908-654-7866</u> Email Address: <u>ataylor@ldlkm.com</u>	6. Total number of applications and patents involved: 1 7. Total fee (37 CFR 1.21(h) & 3.41) \$ <u>40.00</u> <input checked="" type="checkbox"/> Authorized to be charged to deposit account <input type="checkbox"/> Enclosed <input type="checkbox"/> None required (government interest not affecting title)
8. Payment Information Deposit Account Number <u>12-1095</u> Authorized User Name <u>Richard J. Botos</u>	
9. Signature: <div style="display: flex; justify-content: space-between; align-items: flex-end;"> <div style="text-align: center;">  Signature <u>Richard J. Botos - 32,016</u> Name of Person Signing </div> <div style="text-align: right;"> <u>July 20, 2012</u> Date Total number of pages including cover sheet, attachments, and documents: 5 </div> </div>	

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EXHIBIT A**CONFIRMATORY ASSIGNMENT OF PATENT RIGHTS**

This Confirmatory Assignment of Patent Rights (the "Agreement") is entered into this 30th day of December, 2009 (the "Closing Date"), by and between Micron Technology, Inc., a Delaware corporation ("Assignor"), and Round Rock Research, LLC, a Delaware limited liability company ("Assignee").

WHEREAS, Assignor and Assignee are party to that certain Patent Sale and Transfer Agreement dated on or about the date hereof (as in effect from time to time, the "Patent Sale and Transfer Agreement"); capitalized terms used but not otherwise defined in this Agreement shall have the respective meaning assigned thereto in the Patent Sale and Transfer Agreement;

WHEREAS, pursuant to the Patent Sale and Transfer Agreement, Assignor has agreed to sell, convey, transfer, assign and deliver to Assignee all of Assignor's (and its Subsidiaries') right, title and interest, including Patent Rights, in and to the Patents listed on Schedule A hereto (collectively, the "Assigned Patents");

WHEREAS, Assignee is now desirous of acquiring the entire and exclusive right, title and interest in and to the Assigned Patents in the United States and throughout the world; and

WHEREAS, Assignor is now willing to assign to Assignee all rights, title and interest in and to the Assigned Patents in the United States and throughout the world; and

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged by Assignor, Assignor hereby assigns, transfers and conveys to Assignee, its successors, legal representatives and assigns, and Assignee hereby accepts, all of Assignor's right, title and interest in the United States and throughout the world in and to the Assigned Patents and any and all Letters Patent that are or may be granted thereon, and any legal equivalent thereof that may be granted in any country or countries foreign to the United States, in each case including without limitation any extensions, substitutes, continuations, continuations-in-part, divisions, reissues, reexaminations, and renewals thereof, or other equivalents thereof, and further, all rights and privileges pertaining to the Assigned Patents and any and all Letters Patent that are or may be granted thereon, and any legal equivalent thereof that may be granted in any country or countries foreign to the United States, including, without limitation, the right, if any, to petition, sue or otherwise seek and recover damages, profits and any other remedy (monetary, injunctive, declaratory or other) in the United States and anywhere throughout the world for any past, present and future infringement thereof, conversion or misappropriation of, or other injury, offense, violation, breach of duty or wrong relating to, any of the Assigned Patents, or any license, agreement, contract or other matter relating thereto.

Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States of America and the appropriate officers of all other jurisdictions in which the Assigned Patents are or may be registered or in which applications included among the Assigned Patents are pending, to record the title of Assignee, its successors, legal representatives and assigns, as owner of all right, title and interest in and to the Assigned Patents, and to issue to Assignee, its successors, legal representatives and assigns, all Letters Patent and any legal

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JOHN M DESMARAIS

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equivalent thereof that may be granted in any country or countries foreign to the United States and recordings of patent rights resulting from any application included among the Assigned Patents, in accordance with the terms of this instrument.

Except to the extent that federal law preempts state law with respect to the matters covered hereby, this Agreement shall be governed by and construed in accordance with the laws of the State of Delaware, without giving effect to any of the principles of conflicts of laws thereof that would result in the application of the laws of another jurisdiction to this Agreement.

This Agreement shall be binding on, and shall inure to the benefit of, the parties hereto and their respective successors and assigns, and may be executed in two or more counterparts, each of which shall be deemed to be an original, but all of which shall constitute one and the same agreement. Each of the parties hereto agrees to accept and be bound by facsimile signatures hereto.

Each party represents that it has taken all necessary action to authorize the execution and delivery of this Agreement.

[The remainder of this page has been intentionally left blank.]

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Dec 30 09 11:27a Henry

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IN WITNESS WHEREOF, the parties hereto have caused this Agreement to be executed by their respective duly authorized officers, as of the date first written above.

ASSIGNOR:

MICRON TECHNOLOGY, INC.

By: [Signature]
Name: Steven R. Appleton
Title: Chairman and CEO

ASSIGNEE:

ROUND ROCK RESEARCH, LLC

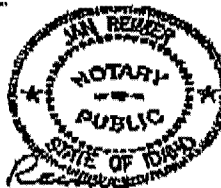
By: [Signature]
Name: John Desmarais
Title: Manager

On this 23rd day of December, 2009, before me appeared Steven R. Appleton, the person who signed this instrument, who acknowledged that he/she signed it as a free act on his/her own behalf or on behalf of the Assignor with authority to do so.

State of IDAHO)

)

ss.

County of Ada)

[Note that federal patent assignments must also include a cover sheet. See 37 C.F.R. 3.28]

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Schedule A Patents

2002-0479.01/US	US	12-Oct-04	METHOD AND SYSTEM FOR CONTROLLING MEMORY ACCESSSES TO MEMORY MODULES HAVING A MEMORY HUB ARCHITECTURE	Issued	24-Jul-07 7249236	10/963,824
2002-0479.02/US	US	24-Jul-07	METHOD AND SYSTEM FOR CONTROLLING MEMORY ACCESSSES TO MEMORY MODULES HAVING A MEMORY HUB ARCHITECTURE	Allowed		11/881,010
2002-0480.00/US	US	20-Jun-03	MEMORY HUB AND ACCESS METHOD HAVING INTERNAL PREFETCH BUFFERS	Issued	21-Aug-07 7260685	10/601,252
2002-0480.01/US	US	23-Aug-06	MEMORY HUB AND ACCESS METHOD HAVING INTERNAL PREFETCH BUFFERS	Issued	12-Aug-08 7412566	11/510,150
2002-0480.02/US	US	04-Aug-08	MEMORY HUB AND ACCESS METHOD HAVING INTERNAL PREFETCH BUFFERS	Pending		12/185,815
2002-0481.00/US	US	28-Aug-02	INSITU CMP PAD METROLOGY USING ULTRASONIC IMAGING	Issued	26-Jun-07 7235488	10/231,801
2002-0481.01/US	US	21-Oct-04	INSITU CMP PAD METROLOGY USING ULTRASONIC IMAGING	Issued	10-Apr-07 7201632	10/970,338
2002-0481.02/US	US	15-Mar-07	IN-SITU CHEMICAL-MECHANICAL PLANARIZATION PAD METROLOGY USING ULTRASONIC IMAGING	Issued	11-Dec-07 7306506	11/725,030
2002-0482.00/US	US	16-Aug-02	MEMORY HUB BYPASS CIRCUIT AND METHOD	Issued	12-Dec-06 7149674	10/222,415
2002-0482.01/US	US	21-Jan-05	MEMORY HUB BYPASS CIRCUIT AND METHOD	Issued	16-May-06 7047351	11/041,071
2002-0482.02/US	US	04-Apr-06	MEMORY HUB BYPASS CIRCUIT AND METHOD	Issued	19-Aug-08 7415567	11/398,018
2002-0483.00/CN	CN	05-Aug-03	SYSTEM AND METHOD FOR SELF TESTING AND REPAIR OF MEMORY MODULES	Allowed		03824359.8
2002-0483.00/DE	DE	05-Aug-03	SYSTEM AND METHOD FOR SELF TESTING AND REPAIR OF MEMORY MODULES	Issued	25-Mar-09 1535131	6030280854.. 4-08
2002-0483.00/EP	EP	05-Aug-03	SYSTEM AND METHOD FOR SELF TESTING AND REPAIR OF MEMORY MODULES	Issued	25-Mar-09 1535131	03788336.0
2002-0483.00/GB	GB	05-Aug-03	SYSTEM AND METHOD FOR SELF TESTING AND REPAIR OF MEMORY MODULES	Issued	25-Mar-09 1535131	03788336.0
2002-0483.00/JP	JP	05-Aug-03	SYSTEM AND METHOD FOR SELF TESTING AND REPAIR OF MEMORY MODULES	Allowed		2004-529260
2002-0483.00/KR	KR	05-Aug-03	SYSTEM AND METHOD FOR SELF TESTING AND REPAIR OF MEMORY MODULES	Issued	18-Jul-08 848254	10-2005- 7002663
2002-0483.00/SG	SG	05-Aug-03	SYSTEM AND METHOD FOR SELF TESTING AND REPAIR OF MEMORY MODULES	Issued	30-Nov-06 110481	2005500914 7
2002-0483.00/TW	TW	15-Aug-03	SYSTEM AND METHOD FOR SELF-TESTING AND REPAIR OF MEMORY MODULES	Issued	01-Nov-05 1242780	092122492
2002-0483.00/US	US	16-Aug-02	SYSTEM AND METHOD FOR SELF-TESTING AND REPAIR OF MEMORY MODULES	Issued	22-Jun-04 6754117	10/222,393
2002-0494.00/US	US	26-Dec-02	TEMPERATURE COMPENSATED OUTPUT BUFFER CIRCUIT	Issued	03-Feb-04 6667165	10/329,839
2002-0494.01/US	US	30-Sep-03	TEMPERATURE COMPENSATED OUTPUT BUFFER CIRCUIT	Issued	14-Jun-05 6908965	10/675,692
2002-0507.00/US	US	31-Mar-03	MEMORY DEVICES WITH BUFFERED COMMAND ADDRESS BUS	Issued	12-Dec-06 7149841	10/405,257
2002-0507.01/US	US	03-Nov-06	MEMORY DEVICES WITH BUFFERED COMMAND ADDRESS BUS	Allowed		11/592,944
2002-0522.00/CN	CN	26-Sep-03	ATOMIC LAYER DEPOSITION METHODS AND ATOMIC LAYER DEPOSITION TOOLS	Issued	ZL038238 31-Oct-07 187	03823818.7
2002-0522.00/DE	DE	26-Sep-03	ATOMIC LAYER DEPOSITION METHODS AND ATOMIC LAYER DEPOSITION TOOLS	Issued	1556888E 01-Apr-09 P	03808098.2 EP
2002-0522.00/EP	EP	26-Sep-03	ATOMIC LAYER DEPOSITION METHODS AND ATOMIC LAYER DEPOSITION TOOLS	Issued	01-Apr-09 1556888	03808098.2
2002-0522.00/KR	KR	26-Sep-03	ATOMIC LAYER DEPOSITION METHODS AND ATOMIC LAYER DEPOSITION TOOLS	Issued	01-Sep-06 0622188	10-2005- 7005823
2002-0522.00/SG	SG	26-Sep-03	ATOMIC LAYER DEPOSITION METHODS AND ATOMIC LAYER DEPOSITION TOOLS	Pending		200502093.8
2002-0522.00/TW	TW	07-Oct-03	ATOMIC LAYER DEPOSITION METHODS AND ATOMIC LAYER DEPOSITION TOOLS	Issued	21-Jun-05 1234812	092127784