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PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	EXECUTIVE ORDER 9424, CONFIRMATORY LICENSE

CONVEYING PARTY DATA

Name	Execution Date
California Institute of Technology	04/01/2005

RECEIVING PARTY DATA

Name:	National Science Foundation	
Street Address:	4201 Wilson Blvd	
Internal Address:	Room 1265	
City:	Arlington	
State/Country:	VIRGINIA	
Postal Code:	22230	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	10974006

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PATENT 502014266 REEL: 028726 FRAME: 0482

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License to the United States Government

1073501-03-0033

Invention Title: Method for Integration of Pre-Fabricated Chip Structures into Functional

Electronic Systems

CIT 3983

Inventors: Yu-Chong Tai; Damien C. Rodger

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Patent Application Serial No: 10/974,006

US Filing/Issue Date: 10/25/2004

Grant/Contract Identification No: EEC-0310723; EEC-9402726

Foreign Applications filed/intended in (countries):

The invention identified above is a Subject Invention under 35 U.S.C. 200, et seq., and the Standard Patent Rights clause at 37 CFR 401.14 or FAR 52.227-11, which are included among the terms of the above-identified grant/contract award from the National Science Foundation. This document is confirmatory of:

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The Government is here by granted an irrevocable power to inspect and make copies of the above-identified patent application.

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The Intellectual Property Counsel

CIT File No: 3983

Dated: April 1, 2005

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