

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	Confirmatory Assignment
CONVEYING PARTY DATA	
Name	Execution Date
NEC Corporation	04/17/2011
RECEIVING PARTY DATA	
Name:	NEC LCD TECHNOLOGIES, LTD.
Street Address:	1753 SHIMONUMABE
Internal Address:	NAKAHARA-KU, KAWASAKI
City:	KANAGAWA
State/Country:	JAPAN
PROPERTY NUMBERS Total: 6	
Property Type	Number
Patent Number:	6953976
Patent Number:	6977422
Patent Number:	7030467
Patent Number:	6949766
Patent Number:	7554164
Patent Number:	6791145
CORRESPONDENCE DATA	
Fax Number:	2166968731
Phone:	216-696-8730
Email:	epas@thepatentattorneys.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
Correspondent Name:	Turocy & Watson, LLP
Address Line 1:	127 Public Square
Address Line 2:	57th Floor, Key Tower
Address Line 4:	Cleveland, OHIO 44114

OP \$240.00 6953976

ATTORNEY DOCKET NUMBER:	VNEC MATTERS
NAME OF SUBMITTER:	Thomas Watson
Total Attachments: 2 source=CA2#page1.tif source=CA2#page2.tif	

CONFIRMATORY ASSIGNMENT OF PATENT RIGHTS

For valuable consideration, the receipt and sufficiency of which is hereby acknowledged, on this 17 day of April, 2011, NEC Corporation, a Japanese corporation ("**Assignor**"), to ensure that the assignment dated April 1, 2003 and filed with the United States Patent and Trademark Office on May 13, 2003 at Reel/Frame 013649/0947 (the "**Original Assignment**"), is completed, hereby sells, assigns, and transfers to NEC LCD Technologies, Ltd. ("**Assignee**"), the full extent of all right, title, and interest in and to any and all of the following (collectively, the "**Rights**");

1. The patents, provisional patent applications and patent applications listed in the table below (individually and collectively referred to herein as the "**Patents**");
2. All inventions claimed or described in the Patents (collectively, the "**Inventions**");
3. All rights with respect to the Inventions, including all U.S. patents or other governmental grants or issuances that may be granted with respect to the Inventions or from any direct or indirect divisionals, continuations, continuations-in-part, or other patent applications claiming priority rights from the Patents ("**Potential Patents**");
4. All reissues, reexaminations, extensions, or registrations of the Patents or Potential Patents;
5. All non-United States patents, patent applications, and counterparts relating to any or all of the Inventions, the Patents, or Potential Patents, including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances ("**Foreign Rights**"), and including the right to file foreign applications directly in the name of Assignee, its successors and assigns;
6. The right to claim priority rights deriving from the Patents; and
7. All causes of action and remedies related to the Patents, the Inventions, Potential Patents, or Foreign Rights (including, without limitation, the right to sue for past, present, or future infringement, misappropriation or violation of rights related to any of the foregoing and the right to collect royalties and other payments under or on account of any of the foregoing).

Patent or Application No.	Filing Date	Title of Patent and First Named Inventor
6,953,976	11/21/2002	Method of deforming a pattern and semiconductor device formed by utilizing deformed pattern Kido, Shusaku
6,977,422	2/17/2004	Method of deforming a pattern and semiconductor device formed by utilizing deformed pattern Kido, Shusaku
7,030,467	2/17/2004	Method of deforming a pattern and semiconductor device formed by utilizing deformed pattern Kido, Shusaku
6,949,766	2/17/2004	Method of deforming a pattern

Patent or Application No.	Filing Date	Title of Patent and First Named Inventor
		and semiconductor device formed by utilizing deformed pattern Kido, Shusaku
7,554,164	7/25/2005	Semiconductor device having a gap between a gate electrode and a dummy gate electrode Kido, Shusaku
6,791,145	2/17/2004	Semiconductor device formed by utilizing deformed pattern Kido, Shusaku

This Confirmatory Assignment of Patent Rights (this "Assignment") is made fully effective on the date of the Original Assignment, thereby confirming the Assignor's intent to transfer all of the Rights as of that date.

Assignor will not sign any writing or do any act conflicting with this Assignment, and, without further compensation, will sign all documents and do such additional acts as Assignee, its successors, legal representatives, and assigns deem necessary or desirable to perfect enjoyment of the Rights, conduct proceedings regarding the Rights (including any litigation or interference proceedings), or perfect or defend title to the Rights. Assignor requests the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models, or other governmental grants or issuances that may be granted upon any of the Rights in the name of the Assignee, as the assignee to the entire interest therein.

The terms and conditions of this Assignment will inure to the benefit of Assignee, its successors, legal representatives, and assigns and will be binding upon Assignor, its successors, legal representatives and assigns.

NEC CORPORATION

By: Kazuhiko Yamada
(Signature)

Name: Kazuhiko Yamada
General Manager,
Intellectual Asset Development
Promotion Division

Date: April 17, 2011