

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
STMicroelectronics S.A., (formerly known as SGS-Thomson Microelectronics S.A.)	05/23/2012
RECEIVING PARTY DATA	
Name:	MICRON TECHNOLOGY, INC.
Street Address:	8000 South Federal Way
City:	Boise
State/Country:	IDAHO
Postal Code:	83707
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7163154
CORRESPONDENCE DATA	
Fax Number:	9497609502
Phone:	9497600404
Email:	debbie.fonseca@knobbe.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
Correspondent Name:	Knobbe, Martens, Olson & Bear LLP
Address Line 1:	2040 Main Street
Address Line 2:	14th Floor
Address Line 4:	Irvine, CALIFORNIA 92614
ATTORNEY DOCKET NUMBER:	MICRON.423NP
NAME OF SUBMITTER:	Zi Y. Wong
Total Attachments: 11 source=MICRON_423NP#page1.tif source=MICRON_423NP#page2.tif source=MICRON_423NP#page3.tif	

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ASSIGNMENT

WHEREAS, STMicroelectronics S.A., (formerly known as SGS-Thomson Microelectronics S.A.), a corporation organized under the laws of France, having a place of business at 29 Boulevard Romain Rolland, 92120 Montrouge, France (hereinafter referred to as "Assignor") has rights in and to the following:

LISTED IN EXHIBIT A

(said patents, patent applications and invention disclosures listed in Exhibit A being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Assignor is a wholly owned subsidiary of STMicroelectronics NV, a Dutch corporation organized under the laws of The Netherlands, acting through its Swiss branch, having an office at 39 Chemin du Champ-des-Filles, 1228 Plan-les-Ouates, Geneva, Switzerland (hereinafter referred to as "Assignee");

WHEREAS, Assignee requested Assignor to develop certain technology for Assignee;

WHEREAS, Assignee sponsored, paid for and is the equitable and beneficial owner of all resulting intellectual property resulting from such technology development, including SAID PATENT RIGHTS and related rights;

WHEREAS, Assignee and Assignor recognize that Assignee already owns all equitable and beneficial ownership in SAID PATENT RIGHTS and related rights;

WHEREAS, Assignor now desires to transfer the legal title in and to SAID PATENT RIGHTS to Assignee and confirm all ownership in Assignee to SAID PATENT RIGHTS and related rights that may have unintentionally remained in Assignor; and

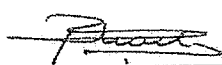
WHEREAS, Assignee desires to perfect registration in Assignee's name all the above mentioned rights, title and interest in and to SAID PATENT RIGHTS and related rights.

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby transfer and assign to Assignee legal title, and any and all rights it may own (beyond said legal title, if any) in and to SAID PATENT RIGHTS, together with any and all continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing (to the extent not already owned by Assignee), together

with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be executed by its duly authorized officer on this 20 day of January, 2008.

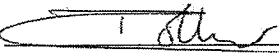
STMicroelectronics S.A.

By: 

Printed Name: PATRICE CHASTAGNER

Title: Chairman

STMicroelectronics NV

By: 

Printed Name: Pierre OLLIVIER

Title: Corporate General Counsel

EXHIBIT A

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
4811291	54924	US	Granted		07052088
RE35,578	010028	US	Granted		08198208
	010028	US			7283214
	010028	KR			1660788
	010028	JP	Granted	2761501	31607488
	010028	IT			88403140.2
	010028	GB			88403140.2
	010028	FR			8717386
	010028	EP	Granted	321327	88403140.2
	010028	DE			88403140.2
RE35,385	010033	US	Granted		08336580
	010033	US			07283305
	010033	US			07912867
	010033	KR			1718688
	010033	JP	Granted	2931864	3247668
	010033	IT			88403141.0
	010033	GB			88403141.0
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6549048	00-RO-099	US	Granted		09925979
	00-RO-099	FR	Granted	2812984	0010554
6807103	00-RO-151	US	Granted		10438733
	00-RO-151	GB	Granted	1342244	01996861.9
	00-RO-151	FR	Granted	1342244	01996861.9
6774663	00-RO-209	US	Granted		10060105
	00-RO-209	GB	Granted	1227329	02290165.6
	00-RO-209	FR	Granted	1227329	02290165.6
6928530	00-RO-354	US	Granted		10081740
	00-RO-354	JP	Publication		200246868
	00-RO-354	EP	Publication		02290278.7
6568510	00-RO-377	US	Granted		09997214
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	00-RO-379	IT	Granted	1368784	02706857.6
	00-RO-379	GB	Granted	1368784	02706857.6
	00-RO-379	FR	Granted	1368784	02706857.6
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	00-RO-379	DE	Granted	1368784	02706857.6
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	01-RO-021	EP	Publication		02366003.8
	01-RO-040	FR	Granted	2831315	0113635
	01-RO-116	EP	Publication		02358013.7
	01-RO-116	EP	Publication		02358012.9
	01-RO-213	FR	Granted	2832566	0114969
	02-RO-010	EP	Notification 51(4)		03358005.1
	02-RO-016	FR	Granted	2843660	0210350
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6965526	02-RO-333	US	Granted		10775032
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7012837	02-RO-394	US	Granted		10903927
	02-RO-394	FR	Publication		0309456
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	02-RO-472	EP	Publication		04012393.7
7050335	03-RO-150	US	Granted		10867378
	03-RO-150	EP	Publication		04013056.9
7068538	03-RO-192	US	Granted		10921365
	03-RO-192	EP	Publication		04291653.6
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5430319	93-RO-025	US	Granted		08246070
5661324	93-RO-025	US	Granted		08399900
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5537349	93-RO-106	US	Granted		08361996
	93-RO-106	JP	Granted	2886472	33625894
5863866	93-RO-115	US	Granted		08379337
6265277	93-RO-115	US	Granted		09220635
5900756	93-RO-123	US	Granted		08767521
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5729182	93-RO-131	US	Granted		08811358
	93-RO-131	JP	Granted	2919299	9956195
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5677870	93-RO-133	US	Granted		08752814
5721706	93-RO-133	US	Granted		08754432
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5612611	93-RO-135	US	Granted		08423398
	93-RO-137	FR	Granted	678868	95470013.4

US Pat. No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
5619451	93-RO-140	US	Granted		08421671
5796653	93-RO-142	US	Granted		08394314
	93-RO-142	JP	Granted	2833646	5972695
5644530	93-RO-143	US	Granted		08418636
5651128	93-RO-144	US	Granted		08484873
5594693	93-RO-149	US	Granted		08408016
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5742548	94-RO-138	US	Granted		08526500
5699293	94-RO-172	US	Granted		08649282
5652720	94-RO-205	US	Granted		08573897
5841314	94-RO-206	US	Granted		08653524
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5717323	94-RO-210	US	Granted		08576881
5774390	94-RO-211	US	Granted		08652710
5812446	95-RO-033	US	Granted		08623044
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5760638	95-RO-064	US	Granted		08664063
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US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
	98-RO-092	FR	Granted	2782421	9810308
6212112	98-RO-186	US	Granted		09452446
	98-RO-186	FR	Granted	2787233	9815786
6249456	98-RO-214	US	Granted		09448632
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	98-RO-214	FR	Granted	1006532	99460065.8
	98-RO-321	FR	Granted	2792761	9805051
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	99-RO-015	JP	Publication		2001508458
	99-RO-015	GB	Granted	1196926	00946012.2
	99-RO-015	FR	Granted	1196926	00946012.2
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	99-RO-050	GB	Granted	1085520	00402549.0
	99-RO-050	FR	Granted	1085520	00402549.0
	99-RO-050	DE	Granted	1085520	00402549.0
6307792	99-RO-051	US	Granted		09675366
6385096	99-RO-051	US	Granted		09952904
	99-RO-051	EP	Publication		00402443.6
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	99-RO-277	EP	Publication		00125557.9
5483492	OBF10725	US	Granted		08075543
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4964079	10127	US	Granted		07342476
	10127	JP	Granted	3101697	10714189

EXHIBIT A
ST Ref - Invent
06-RO-177
06-RO-401
06-RO-542
06-RO-548
06-RO-564

EXHIBIT A

ST Ref.	Country	Filing Number	Patent number	Status
00-RO-049	FR	0006262	2809246	Granted
03-RO-379	US	11008586		Publication
03-RO-379	EP	04029018.1		Publication
03-RO-492	EP	04029017.3		Publication
03-RO-493	US	11008588		Publication
03-RO-493	EP	04029016.5		Publication
99-RO-060	FR	9911858	2798769	Granted
02-RO-017	US	11067345		Notice of Allowance

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CONFIRMATION OF ASSIGNMENT

WHEREAS, STMicroelectronics NV, a Dutch corporation organized under the laws of The Netherlands, acting through its Swiss branch, having an office at 39 Chemin du Champ-des-Filles, 1228 Plan-les-Ouates, Geneva, Switzerland (hereinafter referred to as "Assignor") previously assigned its rights in and to the following:

LISTED IN EXHIBIT A

(said patents, patent applications and invention disclosures listed in Exhibit A being hereinafter referred to as "SAID PATENT RIGHTS"); and

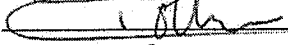
WHEREAS, Numonyx BV is a corporation having a principal place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte de l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignee") previously obtained rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, this agreement confirms that Assignor has sold, conveyed, transferred and assigned to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Confirmation of Assignment to be executed by its duly authorized officer on this 25 day of April, 2012.

ASSIGNOR

STMicroelectronics NV

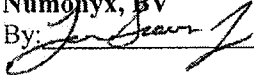
By: 

Printed Name: Pierre OLLIVIER

Title: Corporate General Counsel

ASIGNEE

Numonyx, BV

By: 

Printed Name: Thomas L. Law Jr.

Title: Director

Exhibit A

FMG with US Granted:

US Pat. No.	Country	Non-US Pat. number	Filing Number
6816404	US		10319439
	EP		01830806.4
6946673	US		10345129
	US		110201790
	EP		02425013.6
6891747	US		10372761
	US		11045170
	EP		02425087.0
6972430	US		10371154
	US		11258340
	EP		02425088.8
6930913	US		10372639
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	IT		88403141.0
	JP	2931864	3247668
	KR		1718688
	US		07283305
	US		07912867
RE35,578	US		08198208
	US		7283214
	DE		88403140.2
	EP	321327	88403140.2
	FR		8717386
	GB		88403140.2
	IT		88403140.2
	JP	2761501	31607488
	KR		1660788
7163154	US		10471883
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	FR	1368784	02706857.6
	FR		0103401
	GB	1368784	02706857.6
	IT	1368784	02706857.6
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	FR	0903688	98402303.6
	FR		9711787
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	IT	0903688	98402303.6
	JP	3065586	2693551998
6140869	US		09234194
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6140869	FR	0932283	99400126.1
6140869	FR	2773933	9800584
6842191	US		09705145
	EP		00308954.7
	GB		9925902.0
6519682	US		09205598
	FR	1006448	99309641.1

PATENT ASSIGNMENT AGREEMENT

WHEREAS, Numonyx B.V., a company organized under the laws of Netherlands, having a place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte. De l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignor") has rights in and to the following:

1. U.S. Letters Patents Listed on Exhibit A,
2. U.S. Patent Applications listed on Exhibit B,
3. Foreign patents and patent applications listed on Exhibit C.

(said patents and patent applications listed in Exhibits A, B, and C being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Micron Technology, Inc. is a corporation having a principal place of business at 8000 S. Federal Way, Boise, Idaho 83707, United States (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuation-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be executed by its duly authorized officer effective as of the 23 day of May, 2012

Numonyx B.V.

By:

Thomas L. Laws Jr.
Printed Name: Thomas L. Laws Jr.
Title: Director

Micron Technology, Inc.

By:

Roderic W. Lewis
Printed Name: Roderic W. Lewis
Title: V.P. of Legal Affairs

STMicroelectronics originated

Exhibit A - US Issued Patents

Patent No.	Utility - ORG	Material	Patent Status	Title	Filed Date	Serial No.	Issued Date	Patent No.	Country
NMXXS-0733.00/US	Utility - ORG		Issued	MANUFACTURING PROCESS OF A SEMICONDUCTOR NON-VOLATILE MEMORY CELL	12/18/2002	10/323,615	8/28/2007	7,262,098	US
NMXXS-0734.00/US	Utility - ORG		Issued	PROCESS FOR REMOVING POLYMERS DURING THE FABRICATION OF SEMICONDUCTOR DEVICES	7/2/2002	10/189,152	4/13/2004	6,770,271	US
NMXXS-0735.00/US	Utility - ORG		Issued	LOW POWER CHARGE PUMP CIRCUIT	11/7/2002	10/290,030	11/9/2004	6,816,001	US
NMXXS-0736.00/US	Utility - ORG		Issued	CONTENT ADDRESSABLE MEMORY (CAM) WITH BATTERY BACK-UP	6/29/2001	09/895,491	12/17/2002	6,496,439	US
NMXXS-0737.00/US	Utility - ORG		Issued	Data output buffer with precharge	8/11/2000	09/636,363	9/18/2001	6,292,405	US
NMXXS-0738.00/US	Utility - ORG		Issued	INTERLACED MEMORY DEVICE WITH RANDOM OR SEQUENTIAL ACCESS	10/15/2001	09/977,561	3/2/2004	6,701,419	US
NMXXS-0739.00/US	Utility - ORG		Issued	OUTPUT BUFFER AND METHOD OF DRIVING	8/8/2001	09/925,842	12/3/2002	6,489,807	US
NMXXS-0740.00/US	Utility - ORG		Issued	GENERATOR CIRCUIT FOR VOLTAGE RAMP AND CORRESPONDING VOLTAGE GENERATION METHOD	12/28/2001	10/033,711	11/18/2003	6,650,153	US
NMXXS-0741.00/US	Utility - ORG		Issued	BOOST DEVICE FOR NONVOLATILE MEMORIES WITH AN INTEGRATED STAND-BY CHARGE PUMP	2/13/2002	10/076,134	11/18/2003	6,650,569	US
NMXXS-0742.00/US	Utility - ORG		Abandoned	CHARGE PUMP FOR A NONVOLATILE MEMORY WITH READ VOLTAGE REGULATION IN THE PRESENCE OF ADDRESS SKEW, AND NONVOLATILE MEMORY COMPRISING SUCH A CHARGE PUMP	2/5/2002	10/068,560	5/6/2003	6,560,145	US
NMXXS-0743.00/US	Utility - ORG		Issued	DEVICE AND METHOD FOR TIMING THE READING OF A NONVOLATILE MEMORY WITH REDUCED SWITCHING NOISE	2/15/2002	10/077,687	1/6/2004	6,674,666	US
NMXXS-0744.00/US	Utility - ORG		Issued	DEVICE AND METHOD FOR AUTOMATICALLY GENERATING AN APPROPRIATE NUMBER OF WAIT CYCLES WHILE READING A NONVOLATILE MEMORY	4/3/2002	10/115,888	7/12/2005	6,917,994	US
NMXXS-0745.00/US	Utility - ORG		Issued	CIRCUIT FOR GENERATING A PULSE SIGNAL INDEPENDENT OF VOLTAGE AND TEMPERATURE VARIATIONS	10/31/2001	09/998,903	4/15/2003	6,549,486	US
NMXXS-0746.00/US	Utility - ORG		Issued	NEGATIVE CHARGE PUMP ARCHITECTURE WITH SELF-GENERATED BOOSTED PHASES	10/31/2001	09/998,902	4/13/2004	6,770,822	US
NMXXS-0748.00/US	Utility - ORG		Issued	THRESHOLD AMPLIFIER	8/9/2001	09/925,979	4/15/2003	6,549,048	US
NMXXS-0749.00/US	Utility - NSPCT		Issued	PAGE-ERASABLE FLASH MEMORY	11/14/2001	10/438,733	10/19/2004	6,807,103	US
NMXXS-0750.00/US	Utility - ORG		Issued	CIRCUIT FOR THE DETECTION OF A DEFECTIVE POWER SUPPLY CONNECTION	1/29/2002	10/060,105	8/10/2004	6,774,663	US
NMXXS-0751.00/US	Utility - ORG		Issued	METHOD AND DEVICE FOR SEQUENTIAL READOUT OF A MEMORY WITH ADDRESS JUMP	2/22/2002	10/081,740	8/9/2005	6,928,530	US
NMXXS-0752.00/US	Utility - ORG		Issued	FLASH MEMORY INCLUDING MEANS OF CHECKING MEMORY CELL THRESHOLD VOLTAGES	11/15/2001	09/997,214	5/27/2003	6,568,510	US
NMXXS-0752.01/US	Utility - DIV		Issued	FLASH MEMORY INCLUDING MEANS OF CHECKING MEMORY CELL THRESHOLD VOLTAGES	1/28/2003	10/352,581	3/30/2004	6,714,453	US
NMXXS-0753.00/US	Utility - NSPCT		Issued	CONTACT LESS IC CARD WITH OPERATING SYSTEM USED IN CONTACT TYPE CARDS AND READER FOR SUCH CONTACTLESS CARDS	2/14/2002	10/471,883	1/16/2007	7,163,154	US
NMXXS-0754.00/US	Utility - ORG		Issued	FLASH EEPROM AND EPROM ARRAYS WITH SELECT TRANSISTORS WITHIN THE BIT LINE PITCH	3/11/1994	08/212,176	9/17/1996	5,557,124	US
NMXXS-0755.00/US	Utility - ORG		Issued	METHOD OF RE-PROGRAMMING AN ARRAY OF NON-VOLATILE MEMORY CELLS, IN PARTICULAR OF THE NOR ARCHITECTURE FLASH TYPE, AFTER AN ERASE OPERATION, AND A CORRESPONDING MEMORY DEVICE	6/12/2002	10/171,078	11/25/2003	6,654,287	US
NMXXS-0755.00/US	Utility - ORG		Issued	MEMORY DEVICE	10/24/2001	10/041,684	5/20/2003	6,567,296	US
NMXXS-0757.00/US	Utility - ORG		Issued	WORD LINE SELECTOR FOR A SEMICONDUCTOR MEMORY	2/20/2003	10/372,626	3/8/2005	6,865,114	US
NMXXS-0758.00/US	Utility - ORG		Issued	DESIGN FAILURE MODE EFFECT ANALYSIS (DFMEA)	12/26/2002	10/330,485	4/25/2006	7,085,769	US
NMXXS-0759.00/US	Utility - ORG		Issued	POWER SUPPLY CIRCUIT STRUCTURE FOR A ROW DECODER OF A MULTILEVEL NON-VOLATILE MEMORY DEVICE	12/30/2002	10/334,126	12/7/2004	6,829,168	US
NMXXS-0760.00/US	Utility - ORG		Issued	BINARY ENCODING CIRCUIT	12/20/2002	10/325,707	2/24/2004	6,696,990	US
NMXXS-0761.00/US	Utility - ORG		Issued	REGULATION METHOD FOR THE DRAIN, BODY AND SOURCE TERMINALS VOLTAGES IN A NON-VOLATILE MEMORY CELL DURING A PROGRAM PHASE AND CORRESPONDING PROGRAM CIRCUIT	12/27/2002	10/331,116	10/26/2004	6,809,961	US

PATENT

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