

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
General Magic, Inc.	04/22/2003
RECEIVING PARTY DATA	
Name:	Intellectual Ventures Patent Holding I, L.L.C.
Street Address:	1422 130th Ave. NE
City:	Bellevue
State/Country:	WASHINGTON
Postal Code:	98005
PROPERTY NUMBERS Total: 3	
Property Type	Number
Patent Number:	6839733
Patent Number:	7949752
Application Number:	13112540
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Total Attachments: 2 source=2222030assignment#page1.tif source=2222030assignment#page2.tif	

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ASSIGNMENT OF PATENT RIGHTS

Whereas, General Magic, Inc., a Delaware corporation, which operated its business at an office at 420 N. Mary Avenue, Sunnyvale, CA 94085 (hereinafter "Seller") is the sole and exclusive owner of certain United States patents listed in Exhibit A annexed hereto (collectively referred to as the "Patents"); and

Whereas Intellectual Ventures Patent Holding I, LLC, a Delaware limited liability company, a Delaware corporation, with the office of its manager located at 1422 130th Ave NE, Bellevue, WA 98005 (hereinafter "Purchaser") is desirous of acquiring the right, title and interest in, to and under the said Patents.

Now, Therefore,

For good and valuable consideration, the receipt of which is hereby acknowledged, Seller does hereby sell, assign, transfer and set over to Purchaser, the Patents aforesaid, any reissue or reissues of said Patents already granted and which may be granted, any certificates of reexamination already granted and which may be granted the same to be held and enjoyed by Purchaser for its own use and enjoyment, and for the use and enjoyment of its successors, assigns or other legal representatives, to the end of the term or terms for which said Patents are or may be granted, reissued or extended as fully and entirely as the same would have been held and enjoyed by Seller, if this assignment and sale had not been made.

And, Seller, hereby authorizes and requests the Commissioner of Patents and Trademarks to issue any and all Letters Patents of the United States on said inventions to Purchaser as assignee of the entire interest, and hereby covenants that Seller has full right to convey the entire interest herein assigned, and that, except as otherwise provided between the parties, Seller has not executed, and will not execute, any agreements in conflict therewith.

This Assignment of Patent Rights is entered into pursuant to that certain Asset Purchase Agreement, dated as of April 22, 2003, by and between Seller and Purchaser, and is subject to the terms of such agreement in all respects.

In Witness Whereof, the parties, by their duly authorized representatives, have executed this Assignment.

DATE: April 22, 2003

By: 
Printed/Typed Name
Title: Responsible Individual
General Magic, Inc.

Exhibit A

1. System and Method for Distributed Computation based Upon the Movement, Execution and Interaction of Processes in a Network – US Patent Nos. 5,603,031 (including related foreign filings pending in Canada (#2143672), Hong Kong (#98116124.8) and Japan (7-509799)), 6,016,393
2. Objective Code Structure and Method for Translation of Architecture Independent Program Implementations – US Patent No. 6,021,275
3. Network System Extensible by Users – US Patent No. 6,163,794
4. Mobile Device Patents
 - a) Graphical user interface for modifying object characteristics using coupon objects – U.S. Patent No. 5,611,031
 - b) Graphical user interface for navigating between levels displaying hallway and room metaphors – U.S. Patent Nos. 5,689,669, 5,896,133
 - c) Shadow mechanism having master blocks for a modifiable object oriented system – U.S. Patent Nos. 5,692,187, 5,819,306
 - d) Method for transmitting information over an intelligent low power serial bus – U.S. Patent No. 5,675,811
 - e) Bus interface circuit for an intelligent low power serial bus – U.S. Patent No. 5,787,298
 - f) Method for transmitting bus commands and data over two wires of a serial bus – U.S. Patent No. 5,793,993
 - g) Support structures for an intelligent low power serial bus – U.S. Patent No. 5,812,796
 - h) Method for configuring an intelligent low power serial bus – U.S. Patent No. 5,938,742
5. PCT (#US94/07397) and EPO (#94305058.3) applications