

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
STMicroelectronics SA	07/28/2008
RECEIVING PARTY DATA	
Name:	ST Wireless SA
Street Address:	39 Chemin du Champ-des-Filles
City:	Plan-les-Ouates, Geneva
State/Country:	SWITZERLAND
Postal Code:	1228
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12361636
CORRESPONDENCE DATA	
Fax Number:	9198542084
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	919-854-1844
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ATTORNEY DOCKET NUMBER:	6275-217
NAME OF SUBMITTER:	Edward H. Green III
Total Attachments: 10 source=Deed_of_Transfer_for_ST_SA#page1.tif source=Deed_of_Transfer_for_ST_SA#page2.tif source=Deed_of_Transfer_for_ST_SA#page3.tif source=Deed_of_Transfer_for_ST_SA#page4.tif	

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DEED OF TRANSFER FOR ST PATENTS

WHEREAS, STMicroelectronics NV, a public company with limited liability incorporated under the laws of the Netherlands, with corporate seat in Amsterdam, the Netherlands, and address at WTC Schiphol Airport, Schiphol Boulevard 265, 1118 BH Schiphol Airport, Amsterdam, the Netherlands, and

STMicroelectronics S.A., (formerly known as SGS-Thomson Microelectronics S.A.), a corporation organized under the laws of France, having a place of business at 29 Boulevard Romain Rolland, 92120 Montrouge, France,

(hereinafter collectively referred to as "Assignor") has rights in and to the following:

LISTED IN EXHIBIT A

(said patents, patent applications and invention disclosures listed in Exhibit A being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, ST Wireless SA, a legal entity incorporated under the laws of Switzerland, and address at 39 Chemin du Champ-des-Filles, 1228 Plan-les-Ouates, Geneva, Switzerland (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all its right, title and interest in and to SAID PATENT RIGHTS, together with its right to claim priority, together with any and all continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with its right to sue for and be entitled to any

damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof. Assignee hereby accepts such conveyance, transfer and assignment of SAID PATENT RIGHTS.

Signed this 28th day of July, 2008.

STMicroelectronics NV

By: [Signature]

Printed Name: Christian

Title: General Counsel

Place: Geneva

STMicroelectronics S.A. (formerly known
as SGS-Thomson Microelectronics S.A.)

By: [Signature]

Printed Name: P. CHASTAGNER

Title: Chairman

Place: Grenoble, France

ST Wireless SA

By: [Signature]

Printed Name: P. DIONISIO

Title: President

Place: Geneva

EXHIBIT A

US Patent	Country	Current Status	Non-US Patent	Filing Number	Title
5321390	US	Granted		8908819	BIASING OF JUNCTION
5320389	US	Granted	2314807	941294	VARIABLE GAIN DIFFERENTIAL INPUT AND OUTPUT AMPLIFIER
	US	Granted	5324417	01201622.2	
	US	Granted	5324417	01201622.2	
5321320	US	Granted		8908299	LOW ELECTRICAL CONSUMPTION VOLTAGE REGULATOR
	US	Granted	5148804	01105240.9	
5320379	US	Granted		90111122	LINEAR PREAMPLIFIER FOR RADIO-FREQUENCY POWER AMPLIFIER
5321438	US	Granted		9414870	TRANSCONDUCTANCE STAGES AND DEVICE FOR COMBINATION BY PERTZIAN CHANNEL EQUIPPED WITH SUCH A STAGE
5734281	US	Granted		1027222	CONTROLLABLE CURRENT SOURCE ASSEMBLY
	US	Granted	5108041.4	10483001	
5320405	US	Granted		10483001	INTEGRATED CIRCUIT WITH REDUCED COUPLING VIA THE SUBSTRATE
	US	Granted	2312827	0010360	
5320417	US	Granted		10017911	VOLTAGE REGULATING DEVICE AND PROCESS
	US	Granted	2312827	0010360	
5320748	US	Granted		10101091	BIPOLOAR SIGNAL MULTIPLIER CIRCUIT
5321305	US	Granted		10037194	EMITTER-COUPLED POWER SUPPLY DEVICE ADAPTED FOR LOW CURRENT DRAINS AND CELLULAR PHONE EQUIPPED WITH SUCH A DEVICE
	US	Granted	0228002.8	10037194	
5320404	US	Granted		10036173	FLARE RECEIVER HAVING SEVERAL FINGERS AND METHOD OF PROCESSING PREWIDENED SIGNAL THEREIN
	US	Granted	2312827	0010360	
5320320	US	Granted		10036173	METHOD OF ADJUSTING THE CUTOFF FREQUENCY OF AN ELECTRONIC FILTERING SYSTEM AND CORRESPONDING SYSTEM
	US	Granted	2312827	0010360	
5772013	US	Granted		10036173	METHOD AND CIRCUIT FOR BIASING A TRANSISTOR OF A RADIO FREQUENCY AMPLIFIED STAGE
	US	Granted	2314123	0028717.7	
	US	Granted	2314123	0028717.7	
5320324	US	Granted		10011380	DEVICE AND METHOD FOR GENERATING DIGITAL SIGNALS EACH CODING A VALUE OF AN ANALOG SIGNAL
	US	Granted	1002083	0028717.7	
5320313	US	Granted		1002083	METHOD OF GENERATING A VOLTAGE SINEWAVE AT THE TERMINALS OF A CAPACITOR AND CORRESPONDING DEVICE
	US	Granted	0225470	0021962.5	
	US	Granted	0225470	0021962.5	
5320321	US	Granted		10483001	AMPLIFIER DRIVEN DEVICE FOR ELECTRICAL OSCILLATOR AND ELECTRICAL OSCILLATOR COMPRISING SUCH A DEVICE
	US	Granted	2312827	0010360	
5320327	US	Granted		10483001	PROCESS AND DEVICE FOR CONTROLLING THE OPERATION OF A BIPOLOAR TRANSISTOR IN CLASS A

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EXHIBIT A

US Patent	Country	Current Status	Non-US Patent	Patent Number	Title
585947	US	Granted		1218510	CIRCUIT FOR SHIFTING SWITCHED SIGNALS
5857871	US	Granted		1248519	DEVICE AND METHOD FOR CONTROLLING A SWITCHING POWER SUPPLY AND CORRESPONDING SWITCHING POWER SUPPLY
5957482	US	Granted		1256123.2	METHOD AND CIRCUIT FOR CORRECTING THE OFFSET OF AN AMPLIFICATION CHAIN
594949	US	Granted	2858389	1076532	CIRCUITRY FOR PROTECTING ELECTRONIC CIRCUITS AGAINST ELECTROSTATIC DISCHARGES AND METHODS OF OPERATING THE SAME
5971786	US	Granted	2858389	1076532	DIFFERENTIAL AMPLIFIER WITH A COMMON MODE VOLTAGE LOOP
6003424	US	Granted	2858389	1076532	TEMPERATURE DETECTION CELL AND METHOD TO DETERMINE THE DETECTION THRESHOLD OF SUCH A CELL
6142388	US	Granted	2858389	1076532	ELECTRONIC CIRCUIT WITH A DIFFERENTIAL PAIR OF TRANSISTORS AND LOGIC GATE COMPRISING SUCH A CIRCUIT
6207412	US	Granted		11308828	AUDIO AMPLIFICATION DEVICE WITH ARTIFICIAL CIRCUITRY
6270877	US	Granted		11384103	INTEGRATED CIRCUIT WITH RESISTIVE NETWORK HAVING RESISTOR MISALIGNMENT
6260882	US	Granted	2858389	11304382	METHOD OF CORRECTING THE PHASE DIFFERENCE BETWEEN TWO INPUT SIGNALS OF A PHASE-LOCKED LOOP AND ASSOCIATED DEVICE
6218984	US	Granted		11179742	INTEGRATED CIRCUIT WITH MODULABLE LOW DROPOUT VOLTAGE REGULATOR
6271888	US	Granted	1017211	11001345	AUTO-ADJUSTMENT OF DC CELLS WITHIN A CIRCUIT
6489188	US	Granted		10228174	AMPLIFIER ARCHITECTURE AND APPLICATION THEREOF TO A BAND-GAP VOLTAGE GENERATOR
6584304	US	Granted		10009489	STRUCTURE FOR TESTING INTEGRATED CIRCUITS
6636016	US	Granted		10422840	DIFFERENTIAL ACTIVE FILTER OF THE SECOND ORDER
6668308	US	Granted		10009489	COMMERCIAL LOGIC CIRCUIT DETERMINATION OF AN EXCITATION VECTOR IN CDP ENCODER
6718884	US	Granted		10621584	DEVICE FOR CHANGING THE ACCESS TO A MEMORY BUS
6884582	US	Granted		10478822	APPARATUS FOR MICROPROCESSOR PROCESSING OF INTERRUPTS BY A MICROPROCESSOR
6760445	US	Granted		10400825	LOW-VOLTAGE ANALOG SWITCH HAVING SELECTIVE SILICON BRIDGE DISCONNECT
6841884	US	Granted		10471370	SIGNAL PROCESSING SYSTEM AND METHOD
6190784	US	Granted		10448884	VOLTAGE REGULATOR
	US	Granted	2858389	10678822.2	

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US Patent	Country	Current Status	Non-US Patent	Patent Number	Title
5997173	US	Granted		59973493	METHOD AND DEVICE OF INFORMATION TRANSFER BETWEEN CIRCUITS THAT EXCHANGE DATA VIA CONVERTERS
5997362	US	Granted		59973630	HIGH VARIATION LOW-NOISE
5998440	US	Granted		59973709	TIME CONSTANT CALIBRATION DEVICE
6078384	US	Granted		60686034	ELECTRICAL CONNECTION DEVICE FOR ELECTRICALLY CONNECTING AN ELECTRONIC COMPONENT
6054262	US	Granted		60374375	PROTECTION DEVICE AGAINST ELECTROSTATIC DISCHARGES
5997375	US	Abandoned	5752584	5997375	NO NOISE TEST
5997375	US	Granted		59973759	VOLTAGE RAMP GENERATOR AND CURRENT RAMP GENERATOR INCLUDING SUCH A GENERATOR
6022681	US	Abandoned	5755312	6031330	LOW-LEVEL ELEMENTARY REFERENCE STRUCTURE FOR THE CALIBRATION OF AN INTEGRATED CIRCUIT PROCESS
	US	Granted		6050864	
	US	Abandoned	5977054	59428925.5	
	US	Abandoned	5942892	59428925.5	
5438228	US	Granted		59759012	REGULATING DEVICE
5440387	US	Abandoned		59759012	
	US	Granted		59891145	A LINEAR REGULATOR WITH A LOW SERIES VOLTAGE DROP
6059287	US	Granted		60672692	A LINEAR REGULATOR WITH A SELECTION OF THE OUTPUT VOLTAGE
	US	Not Granted		60948602	INTEGRATED STRUCTURE OF INDUCTANCES WITH SHARED WINDINGS ON A SEMICONDUCTOR SUBSTRATE
5995313	US	Granted		60316732	
	US	Not Granted		60338383.7	
5992271	US	Granted		59949271	INTEGRATED CIRCUIT PROVIDED WITH A PROTECTION AGAINST ELECTROSTATIC DISCHARGES
	US	Abandoned	5975635	60410271	
5994973	US	Granted		60036925	VOLTAGE GENERATOR FOR GENERATING A STABLE VOLTAGE INDEPENDENT OF VARIATIONS IN THE AMBIENT TEMPERATURE AND DEVIATIONS IN THE SUPPLY VOLTAGE
5943788	US	Granted		59931332	DIFFERENTIAL CURRENT LOOP
5974273	US	Granted		60100164	NEGATIVE FEEDBACK CROSS DIFFERENTIAL AMPLIFIER OPERATING IN COMMON MODE
5942572	US	Granted		59327145	

Exhibit A (continued)

Country	Document Number	US Publication	Foreign Patent	Foreign Number	Title
US	Publication	2001021186208		00103870	FREQUENCY-AGGREGATED SIGNAL RECEIVER WITH DIGITAL DEMODULATOR
US	Granted		1348421	02084919.4	
US	Granted		1348421	02084919.4	
US	Publication	US20020118129		00103870	RECEIVER OF FREQUENCY-MODULATED SIGNALS WITH DIGITAL DEMODULATOR
US	Granted		1291193	00104021.3	
US	Publication	20020007707		00031914	VOLTAGE-CONTROLLED OSCILLATOR COMPRISING A CIRCUIT FOR COMPENSATING FREQUENCY PULLING
US	Publication			00178788.3	
US	Publication	20020032519		00072323	MANAGEMENT AND STORAGE FOR NON-RELATABLE DATA IN A COMMUNICATIONS APPARATUS
US	Publication			0004017064	
US	Publication	2002017577		04021528.3	SYNTHESIZER ARCHITECTURE TO AVOID PULLING IN ZERO OR LOW IF RADIO TRANSCENDERS WITH INTEGRATED VCO
US	Publication	20020049329		0000725.2	
US	Publication			00484442	METHOD OF DELAYING SYMBOLS WITHIN A "NAKED" RECEIVER AND CORRESPONDING "NAKED" RECEIVER
US	Publication	20020198094		00000000	
US	Publication	20020198094		00000000	ATTENUATION CELL WITH AN ATTENUATION FACTOR CONTROL DEVICE
US	Publication	2002014888		0408007.8	
US	Publication			01148704	WAVE RECEIVER COMBING FINGER SYNCHRONIZATION
US	Granted		0471830	0408007.8	
US	Publication	20020240170		00070008	CIRCUIT FOR CONVERTING A DIFFERENTIAL SIGNAL INTO A NON-DIFFERENTIAL SIGNAL, AND RF TRANSMITTER COMPRISING SUCH A CIRCUIT
US	Publication	20020240170		00070008	
US	Publication	20020240170		00070008	RECEIVERLESS LOW-POWER BY RATE ADAPTATION
US	Publication	20020271937		00000000	
US	Publication	20020271937		00000000	STRUCTURE FOR ARBITRARY ORDER BRIC OR SOME OR PASSIVE DISCRETE-TIME ANALOGUE FILTERING
US	Granted		1548147	00108262.8	
US	Granted		1548147	00108262.8	
US	Publication	20020167172		00000010	RF TRANSCONDUCTANCE GAIN IMPROVEMENT: THE PATENT PRESENTS A METHOD TO REDUCE THE EFFECT OF LOUD CHA TRANSDUCER
US	Granted		0881208	00007007	
US	Publication	20020049329		00104497	INTERMEDIATE FREQUENCY FOR RADIO SIGNAL RECEIVER
US	Publication	20020111883		00000000	
US	Publication			00000001	CONTROLLABLE VARIABLE GAIN RADIOFREQUENCY STAGE, IN PARTICULAR INCORPORATED IN THE TX CHAIN OF A CELLULAR MOBILE PHONE AND CORRESPONDING CONTROL PROCESS
US	Granted		0510787	0510787	
US	Publication	2002007019107		000850728	FUEL CELL TO BATTERY POWER CONVERTER FOR MOBILE PHONE AUTONOMOUS CHARGER APPLICATION
US	Publication	20020082041		00030010	TRANSDUCER WITH LOW SECOND ORDER INTERMODULATION AND LOW VOLTAGE DROP USED TO IMPROVE RF RECEIVER
US	Granted		0509704	0509704	
US	Publication	20020198094		00000000	PRECISE DC CALIBRATION WITH OSCILLATION BY DISPOSITIF DE CALIBRATION CORRESPONDANT
US	Publication	2002000910		00000000	
US	Publication	2002000910		00110038	ELECTRONIC SIGNAL GENERATOR WITH MODULATED CYCLIC RATIO, COMPENSATED FOR VARIATIONS IN ITS POWER SUPPLY VOLTAGE
US	Publication			0000001.3	
US	Publication			00071004	TRANSMISSION OF ANALOGUE SIGNALS IN A SYSTEM ON A CHIP
US	Publication			00021382	FEEDBACK LOOP

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Sheet A page 8 of 8

Sheet	Page	US Patent	US Patent	US Patent	US Patent
1	1			2010011	CONTACT CIRCUIT FOR DC CONVERTER
2	2			2010012	CONTACT CIRCUIT FOR DC CONVERTER
3	3			2010013	CONTACT CIRCUIT FOR DC CONVERTER
4	4			2010014	CONTACT CIRCUIT FOR DC CONVERTER
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80	80			2010090	CONTACT CIRCUIT FOR DC CONVERTER
81	81			2010091	CONTACT CIRCUIT FOR DC CONVERTER
82	82			2010092	CONTACT CIRCUIT FOR DC CONVERTER
83	83			2010093	CONTACT CIRCUIT FOR DC CONVERTER
84	84			2010094	CONTACT CIRCUIT FOR DC CONVERTER
85	85			2010095	CONTACT CIRCUIT FOR DC CONVERTER
86	86			2010096	CONTACT CIRCUIT FOR DC CONVERTER
87	87			2010097	CONTACT CIRCUIT FOR DC CONVERTER
88	88			2010098	CONTACT CIRCUIT FOR DC CONVERTER
89	89			2010099	CONTACT CIRCUIT FOR DC CONVERTER
90	90			2010100	CONTACT CIRCUIT FOR DC CONVERTER
91	91			2010101	CONTACT CIRCUIT FOR DC CONVERTER
92	92			2010102	CONTACT CIRCUIT FOR DC CONVERTER
93	93			2010103	CONTACT CIRCUIT FOR DC CONVERTER
94	94			2010104	CONTACT CIRCUIT FOR DC CONVERTER
95	95			2010105	CONTACT CIRCUIT FOR DC CONVERTER
96	96			2010106	CONTACT CIRCUIT FOR DC CONVERTER
97	97			2010107	CONTACT CIRCUIT FOR DC CONVERTER
98	98			2010108	CONTACT CIRCUIT FOR DC CONVERTER
99	99			2010109	CONTACT CIRCUIT FOR DC CONVERTER
100	100			2010110	CONTACT CIRCUIT FOR DC CONVERTER

Exhibit A (continued)

Invention Disclosure Title
INTEGRATED RC TIME CONSTANT MEASUREMENT
SUPPLY PUSHING COMPENSATION IN SWITCHED CAPACITORS USED FOR FREQUENCY CALIBRATION OF OSCILLATORS
REFERENCE NOISE SOURCE MINIMISATION IN PDM SIGMA-DELTA CONVERSION
INEXPENSIVE CURRENT-LIMIT BASED ON PMOS SENSING
HIGH EFFICIENCY NON INVERTING BUCK BOOST CONVERTER WITH CURRENT MODE CONTROL
GATE ASSISTED NPN BJT FOR BOOTSTRAP CAPACITOR LOADING OF BUCK DC/DC CONVERTERS USING DUAL NMOS IN ADVANCED CMOS TECHNOLOGIES
TOPOLOGY FOR SINGLE-ENDED "CAPLESS" CLASS D AMPLIFIER
AUTO-CALIBRATION SYSTEM OF THE RC TIME CONSTANT FOR A CONTINUOUS-TIME DELTA-SIGMA ADC
INNOVATIVE CLOCK AND POWER CONTROL TECHNIQUE TO REDUCE THE POWER SUPPLY WAKEUP SYSTEM LATENCY OF A DIGITAL BLOCK
ADAPTIVE MITERSI DECISION FEEDBACK IC CANCELLER
INNOVATIVE DEBUG INTERFACE FOR PROCESSORS WITH POWER ON/OFF CAPABILITIES
FAST RECOVER APPROACH FOR DIGITAL CONTROLLED SMPS
FREQUENCY INTERPOLATION FILTER FOR AN OFDM RECEIVER
SEMI DIGITAL ON TOP IN A CONTEXT OF ANALOG AND MIXED DESIGNS
INTERNAL HIGH BANDWIDTH LINK BASED ON STANDARD INTERFACE
SUPPLY PUSHING COMPENSATION IN SWITCHED CAPACITORS USED FOR FREQUENCY CALIBRATION OF OSCILLATORS