

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT						
NATURE OF CONVEYANCE:	ASSIGNMENT						
CONVEYING PARTY DATA							
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Akshay K. Pathak</td> <td>10/12/2012</td> </tr> <tr> <td>Rakesh Pandey</td> <td>10/12/2012</td> </tr> </tbody> </table>		Name	Execution Date	Akshay K. Pathak	10/12/2012	Rakesh Pandey	10/12/2012
Name	Execution Date						
Akshay K. Pathak	10/12/2012						
Rakesh Pandey	10/12/2012						
RECEIVING PARTY DATA							
Name:	Freescale Semiconductor, Inc.						
Street Address:	6501 William Cannon Drive West						
Internal Address:	Law Department						
City:	Austin						
State/Country:	TEXAS						
Postal Code:	78735						
PROPERTY NUMBERS Total: 1							
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>13650141</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	13650141		
Property Type	Number						
Application Number:	13650141						
CORRESPONDENCE DATA							
Fax Number:	5128956630						
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>							
Phone:	512-895-6979						
Email:	USADOCKETING@freescale.com						
Correspondent Name:	Dee Matocha						
Address Line 1:	6501 William Cannon Drive West						
Address Line 2:	TX30/OE62						
Address Line 4:	Austin, TEXAS 78735						
ATTORNEY DOCKET NUMBER:	AC50592AC						
NAME OF SUBMITTER:	Charles Bergere						
Total Attachments: 3 source=AC50592AC-US-20121012-ASSIGNMENT#page1.tif source=AC50592AC-US-20121012-ASSIGNMENT#page2.tif source=AC50592AC-US-20121012-ASSIGNMENT#page3.tif							

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ASSIGNMENT AND AGREEMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, we, **AKSHAY K. PATHAK AND RAKESH PANDEY, BOTH OF INDIA**, have sold, assigned and transferred, and do hereby sell, assign and transfer, unto **FREESCALE SEMICONDUCTOR, INC.**, a corporation of the State of Delaware, having its principal office in Austin, State of Texas, United States of America, and its successors, assigns, and legal representatives, the entire right, title and interest for the United States of America in and to certain inventions relating to improvements in **PROCESSOR SWITCHABLE BETWEEN TEST AND DEBUG MODES** (Docket No. **AC50592AC**), described, illustrated and claimed in an application for Letters Patent of the United States of America executed by us on the dates indicated by our signatures below, together with the entire right, title and interest in and to the application, and in and to Letters Patent which may be issued upon the application, and upon any division, extension, continuation or reissue thereof.

We hereby also sell, assign and transfer unto **FREESCALE SEMICONDUCTOR, INC.**, the entire right, title and interest in and to the invention and in and to applications for Letters Patent therefor in all countries foreign to the United States of America, including all rights under any and all international conventions and treaties in respect of the invention and the applications for Letters Patent in foreign countries, and we further authorize **FREESCALE SEMICONDUCTOR, INC.** to apply for Letters Patent in foreign countries directly in its own name, and to claim priority of the filing date of the application for Letters Patent of the United States of America under the provisions of any and all international conventions and treaties.

We hereby authorize and request the Commissioner of Patents of the United States of America to issue Letters Patent upon the aforesaid application, division, extension, continuation or reissue, to **FREESCALE SEMICONDUCTOR, INC.**, for the sole use and benefit of **FREESCALE SEMICONDUCTOR, INC.**, its successors, assigns and legal representatives, to the full end of the term for which Letters Patent may be granted, the same as they would have been held and enjoyed by me had this assignment not been made, and we hereby authorize and request the equivalent authorities in foreign countries to issue the patents of their respective countries to **FREESCALE SEMICONDUCTOR, INC.**

We agree that, when requested, we will, without charge to **FREESCALE SEMICONDUCTOR, INC.**, but at its expense, sign all papers, take all rightful oaths, and do all acts which may be necessary, desirable or convenient for securing and maintaining patents for the inventions in any and all countries and for vesting title thereto in **FREESCALE SEMICONDUCTOR, INC.**, its successors, assigns and legal representatives or nominees.

We covenant with **FREESCALE SEMICONDUCTOR, INC.**, its successors, assigns and legal representatives, that the interest and property hereby conveyed is free from all prior assignment, grant, mortgage, license or other encumbrance.

AKS

DATE: 12-OCT-2012

AKSHAY K. PATHAK

First Witness

Naveen Jakhhar
SIGNATURE OF WITNESS

DATE: 12-OCT-2012
(Month-Day-Year)

NAVEEN JAKHAR
PRINTED NAME OF WITNESS

H.No. 57/52-A GOPAL NAGAR,
NAJAFGARH, NEW DELHI-110043
PRINTED ADDRESS OF WITNESS


Second Witness

Ankush
SIGNATURE OF WITNESS

DATE: 12-Oct-2012
(Month-Day-Year)


ANKUSH SETHI
PRINTED NAME OF WITNESS

2274 Sel 9 Faridabad
(Haryana)
PRINTED ADDRESS OF WITNESS


RAKESH PANDEY

DATE: 12-OCT-2012

First Witness


SIGNATURE OF WITNESS

DATE: 12-OCT-2012
(Month-Day-Year)

NAVEEN JAKHUR
PRINTED NAME OF WITNESS

HNO. 57/SE-A, GOPAL NAGAR,
NARAINGARH, NEW DELHI - 110043
PRINTED ADDRESS OF WITNESS

Second Witness


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PRINTED NAME OF WITNESS

2274 Sector 9 Faridabad (Haryana)
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