

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
STMICROELECTRONICS S.R.L.	02/06/2008
RECEIVING PARTY DATA	
Name:	MICRON TECHNOLOGY, INC.
Street Address:	8000 S. Federal Way
City:	Boise
State/Country:	IDAHO
Postal Code:	83707
PROPERTY NUMBERS Total: 66	
Property Type	Number
Patent Number:	6903595
Patent Number:	6908805
Patent Number:	6884682
Patent Number:	7072216
Patent Number:	6974747
Patent Number:	6927151
Patent Number:	6964921
Patent Number:	7041555
Patent Number:	6803277
Patent Number:	7015743
Patent Number:	7259067
Patent Number:	6958289
Patent Number:	7034360
Patent Number:	7053689
Patent Number:	6122200

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Patent Number:	6111809
Patent Number:	6181602
Patent Number:	6251728
Patent Number:	6281077
Patent Number:	6274411
Patent Number:	6222775
Patent Number:	6301149
Patent Number:	6366496
Patent Number:	6381177
Patent Number:	6501147
Patent Number:	6509222
Patent Number:	6603171
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Patent Number:	6414875
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Patent Number:	6470431
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Patent Number:	6462987
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Patent Number:	7355908
Patent Number:	7060995
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Patent Number:	7468535
Patent Number:	6956787
Patent Number:	7304485
Patent Number:	7288008
Patent Number:	7023047
Patent Number:	7091570

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Patent Number:	7177217
Patent Number:	7254062
Patent Number:	7519751
Patent Number:	7532060
Patent Number:	7544620
Patent Number:	7289368
Patent Number:	7646644
Patent Number:	7321512
Patent Number:	7242619
Patent Number:	7535774
Patent Number:	7304896
Patent Number:	7567107
Patent Number:	7515464
Patent Number:	7646655
Application Number:	11152675

CORRESPONDENCE DATA

Fax Number: 5037962900

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 503 222 9981

Email: shouk@schwabe.com

Correspondent Name: Schwabe Williamson & Wyatt

Address Line 1: Pacwest Center, Suite 1900

Address Line 2: 1211 SW Fifth Avenue

Address Line 4: Portland, OREGON 97204

ATTORNEY DOCKET NUMBER:	118264-157934
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NAME OF SUBMITTER:	Sally Houk
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Total Attachments: 128

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PATENT
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ASSIGNMENT

WHEREAS, STMicroelectronics S.r.l., (formerly known as SGS-Thomson Microelectronics S.r.l.), a corporation organized under the laws of Italy, having a place of business at Via C. Olivetti, 2-20041 Agrate Brianza, Milano, Italy (hereinafter referred to as "Assignor") has rights in and to the following:

LISTED IN EXHIBIT A

(said patents, patent applications and invention disclosures listed in Exhibit A being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Assignor is a wholly owned subsidiary of STMicroelectronics NV, a Dutch corporation organized under the laws of The Netherlands, acting through its Swiss branch, having an office at 39 Chemin du Champ-des-Filles, 1228 Plan-les-Ouates, Geneva, Switzerland (hereinafter referred to as "Assignee");

WHEREAS, Assignee requested Assignor to develop certain technology for Assignee;

WHEREAS, Assignee sponsored, paid for and is the equitable and beneficial owner of all resulting intellectual property resulting from such technology development, including SAID PATENT RIGHTS and related rights;

WHEREAS, Assignee and Assignor recognize that Assignee already owns all equitable and beneficial ownership in SAID PATENT RIGHTS and related rights;

WHEREAS, Assignor now desires to transfer the legal title in and to SAID PATENT RIGHTS to Assignee and confirm all ownership in Assignee to SAID PATENT RIGHTS and related rights that may have unintentionally remained in Assignor; and

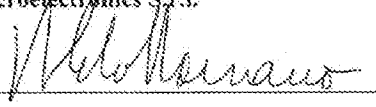
WHEREAS, Assignee desires to perfect registration in Assignee's name all the above mentioned rights, title and interest in and to SAID PATENT RIGHTS and related rights.

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby transfer and assign to Assignee legal title, and any and all rights it may own (beyond said legal title, if any) in and to SAID PATENT RIGHTS, together with any and all continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing (to the extent not already owned by Assignee), together

with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be executed by its duly authorized officer on this 16th day of February, 2008.

STMicroelectronics S.r.l.

By: 

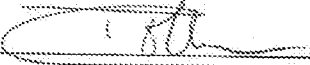
Printed Name: ALDO ROMANO

Title: CEO and Managing Director


STMicroelectronics Srl

Direttore Generale
(Ing. Pietro Palella)

STMicroelectronics NV

By: 

Printed Name: Pierre Olivier

Title: Corporate General Counsel

EXHIBIT A

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
6300195	98-AG-247	US	Granted		09512900
	98-AG-247	IT	Granted	1032035	99830100.6
	98-AG-247	GB	Granted	1032035	99830100.6
6448842	00-AG-004	US	Granted		09788623
	00-AG-004	IT	Granted	1318841	T000A00146
6404272	00-AG-005	US	Granted		09785042
	00-AG-005	IT	Granted	1158654	00830105.3
	00-AG-005	GB	Granted	1158654	00830105.3
	00-AG-005	DE	Granted	1158654	00830105.3
6734490	00-AG-009	US	Granted		09919341
	00-AG-009	EP	Publication		00830546.8
6519183	00-AG-014	US	Granted		09908986
	00-AG-014	JP	Publication		01207874
	00-AG-014	EP	Publication		00830504.7
6504758	00-AG-019	US	Granted		09960851
	00-AG-019	IT	Granted	1320666	T000A00892
6501673	00-AG-029	US	Granted		09881661
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6462987	00-AG-030	US	Granted		09830876
	00-AG-030	EP	Publication		00830582.3
6480436	00-AG-031	US	Granted		09909467
	00-AG-031	EP	Publication		00830529.4
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6404273	00-AG-087	US	Granted		09785083
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6532171	00-AG-096	US	Granted		09919789
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6731557	00-AG-294	US	Granted		10176387
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US Pat No.	ST Ref	Country	Status	Non-US Pat number	Filing Number
6700819	00-AG-329	US	Granted		10178954
	00-AG-329	IT	Granted	1325884	MI01A01311
6728141	00-AG-334	US	Granted		10123874
	00-AG-334	EP	Publication		01830266.1
6639833	00-AG-335	US	Granted		10076023
	00-AG-335	EP	Publication		01830097.0
6724658	00-AG-336	US	Granted		10133231
	00-AG-336	EP	Publication		01830276.0
7017099	00-AG-337	US	Granted		10159782
	00-AG-337	IT	Publication		TO01A00529
7084032	00-AG-338	US	Granted		10356351
	00-AG-338	US	Filing		11476361
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	00-AG-341	US	Publication		10323515
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6720271	00-AG-342	US	Granted		10189152
6816001	00-AG-353	US	Granted		10290030
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	01-AG-348	EP	Publication		03009600.2

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
6,801,466	01-CT-076	US	Granted		10331147
	01-CT-076	IT	Published		1830817.1
	01-CT-076	GB	Published		1830817.1
	01-CT-076	EP	Published		1830817.1
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	01-CT-116	IT	Publication		VA01A00035
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6826083	02-AG-012	US	Granted		10426924
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6972454	02-AG-101	US	Granted		10715887
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7031193	02-AG-148	US	Granted		10729875
	02-AG-148	IT	Filing		MI02A02569
	02-AG-148	EP	Publication		03104383.9
6956773	02-AG-149	US	Granted		10706306
	02-AG-149	IT	Filing		MI02A02387
	02-AG-149	EP	Publication		03104130.4
6944061	02-AG-164	US	Granted		10724022
	02-AG-164	IT	Granted	1424700	02425727.1
	02-AG-164	GB	Granted	1424700	02425727.1
	02-AG-164	FR	Granted	1424700	02425727.1
6,856,547	02-AG-178	US	Granted		10664606
	02-AG-178	EP	Publication		2425562.2
6836442	02-AG-206	US	Granted		10614693
	02-AG-206	IT	Publication		MI02A01486
	02-AG-206	IT	Granted	1378913	03425442.5
	02-AG-206	GB	Granted	1378913	03425442.5
	02-AG-206	FR	Granted	1378913	03425442.5
7110300	02-AG-210	US	Granted		10782725
	02-AG-210	EP	Publication		03425096.9
6967876	02-AG-213	US	Granted		10651019
6956787	02-AG-223	US	Granted		10700322
	02-AG-223	EP	Publication		02425676.0
6940756	02-AG-224	US	Granted		10739928
	02-AG-224	IT	Filing		MI02A02668
	02-AG-224	EP	Publication		03104713.7
6950324	02-AG-226	US	Granted		10727150
	02-AG-226	IT	Filing		VA02A00067
6950337	02-AG-227	US	Granted		10719650
	02-AG-227	IT	Filing		TO02A01035
	02-AG-227	EP	Publication		03104345.8
7035142	02-AG-231	US	Granted		10748696
	02-AG-231	EP	Publication		02425808.9
6934185	02-AG-234	US	Granted		10724023
	02-AG-234	IT	Filing		MI02A02531
7125808	02-AG-315	US	Granted		10749020
	02-AG-315	IT	Filing		MI02A02784
7125807	02-AG-316	US	Granted		10746878
	02-AG-316	IT	Filing		MI02A02785
6947329	02-AG-331	US	Granted		10675905
	02-AG-331	EP	Publication		02425593.7
6894934	02-AG-341	US	Granted		10728372
	02-AG-341	EP	Publication		02425747.9
7027317	02-AG-342	US	Granted		10720013
	02-AG-342	IT	Filing		MI02A02464
	02-AG-342	EP	Publication		03103920.9
7023738	02-AG-358	US	Granted		10835538
	02-AG-358	EP	Publication		03425264.3
7009025	02-AG-363	US	Granted		10861340
	02-AG-363	IT	Granted	1484764	03425353.4
	02-AG-363	GB	Granted	1484764	03425353.4
	02-AG-363	FR	Granted	1484764	03425353.4
7098540	02-AG-381	US	Granted		10729829
	02-AG-381	IT	Filing		MI02A02570
	02-AG-381	EP	Publication		03104384.7
6953609	02-AG-383	US	Granted		10686556
	02-AG-383	EP	Publication		02425615.8
6897710	02-AG-391	US	Granted		10748697
	02-AG-391	EP	Publication		02425609.7
7075844	02-AG-407	US	Granted		10762195
	02-AG-407	IT	Filing		MI03A00075
7023047	02-AG-516	US	Granted		10745295
	02-AG-516	IT	Filing		TO02A01119
	02-AG-516	EP	Publication		03104939.8
7091570	02-AG-517	US	Granted		10745297

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
	02-AG-517	US	Filing decision		
	02-AG-517	IT	Filing		TO02A01118
	02-AG-517	EP	Publication		03104938.0
7054197	02-AG-527	US	Granted		10820458
	02-AG-527	EP	Publication		03425224.7
6785174	02-CT-017	US	Granted		10447293
	02-CT-017	EP	Publication		02425361.9
6975559	02-CT-018	US	Granted		10452762
	02-CT-018	IT	Publication		MI02A01185
6922362	02-CT-161	US	Granted		10686552
	02-CT-161	IT	Filing		MI02A02192
6927991	02-PA-237	US	Granted		10653459
	02-PA-237	IT	Filing		VA02A00045
6885584	02-PA-322	US	Granted		10748447
	02-PA-322	IT	Granted	1435624	02425802.2
	02-PA-322	GB	Granted	1435624	02425802.2
7092277	03-AG-109	US	Granted		10926784
	03-AG-109	EP	Publication		03077667.8
7001800	03-AG-112	US	Granted		10911220
	03-AG-112	EP	Publication		03425532.3
5534448	92-AG-047	US	Granted		08282408
5589701	92-AG-047	US	Granted		08486747
5627403	93-AG-008	US	Granted		08473552
5795821	93-AG-008	US	Granted		08751244
	93-AG-008	IT	Granted	0627763	93830243.7
	93-AG-008	GB	Granted	0627763	93830243.7
	93-AG-008	FR	Granted	0627763	93830243.7
	93-AG-008	DE	Granted	69333722	93830243.7
5546044	93-AG-022	US	Granted		08311941
5535157	93-AG-032	US	Granted		08347653
5612641	93-AG-035	US	Granted		08380309
5659501	93-AG-039	US	Granted		08639931
5818760	93-AG-039	US	Granted		08671226
	93-AG-039	JP	Granted	2718902	28634994
	93-AG-039	GB	Granted	0654791	93830464.9
	93-AG-039	FR	Granted	0654791	93830464.9
5566114	93-AG-045	US	Granted		08349783
5644529	93-AG-046	US	Granted		08635455
5659509	93-AG-047	US	Granted		08391999
5493531	93-AG-048	US	Granted		08350961
5708601	93-AG-048	US	Granted		08602237
5559743	93-AG-050	US	Granted		08412550
5546054	93-AG-051	US	Granted		08377524
	93-AG-051	JP	Granted	2743853	720895
	93-AG-051	IT	Granted	0665485	94830022.3
	93-AG-051	GB	Granted	0665485	94830022.3
	93-AG-051	FR	Granted	0665485	94830022.3
	93-AG-051	DE	Granted	69413793	94830022.3
5515332	93-AG-052	US	Granted		08391180
5663921	93-AG-053	US	Granted		08391159
5717642	93-AG-054	US	Granted		08803916
5887135	93-AG-055	US	Granted		08700126
5594703	93-AG-057	US	Granted		08365510
5788115	93-AG-058	US	Granted		08855922
5499217	93-AG-059	US	Granted		08348461
5827790	93-AG-060	US	Granted		08408589
5717698	93-AG-061	US	Granted		08345530
5600600	93-AG-062	US	Granted		08381530
5600594	93-AG-063	US	Granted		08412326
5687124	93-AG-064	US	Granted		08521504
5583820	93-AG-065	US	Granted		08366211
5748548	93-AG-065	US	Granted		08711132
	93-AG-065	IT	Granted	0661713	93830537.2
	93-AG-065	GB	Granted	0661713	93830537.2
	93-AG-065	FR	Granted	0661713	93830537.2
	93-AG-065	DE	Granted	69325277	93830537.2
6392459	93-AG-066	US	Granted		08347788
5617356	93-AG-067	US	Granted		08395361
5596077	93-AG-069	US	Granted		08366212
5519658	93-AG-071	US	Granted		08366259
5659510	93-AG-072	US	Granted		08368211
5905677	93-AG-072	US	Granted		08831046
5548554	93-AG-073	US	Granted		08366154
5541884	93-AG-076	US	Granted		08391147
	93-AG-076	JP	Granted	2737986	753777
5532972	93-AG-077	US	Granted		08391920
5563828	93-AG-079	US	Granted		08422813
5638327	93-AG-080	US	Granted		08412162
5602768	93-AG-081	US	Granted		08389599
5541880	93-AG-082	US	Granted		08411904
5576990	93-AG-085	US	Granted		08367538
5590075	94-AG-003	US	Granted		08479081
5696716	94-AG-019	US	Granted		08794965
5715204	94-AG-024	US	Granted		08432838
5588028	94-AG-026	US	Granted		08468282
6153537	94-AG-044	US	Granted		08577125
6531714	94-AG-044	US	Granted		08211065
	94-AG-044	IT	Granted	0720223	94830591.7
	94-AG-044	DE	Granted	69432362	94830591.7
5682349	94-AG-067	US	Granted		08454650
	94-AG-067	JP	Granted	3725581	7145034
5712814	94-AG-083	US	Granted		08503303
5920776	94-AG-083	US	Granted		08712373
	94-AG-083	JP	Granted	3024519	7197943
5761222	94-AG-088	US	Granted		08538161
	94-AG-088	JP	Granted	3704184	7254046
5729492	94-AG-096	US	Granted		08639192
5982666	94-AG-096	US	Granted		09013141
6144588	94-AG-101	US	Granted		09150802
	94-AG-101	GB	Granted	0715312	94830554.5
5850360	94-AG-102	US	Granted		08607779
	94-AG-102	US	Unfiled (do not use!)		
5644526	94-AG-103	US	Granted		08538302

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
5710739	94-AG-105	US	Granted		08476547
5633834	94-AG-109	US	Granted		08560324
	94-AG-109	IT	Granted	0713221	94830538.8
5850361	94-AG-112	US	Granted		08628587
	94-AG-112	GB	Granted	0736876	95830133.6
	94-AG-112	FR	Granted	0736876	95830133.5
5742187	94-AG-113	US	Granted		08580090
5657276	94-AG-114	US	Granted		08649468
	94-AG-114	GB	Granted	0743648	95830211.9
	94-AG-114	FR	Granted	0743648	95830211.9
5731716	94-AG-115	US	Granted		08592122
	94-AG-125	IT	Granted	0714133	94830544.6
	94-AG-125	OE	Granted	0714133	94830544.6
5717636	94-AG-132	US	Granted		08642325
5612913	94-AG-134	US	Granted		08533631
	94-AG-134	JP	Filing		7274935
5696461	94-AG-136	US	Granted		08521666
5793676	94-AG-143	US	Granted		08649857
5949713	94-AG-143	US	Granted		09094916
	94-AG-143	GB	Granted	741387	95830184.8
	94-AG-143	FR	Granted	741387	95830184.8
6004847	94-AG-184	US	Granted		08667097
6248630	94-AG-164	US	Granted		09300029
	94-AG-164	GB	Granted	0751559	95830281.2
5856221	94-AG-165	US	Granted		08670179
	94-AG-165	GB	Granted	0751560	95830282.0
6114203	94-AG-166	US	Granted		08644892
	94-AG-166	JP	Granted	3088454	854682
5894085	94-AG-178	US	Granted		08802619
6104058	94-AG-178	US	Granted		08898155
5812017	94-AG-189	US	Granted		08567328
	94-AG-189	IT	Granted	716368	94830563.6
	94-AG-189	DE	Granted	69430806	94830563.6
5914867	94-AG-191	US	Granted		08631574
	94-AG-191	JP	Granted	2963047	891312
	94-AG-191	IT	Granted	0737643	95830153.3
	94-AG-191	GB	Granted	0737643	95830153.3
	94-AG-191	FR	Granted	0737643	95830153.3
	94-AG-191	DE	Granted	69518826	95830153.3
5764570	94-AG-192	US	Granted		08691796
	94-AG-192	JP	Unfiled (do not use!)		
	94-AG-192	GB	Granted	0757357	95830354.7
	94-AG-192	FR	Granted	0757357	95830354.7
5841728	94-AG-194	US	Granted		08724495
	94-AG-194	GB	Granted	768672	95830405.7
	94-AG-194	FR	Granted	768672	95830405.7
5701265	95-AG-003	US	Granted		08593650
5638612	95-AG-004	US	Granted		08669072
	95-AG-004	EP	Publication		95830127.7
5734810	95-AG-012	US	Granted		08690530
	95-AG-012	GB	Granted	0757358	95830367.0
	95-AG-012	FR	Granted	0757358	95830367.0
5673221	95-AG-032	US	Granted	RE38166	09410164
	95-AG-032	JP	Granted	2857649	886710
	95-AG-032	GB	Granted	734024	95830110.3
	95-AG-032	FR	Granted	734024	95830110.3
5694363	95-AG-033	US	Granted		08638976
	95-AG-033	GB	Granted	747903	95830172.3
	95-AG-033	FR	Granted	747903	95830172.3
5737268	95-AG-036	US	Granted		08684431
	95-AG-036	GB	Granted	0756285	95830336.4
	95-AG-036	FR	Granted	0756285	95830336.4
5847584	95-AG-037	US	Granted		08688965
	95-AG-037	GB	Granted	0757352	95830360.4
	95-AG-037	FR	Granted	0757352	95830360.4
5760497	95-AG-038	US	Granted		08684192
	95-AG-038	GB	Granted	0756220	95830335.6
	95-AG-038	FR	Granted	0756220	95830335.6
6,507,067	95-AG-042	US	Granted		08692936
	95-AG-042	IT	Abandon requested	0758129	95830351.3
	95-AG-042	GB	Abandon requested	0758129	95830351.3
	95-AG-042	FR	Abandon requested	0758129	95830351.3
	95-AG-042	EP	Granted	0758129	95830351.3
	95-AG-042	DE	Abandon requested	0758129	95830351.3
5659502	95-AG-056	US	Granted		08665862
5820505	95-AG-056	US	Granted		08881713
	95-AG-056	GB	Granted	750314	95830253.1
	95-AG-056	FR	Granted	750314	95830253.1
5781474	95-AG-057	US	Granted		08722378
	95-AG-057	GB	Granted	766255	95830406.5
	95-AG-057	FR	Granted	766255	95830406.5
5784314	95-AG-058	US	Granted		08679656
	95-AG-058	GB	Granted	753859	95830302.6
	95-AG-058	FR	Granted	753859	95830302.6
5844404	95-AG-059	US	Granted		08720491
	95-AG-059	GB	Granted	0766256	95830407.3
	95-AG-059	FR	Granted	0766256	95830407.3
5848013	95-AG-060	US	Granted		08824616
6137725	95-AG-060	US	Granted		09203937
6320792	95-AG-060	US	Granted		09633334
	95-AG-060	GB	Granted	798735	98830174.7
	95-AG-060	FR	Granted	798735	98830174.7
5822247	95-AG-061	US	Granted		08775111
	95-AG-061	GB	Granted	782149	98830352.9
	95-AG-061	FR	Granted	782149	98830352.9
5864513	95-AG-082	US	Granted		08827409
	95-AG-082	GB	Granted	0796740	98830183.0
5821806	95-AG-083	US	Granted		08787484
	95-AG-083	IT	Abandon requested	0786777	98830026.9
5659498	95-AG-091	US	Granted		08684406
	95-AG-091	FR	Granted	756379	95830337.2

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
5754483	95-AG-092	US	Granted		08835033
	95-AG-092	JP	Publication		975469
	95-AG-092	IT	Abandon requested	798729	96830160.6
	95-AG-092	GB	Granted	798729	96830160.6
	95-AG-092	FR	Granted	798729	96830160.6
6438669	95-AG-094	US	Granted		08813667
	95-AG-094	JP	Publication		959519
	95-AG-094	GB	Granted	797209	96830129.1
5729490	95-AG-095	US	Granted		08690059
	95-AG-095	IT	Granted	757355	95830347.1
	95-AG-095	GB	Granted	757355	95830347.1
	95-AG-095	FR	Granted	757355	95830347.1
	95-AG-095	DE	Granted	757355	95830347.1
6040734	95-AG-097	US	Granted		09109630
	95-AG-097	GB	Granted	782268	96830353.7
	95-AG-097	FR	Granted	782268	96830353.7
5724290	95-AG-098	US	Granted		08774110
	95-AG-098	EP	Publication		95830555.9
6157054	95-AG-099	US	Granted		09032179
	95-AG-099	IT	Granted	0772200	95830461.0
	95-AG-099	GB	Granted	0772200	95830461.0
	95-AG-099	EP	Publication		03012504.1
6195291	95-AG-100	US	Granted		08687145
6483750	95-AG-100	US	Granted		09766744
	95-AG-100	GB	Granted	756286	95830317.4
	95-AG-100	FR	Granted	756286	95830317.4
5754476	95-AG-102	US	Granted		08751299
	95-AG-102	GB	Granted	0772282	95830456.0
	95-AG-102	FR	Granted	0772282	95830456.0
5905387	95-AG-105	US	Granted		08742378
5778012	95-AG-106	US	Granted		08671848
	95-AG-106	GB	Granted	766174	95830408.1
	95-AG-106	FR	Granted	766174	95830408.1
5801988	95-AG-107	US	Granted		08835031
	95-AG-107	IT	Abandon requested	0798743	96830168.9
	95-AG-107	GB	Granted	0798743	96830168.9
	95-AG-107	FR	Granted	0798743	96830168.9
5821788	95-AG-108	US	Granted		08790832
	95-AG-108	GB	Granted	788114	96830048.7
5838619	95-AG-111	US	Granted		08828039
	95-AG-111	GB	Granted	0798642	96830167.1
	95-AG-111	FR	Granted	0798642	96830167.1
6000980	95-AG-117	US	Granted		08807113
	95-AG-117	GB	Granted	0779642	95830520.3
	95-AG-117	FR	Granted	0779642	95830520.3
5977591	95-AG-137	US	Granted		08824888
	95-AG-137	GB	Granted	0798785	95830175.4
6448138	95-AG-155	US	Granted		09548782
6222245	95-AG-158	US	Granted		08739897
	95-AG-158	IT	Granted	0772246	95830459.4
	95-AG-158	GB	Granted	0772246	95830459.4
	95-AG-158	FR	Granted	0772246	95830459.4
5926416	95-AG-165	US	Granted		08807574
	95-AG-165	IT	Publication		96830088.9
	95-AG-165	GB	Publication		96830088.9
5817557	95-AG-168	US	Granted		08792893
	95-AG-168	FR	Granted	0788144	96830039.2
5923975	95-AG-173	US	Granted		08784967
	95-AG-173	IT	Abandon requested	785570	96830021.0
	95-AG-173	GB	Granted	785570	96830021.0
	95-AG-173	FR	Granted	785570	96830021.0
5859795	95-AG-185	US	Granted		08791348
	95-AG-185	JP	Publication		918819
	95-AG-185	IT	Granted	788113	96830044.2
	95-AG-185	FR	Granted	788113	96830044.2
	95-AG-187	WO	Publication		IT9600198
6101121	95-AG-187	US	Granted		09202657
	95-AG-187	JP	Publication		10502636
	95-AG-187	GB	Granted	906622	96937493.3
	95-AG-187	FR	Granted	906622	96937493.3
5986921	95-AG-193	US	Granted		08663822
	95-AG-193	JP	Notice of Allowance		9172303
	95-AG-193	GB	Granted	0817200	96830371.9
	95-AG-193	FR	Granted	0817200	96830371.9
	95-AG-195	WO	Publication		IT9600199
6097628	95-AG-195	US	Granted		09202656
	95-AG-195	JP	Publication		10502637
	95-AG-195	GB	Granted	906623	96937494.1
	95-AG-195	FR	Granted	906623	96937494.1
5854764	95-AG-198	US	Granted		08821804
	95-AG-198	JP	Publication		966172
	95-AG-198	GB	Granted	797145	96830144.0
	95-AG-198	FR	Granted	797145	96830144.0
5999450	95-AG-199	US	Granted		08853756
	95-AG-199	GB	Granted	806773	96830267.9
	95-AG-199	FR	Granted	806773	96830267.9
5917753	95-AG-200	US	Granted		08845916
6055187	95-AG-200	US	Granted		09209319
5818763	95-AG-201	US	Granted		08774880
	95-AG-201	GB	Granted	782145	95830554.2
	95-AG-201	FR	Granted	782145	95830554.2
5886949	95-AG-202	US	Granted		08978665
	95-AG-202	GB	Granted	845784	96830598.7
6195290	95-AG-203	US	Granted		09407135
	95-AG-203	US	Unfiled (do not use!)		
	95-AG-203	GB	Granted	782148	96830245.5
	95-AG-203	FR	Granted	782148	96830245.5
5721707	95-AG-204	US	Granted		08787907
	95-AG-204	IT	Abandon requested	786779	96830025.1
	95-AG-204	GB	Granted	786779	96830025.1
	95-AG-204	FR	Granted	786779	96830025.1
5784319	95-AG-205	US	Granted		08788530
	95-AG-205	IT	Abandon requested	786778	96830024.4
	95-AG-205	IT	Abandon requested	786778	96830024.4

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
	95-AG-205	GB	Granted	786778	96830024.4
	95-AG-205	GB	Granted	786778	96830024.4
	95-AG-205	FR	Granted	786778	96830024.4
	95-AG-205	FR	Granted	786778	96830024.4
6005411	95-AG-210	US	Granted		08777296
	95-AG-210	GB	Granted	0782144	96830552.6
	95-AG-210	FR	Granted	0782144	96830552.6
5757719	95-AG-211	US	Granted		08869208
	95-AG-211	GB	Granted	811986	96830318.0
	95-AG-211	FR	Granted	811986	96830318.0
5796275	95-AG-213	US	Granted		08791700
	95-AG-213	IT	Abandon requested	786231	96830043.4
	95-AG-213	GB	Granted	786231	96830043.4
6057591	95-AG-216	US	Granted		09014437
6210994	95-AG-216	US	Granted		09534675
	95-AG-216	JP	Publication		1018490
	95-AG-216	IT	Abandon requested	0856886	97830029.1
	95-AG-216	GB	Granted	0856886	97830029.1
	95-AG-216	FR	Granted	0856886	97830029.1
5917768	95-AG-223	US	Granted		08847385
	95-AG-223	GB	Granted	0805453	96830239.8
5878049	95-AG-224	US	Granted		08840056
	95-AG-224	GB	Granted	0805452	96830238.0
	95-AG-224	FR	Granted	0805452	96830238.0
5777941	95-AG-225	US	Granted		08853732
	95-AG-225	GB	Granted	0810606	96830275.2
	95-AG-225	FR	Granted	0810606	96830275.2
5751654	95-AG-226	US	Granted		08826223
	95-AG-226	GB	Granted	0798742	96830166.3
6148413	95-AG-229	US	Granted		08835030
	95-AG-229	GB	Granted	0798726	96830161.4
5854770	95-AG-230	US	Granted		08791746
5946237	95-AG-231	US	Granted		08826489
6212098	95-AG-231	US	Granted		09351603
	95-AG-231	IT	Abandon requested	0798727	96830162.2
	95-AG-231	GB	Granted	0798727	96830162.2
5831891	95-AG-234	US	Granted		08812595
	95-AG-234	GB	Granted	797208	96830128.3
	95-AG-234	FR	Granted	797208	96830128.3
5859810	95-AG-235	US	Granted		08826009
	95-AG-235	GB	Granted	0798730	96830169.7
	95-AG-235	FR	Granted	0798730	96830169.7
5850092	96-AG-008	US	Granted		08833925
6074916	96-AG-008	US	Granted		09164761
	96-AG-008	GB	Granted	0802569	96830212.5
5881001	96-AG-014	US	Granted		08825098
	96-AG-014	GB	Granted	0798741	96830165.5
	96-AG-014	FR	Granted	0798741	96830165.5
5959935	96-AG-015	US	Granted		08865748
5929674	96-AG-016	US	Granted		08848757
	96-AG-016	IT	Abandon requested	0805556	96830247.1
	96-AG-016	GB	Granted	0805556	96830247.1
5822259	96-AG-017	US	Granted		08846755
	96-AG-017	IT	Abandon requested	0806771	96830243.0
	96-AG-017	GB	Granted	0806771	96830243.0
5859797	96-AG-018	US	Granted		08846753
	96-AG-018	IT	Abandon requested	805456	96830242.2
	96-AG-018	GB	Granted	805456	96830242.2
6018255	96-AG-019	US	Granted		08862563
6094073	96-AG-019	US	Granted		09432642
	96-AG-019	GB	Granted	809254	96830299.2
6128225	96-AG-020	US	Granted		08879017
	96-AG-020	JP	Publication		9161556
	96-AG-020	GB	Granted	814480	96830344.6
	96-AG-020	FR	Granted	814480	96830344.6
5903498	96-AG-021	US	Granted		08877927
	96-AG-021	GB	Publication		96830345.3
	96-AG-021	EP	Publication		96830345.3
5805500	96-AG-022	US	Granted		08877921
	96-AG-022	JP	Publication		9161557
	96-AG-022	GB	Granted	814482	96830346.1
	96-AG-022	FR	Granted	814482	96830346.1
6590247	96-AG-023	US	Granted		09916954
	96-AG-023	US	Filing		08900328
	96-AG-023	IT	Granted	0822601	96830420.4
	96-AG-023	GB	Granted	0822601	96830420.4
6184741	96-AG-024	US	Granted		08900165
	96-AG-024	GB	Granted	822556	96830441.0
	96-AG-024	FR	Granted	822556	96830441.0
6016073	96-AG-025	US	Granted		08965068
6075402	96-AG-026	US	Granted		08946727
6215329	96-AG-027	US	Granted		08999228
	96-AG-027	FR	Granted	821362	96830411.3
6060753	96-AG-028	US	Granted		08899653
6150844	96-AG-029	US	Granted		08898811
	96-AG-029	GB	Granted	821484	96830413.9
	96-AG-029	FR	Granted	821484	96830413.9
5946238	96-AG-032	US	Granted		08877066
	96-AG-032	GB	Granted	814484	96830348.7
5923076	96-AG-033	US	Granted		08811577
6130572	96-AG-034	US	Granted		09012331
	96-AG-034	IT	Abandon requested	855788	97830014.3
	96-AG-034	GB	Granted	855788	97830014.3
5815464	96-AG-035	US	Granted		08811869
	96-AG-035	GB	Granted	794618	96830096.2
	96-AG-035	FR	Granted	794618	96830096.2
6055665	96-AG-036	US	Granted		08816766
	96-AG-036	GB	Granted	0797147	96830136.6
	96-AG-036	FR	Granted	0797147	96830136.6
5859476	96-AG-049	US	Granted		08828791
	96-AG-049	GB	Granted	0798728	96830177.0
5901087	96-AG-050	US	Granted		08828790
6078523	96-AG-050	US	Granted		09048941
	96-AG-050	GB	Granted	0798732	96830164.8

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
	96-AG-050	FR	Granted		96830164.8
5886945	96-AG-051	US	Granted	0798732	08825138
	96-AG-051	GB	Granted	0798744	96830176.2
5889723	96-AG-052	US	Granted		08826008
	96-AG-052	IT	Granted	0800259	96830178.8
	96-AG-052	GB	Granted	0800259	96830178.8
	96-AG-052	FR	Granted	0800259	96830178.8
	96-AG-052	DE	Granted	89616019	96830178.8
5805435	96-AG-053	US	Granted		08824958
	96-AG-053	GB	Granted	0800260	96830179.6
	96-AG-053	FR	Granted	0800260	96830179.6
5754473	96-AG-054	US	Granted		08835294
	96-AG-054	IT	Abandon requested	0800177	96830193.7
	96-AG-054	GB	Granted	0800177	96830193.7
5864500	96-AG-055	US	Granted		08835296
	96-AG-055	IT	Abandon requested	0800178	96830191.1
	96-AG-055	GB	Granted	0800178	96830191.1
	96-AG-055	FR	Granted	0800178	96830191.1
6175521	96-AG-056	US	Granted		08833336
	96-AG-056	IT	Abandon requested	0800178	96830192.9
	96-AG-056	GB	Granted	0800178	96830192.9
5959917	96-AG-057	US	Granted		08819519
	96-AG-057	GB	Granted	0797144	96830143.2
	96-AG-057	FR	Granted	0797144	96830143.2
5838623	96-AG-058	US	Granted		08841904
5936907	96-AG-058	US	Granted		09183469
	96-AG-058	GB	Granted	0802541	96830216.6
	96-AG-058	FR	Granted	0802541	96830216.6
5889710	96-AG-059	US	Granted		08842835
	96-AG-059	IT	Granted	0802483	96830217.4
	96-AG-059	GB	Granted	0802483	96830217.4
5812467	96-AG-060	US	Granted		08841903
	96-AG-060	GB	Granted	0802482	96830215.8
	96-AG-060	FR	Granted	0802482	96830215.8
5884562	96-AG-081	US	Granted		08869859
	96-AG-081	GB	Granted	0811917	96830323.0
5887504	96-AG-082	US	Granted		08869367
	96-AG-082	GB	Granted	0811988	96830326.3
5968183	96-AG-083	US	Granted		08868214
	96-AG-083	IT	Granted	0811918	96830324.8
5831915	96-AG-084	US	Granted		08868213
6061273	96-AG-079	US	Granted		08835347
5886925	96-AG-092	US	Granted		08877922
	96-AG-092	GB	Granted	814483	96830347.9
	96-AG-092	FR	Granted	814483	96830347.9
5998445	96-AG-094	US	Granted		08916874
	96-AG-094	GB	Granted	825611	96830455.0
	96-AG-094	FR	Granted	825611	96830455.0
5946235	96-AG-096	US	Granted		08940278
6236592	96-AG-096	US	Granted		09373813
6252802	96-AG-096	US	Granted		09636397
	96-AG-096	GB	Granted	833267	96830492.3
	96-AG-096	FR	Granted	833267	96830492.3
6046619	96-AG-101	US	Granted		08900424
5905678	96-AG-103	US	Granted		08934498
	96-AG-103	GB	Granted	0831492	96830475.8
	96-AG-103	FR	Granted	0831492	96830475.8
5854762	96-AG-104	US	Granted		08961368
	96-AG-104	GB	Granted	0840327	96830553.2
6239042	96-AG-111	US	Granted		08987454
6472750	96-AG-111	US	Granted		09645711
6054731	96-AG-114	US	Granted		08940866
6147380	96-AG-114	US	Granted		09521867
6399444	96-AG-114	US	Granted		09521876
6710394	96-AG-114	US	Granted		10158706
6841445	96-AG-114	US	Granted		10792308
	96-AG-114	EP	Publication		96830493.1
5994231	96-AG-115	US	Granted		08996920
6156637	96-AG-116	US	Granted		08997403
6630739	96-AG-116	US	Granted		09687217
5883837	96-AG-124	US	Granted		08940115
5973966	96-AG-124	US	Granted		09203798
	96-AG-124	JP	Filing		9263781
	96-AG-124	GB	Granted	833340	96830494.9
	96-AG-124	FR	Granted	833340	96830494.9
5949666	96-AG-125	US	Granted		09032282
6081911	96-AG-131	US	Granted		08855642
5966954	96-AG-132	US	Granted		08960926
5914901	96-AG-147	US	Granted		08866283
	96-AG-147	IT	Abandon requested	1289863	Mi97A00009
5903166	96-AG-153	US	Granted		08811548
5990526	96-AG-155	US	Granted		09027343
6071778	96-AG-155	US	Granted		09389955
	96-AG-155	IT	Abandon requested	1289933	Mi97A00357
6188121	96-AG-157	US	Granted		09119115
	96-AG-157	EP	Publication		97830384.0
5793699	96-AG-158	US	Granted		08811366
	96-AG-158	IT	Abandon requested	1289864	Mi97A00010
6100740	96-AG-159	US	Granted		08957685
6320440	96-AG-159	US	Granted		09611258
6525591	96-AG-159	US	Granted		09558958
5964503	96-AG-165	US	Granted		08866531
6130165	96-AG-168	US	Granted		08997499
6380582	96-AG-168	US	Granted		09528406
	96-AG-168	EP	Publication		96830649.8
5969977	96-AG-169	US	Granted		08998854
6101124	96-AG-169	US	Granted		09327111
6284585	96-AG-169	US	Granted		09387103
5876933	96-AG-203	US	Granted		08897799
6353243	96-AG-203	US	Granted		09356080
	96-AG-203	IT	Granted	0892430	97830359.2
	96-AG-203	GB	Granted	0892430	97830359.2
	96-AG-203	FR	Granted	0892430	97830359.2
6115801	96-AG-204	US	Granted		09049858

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	96-AG-204	IT	Abandon requested	867885	97830151.3
	96-AG-204	GB	Granted	867885	97830151.3
	96-AG-204	FR	Granted	867885	97830151.3
5880993	96-AG-213	US	Granted		08941882
	96-AG-213	JP	Filing		9266634
	96-AG-213	GB	Granted	0833348	96830612.6
	96-AG-213	FR	Granted	0833348	96830612.6
5915185	96-AG-218	US	Granted		09062859
	96-AG-218	JP	Publication		10109568
	96-AG-218	EP	Publication		97830182.8
	96-AG-219	IT	Granted	953984	98830265.9
	96-AG-219	GB	Granted	953984	98830265.9
5888836	96-AG-223	US	Granted		08990328
5959902	96-AG-238	US	Granted		09030604
	96-AG-238	IT	Abandon requested	862183	97830085.3
	96-AG-238	GB	Granted	862183	97830085.3
	96-AG-238	FR	Granted	862183	97830085.3
6063663	96-AG-239	US	Granted		09139909
	96-AG-239	IT	Granted	0902466	97830428.5
	96-AG-239	GB	Granted	0902466	97830428.5
	96-AG-240	IT	Granted	1293610	M197A01682
6326286	96-AG-242	US	Granted		09141849
	96-AG-242	JP	Publication		10242144
	96-AG-242	EP	Publication		97830427.7
6888225	97-AG-008	US	Granted		10323961
6061286	97-AG-018	US	Granted		09053720
	97-AG-018	IT	Abandon requested	0869506	97830160.4
	97-AG-018	GB	Granted	0869506	97830160.4
8075718	97-AG-019	US	Granted		09039588
	97-AG-019	IT	Abandon requested	1291209	TO97A00227
8212128	97-AG-023	US	Granted		09093382
	97-AG-023	IT	Publication		TO98A00840
8018271	97-AG-024	US	Granted		09141250
	97-AG-024	IT	Abandon requested	899742	97830435.0
	97-AG-024	GB	Granted	899742	97830435.0
	97-AG-024	FR	Granted	899742	97830435.0
8194270	97-AG-027	US	Granted		09130720
6437395	97-AG-027	US	Granted		09776105
	97-AG-027	IT	Granted	1294312	M197A01902
6187683	97-AG-030	US	Granted		09059740
	97-AG-030	EP	Publication		03077997.9
6195283	97-AG-034	US	Granted		09076013
	97-AG-034	IT	Granted	0877386	97830216.4
	97-AG-034	GB	Granted	0877386	97830216.4
8598190	97-AG-037	US	Granted		09175220
	97-AG-037	GB	Granted	0911747	97830525.8
6376306	97-AG-040	US	Granted		09459754
	97-AG-040	IT	Granted	1304059	M198A02843
6195286	97-AG-051	US	Granted		09353756
	97-AG-051	IT	Granted	974976	98830438.2
	97-AG-051	GB	Granted	974976	98830438.2
6081448	97-AG-055	US	Granted		09162639
	97-AG-055	IT	Abandon requested	0905712	97830477.2
	97-AG-055	GB	Granted	0905712	97830477.2
6369406	97-AG-072	US	Granted		09311258
	97-AG-072	IT	Granted	1301100	M198A01123
6330188	97-AG-073	US	Granted		09413383
	97-AG-073	IT	Publication		TO98A00839
6122200	97-AG-074	US	Granted		09200002
	97-AG-074	EP	Publication		97830625.6
	97-AG-075	IT	Granted	1293644	TO97A00867
6362658	97-AG-083	US	Granted		09716747
	97-AG-083	IT	Granted	1313847	M199A02465
6089822	97-AG-088	US	Granted		09181230
	97-AG-088	EP	Notification 51(4)		97830550.6
6233046	97-AG-116	US	Granted		09343207
	97-AG-116	IT	Granted	1306911	M198A01504
6401164	97-AG-130	US	Granted		09159322
	97-AG-130	EP	Publication		97830467.3
5926059	97-AG-131	US	Granted		08927391
5994948	97-AG-132	US	Granted		08921930
6197606	97-AG-133	US	Granted		09108439
	97-AG-133	EP	Publication		97830354.3
6028793	97-AG-142	US	Granted		09224330
6242971	97-AG-143	US	Granted		09321961
	97-AG-143	IT	Granted	0961288	98830332.7
	97-AG-143	GB	Granted	0961288	98830332.7
8172908	97-AG-151	US	Granted		09169239
6275960	97-AG-155	US	Granted		09212896
	97-AG-155	IT	Abandon requested	0926687	97830693.4
	97-AG-155	FR	Granted	0926687	97830693.4
5982677	97-AG-166	US	Granted		09163755
	97-AG-166	IT	Abandon requested	905710	97830484.8
	97-AG-166	FR	Granted	905710	97830484.8
6011715	97-AG-177	US	Granted		09185906
	97-AG-177	GB	Granted	0913832	97830566.2
6075750	97-AG-178	US	Granted		09188497
	97-AG-178	IT	Abandon requested	915477	97830576.1
	97-AG-178	GB	Granted	915477	97830576.1
	97-AG-178	FR	Granted	915477	97830576.1
6169423	97-AG-179	US	Granted		09186496
	97-AG-179	JP	Publication		10315066
	97-AG-179	IT	Abandon requested	915476	97830573.8
	97-AG-179	GB	Granted	915476	97830573.8
	97-AG-179	FR	Granted	915476	97830573.8
6144589	97-AG-180	US	Granted		09188758
	97-AG-180	EP	Publication		97830572.0
6153914	97-AG-182	US	Granted		09170788
	97-AG-182	IT	Abandon requested	911974	97830542.3
	97-AG-182	GB	Granted	911974	97830542.3
6184670	97-AG-183	US	Granted		09186498
	97-AG-183	EP	Publication		97830574.6
6275099	97-AG-193	US	Granted		09239908
	97-AG-193	IT	Granted	1301167	TO98A00077

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6249172	97-AG-204	US	Granted		09275200
	97-AG-204	IT	Granted	1298817	M198A00639
6064598	97-AG-205	US	Granted		09275594
	97-AG-205	IT	Granted	1298819	M198A00641
6286086	97-AG-206	US	Granted		09220127
	97-AG-206	JP	Publication		10367930
	97-AG-206	EP	Publication		97830717.1
6101118	97-AG-207	US	Granted		09198204
6285614	97-AG-207	US	Granted		09602669
	97-AG-207	IT	Granted	1296488	M197A02594
6204722	97-AG-208	US	Granted		09218798
	97-AG-208	IT	Granted	1296888	M197A02811
6069837	97-AG-209	US	Granted		09222022
	97-AG-209	IT	Granted	928003	98114061.9
	97-AG-209	FR	Granted	928003	98114061.9
6237104	97-AG-210	US	Granted		09222070
	97-AG-210	IT	Granted	1296908	M197A02883
6157225	97-AG-211	US	Granted		09234016
	97-AG-211	IT	Granted	1298186	M198A00115
6151251	97-AG-212	US	Granted		09295867
6304490	97-AG-212	US	Granted		09875985
	97-AG-212	IT	Granted	952615	98830238.6
	97-AG-212	GB	Granted	952615	98830238.6
	97-AG-212	FR	Granted	952615	98830238.6
6031761	97-AG-213	US	Granted		09275255
	97-AG-213	IT	Granted	1298818	M198A00638
6009041	97-AG-218	US	Granted		09032272
6313480	97-AG-220	US	Granted		09209049
6307399	97-AG-222	US	Granted		09231130
6288594	97-AG-224	US	Granted		09674395
	97-AG-224	IT	Granted	1097454	99926682.8
	97-AG-224	GB	Granted	1097454	99926682.8
	97-AG-224	FR	Granted	1097454	99926682.8
	97-AG-224	DE	Granted	69901259	99926682.8
	97-AG-224	CN	Granted	261489	99805467.4
6587914	97-AG-226	US	Granted		09349702
	97-AG-226	IT	Granted	1301555	M198A01564
6101127	97-AG-229	US	Granted		09229474
	97-AG-229	EP	Publication		04023387.6
6091642	97-AG-240	US	Granted		09234942
	97-AG-240	IT	Abandon requested	0932161	98830024.0
6266222	97-AG-241	US	Granted		09223621
	97-AG-241	IT	Granted	0932202	97830741.1
	97-AG-241	FR	Granted	0932202	97830741.1
6242793	97-AG-248	US	Granted		09231129
6372597	97-AG-248	US	Granted		09637137
	97-AG-248	JP	Publication		10370047
	97-AG-248	EP	Publication		97830742.9
6147902	97-AG-252	US	Granted		09320315
	97-AG-252	IT	Granted	961287	98830324.4
	97-AG-252	GB	Granted	961287	98830324.4
	97-AG-252	FR	Granted	961287	98830324.4
6218265	97-AG-275	US	Granted		09336089
	97-AG-275	IT	Granted	0971415	98830388.9
	97-AG-275	GB	Granted	0971415	98830388.9
	97-AG-275	FR	Granted	0971415	98830388.9
	97-AG-275	DE	Granted	0971415	98830388.9
6111791	97-AG-283	US	Granted		09322644
	97-AG-283	IT	Granted	1301398	M198A01183
6208705	97-AG-286	US	Granted		09262500
	97-AG-286	IT	Granted	1298594	M198A00454
6097633	97-CT-105	US	Granted		09182843
	97-CT-105	IT	Granted	1295910	M197A02458
6867113	97-CT-174	US	Granted		09191743
	97-CT-174	EP	Publication		97830603.3
6133718	97-CT-203	US	Granted		09246029
	97-CT-203	IT	Granted	1298560	M198A00219
	97-CT-235	IT	Granted	1298938	M198A00351
	97-CT-236	IT	Granted	1298939	M198A00352
6271571	97-CT-238	US	Granted		09276263
	97-CT-238	IT	Granted	1298607	M198A00628
6466059	97-CT-290	US	Granted		09249834
	97-CT-290	IT	Granted	938627	98830064.6
	97-CT-290	GB	Granted	938627	98830064.6
6130844	97-CT-294	US	Granted		09257682
	97-CT-294	IT	Granted	1302079	T098A00165
6064594	97-CT-295	US	Granted		09258224
	97-CT-295	IT	Granted	1302080	T098A00166
6198672	97-CT-296	US	Granted		09257684
	97-CT-296	IT	Granted	1302081	T098A00167
6111809	98-AG-006	US	Granted		09324087
	98-AG-006	US	Filing		
	98-AG-006	IT	Granted	1300989	T098A00497
6181602	98-AG-007	US	Granted		09322480
	98-AG-007	JP	Publication		11152232
	98-AG-007	IT	Granted	961285	98830333.5
	98-AG-007	GB	Granted	961285	98830333.5
	98-AG-007	FR	Granted	961285	98830333.5
6157579	98-AG-008	US	Granted		09295263
6097213	98-AG-044	US	Granted		09275691
	98-AG-044	IT	Granted	1298818	M198A00640
6278159	98-AG-046	US	Granted		09344426
6551892	98-AG-046	US	Granted		09891438
	98-AG-046	IT	Granted	1301042	T098A00556
	98-AG-047	US	Filing		11033776
	98-AG-047	IT	Granted	1301880	M198A01769
6084797	98-AG-055	US	Granted		09276214
	98-AG-055	IT	Granted	0945869	98830188.3
	98-AG-055	GB	Granted	0945869	98830188.3
	98-AG-055	FR	Granted	0945869	98830188.3
6515911	98-AG-082	US	Granted		09894975
	98-AG-082	JP	Filing		11280024
	98-AG-082	IT	Granted	961075	98830570.2
	98-AG-082	GB	Granted	991075	98830570.2

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
	98-AG-082	FR	Granted	991075	98830570.2
6251728	98-AG-092	US	Granted		09359923
6420769	98-AG-092	US	Granted		09861404
	98-AG-092	EP	Publication		98120033.0
6300181	98-AG-093	US	Granted		09358075
	98-AG-093	IT	Granted	0975021	98830444.0
	98-AG-093	GB	Granted	0975021	98830444.0
6461922	98-AG-101	US	Granted		09464066
	98-AG-101	EP	Publication		98830757.5
6495455	98-AG-103	US	Granted		09836937
	98-AG-103	IT	Granted	1301840	MI98A01494
6255163	98-AG-109	US	Granted		09330540
	98-AG-109	IT	Granted	1301007	TO98A00516
6284607	98-AG-111	US	Granted		09411134
	98-AG-111	IT	Granted	1302589	MI98A02124
6172913	98-AG-112	US	Granted		09344425
	98-AG-112	IT	Granted	1301043	TO98A00557
6180460	98-AG-116	US	Granted		09337051
	98-AG-116	IT	Granted	1301799	MI98A01449
6444526	98-AG-118	US	Granted		09419403
	98-AG-118	IT	Granted	0994513	98830614.8
	98-AG-118	GB	Granted	0994513	98830614.8
	98-AG-118	FR	Granted	0994513	98830614.8
6301152	98-AG-119	US	Granted		09570332
	98-AG-119	JP	Publication		00139757
	98-AG-119	IT	Granted	1052572	98830286.3
	98-AG-119	GB	Granted	1052572	98830286.3
	98-AG-119	FR	Granted	1052572	98830286.3
6251736	98-AG-121	US	Granted		09473368
	98-AG-121	EP	Publication		98830794.8
6313040	98-AG-123	US	Granted		09406903
	98-AG-123	JP	Publication		11261939
	98-AG-123	EP	Publication		98830564.5
6281077	98-AG-125	US	Granted		09392937
6396101	98-AG-125	US	Granted		09836590
	98-AG-125	EP	Publication		98830645.2
6602774	98-AG-127	US	Granted		09473367
	98-AG-127	IT	Granted	1017088	98830793.0
	98-AG-127	GB	Granted	1017088	98830793.0
6530058	98-AG-129	US	Granted		09513286
	98-AG-129	IT	Granted	1035473	98830099.0
	98-AG-129	GB	Granted	1035473	98830099.0
	98-AG-129	FR	Granted	1035473	98830099.0
	98-AG-129	DE	Granted	69905237	98830099.0
6456527	98-AG-159	US	Granted		09501131
	98-AG-159	JP	Publication		0034209
	98-AG-159	IT	Granted	1028433	98830071.9
	98-AG-159	GB	Granted	1028433	98830071.9
	98-AG-159	FR	Granted	1028433	98830071.9
6301157	98-AG-160	US	Granted		09415024
	98-AG-160	IT	Granted	997913	98830654.4
	98-AG-160	GB	Granted	997913	98830654.4
6323799	98-AG-171	US	Granted		09421615
	98-AG-171	IT	Granted	0997912	98830626.2
	98-AG-171	GB	Granted	0997912	98830626.2
6466481	98-AG-172	US	Granted		09438232
	98-AG-172	IT	Granted	1305181	TO98A00961
6163483	98-AG-173	US	Granted		09447531
	98-AG-173	IT	Granted	1303201	TO98A00990
6128228	98-AG-174	US	Granted		09438823
	98-AG-174	IT	Granted	1305182	TO98A00962
6410387	98-AG-175	US	Granted		09449044
6713347	98-AG-175	US	Granted		10158424
	98-AG-175	JP	Publication		11334205
	98-AG-175	EP	Publication		98830709.6
6169891	98-AG-176	US	Granted		09397387
	98-AG-176	IT	Granted	987715	98830536.3
	98-AG-176	FR	Granted	987715	98830536.3
6350637	98-AG-177	US	Granted		09543400
	98-AG-177	EP	Publication		98830194.9
6195289	98-AG-178	US	Granted		09425446
	98-AG-178	IT	Granted	1305138	TO98A00906
6259635	98-AG-182	US	Granted		09491476
	98-AG-182	IT	Granted	1306964	MI99A00081
6259632	98-AG-183	US	Granted		09491475
	98-AG-183	IT	Granted	1306963	MI99A00080
6462400	98-AG-201	US	Granted		09463656
	98-AG-201	EP	Publication		98830007.3
6387763	98-AG-206	US	Granted		09442834
6737715	98-AG-206	US	Granted		10090978
	98-AG-206	EP	Publication		98830694.0
6232753	98-AG-208	US	Granted		09467726
	98-AG-208	IT	Granted	1304046	MI98A02787
6274411	98-AG-212	US	Granted		09469849
	98-AG-212	EP	Publication		98830771.6
6392931	98-AG-213	US	Granted		09449168
	98-AG-213	IT	Granted	1303204	TO98A01003
6294431	98-AG-215	US	Granted		09547520
	98-AG-215	EP	Publication		98830211.1
6222775	98-AG-227	US	Granted		09599356
	98-AG-227	JP	Publication		00188781
	98-AG-227	IT	Granted	1067557	98830390.3
	98-AG-227	GB	Granted	1067557	98830390.3
	98-AG-227	FR	Granted	1067557	98830390.3
6477625	98-AG-228	US	Granted		09474932
	98-AG-228	EP	Publication		98830801.1
6424575	98-AG-233	US	Granted		09716759
	98-AG-233	IT	Granted	1313853	MI99A02480
6365456	98-AG-234	US	Granted		09507777
	98-AG-234	EP	Publication		98830101.4
6324238	98-AG-248	US	Granted		09464631
6275416	98-AG-257	US	Granted		09472496
6373781	98-AG-258	US	Granted		09489799
6350671	98-AG-262	US	Granted		09579778

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
	98-AG-262	IT	Granted	1058304	99830336.6
	98-AG-262	GB	Granted	1058304	99830336.6
	98-AG-262	FR	Granted	1058304	99830336.6
6535431	98-AG-264	US	Granted		09552933
6803630	98-AG-264	US	Granted		10350745
	98-AG-264	IT	Granted	1047078	99830235.0
	98-AG-264	GB	Granted	1047078	99830235.0
	98-AG-264	FR	Granted	1047078	99830235.0
	98-AG-264	EP	Filing		06012616.6
	98-AG-264	DE	Granted	1047078	99830235.0
6249463	98-AG-265	US	Granted		09457500
	98-AG-282	IT	Granted	1073119	99830491.9
	98-AG-282	GB	Granted	1073119	99830491.9
	98-AG-282	FR	Granted	1073119	99830491.9
6396132	98-AG-283	US	Granted		09502070
	98-AG-283	JP	Publication		0031789
	98-AG-283	IT	Granted	1028464	99830072.7
	98-AG-283	GB	Granted	1028464	99830072.7
	98-AG-283	FR	Granted	1028464	99830072.7
6301149	98-AG-290	US	Granted		09513598
	98-AG-290	JP	Publication		0049429
	98-AG-290	IT	Granted	1031991	99830108.9
	98-AG-290	GB	Granted	1031991	99830108.9
	98-AG-290	FR	Granted	1031991	99830108.9
6249112	98-AG-292	US	Granted		09608445
	98-AG-292	JP	Publication		00199353
	98-AG-292	IT	Granted	1065580	99830418.2
	98-AG-292	GB	Granted	1065580	99830418.2
	98-AG-292	FR	Granted	1065580	99830418.2
6294798	98-CT-035	US	Granted		09415992
6511874	98-CT-035	US	Granted		09912638
	98-CT-035	EP	Publication		98830597.5
6240002	98-CT-260	US	Granted		09561271
	98-CT-260	EP	Publication		99201363.1
6310801	98-CT-270	US	Granted		09548783
	98-CT-270	IT	Granted	1307685	TO99A00289
6324098	98-CT-271	US	Granted		09546957
	98-CT-271	IT	Granted	1307686	TO99A00290
6243310	98-CT-272	US	Granted		09546975
	98-CT-272	IT	Granted	1307687	TO99A00291
6410404	98-CT-273	US	Granted		09561272
	98-CT-273	JP	Filing		00130898
	98-CT-273	EP	Publication		99830262.4
6282125	99-AG-012	US	Granted		09553526
	99-AG-012	IT	Granted	1312212	MI99A00859
6363016	99-AG-016	US	Granted		09589723
	99-AG-016	JP	Publication		00182753
	99-AG-016	IT	Granted	1063654	99830382.0
	99-AG-016	GB	Granted	1063654	99830382.0
	99-AG-016	FR	Granted	1063654	99830382.0
	99-AG-016	DE	Granted	1063654	99830382.0
6314043	99-AG-017	US	Granted		09592796
	99-AG-017	IT	Granted	1065668	99830381.2
	99-AG-017	GB	Granted	1065668	99830381.2
	99-AG-017	FR	Granted	1065668	99830381.2
6351413	99-AG-022	US	Granted		09552945
6292398	99-AG-023	US	Granted		09589232
	99-AG-023	IT	Granted	1312471	MI99A01017
6456530	99-AG-035	US	Granted		09602680
	99-AG-035	JP	Publication		00196290
	99-AG-035	IT	Granted	1063653	99830396.0
	99-AG-035	GB	Granted	1063653	99830396.0
	99-AG-035	FR	Granted	1063653	99830396.0
6198660	99-AG-041	US	Granted		09572127
	99-AG-041	IT	Granted	1058269	99830331.7
	99-AG-041	GB	Granted	1058269	99830331.7
6333885	99-AG-046	US	Granted		09603275
	99-AG-046	IT	Granted	1071094	99830403.4
	99-AG-046	GB	Granted	1071094	99830403.4
	99-AG-046	FR	Granted	1071094	99830403.4
6320790	99-AG-047	US	Granted		09586399
	99-AG-047	JP	Publication		00171988
	99-AG-047	EP	Notification 51(4)		99830348.1
6507183	99-AG-051	US	Granted		09608847
	99-AG-051	IT	Granted	1313225	MI99A01474
6392936	99-AG-052	US	Granted		09608239
	99-AG-052	IT	Granted	1313226	MI99A01475
6292400	99-AG-075	US	Granted		09620765
	99-AG-075	IT	Granted	1313199	MI99A01618
6366496	99-AG-081	US	Granted		09631187
	99-AG-081	JP	Publication		00236205
	99-AG-081	JP	Publication		00236205
	99-AG-081	IT	Granted	1074995	99830501.5
	99-AG-081	IT	Granted	1074995	99830501.5
	99-AG-081	GB	Granted	1074995	99830501.5
	99-AG-081	GB	Granted	1074995	99830501.5
	99-AG-081	FR	Granted	1074995	99830501.5
	99-AG-081	FR	Granted	1074995	99830501.5
	99-AG-081	DE	Granted	1074995	99830501.5
	99-AG-081	DE	Granted	1074995	99830501.5
6349059	99-AG-085	US	Granted		09716746
	99-AG-085	EP	Publication		99830722.7
6469934	99-AG-088	US	Granted		09738253
	99-AG-088	IT	Granted	1311314	TO99A01098
6670229	99-AG-098	US	Granted		10077288
	99-AG-098	EP	Publication		99830468.7
	99-AG-102	IT	Granted	1314142	MI99A02650
6320361	99-AG-103	US	Granted		09736984
	99-AG-103	IT	Granted	1314122	MI99A02576
6912598	99-AG-104	US	Granted		09627703
	99-AG-104	EP	Publication		99830485.0
6442068	99-AG-105	US	Granted		09619589
	99-AG-105	JP	Publication		00227222
	99-AG-105	EP	Publication		99830494.3

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
6381177	99-AG-108	US	Granted		09699309
	99-AG-109	IT	Publication		TO99A00942
6501147	99-AG-126	US	Granted		09713144
	99-AG-126	EP	Publication		99830717.7
6327184	99-AG-127	US	Granted		09821019
	99-AG-127	JP	Granted	3655538	00253119
	99-AG-127	IT	Granted	1071096	99830469.5
	99-AG-127	GB	Granted	1071096	99830469.5
	99-AG-127	FR	Granted	1071096	99830469.5
	99-AG-129	IT	Granted	1316286	M100A00065
6438040	99-AG-130	US	Granted		09629229
6638833	99-AG-132	US	Granted		09804693
6307778	99-AG-137	US	Granted		09656236
	99-AG-137	JP	Granted	3621334	00272586
	99-AG-137	EP	Publication		99830567.6
6574146	99-AG-138	US	Granted		09738899
	99-AG-138	IT	Granted	1313878	M199A02624
6854040	99-AG-143	US	Granted		09717938
	99-AG-143	EP	Publication		99830723.5
6424121	99-AG-146	US	Granted		09714837
	99-AG-146	IT	Granted	1311440	TO99A00993
6528885	99-AG-147	US	Granted		09958682
	99-AG-147	EP	Publication		00830646.6
6400607	99-AG-148	US	Granted		09699304
	99-AG-148	IT	Granted	1308856	TO99A00943
6473340	99-AG-149	US	Granted		09699043
	99-AG-149	IT	Granted	1308857	TO99A00944
6507534	99-AG-150	US	Granted		09797171
	99-AG-150	IT	Granted	1130601	00830149.1
	99-AG-150	GB	Granted	1130601	00830149.1
6362053	99-AG-151	US	Granted		09630633
	99-AG-151	IT	Granted	1313155	M199A01769
6386107	99-AG-153	US	Granted		09710067
	99-AG-153	IT	Granted	1313873	M199A02372
6549489	99-AG-154	US	Granted		09672753
	99-AG-154	IT	Granted	1318978	M100A02163
6509222	99-AG-155	US	Granted		09718871
6603171	99-AG-155	US	Granted		10225315
	99-AG-155	EP	Publication		99830735.9
6420223	99-AG-159	US	Granted		09730518
	99-AG-159	EP	Publication		99830755.7
6373780	99-AG-161	US	Granted		09627273
	99-AG-161	JP	Publication		00227650
	99-AG-161	IT	Granted	1073060	99830483.6
	99-AG-161	GB	Granted	1073060	99830483.6
	99-AG-161	FR	Granted	1073060	99830483.6
6128219	99-AG-162	US	Granted		09428683
6525961	99-AG-183	US	Granted		09952957
	99-AG-183	EP	Notification 51(4)		00830620.1
6381173	99-AG-193	US	Granted		09686382
	99-AG-193	IT	Granted	1309102	TO99A00884
6437638	99-AG-219	US	Granted		09747312
	99-AG-219	EP	Publication		99830925.8
6418051	99-AG-225	US	Granted		09785079
	99-AG-225	IT	Granted	1126372	00830103.8
	99-AG-225	GB	Granted	1126372	00830103.8
6898745	99-AG-234	US	Granted		09798347
	99-AG-234	IT	Granted	1319937	TO00A00207
6429634	99-AG-258	US	Granted		09778330
	99-AG-258	IT	Granted	1124313	00830088.1
	99-AG-258	GB	Granted	1124313	00830088.1
	99-AG-258	FR	Granted	1124313	00830088.1
6677206	99-AG-260	US	Granted		09740407
	99-AG-260	IT	Granted	1314143	M199A02651
6351434	99-AG-264	US	Granted		09767762
	99-AG-264	EP	Publication		00830100.4
6804756	99-AG-265	US	Granted		09827369
	99-AG-265	EP	Publication		00830269.7
6501700	99-AG-266	US	Granted		09974737
	99-AG-266	EP	Publication		00830655.7
6414875	99-AG-269	US	Granted		09817363
6683808	99-AG-269	US	Granted		10179553
6954395	99-AG-269	US	Granted		10742181
7072212	99-AG-269	US	Granted		10742429
	99-AG-269	EP	Publication		00830209.3
6451653	99-AG-273	US	Granted		09858335
	99-AG-273	EP	Publication		00201716.8
6581134	99-AG-278	US	Granted		09817804
	99-AG-278	IT	Granted	1139210	00830228.3
	99-AG-278	GB	Granted	1139210	00830228.3
	99-AG-278	FR	Granted	1139210	00830228.3
6466479	99-AG-320	US	Granted		09898744
	99-AG-320	EP	Notification 51(4)		00204838.7
6650173	99-AG-321	US	Granted		09714852
	99-AG-321	IT	Granted	1311441	TO99A00994
6686241	99-AG-358	US	Granted		09798778
	99-AG-358	EP	Publication		00830162.4
6507517	99-AG-368	US	Granted		09871235
	99-AG-368	EP	Publication		00830392.7
6549473	99-AG-369	US	Granted		09871234
	99-AG-369	EP	Publication		00830393.5
6476664	99-AG-370	US	Granted		09823262
	99-AG-370	EP	Publication		00830239.0
6559709	99-AG-372	US	Granted		09821827
	99-AG-372	EP	Publication		00830238.2
6537879	99-AG-376	US	Granted		09900501
6750505	99-AG-376	US	Granted		10337556
	99-AG-376	IT	Granted	1318145	M100A01567
6580637	99-AG-393	US	Granted		09972769
	99-AG-393	IT	Granted	1318979	M100A02165
6420926	99-CT-067	US	Granted		09738890
	99-CT-067	IT	Granted	1313877	M199A02623
6438048	99-CT-195	US	Granted		09670248
	99-CT-195	EP	Publication		99830618.1

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
6282134	99-CT-196	US	Granted		09670471
	99-CT-196	EP	Publication		98830617.9
6442072	99-CT-291	US	Granted		09745286
	99-CT-291	IT	Granted	1313403	VA99A00037
6470431	99-CT-339	US	Granted		09774542
	99-CT-339	EP	Publication		00830291.1
6452864	99-CT-343	US	Granted		09774539
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6473339	99-CT-345	US	Granted		09773272
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	99-CT-345	GB	Granted	1130517	00830158.2
	99-CT-345	FR	Granted	1130517	00830158.2
	99-CT-345	DE	Granted	1130517	00830158.2
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6625708	99-CT-349	US	Granted		09773760
	99-CT-349	EP	Publication		00830313.3
6587913	99-CT-355	US	Granted		09773300
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	99-CT-355	GB	Granted	1122734	00830088.3
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4988639	DB 442	US	Granted		07284272
4949307	DB 456	US	Granted		07298487
	DB 456	FR	Granted	326004	89100780.9
4922402	DB 479	US	Granted		07372493
4956569	DB 482	US	Granted		07373203
4987088	DB 488	US	Granted		07381283
4997782	DB 491	US	Granted		07386189
5054834	DB 528	US	Granted		07446196
4992680	DB 532	US	Granted		07456782
5075246	DB 603	US	Granted		07625784
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5466487	DB 632	US	Granted		08139800
5179300	DB 636	US	Granted		07691768
	DB 636	IT	Granted	1240012	20167A90
	DB 636	GB	Granted	455002	91105323.9
	DB 636	FR	Granted	455002	91105323.9
	DB 636	DE	Granted	69128494	91105323.9
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4968645	DB 274	US	Granted		07289391
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5528536	93-AG-39	US	Granted		8344232
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RE36579	DB 622	US	Granted		08488718
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5663080	DB 752	US	Granted		08524080
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5515318	DB 822	US	Granted		84654
5793675	DB 822	US	Granted		08829935
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	DB 822	JP	Publication		5272484

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5372958	DB 825	US	Granted		08153620
	DB 825	JP	Filing		5311209
	DB 825	GB	Granted	0598168	92830625.7
	DB 825	FR	Granted	0598168	92830625.7
4823175	DB 303	US	Granted		54712
4816883	DB 314	US	Granted		64480
4897365	DB 177	US	Granted		07124440
6587296	01-AG-008	US	Granted		09041684
	01-AG-008	IT	Granted	1306852	02078984.8
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6754107	01-AG-166	US	Granted		10331185
	01-AG-166	EP	Publication		02028616.7
6816404	01-AG-191	US	Granted		10319439
	01-AG-191	EP	Publication		01830806.4
6946673	01-AG-212	US	Granted		10345129
	01-AG-212	US			110201790
	01-AG-212	EP	Notification 51(4)		02425013.6
6891747	01-AG-301	US	Granted		10372761
	01-AG-301	US	Publication		11045170
	01-AG-301	EP	Publication		02425087.0
6972430	01-AG-309	US	Granted		10371154
	01-AG-309	US	Filing		11258340
	01-AG-309	EP	Publication		02425088.8
6930913	01-AG-310	US	Granted		10372639
	01-AG-310	US	Filing		11156999
	01-AG-310	EP	Granted	1339111	02425089.6
7135756	02-AG-077	US	Granted		10680727
	02-AG-077	US	Filing decision		
	02-AG-077	EP	Notification 51(4)		02425605.9
6989580	02-AG-081	US	Granted		10680721
	02-AG-081	US	Filing		11264084
	02-AG-081	EP	Notification 51(4)		02425604.2
6433583	99-AG-082	US	Granted		09585916
	99-AG-082	JP	Granted	3707047	00171999
	99-AG-082	IT	Granted	1058271	99830345.7
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	99-AG-082	FR	Granted	1058271	99830345.7
6356481	99-AG-083	US	Granted		09595054
	99-AG-083	JP	Publication		00187530
	99-AG-083	IT	Granted	1061525	99830378.8
	99-AG-083	GB	Granted	1061525	99830378.8
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00-AG-061	IT	MI00A02593	1319493	Granted
00-AG-198	IT	MI00A02755	1319590	Granted
00-AG-271	EP	00830855.3		Publication
00-AG-341	US	10323615		Publication
00-AG-341	EP	02014408.5		Publication
01-AG-185	US	10309759		Publication
01-AG-193	US	10735250		Publication
01-AG-193	IT	MI02A02629		Filing
01-AG-285	US	10390556		Notice of Allowance
01-AG-285	EP	02425152.2		Publication
01-AG-304	IT	MI03A01403		Filing
01-CT-183	US	10367141		Publication
01-CT-256	US	10360515		Abandon decision
01-CT-256	IT	VA02A00012		Publication
01-CT-295	US	10639240		Publication
01-CT-295	EP	02425529.1		Publication
01-CT-337	US	10789443		Notice of Allowance
01-CT-337	IT	03425126.4	1453062	Granted
01-CT-337	GB	03425126.4	1453062	Granted
01-CT-337	FR	03425126.4	1453062	Granted
02-AG-001	US	10675221		Publication
02-AG-001	EP	02425591.1		Publication
02-AG-013	US	10137096		Filing
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02-AG-035	US	10606855		Publication
02-AG-035	US	10606855		Publication
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02-AG-046	US	10631463		Notice of Allowance
02-AG-046	US	10631463		Notice of Allowance
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02-AG-193	EP	02425586.1		Publication
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02-AG-211	IT	MI02A02669		Filing
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02-AG-211	EP	03104709.5		Notification 51(4)
02-AG-211	EP	03104709.5		Notification 51(4)
02-AG-219	US	10746555		Publication
02-AG-219	EP	02425796.6		Publication
02-AG-228	US	10652862		Publication
02-AG-228	US	10652862		Publication
02-AG-228	EP	02425536.6		Publication
02-AG-228	EP	02425536.6		Publication
02-AG-236	US	10748701		Notice of Allowance
02-AG-236	EP	02425801.4		Publication
02-AG-242	US	10781974		Publication
02-AG-242	EP	03425093.6		Publication
02-AG-244	US	10836651		Publication
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02-AG-258	IT	RM03A00255		Filing
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02-AG-314	US	10860834		Publication
02-AG-314	US	10860834		Publication
02-AG-314	EP	03425336.9		Publication
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02-AG-317	US	10749130		Publication
02-AG-317	US	10749130		Publication
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02-AG-317	EP	02425805.5		Publication
02-AG-344	IT	MI03A01102		Filing
02-AG-357	US	10893760		Filing
02-AG-357	EP	03077228.9		Publication
02-AG-384	US	10777457		Publication
02-AG-384	US	10777457		Publication
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02-AG-404	US	11089942		Filing
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02-AG-469	US	10909749		Publication
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02-AG-491	DE	IT04000555		Filing
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02-AG-509	EP			Filing requested
02-AG-523	US	11018550		Publication
02-AG-523	EP	04106858.6		Filing
02-CT-061	US	10623474		Publication
02-CT-061	IT	MI02A01583		Publication
02-CT-438	US	10789361		Notice of Allowance
02-CT-438	EP	03425133.0		Publication
02-CT-439	US	10788525		Notice of Allowance
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03-AG-006	EP	03425459.9		Publication
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03-AG-011	IT	RM03A00512		Filing
03-AG-041	US	10913788		Notice of Allowance
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03-AG-077	US	10805168		Publication
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03-AG-135	EP	03425687.5		Publication
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03-AG-179	US	10971776		Publication
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03-AG-194	US	11194739		Filing
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03-AG-224	EP	04425937.2		Publication
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03-AG-233	US	11181364		Filing
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03-AG-234	US	11166354		Filing
03-AG-234	EP	04102942.2		Publication
03-AG-251	US	11415879		Filing
03-AG-251	EP	05103557.4		Filing
03-AG-270	US			Filing
03-AG-270	EP	04425486.0		Publication
03-AG-296	US	11178240		Filing
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03-AG-307	US			Filing
03-AG-307	EP	05110548.2		Filing
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03-AG-308	US	11317641		Filing
03-AG-308	US	11317641		Filing
03-AG-308	EP	04425936.4		Filing
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03-AG-316	US	11183229		Publication
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03-AG-316	DE	04103354.9	1617436	Granted
03-AG-319	US	11169497		Filing
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03-AG-342	US	11319750		Filing
03-AG-342	US	11319750		Filing
03-AG-342	EP	05028380.3		Publication
03-AG-342	EP	05028380.3		Publication
03-AG-358	US	11134158		Filing
03-AG-358	EP	04102232.8		Publication
03-AG-370	US	11445491		Filing
03-AG-370	EP	05104742.1		Filing
03-AG-377	EP	05425942.9		Filing
03-AG-377	EP	05425942.9		Filing
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03-CT-278	US	11120766		Filing
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03-PA-111	EP	04103101.4		Publication
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04-AG-103	EP	04425643.6		Publication
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04-AG-110	US	11279385		Filing
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04-AG-112	EP	05022651.3		Publication
04-AG-113	US	11250176		Filing
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04-AG-114	EP	04425754.1		Publication
04-AG-122	US	11300053		Filing
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04-AG-122	EP	05027285.5		Publication
04-AG-148	EP	05425059.0		Filing
04-AG-167	US	11434564		Filing
04-AG-167	IT	MI05A000868		Filing
04-AG-168	US	11279381		Filing
04-AG-168	IT	MI05A000810		Filing
04-AG-173	EP			Filing decision
04-AG-173	EP			Filing decision
04-AG-199	EP	06101314.0		Filing
04-AG-199	EP	06101314.0		Filing
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04-AG-260	EP	05425052.7		Filing
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04-AG-293	US	11323937		Filing
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04-AG-299	US	11437405		Filing
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04-AG-336	US			Filing decision
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04-AG-344	US	11411982		Filing
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04-AG-405	IT	MI05A000780		Filing
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04-AG-469	IT	VA05A000028		Filing
04-CT-108	US	11236137		Publication
04-CT-108	EP	04425724.4		Publication

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04-CT-179	US	11482524		Filing
04-CT-179	EP	05106180.2		Filing
04-CT-310	US	11337030		Filing
04-CT-310	IT	VA05A00002		Filing
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04-CT-313	EP	05100308.5		Publication
04-CT-314	US	11337085		Filing
04-CT-314	EP	05100310.1		Filing
04-CT-375	US	11279663		Filing
04-CT-375	IT	VA05A000024		Filing
04-CT-462	US	11465896		Filing
04-CT-462	EP	05425440.4		Filing
04-NP-045	US	11093012		Filing
04-NP-045	EP	04101301.2		Publication
04-NP-220	US	11035013		Filing
04-NP-240	US	11036179		Filing
04-PA-011	US	11261131		Filing
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05-AG-159	WO	IT08000170		Filing
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05-NP-047	EP	05425559.1		Filing
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06-AG-079	EP	05425283.6		Filing

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06-AG-086	EP	06119479.1		Filing
06-AG-124	EP	06425632.4		Filing
06-AG-136	EP	06119462.8		Filing
06-AG-140	EP	06115812.7		Filing
06-AG-164	EP	06119440.3		Filing
06-AG-188	EP	06425536.7		Filing
06-AG-255	EP	06120719.7		Filing
06-AG-255	EP	06120719.7		Filing
06-AG-311	IT	VA06A000065		Filing
06-CT-186	EP	06425503.7		Filing
06-ICRH-153	EP	06425535.9		Filing
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94-AG-125	DE	94830544.6	0714133	Granted
96-AG-240	IT	MI97A01682	1293610	Granted
97-AG-184	IT	97830575.3	0915408	Abandon requested
97-AG-184	GB	97830575.3	0915408	Granted
97-AG-184	FR	97830575.3	0915408	Granted
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99-AG-102	IT	MI99A02850	1314142	Granted
99-AG-129	IT	MI00A000065	1316288	Granted
99-AG-168	IT	T099A01112	1311325	Granted
99-AG-190	EP	99830770.6		Publication
99-AG-363	US	09823926		Publication
99-AG-363	IT	MI00A00687	1316870	Granted
DB 863	US			Unfiled (do not use)
01-AG-215	US	10313991		Notice of Allowance
01-AG-215	JP	02353352		Publication
01-AG-215	EP	01128461.9		Publication
03-AG-264	US	11195359		Publication
03-AG-264	EP	04425601.4		Publication
03-AG-290	US	11312253		Filing
03-AG-290	EP	04425693.5		Publication
04-AG-060	US	11337787		Publication
04-AG-060	EP	05425024.6		Publication
04-AG-270	US	11312231		Publication
04-AG-283	US	11398849		Publication
04-AG-283	EP	06112075.4		Publication
04-AG-316	US	11398858		Filing
04-AG-316	EP	05102811.6		Publication
04-AG-437	WO	EP06062812		Filing
04-AG-437	EP	05104877.5		Publication
06-AG-070	EP	06425531.8		Filing
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03-AG-232	EP	4103488.5		Filing
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03-AG-318	EP	4425436.5		Publication
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03-AG-030	EP	4425112.2		Publication
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04-AG-470	IT	MI06A000220		Publication
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05-AG-141	EP	5425681.3		Publication
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05-AG-389	IT			Filing requested
06-AG-032	IT	MI06A000585		Publication
06-AG-304	IT	VA07A000042		Publication
06-AG-305	IT	VA07A000026		Publication
07-AG-096	IT	MI07A000787		Publication

Exhibit A

ST Ref - Invention Disclosure

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ST Ref - Invention Disclosure
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07-CT-029
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07-CT-092
07-CT-093
07-CT-214

CONFIRMATION OF ASSIGNMENT

WHEREAS, STMicroelectronics NV, a Dutch corporation organized under the laws of The Netherlands, acting through its Swiss branch, having an office at 39 Chemin du Champ-des-Filles, 1228 Plan-les-Ouates, Geneva, Switzerland (hereinafter referred to as "Assignor") previously assigned its rights in and to the following:

LISTED IN EXHIBIT A

(said patents, patent applications and invention disclosures listed in Exhibit A being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Numonyx BV is a corporation having a principal place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte de l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignee") previously obtained rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, this agreement confirms that Assignor has sold, conveyed, transferred and assigned to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Confirmation of Assignment to be executed by its duly authorized officer on this 25 day of April, 2012.

ASSIGNOR

STMicroelectronics, NV

By: [Signature]

Printed Name: Pierre Olivier

Title: Corporate General Counsel

ASIGNEE

Numonyx, BV

By: [Signature]

Printed Name: Thomas L. Law Jr.

Title: Director

PATENT

REEL: 029186 FRAME: 0792

Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
4964079	US		07342476
	JP	3101697	10714189
5544114	US		08478463
5581511	US		08096684
5638332	US		08486363
	JP	3398722	2035991993
	GB	585150	93401861.5
	FR	585150	93401861.5
5473496	US		08100867
5589793	US		08128871
5592115	US		08467297
	FR	591022	93402229.4
6118315	US		08908583
	JP	3520103	3461071993
4811291	US		07052068
6448842	US		09788623
	IT	1319841	TO00A00146
6404272	US		09785042
	IT	1158654	00830105.3
	GB	1158654	00830105.3
	DE	1158654	00830105.3
6734490	US		09919341
	EP		00830546.8
6519183	US		09908986
	JP		01207874
	EP		00830504.7
6504758	US		09960851
	IT	1320666	TO00A00892
6501673	US		09881661
	IT	1318013	MI00A01315
6462987	US		09930875
	EP		00830582.3
6480436	US		09909467
	EP		00830529.4
6359500	US		09735277
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6563737	US		09953070
	IT	1318892	MI00A02018
6624471	US		09960254
	EP		00830628.4
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	IT	1321049	TO00A01049
6603681	US		09002599
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6532171	US		09919789
	EP		00830553.4
6839818	US		10036088
	EP		00830878.5
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Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
6493260	US		09957628
	JP		01287008
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6567318	US		09991493
	IT	1319130	MI00A02529
6535428	US		09882535
6493268	US		09905163
	IT	1318158	MI00A01585
6674385	US		10060076
	EP		00127649.2
6480421	US		09003474
	IT	1319037	MI00A02337
6788579	US		10119523
	EP		01830247.1
6788517	US		09917613
	IT	1318266	MI00A01729
6700226	US		10035006
6594180	US		10158554
	EP		01830345.3
6788586	US		10161053
	IT		TO01A00530
6829177	US		10161055
	IT		TO01A00531
6687167	US		10225513
	IT	RM20010525	RM01A00525
6687159	US		10036337
	IT	1319614	MI00A02807
6552952	US		10158553
	IT	1325839	MI01A01150
6667903	US		10017502
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6643179	US		10047918
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6655758	US		10035909
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	EP		01830832.0
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	EP		01830248.9
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	EP		02013242.9
6605985	US		10162135
	IT		TO01A00537
6618315	US		10057769
	IT	1227499	01830039.2
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6728141	US		10123874
	EP		01830266.1

Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
6639833	US		10076023
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7017099	US		10159782
	IT		TO01A00529
7084032	US		10356351
	US		11476361
	EP		02425044.1
	US		10323615
	EP		02014408.5
6720271	US		10189152
6816001	US		10290030
	JP		02302131
	EP		01830695.1
6496439	US		09895491
6292405	US		09636363
6701419	US		09977561
	EP		00830675.5
6489807	US		09925842
	IT	VA20000027	VA00A00027
6650153	US		10033711
	IT	1220448	01130836.8
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	DE	60102212	01130836.8
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6674666	US		10077687
	IT		TO01A00148
6917994	US		10115888
	IT		TO01A00333
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6720822	US		09998902
6549048	US		09925979
	FR	2812984	0010554
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	JP		200246868
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6714453	US		10352581
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5557124	US		08212176
6654287	US		10171078
	IT	1325862	MI01A01232
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Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
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6865114	US		10372626
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7035769	US		10330485
	US		11294044
6829168	US		10334126
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6696990	US		10325707
	IT	1328967	MI01A02795
6809961	US		10331116
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6816407	US		10259252
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6822906	US		10331158
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	GB		
	FR		
	EP		02019284.5
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6754107	US		10331185
	EP		02028616.7
6825523	US		10267033
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6981107	US		10281078
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	EP		02425416.1
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	EP		01830827.0
6882231	US		10406628
	IT		TO02A00288
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Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
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6731550	US		10161501
	EP		03253297.0
6892269	US		10271352
	IT		VA01A00035
6990596	US		10325486
	EP		01830782.7
7050322	US		10360840
	IT		TO02A00118
6996697	US		10371221
	IT		VA02A00016
6490197	US		09922043
	JP		2002224398
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6552935	US		09922044
6563732	US		09922068
6594177	US		09922176
	EP		02255361.4
6621737	US		10096531
	EP		02366003.8
	FR	2831315	0113635
	EP		02358013.7
	EP		02358012.9
	FR	2832566	0114969
7031189	US		10434395
	IT		MI02A00984
6826083	US		10426924
	IT	1359591	02425273.6
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6822905	US		10331106
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6909264	US		10608998
	IT		TO02A00566
7135756	US		10680727
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	EP		02425605.9
6989580	US		10680721
	US		11264084
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6901011	US		10417416
	EP		02425231.4
6922366	US		10423845
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6930927	US		10616414
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6949803	US		10675245
	EP		02425592.9
6972454	US		10715887
	US		
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7060995	US		10657801

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US Pat No.	Country	Non-US Pat. number	Filing Number
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7031193	US		10729875
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	EP		03104383.9
6956773	US		10706306
	IT		MI02A02387
	EP		03104130.4
6944061	US		10724022
	IT	1424700	02425727.1
	GB	1424700	02425727.1
	FR	1424700	02425727.1
6836442	US		10614693
	IT		MI02A01486
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	GB	1378913	03425442.5
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7110300	US		10782725
	EP		03425096.9
6967876	US		10651019
6956787	US		10700322
	EP		02425676.0
6940756	US		10739928
	IT		MI02A02668
	EP		03104713.7
6950324	US		10727150
	IT		VA02A00067
6950337	US		10719650
	IT		TO02A01035
	EP		03104345.8
7035142	US		10748696
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6934185	US		10724023
	IT		MI02A02531
7125808	US		10749020
	IT		MI02A02784
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	IT		MI02A02785
6947329	US		10675805
	EP		02425593.7
6894934	US		10728372
	EP		02425747.9
7027317	US		10720013
	IT		MI02A02464
	EP		03103920.9
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	EP		03425264.3
7006025	US		10861340
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	GB	1484764	03425353.4
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7068540	US		10729829
	IT		MI02A02570
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6953609	US		10686556
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6897710	US		10748697
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7023047	US		10745295
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7091570	US		10745297
	US		
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	EP		03104938.0
7054197	US		10820458
	EP		03425224.7
6785174	US		10447293
	EP		02425361.9
6975559	US		10452762
	IT		MI02A01185
6922362	US		10686552
	IT		MI02A02192
6927991	US		10653459
	IT		VA02A00045
6885584	US		10748447
	IT	1435624	02425802.2
	GB	1435624	02425802.2
	EP		03358005.1
	FR	2843660	0210350
7031212	US		10816204
	FR	304077	0304077
6891756	US		10789449
	FR	2851843	0302398
7046577	US		10760631
	EP		04000808.8
6965526	US		10775032
	FR	2851074	0301558
7012837	US		10903927
	FR		0309456
7079448	US		10867381
	EP		04012393.7
7001800	US		10911220
	EP		03425532.3
7064534	US		10695294
7012417	US		10690332
7050335	US		10867378
	EP		04013056.9
7068538	US		10921365
	EP		04291653.6
6975696	US		11076785
4802124	US		07081032
4787066	US		07081037
6373737	US		08478429
5005158	US		07464219
5279887	US		07843818
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5121346	US		07454408
5502655	US		08221986
5206817	US		07600201

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5031152	US		07414775
	KR	223623	9013107
5072137	US		07569009
5053996	US		07661555
5265100	US		07552567
5311473	US		08071448
5161159	US		07568968
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5408435	US		07984233
5706232	US		08424722
5119340	US		07588601
5128897	US		07588577
5072138	US		07570203
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5121358	US		07588609
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5124951	US		07588600
5124584	US		07601287
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	GB	482868	91309720.0
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5297090	US		07627050
5258952	US		07627236
	JP	3609837	3330242
	GB	490679	91311570.5
	FR	490679	91311570.5
5355340	US		08101347
5311477	US		07731803
5319768	US		07694451
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	FR	0514050	92303960.6
5428632	US		07693670
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	GB	0506474	92302736.1
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5734602	US		08651233
5285419	US		07809393
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5471426	US		07830315
	JP	3598119	514481
5257229	US		07830314
5311467	US		07864481
5357235	US		07876959
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5357236	US		07890919
5355113	US		07901667
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5508679	US		08303172
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	FR	586207	93306835.5
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5485430	US		08306527
5455802	US		07995581
5389577	US		08180692
5400007	US		08020045
	JP	3517442	622814
5424988	US		07954276
5644542	US		08460409
5300828	US		07938399
5365129	US		08055080
5339277	US		08056078
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5341336	US		08056376
5349246	US		07995666
5331228	US		07923135
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5612641	US		08380309
5659501	US		08639931
5818760	US		08871226
	JP	2718902	28634994
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5687135	US		08700126
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5499217	US		08348461
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5583820	US		08366211
5748548	US		08711132
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5586077	US		08366212
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5659516	US		08368211
5905677	US		08831046
5548554	US		08365154
5541884	US		08391147
	JP	2737686	753777
5532972	US		08391920
5563826	US		08422813
5638327	US		08412162
5602786	US		08389599
5541880	US		08411904
5576990	US		08367538
5486486	US		831792
5528536	US		8344232
5781043	US		08932930
	US		09616821
5471431	US		08051184
RE36319	US		08966042
5349243	US		08085760
5428311	US		08085580
5546537	US		08410383
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5469116	US		08188153
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5473567	US		08298766
5402379	US		08114749

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5835427	US		08172854
6101618	US		08172848
5568061	US		08492219
5708789	US		08717139
5396108	US		08129257
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5418756	US		08129763
5568073	US		08172853
5440178	US		08160608
5491444	US		08174139
5577051	US		08173197
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5493537	US		08202830
	JP	2720013	3140291994
5430319	US		08246070
5661324	US		08399900
	IT	651394	94460037.8
	GB	651394	94460037.8
	FR	651394	94460037.8
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5555216	US		08361313
5537349	US		08361996
	JP	2886472	33625894
5886386	US		08379337
6265277	US		09220635
5900756	US		08767521
5579265	US		08393464
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	GB	0669576	95400420.6
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	DE	0669576	95400420.6
5610860	US		08402519
5729162	US		08811358
	JP	2919299	9956195
5606523	US		08380737
5592417	US		08380738
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5561621	US		08381529
5612611	US		08423398
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5619451	US		08421671
5796653	US		08394314
	JP	2833646	5972695
5644530	US		08418636
5651128	US		08484873
5594693	US		08408016
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5696716	US		08794965
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5682349	US		08454650
	JP	3725581	7145034
5712814	US		08503303
5920776	US		08712373
	JP	3024519	7197943
5761222	US		08538161
	JP	3704184	7254046
5729492	US		08639192
5982666	US		09013141
6144588	US		09150802
	GB	0715312	94830554.5
5850360	US		08607779
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5644526	US		08538302
5710739	US		08476547
5633834	US		08560324
	IT	0713221	94830538.8
5850361	US		08628587
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5742187	US		08560090
5657276	US		08649468
	GB	0743648	95830211.9
	FR	0743648	95830211.9
5731716	US		08592122
	IT	0714133	94830544.6
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5717636	US		08642325
5612913	US		08533631
	JP		7274935
5696461	US		08521666
5793676	US		08649857
5949713	US		09094916
	GB	741387	95830184.8
	FR	741387	95830184.8
6004847	US		08667097
6248630	US		09300029
	GB	0751559	95830281.2
5856221	US		08670179
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6114203	US		08644892
	JP	3068454	854682
5894065	US		08802619
6104058	US		08898155
5812017	US		08567328
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	DE	69430806	94830563.6
5914867	US		08631574
	JP	2963047	891312
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US Pat No.	Country	Non-US Pat. number	Filing Number
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	FR	768672	95830405.7
5905683	US		08940045
5517455	US		08220976
5471157	US		08220834
	JP	3573372	774338
	GB	675597	94309218.9
	FR	675597	94309218.9
5530674	US		08235161
5583816	US		08267667
5455799	US		08267666
5521880	US		08251718
5493532	US		08251337
5589783	US		08282177
5982188	US		08282047
5566112	US		08288334
5570316	US		08438276
5495446	US		08316087
5502678	US		08315337
5517148	US		08331892
5610866	US		08331699
5574688	US		08438903
5579263	US		08362187
5568084	US		08357664
5654663	US		08631063
5596297	US		08360228
5548241	US		08360229
	JP	3679178	7330189
5594373	US		08359397
5581209	US		08359927
	JP	3596637	33198395
5598122	US		08359926
5589794	US		08360227
5917353	US		08848342
5572470	US		08438349
6052806	US		08519192
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5734608	US		08580549
5675539	US		08575953
5742546	US		08526500
5699295	US		08649282
5652720	US		08573897
5841314	US		08663524
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5838612	US		08869072
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	FR	0757358	95830357.0
5673221	US	RE38166	09410164
	JP	2857649	866710
	GB	734024	95830110.3
	FR	734024	95830110.3
5694363	US		08638976
	GB	747903	95830172.3
	FR	747903	95830172.3
5737268	US		08684431
	GB	0756285	95830336.4
	FR	0756285	95830336.4
5847584	US		08688956
	GB	0757352	95830360.4
	FR	0757352	95830360.4
5760497	US		08684192
	GB	0756220	95830335.6
	FR	0756220	95830335.6
5659502	US		08665862
5920505	US		08881713
	GB	750314	95830253.1
	FR	750314	95830253.1
5781474	US		08722378
	GB	766255	95830406.5
	FR	766255	95830406.5
5784314	US		08679656
	GB	753859	95830302.6
	FR	753859	95830302.6
5844404	US		08720491
	GB	0766256	95830407.3
	FR	0766256	95830407.3
5848013	US		08824616
6137725	US		09203937
6320792	US		09633334
	GB	798735	96830174.7
	FR	798735	96830174.7
5822247	US		08775111
	GB	782149	96830352.9
	FR	782149	96830352.9
5864513	US		08827409
	GB	0798740	96830163.0
5821806	US		08787494
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5659498	US		08684406
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5754483	US		08835033
	JP		975469
	IT	798729	96830160.6
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6438669	US		08813687
	JP		959519
	GB	797209	96830129.1

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5729490	US		08690059
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	DE	757355	95830347.1
6040734	US		09109630
	GB	782268	96830353.7
	FR	782268	96830353.7
5724290	US		08774110
	EP		95830555.9
6157054	US		09032179
	IT	0772200	95830461.0
	GB	0772200	95830461.0
	EP		03012504.1
6195291	US		08687145
6483750	US		09768744
	GB	756286	95830317.4
	FR	756286	95830317.4
5754476	US		08751299
	GB	0772282	95830456.0
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5905387	US		08742978
5778012	US		08671848
	GB	766174	95830408.1
	FR	766174	95830408.1
5801988	US		08835031
	IT	0798743	96830168.9
	GB	0798743	96830168.9
	FR	0798743	96830168.9
5821788	US		08790832
	GB	788114	96830046.7
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	GB	0798642	96830167.1
	FR	0798642	96830167.1
6000980	US		08807113
	GB	0779642	95830520.3
	FR	0779642	95830520.3
5977591	US		08824888
	GB	0798785	96830175.4
6448138	US		09548782
6222245	US		08739997
	IT	0772246	95830459.4
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5926416	US		08807574
	IT		96830088.9
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5817557	US		08792893
	FR	0788144	96830039.2
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5859795	US		08791348
	JP		918819
	IT	788113	96830044.2

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	FR	788113	96830044.2
	WO		IT9600198
6101121	US		09202657
	JP		10502636
	GB	906622	96937493.3
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6057591	US		09014437
6210994	US		09534675
	JP		1018490

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FMG with US Granted:

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	GB	0810606	96830275.2
	FR	0810606	96830275.2
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	GB	0798742	96830166.3
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	GB	0798726	96830161.4
5854770	US		08791746
5946237	US		08826489
6212096	US		09351603
	IT	0798727	96830162.2
	GB	0798727	96830162.2
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	GB	797208	96830128.3
	FR	797208	96830128.3
5859810	US		08826009
	GB	0798730	96830169.7
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5793247	US		08667481
5640122	US		08464551
	GB	747800	96303214.9
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5768206	US		08484491
	GB	747824	96303448.3
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5592422	US		08476658
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5801563	US		08588988
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5745432	US		08589141
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FMG with US Granted:

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5828622	US		08944649
5848018	US		08926833
5864696	US		08587711
6006339	US		09020430
	JP		96721
5619466	US		08589024
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	FR	760518	96305856.5
	DE	69617336	96305856.5
5557573	US		08484609
	JP		8219954
5812446	US		08623044
	JP	2727527	10419096
5760638	US		08664083
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5801577	US		08762677
5737266	US		08738231
5950224	US		08797948
5886549	US		08792962
5850092	US		08833925
6074916	US		09164761
	GB	0802569	96830212.5
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5959935	US		08865748
5929674	US		08846757
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5822259	US		08846755
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6094073	US		09432642
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	GB	0798732	96830164.8
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	GB	0802482	96830215.8
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5867504	US		08869367
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6399444	US		09521876
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6841445	US		10792308
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5994231	US		08996920
6156637	US		08997403
6630739	US		09687217
5883837	US		08940115
5973966	US		09203798
	JP		9263781
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6101124	US		09327111
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5880993	US		08941882
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	JP		10109568
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5888836	US		08990328
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	IT	0902466	97830428.5
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5784331	US		08775664
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5764592	US		08771642
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6037792	US		08771645
6310485	US		09491664
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6281723	US		08993071
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	FR	2758022	9616355
6097631	US		09275327
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5969961	US		09059088
6140876	US		09188071
	FR	2770947	9714018
5966034	US		09087478
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6127898	US		09005722
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6888225	US		10323961
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6212128	US		09093382

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	GB	0911747	97830525.8
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	IT	1304059	MI98A02843
6195286	US		09353756
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	IT	1301100	MI98A01123
6330188	US		09413383
	IT		TO98A00839
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6101118	US		09196204
6285614	US		09602669
	IT	1296486	MI97A02594
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6288594	US		09674395
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	IT	1301555	MI98A01564
6101127	US		09229474
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6372597	US		09837137
	JP		10370047
	EP		97830742.9
6147902	US		09320315
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	DE	0971415	98830388.9
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	IT	1298594	MI98A00454
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	IT	961285	98830333.5
	GB	961285	98830333.5
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6157579	US		09295263
6097213	US		09275691
	IT	1298818	MI98A00640
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	IT	1301042	TO98A00556
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	IT	1301880	MI98A01769
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6444526	US		09419403
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6301152	US		09570332
	JP		00139757
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	FR	1052572	99830286.3
6251736	US		09473368
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6313040	US		09406903
	JP		11261939
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6281077	US		09392937
6396101	US		09836590
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6602774	US		09473367
	IT	1017088	98830793.0
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6301157	US		09415024
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6323799	US		09421616
	IT	0997912	98830626.2
	GB	0997912	98830626.2
6466481	US		09438232
	IT	1305181	TO98A00961
6163483	US		09447531
	IT	1303201	TO98A00990
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	IT	1305182	TO98A00962
6410387	US		09449044
6713347	US		10158424
	JP		11334205
	EP		98830709.6
6169691	US		09397387
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6350637	US		09543400
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6195289	US		09425446
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6259632	US		09491475
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6462400	US		09483656
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6387763	US		09442834
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	EP		98830694.0
6232753	US		09467726
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6274411	US		09469849
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	EP		99830211.1
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	JP		00186781
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6477625	US		09474932
	EP		98830801.1
6424575	US		09716759
	IT	1313853	MI99A02480
6365456	US		09507777
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6300195	US		09512900
	IT	1032035	99830100.6
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6275416	US		09472496
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	GB	1058304	99830336.6
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5946264	US		09183231

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6240002	US		09561271
	EP		99201363.1
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6212112	US		09452446
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6249456	US		09448632
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6954395	US		10742181
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	EP		00830209.3
6451653	US		09858335
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6581134	US		09817804
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6650173	US		09714852
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6476664	US		09823262
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	JP		2001508458
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	JP		2000281033
	GB	1085520	00402549.0
	FR	1085520	00402549.0
	DE	1085520	00402549.0
6307792	US		09675366
6385096	US		09952904
	EP		00402443.6
	GB	1085518	00402548.2
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6839285	US		09737170
	EP		00125553.8
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4897365	US		07124440
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4823175	US		54712
4816883	US		64480
4888497	US		07187581
4968639	US		07284272
4949307	US		07298487
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4922402	US		07372493
4956569	US		07373203
4987088	US		07381283
4997782	US		07386189
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4992680	US		07456782
5075246	US		07625764
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5486487	US		08139800
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5179300	US		07691768
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5321317	US		07936857
5347493	US		07938731
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5407861	US		08068139
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5793086	US		08772301
5515318	US		84654
5793675	US		08829935
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5372956	US		08153620
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5724395	US		08188569
5637520	US		08195369
	JP		616744
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5913120	US		08502329
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6816404	US		10319439
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	US		11258340
	EP		02425088.8
6930913	US		10372639
	US		11156989
	EP	1339111	02425089.6
7092277	US		10926784
	EP		03077667.8
RE35,385	US		08336560
	DE		88403141.0
	FR		8717900
	GB		88403141.0
	IT		88403141.0
	JP	2931864	3247668
	KR		1718688
	US		07283305
	US		07912867
RE35,578	US		08198208
	US		7283214
	DE		88403140.2
	EP	321327	88403140.2
	FR		8717386
	GB		88403140.2
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7163154	US		10471883
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	GB	1368784	02706857.6
	IT	1368784	02706857.6
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	FR		9711787
	GB	0903688	98402303.6
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	FR	2850205	0300681
6856547	US		10664606
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US	10639240	
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IT	MI03A01591	
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WO	IT04000555	
US	10574868	
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IT	VA03A00037	
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DE	IT04000555	
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EP		
US	11018550	
EP	04106858.6	
US	10623474	
IT	MI02A01583	
US	10789351	

Exhibit A

FMG Pending

Country	Filing Number	Patent number
EP	03425133.0	
US	10788525	
IT	03425134.8	1453059
GB	03425134.8	1453059
FR	03425134.8	1453059
US	10779856	
IT	TO03A00115	
US	10727341	
EP	02425730.5	
US	11067345	
US	10890529	
US	10890529	
EP	03425459.9	
EP	03425459.9	
US	10982528	
IT	RM03A00512	
US	10913788	
IT	MI03A01619	
US	11009687	
US	11009687	
IT	MI03A02444	
IT	MI03A02444	
US	10805168	
EP	03425172.8	
US	10805182	
EP	03425171.0	
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US	10971774	
US	10971774	
EP	03425687.5	
EP	03425687.5	
US	10913128	
EP	03017939.4	
US	10971776	
EP	03103911.8	
US	11194739	
EP	04103714.4	
US	11317679	
US	11317679	
EP	04425937.2	
EP	04425937.2	
US	11319798	
US	11319798	
IT	MI04A002533	
IT	MI04A002533	
US	11280186	
US	11280186	
IT	MI04A02206	
IT	MI04A02206	
US	11181364	
EP	04103353.1	
US	11166354	
EP	04102942.2	
US	11415879	
EP	05103557.4	
US		
EP	04425486.0	

Exhibit A

FMG Pending

Country	Filing Number	Patent number
US	11178240	
IT	TO04000470	
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EP	05110648.2	
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US	11317641	
US	11317641	
EP	04425936.4	
EP	04425936.4	
US	11183229	
IT	04103354.9	1617438
GB	04103354.9	1617438
FR	04103354.9	1617438
DE	04103354.9	1617438
US	11169497	
EP	04425485.2	
US	11319750	
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EP	05028380.3	
EP	05028380.3	
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EP	04102232.8	
US	11445491	
EP	05104742.1	
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US	11401523	
IT	MI05A000607	
US	10690368	
JP	2004307123	
EP	04256444.3	
US	10788581	
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US	10783935	
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US	11013123	
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US	11121615	
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US	11120766	
IT	VA04A00020	
US	10861341	
IT	MI03A01126	
US	11152675	
EP	04425435.7	
US	11202632	
EP	04425626.1	
US	10886003	
IT	VA03A00022	
EP	04103101.4	
US	10984372	
IT	MI03A02134	
US	11138884	

Exhibit A

FMG Pending

Country	Filing Number	Patent number
IT	MI04A01038	
US	11008586	
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EP	04029017.3	
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US	11233464	
EP	04104595.6	
US	11401521	
IT	MI05A000608	
EP	06007497.8	
US	11221584	
EP	04425678.2	
US	11249763	
EP	04105094.9	
US	11270308	
IT	TO04A00791	
US	11279384	
IT	MI05A000609	
US	11205766	
EP	04425643.6	
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EP	05425209.3	
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EP	05425207.7	
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IT	MI04A02071	
EP	05022651.3	
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EP	04425754.1	
US	11300053	
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US	11434564	
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EP	06001378.6	
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US	11331826	
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IT	MI04A02241	
EP	05025286.5	
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EP	05425930.4	

Exhibit A

FMG Pending		
Country	Filing Number	Patent number
US	10816271	
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EP	05425052.7	
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US	11323937	
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US	11437405	
EP	05425347.1	
US	11437268	
EP	05425348.9	
US	11300145	
IT	MI04A002374	
EP	05027286.3	
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EP	05425814.0	
US	11411982	
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EP	05425261.4	
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US	11334205	
IT	VA05A000001	
US	11340414	
JP	06015176	
EP	05100551.0	
CN	0610059263.5	
US	11444892	
EP	05104656.3	
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IT	VA05A000061	
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IT	MI05A000838	
US	11495886	
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EP	04425724.4	

Exhibit A

FMG Pending		
Country	Filing Number	Patent number
US	11482524	
EP	05106180.2	
US	11337030	
IT	VA05A00002	
US	11336411	
EP	05100308.5	
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EP	05100310.1	
US	11279663	
IT	VA05A000024	
US	11455896	
EP	05425440.4	
US	11093012	
EP	04101301.2	
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US	11036179	
US	11261131	
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US	11495874	
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EP	05106782.5	
IT	MI06A 001037	
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IT	MI05A001628	
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IT	MI05A001679	
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EP	05425558.3	
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WO	IT06000223	
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Exhibit A

FMG Pending		
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US	11495876	
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EP	05425718.3	
IT	MI05A002350	
EP	06425452.7	
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US		
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US	11460835	
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EP	06120719.7	
IT	VA06A000065	
EP	06425503.7	
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EP	06425535.9	
IT	94830544.6	0714133
DE	94830544.6	0714133
IT	MI97A01682	1293610
IT	97830575.3	0915408
GB	97830575.3	0915408
FR	97830575.3	0915408
IT	98830491.1	0978844
FR	98830491.1	0978844
DE	98830491.1	0978844
IT	99830491.9	1073119
GB	99830491.9	1073119
FR	99830491.9	1073119
IT	MI99A02650	1314142
IT	MI00A00065	1316286
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US	09823926	
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Exhibit A

FMG Pending		
Country	Filing Number	Patent number
FR	9911858	2798769
US		
US	10313991	
JP	02353352	
EP	01128461.9	
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EP	06112075.4	
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EP	05102811.6	
WO	EP06062812	
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US	11318053	
IT	MI04A02462	
US	11113818	
EP	4101718.7	
US	11605209	
US	11713081	
IT	MI06A000627	
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IT	VA06A000011	
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IT	MI06A000220	
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IT	MI07A001012	
US	11741462	
IT		
IT	MI06A000585	
IT	VA07A000042	
IT	VA07A000026	
IT	MI07A000787	

Exhibit A

FMG Invention Disclosures

Invention Disclosure
TECNICA DI SCRITTURA DI UNA MEMORIA A CAMBIAMENTO DI FASE CON SELETTORE BIPOLARE A RIDUZIONE DI TENSIONE
PROCESSO PER LA REALIZZAZIONE DI UNA MEMORIA NON VOLATILE AD ALTA DENSITA' INTEGRABILE CON CIRCUITERIA AD ALTRE PRESTAZIONI MEDIANTE
CIRCUITERIA PER DISPOSITIVI DI MEMORIA NON VOLATILE REALIZZATI INTERAMENTE CON OSSIDI LOW VOLTAGE
SHAPES OF FLOATING GATE WITH A BROKEN PROFILE IN A PROCESS FLOW WITHOUT SELF ALIGNED FLOATING GATE
PROCESSO PER INTEGRARE MEMORIE FLASH CON LOGICHE AVANZATE
ARCHITETTURA CIRCUITALE E PROTOCOLLO DI COMUNICAZIONE PER L'INDIRIZZAMENTO DELLO SPAZIO DEI REGISTRI DI PROTEZIONE DEI SOTTO-
TECNICA DI RIDONDANZA HARD SOFT
ELEVATOR A RISPARMIO ENERGETICO
SANDWICH MULTI-LAYER HARDMASK
METODO PER LA DEFINIZIONE DEI CONTATTI IN UNA MEMORIA MEDIANTE DOPPIA ESPOSIZIONE LITOGRAFICA
CONSTANT ACCESS TIME WITH TEMPERATURE VARIATION
PIPELINE ROW DECODER
CONTINUOUS GATE RAMP REFERENCE SETTING
METODOLOGIA DI PRECARICA CONTROLLATA PER APPLICAZIONI LOW VOLTAGE E HIGH PERFORMANCE
MODALITA' ALTERNATIVA DI TESTING DI DISPOSITIVO A SEMICONDUTTORE CHE ESCLUDA L'UTILIZZO DI MACCHINE DI ESTING E IMPIEGHI SCATOLE PER AUTOTEST
DISPOSITIVO A SEMICONDUTTORE PREDISPOSTO ALLA CONTATTATURA DAL RETRO MEDIANTE VIE PASSANTI ATTRAVERSO IL SUBSTRATO
NUOVA PROGRAMMAZIONE PER MEMORIE FLASH NOR
METODO DI FABBRICAZIONE DI UNA CELLA FOTOVOLTAICA FLESSIBILE A STATO SOLIDO
METODO PER RIDURRE IL DISTURBO DI WORD LINE DURANTE LA FASE DI CANCELLAZIONE DI MEMORIE FLASH LA CUI ORGANIZZAZIONE PREVEDA LA
STRUCTURE AND METHOD TO OBTAIN A FAST PROGRAM NAND DEVICE
PARTITION SUPPLY INDEPENDENCE
METODO LITOGRAFICO PER LA DEFINIZIONE DELLE GEOMETRIE DI UN DISPOSITIVO MEDIANTE UTILIZZO DELLA POLARIZZAZIONE DELLA LUCE DI ESPOSIZIONE E
PROCESS FOR HAVING LITHOGRAPHIC PITCH TO DEFINE MEMORY ARRAYS
SCHEMA DI INTEGRAZIONE MEDIANTE TECNICA DPCC DI LOGICA AD ALTE PRESTAZIONI IN UN PROCESSO PER MEMORIE NON VOLATILI A FLOATING GATE DI
HIGH IMPEDANCE DRAIN VOLTAGE REFERENCE DISTRIBUTION ARCHITECTURE
MEMORY CELL FOR SUB-50NM FLASH NON VOLATILE MEMORIES
DEFINIZIONE DI CONTATTI TRAMITE ESPOSIZIONE DI MASCHERE LINEE-SPAZI E ATTACCHI DRY ALTAMENTE SELETTIVI
REALIZZAZIONE INNOVATIVA DEL LIVELLO DI CONTATTATURA IN MEMORIE NON VOLATILI IN PRESENZA DI ALTA DENSITA' DELLE STRUTTURE, CON
REFERENCE CELL WITH ENHANCED RTN IMMUNITY
A NEW ARCHITECTURE FOR A SINGLE POLYSILICON LEVEL FLASH EEPROM CELL IN ARRAY
VIRTUAL GROUND FLASH NAND MEMORY
CONTATTI CON STRATO DI ADESIONE E SILICIURIZZAZIONE MEDIANTE FILM DI COBALTO PER LOGICHE E MEMORIE
REGOLATORE DI TENSIONE "ON CHIP" PER MEMORIE FLASH NOR UTILIZZANTE SOLO TRANSISTORI HV
BUFFER DI USCITA CON SPEED UP AUTO-ADATTATIVO DEI SEGNALE DI CONTROLLO DEI DRIVERS FINALI

Exhibit A

FMG Invention Disclosures

Invention Disclosure
PORTABLE UNIVERSAL FLASH MEMORY FUNCTIONAL DIAGNOSTIC PLATFORM
GAP FILL MODULATION BY USING RAMPED INPUT PARAMETERS PROCESS (RIPP) IN HIGH DENSITY PLASMA REACTORS
REALIZZAZIONE DI UNA NUOVA TECNICA LITOGRAFICA CHE PERMETTA L'ESPOSIZIONE DIFFERENZIATA DI PATTERN PRESENTI SULLA STESSA MASCHERA
SISTEMI DI RIFERIMENTI PER IL TRACCIAMENTO DELLO SPOSTAMENTO DELLE SOGLIE DOPO CICLATURA, IN MEMORIE FLASH
PROCESS TO INTEGRATE IN A SINGLE CHIP FLASH MEMORY AND ADVANCED CMOS: AN IMPROVEMENT WITH RESPECT TO PATENT 99-AG-328
NUOVO ALGORITMO DI PROGRAMMAZIONE - CANCELLAZIONE PER MIGLIORARE PRESTAZIONI E AFFIDABILITA' DI UN DISPOSITIVO DI MEMORIA NON VOLATILE
TECNICA DI SCRITTURA DI UNA MEMORIA A CAMBIAMENTO DI FASE MULTILIVELLO
PROCESSO INNOVATIVO PER LA RIMOZIONE SLETTIVA DEI METALLI NON REAGITI DURANTE LA DORMAZIONE DI SILICIURI AUTOALLINEATI MEDIANTE L'USO DI
NUOVA STRUTTURA DELLA STRINGA DI UNA MEMORIA FLASH NAND
FLUSSO SASTI+POLY1-1 CON POLYCMP OTTIMIZZATO PER OTTENERE UN MIGLIOR ACCOPPIAMENTO CAPACITIVO
FLUSSO DSASTI
ARCHITETTURA CON CELLE DI RIFERIMENTO MULTIPLE PER UN SINGOLO LIVELLO D'INFORMAZIONE IN UNA MEMORIA NOR FLASH
ERROR CORRECTION CODE METHOD WITH DYNAMIC ENABLING/DISABLING
OTTIMIZZAZIONE DELLA LARGHEZZA DELLA DISTRUBUZIONE DU SOFT PROGRAM IN UNA MEMORIA FLASH NOR
REFRESH DELLE CELLE PROGRAMMATE DURANTE L'OPERAZIONE MDI CANCELLAZIONE
ARCHITECTURE OPTIMIZATION FOR TRISTATE NORFLASH MEMORY
METODO DI PROGRAMMAZIONE E BIT-MANIPULATION PER MEMORIE FLASH TRISTATO CON RECUPERO DAL POWER LOSS COMPATIBILE CON FLASH 2 BIT PER
COMPENSAZIONE DEI TEMPI DI SET UP NELLA GENERAZIONE DEGLI STATI DI WAIT
METODO PER UNA GESTIONE MISTA VOLATILE/NON VOLATILE DELLO STATO DI PROTEZIONE DEI SETTORI DI UNA MEMORIA FLASH
PARITY CHECK SOLUTION FOR POWER ON INFORMATION
PATTERN BINARIO PER CELLE TRI-STATO IDONEO ALLA BIT MANIPULATION IN DI RECOVERY DOPO POWER LOSS
SEPARAZIONE REFERENCE DI READ E PROGRAM SU PIU' WORD LINE PER MINIMIZZARE L'EFFETTO DEL READ DISTURB DURANTE L'UTILIZZO DELLE REFENCE
IC SEMICONDUCTOR DEVICE INFORMATION WITH THROUGH SILICON VIAS TECHNOLOGY FOR STACKED DICE
A MEMORY HAVING AN EMBEDDED ECC WITH WORDS MANAGEMENT CAPABILITIES
A MEMORY WITH SIMULTANEOUS SLC AND MLC READ MODES CAPABILITIES
FLASH MEMORY ARCHITECTURE WITH ROW ADDRESS SCRAMBLING
REFERENCE CELLS NUMBER REDUCTION IN GATE RAMP READING SYSTEM BY MEANS OF DIODES
PRPOSTA DI BREVETTO PER LA SCRITTURA MULTILIVELLO DI CELLE DI MEMORIA CON TRIMMING DEL RESET
UTILIZZO DI UNA MATRICE DI MEMORIA FLASH PER APPLICAZIONE DI DOSIMETRIA
DISPOSITIVO BARRIERA ALLA CRESCITA DI WHISKER PER PACKAGES A INSERZIONE MONTATI SU BOARD
A MEMORY WIH SIMULTANEUS SLC AND HLC PROGRAM MODES CAPABILITIES
A METHODOLOGY FOR REDUCING CONTAMINATION COMING FROM THE SUBSTRATE DURING HIGH THEMPERATURE OXIDATION
RAPID ACCESS WORDLINE

Exhibit A

FMG Invention Disclosures

Invention Disclosure
ARCHITETTURA DELLE LINEE DI ALIMENTAZIONE PER MEMORIE CON FUNZIONAMENTO DUAL WORK AD ALTE PERFORMANCE
ARCHITETTURA PER UNA MEMORIA FLASH AD ELEVATA SICUREZZA CONTROLLABILE DAL CLIENTE
ARCHITETTURA DI LETTURA PER MEMORIE INSENSIBILI A DEGRADO DELLE CELLE RAM FOR FIRMWARE PATCH E DEBUGGING
PROPOSTA DI ARCHITETTURA DI POMPA DI CARICA NEGATIVA AD ELEVATA EFFICIENZA
ARCHITETTURA E METODOLOGIA PER UN PRECISO E VELOCE SETTING DELLE CELLE DI RIFERIMENTO IN MEMORIE FLASH
METODOLOGIA DI PROGRAMMAZIONE DI UNA CELLA FLASH SINGLE BIT ADATTATIVA ALL'INVECCHIAMENTO E SENZA VERIFICA
4GB 60nm NAND FLASH
CIRCUITO DI LETTURA DELLE CELLE DI RIFERIMENTO PER FLASH MEMORY MULTILIVELLO
CIRCUITO DI RIDUZIONE DEL RUMORE DALL'ALIMENTAZIONE IN UN SENSE AMPLIFIER PER FLASH MEMORY MULTILIVELLO
CIRCUITO DI LETTURA DI UNA FLASH MEMORY MULTILIVELLO CON RIFERIMENTO "VIRTUALE"
CIRCUITO DI LETTURA PER FLASH MEMORY 2b/c VS 1b/c
INVALIDATION D'UN PLAN MEMOIRE
GESTION DES RAFFRAICHISSEMENTS SUR UNE MEMOIRE FLASH EFFACABLE PAR PAGE ET SOUS-SECTEURS
GENERATEUR DE RAMPE HAUTE TENSION POUR MEMOIRE RF
DETECTEUR D'HORLOGE
GENERATEUR DE RAMPE HAUTE TENSION POUR MEMOIRE RF AVEC DIFFERENTIEL HAUTE TENSION
NEW WRITE METHOD FOR PHASE CHANGE MEMORIES
NEGATIVE POLARITY FOR READING PHASE CHANGE CELLS WITH VERY SHORT DELAY AFTER PROGRAMMING
HEATER ENGINEERING FOR PHASE CHANGE MULTI BIT STORAGE DEVICES
PHASE CHANGE MEDIA FINE PATTERNING WITH ELECTRODEPOSITION OF A CONDUCTIVE MATERIAL
PRECARIA E SENSE PER UN DISPOSITIVO PCM CON SELETTORE A BIPOLARE
NITRIDE PROTECTIVE LAYER FOR TISIN CVD HEATER ELEMENT FOR PCM
SCRITTURA MULTILIVELLO DI CELLE DI MEMORIA PCM CON RISCALDAMENTO E RAFFREDDAMENTO GRADUALE
TECNICA DI LETTURA PER MEMORIE PCM-MLC
PROPOSTA DI BREVETTO PER IL CRIPTAGGIO DI DATI IN UNA MEMORIA MULTILIVELLO PCM
A NEW VIRTUAL BLOCKS ARCHITECTURE FOR FLASH MEMORIES
A NOVEL MECHANISM OF INHERITING HASH TABLE FOR WEAR LEVELING
A DEFERRED MINIMIZE STRUCTURE MECHANISM IN FTL
A CYCLE BASED WEAR LEVELLING ARCHITECTURE
CONFIGURABLE MEMORY ARCHITECTURE SIMULATION
METHOD FOR THE REDUCTION OF THE SWITCHING NOISE AFFECTING VOLTAGE REFERENCE LINES
ARCHITECTURE TO OPTIMIZE PROGRAMMING IN MEMORIES
STRUCTURE AND METHOD TO OBTAIN AN ENHANCED MULTILEVEL NAND FLASH MEMORY
WRITE WITH FLUSH COMMAND FOR A LOW POWER DOUBLE RATE NON VOLATILE MEMORY
METODO DI LETTURA AD ALTA PRECISIONE PER LE CELLE DI UNA MEMORIA FLASH

Exhibit A

FMG Invention Disclosures

Invention Disclosure
METODO DI LETTURA A TEMPO VARIABILE PER UNA MEMORIA FLASH
FAST PROGRAM VERIFY
ARCHITETTURA CIRCUITO DI LETTURA PER MEMORIE A DUE BLOCCHI
METODO DI COMPENSAZIONE NEI DISTURBI DI PROGRAMMAZIONE IN UNA MEMORIA NON VOLATILE
METODO DI LETTURA AD ALTA PRECISIONE MMEDIANTE CAMPIONAMENTO DEL VALORE ANALOGICO DELLA TENSIONE DI BIT LINE PER CELLE DI MEMORIA NAND
RIDUZIONE DELLA PROBABILITA' DI FALLIMENTO PER SILC TRAMITE POLARIZZAZIONE DINAMICA DI RIPOSO PER ARRAY DI MEMORIE FLASH
READING METHOD FOR NAND MEMORIES
NUOVO ALGORITMO DI PROGRAMMAZIONE PER MIGLIORARE PRESTAZIONI E AFFIDABILITA' DI UN DISPOSITIVO DI MEMORIA NON VOLATILE
PROGRAMMING METHODOLOGY TO INCREASE CYCLING PERFORMANCE
METODO DI PROGRAMMAZIONE PER UNA MEMORIA NAND
ALGORITMO DI CODIFICA LOGICA DEI DATI IN MEMORIE DIGITALI PER IL MIGLIORAMENTO DELLE PRESTAZIONI AFFIDABILISTICHE
BULK ERASE TIME REDUCTION IN NON VOLATILE MEMORY DEVICES
METODO DI CAMPIONAMENTO DI CELLE DI RIFERIMENTO PER MEMORIE CON ARCHITETTURA SINCRONA E DUAL WORK
MEMORIE MULTILIVELLO SINGLE BIT
MEMORIE MULTILIVELLO A CODIFICA MULTIBIT RIDONDATA
ALGORITMO PER LA PROTEZIONE DATI IN MEMORIE MULTILIVELLO A CODIFICA RIDONDATA
ALGORITMO DEDICATO A OTTIMIZZARE LE PRESTAZIONI DI SCRITTURA IN MEMORIE MULTILEVEL SINGLE BIT
EFFICACE SISTEMAPER IL RECUPERO DELLO STATO DELLA MEMORIA FLASH DOPO POWER LOSS
TWO-DIMENSION HASH TABLE MECHANISM FOR BLOCK MANAGER

Exhibit A

Hynix: Co-Owned

Priority	COUNTRY	APP.NUM	REG.NUM
20040078954	KR	20040078954	0624960
	US	11/010664	
20040097161	KR	20040097161	0582421
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	US	10/737559	6991983
20030034695	KR	20030034695	0526576
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	US	10/727470	6869848
20030038233	KR	20030038233	0532780
	US	10/739649	6908805
20030042420	KR	20030042420	0511679
	US	10/720457	
20030040155	KR	20030040155	0497474
	US	10/722814	7179707
20030062485	KR	20030062485	0542394
	US	10/744495	6803277
20030060792	KR	20030060792	0538075
	US	10/883279	7041555
20030043614	KR	20030043614	0535651
	US	11/379455	7262995
	US	10/740100	7072216
	US	11/379456	7280402
20030059419	KR	20030059419	0481890
	US	10/880691	7169670
20030043792	KR	20030043792	0554836
	US	10/736720	7067425
20030062073	KR	20030062073	0537278
	US	10/881461	6835620
20030067157	KR	20030067157	0613078
	US	10/886867	7061803
20030081959	KR	20030081959	0560936
	US	10/743934	6998873
20030096240	KR	20030096240	0543310
	US	10/878093	7110296
20030069424	KR	20030069424	0739240
	US	10/879434	7015743
20030043798	KR	20030043798	0568425
	US	10/734389	6964921
20030098833	KR	20030098833	0507703
	US	10/882451	6987046
20030043627	KR	20030043627	0524465
	US	10/727478	6927151
20030100158	KR	20030100158	0562985
	US	10/879785	
20030081957	KR	20030081957	0538886
	US	10/878273	7034360
20030079422	KR	20030079422	0538379
	US	10/878811	6958289
20030043624	KR	20030043624	0543655
	US	10/739746	7199028
20030042430	KR	20030042430	0470942
	US	10/730688	6858543

Exhibit A

Hynix: Co-Owned

Priority	COUNTRY	APP. NUM	REG. NUM
20040021780	KR	20040021780	0538884
	US	11/018701	7166510
20030043400	KR	20030043400	0490288
	US	10/734533	6884682
20030088265	KR	20030088265	0582335
	US	10/876065	6930001
	US	11/137895	
20030077490	KR	20030077490	0562675
	US	10/880035	7078332
20030043621	KR	20030043621	
	US	10/740089	6974747
20030043619	KR	20030043619	0578656
	US	10/720408	6964913
20030049469	KR	20030049469	0542368
	US	10/731480	7037822
20030093726	KR	20030093726	0705937
	US	10/879840	7271439
20030043688	KR	20030043688	0538882
	US	10/743578	7179735
20030080047	KR	20030080047	0542395
	US	10/887964	6979618
20030081960	KR	20030081960	0520684
	US	10/745008	7211484
20030043410	KR	20030043410	
	US	10/740352	7022624
20030043686	KR	20030043686	
	US	10/727825	6927168
20030070557	KR	20030070557	0714034
	US	10/876144	7030684
20040001648	KR	20040001648	0562134
	US	11/007182	7099213
20030088269	KR	20030088269	0554841
	US	10/879848	7053689
20030081714	KR	20030081714	0542701
	US	10/879540	7031210
20030079476	KR	20030079476	0500418
	US	10/876121	7102942
20030077246	KR	20030077246	0562674
	US	10/872725	7259067
20030100169	KR	20030100169	0568790
	US	10/887944	7109109
20030091653	KR	20030091653	
	US	10/878916	
20030100172	KR	20030100172	0567530
	US	10/880282	7030036
20030094453	KR	20030094453	0600316
	US	10/881423	
20040001656	KR	20040001656	0533772
	US	10/887258	7067389
20030100171	KR	20030100171	0567529
	US	10/872883	6967151
20030100170	KR	20030100170	0550779
	US	10/883414	6943075

Exhibit A

Hynix: Co-Owned

Priority	COUNTRY	APP.NUM	REG.NUM
20040012705	KR	20040012705	0520682
	US	11/006729	7157957
20040034450	KR	20040034450	0635202
	US	11/008362	7269064
20040033206	KR	20040033206	0530930
	US	11/006082	7027330
20040027107	KR	20040027107	
	US	10/879857	6963509
20040027109	KR	20040027109	0539158
	US	10/879680	6984562
20040011753	KR	20040011753	0541157
	US	10/883402	7148109
20030098846	KR	20030098846	0612557
	US	10/878173	
20040033207	KR	20040033207	0604561
	US	11/010987	7196373
20040022677	KR	20040022677	0559716
	US	10/876319	7035143
20040012714	KR	20040012714	0559715
	US	11/007183	7095656
20040011754	KR	20040011754	0537554
	US	11/007181	
20040001749	KR	20040001749	0609942
	US	10/880202	7157332
20040019745	KR	20040019745	0630535
	US	11/008486	7187584
20040034443	KR	20040034443	0635203
	US	11/014581	7072221
20040033199	KR	20040033199	0624287
	US	11/010515	7236397
20040026766	KR	20040026766	0559714
	US	10/888067	7212439
20040033214	KR	20040033214	0613079
	US	11/010165	7126434
20040027108	KR	20040027108	0602322
	US	10/879671	7084031
20040034518	KR	20040034518	0582422
	US	11/016286	7075824
20040038468	KR	20040038468	0567912
	US	11/009749	7046554
20040001746	KR	20040001746	0665396
	US	11/007311	7132328
20040019479	KR	20040019479	0559040
	US	11/009712	7192883
20040024186	KR	20040024186	0554828
	US	10/880278	7205242
20040043618	KR	20040043618	0624290
	US	10/887260	7015097
20040070228	KR	20040070228	0705221
	US	11/011725	7161842
20040070229	KR	20040070229	0580117
	US	11/016437	7259078
20040054061	KR	20040054061	0539449
	US	11/014570	7138314

Exhibit A

Hynix: Co-Owned

Priority	COUNTRY	APP.NUM	REG.NUM
20040049763	KR	20040049763	
	US	11/016413	7268064
20040054510	KR	20040054510	0519170
	US	11/018698	7229912
20040087145	KR	20040087145	0650835
	US	11/020330	7183173
20040055273	KR	20040055273	0609568
	US	11/014507	7061813
20040085423	KR	20040085423	0650839
	US	11/011962	
20040073679	KR	20040073679	0616193
	US	11/010986	7259063
20040079505	KR	20040079505	0650846
	US	11/016436	7279394
20040085429	KR	20040085429	0607330
	US	11/010755	7235458
20040057185	KR	20040057185	0694982
	US	11/119646	
20040085430	KR	20040085430	
	US	11/015451	7122443
20040079903	KR	20040079903	0616187
	US	11/022460	
20040084179	KR	20040084179	0671583
	US	11/027153	
20040078957	KR	20040078957	0609576
	US	11/010156	7206228
20040093176	KR	20040093176	0635205
	US	11/103383	
	US	11/007918	7189618
	US	11/138560	7223655

PATENT ASSIGNMENT AGREEMENT

WHEREAS, Numonyx B.V., a company organized under the laws of Netherlands, having a place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte. De l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignor") has rights in and to the following:

1. U.S. Letters Patents Listed on Exhibit A,
2. U.S. Patent Applications listed on Exhibit B,
3. Foreign patents and patent applications listed on Exhibit C.

(said patents and patent applications listed in Exhibits A, B, and C being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Micron Technology, Inc. is a corporation having a principal place of business at 8000 S. Federal Way, Boise, Idaho 83707, United States (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuation-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be executed by its duly authorized officer effective as of the 23 day of May, 2012

Numonyx B.V.

By: Thomas L. Laws Jr.

Printed Name: Thomas L. Laws Jr.

Title: Director

Micron Technology, Inc.

By: Roderic W. Lewis

Printed Name: Roderic W. Lewis

Title: V.P. of Legal Affairs

Exhibit A - US Issued Patents

File/Doc No	Matter Type	Matter Status	Title	Filed Date	Serial No	Issue Date	Patent No	Country
NMXS-0001.00/RE	Utility - REIS	Expired	METHOD TO INSTALL AN ELECTRONIC COMPONENT AND ITS ELECTRICAL CONNECTIONS ON A SUPPORT, AND PRODUCT OBTAINED THEREBY	12/12/1986	08/198,208	3/20/1990	RE35,578	US
NMXS-0001.00/US	Utility - ORG	Expired	METHOD TO INSTALL AN ELECTRONIC COMPONENT AND ITS ELECTRICAL CONNECTIONS ON A SUPPORT, AND PRODUCT OBTAINED THEREBY	12/12/1986	07/283,214	3/20/1990	4,908,937	US
NMXS-0002.00/US	Utility - ORG	Abandoned	METHOD FOR FIXING AN ELECTRONIC COMPONENT AND ITS CONTACTS TO A SUPPORT	12/12/1986	07/283,305	7/17/1990	4,941,257	US
NMXS-0002.01/RE	Utility - REIS	Expired	METHOD FOR FIXING AN ELECTRONIC COMPONENT AND ITS CONTACTS TO A SUPPORT	11/9/1994	08/336,560	7/17/1990	RE35,385	US
NMXS-0003.00/US	Utility - ORG	Abandoned	ELECTRICALLY PROGRAMMABLE MEMORY WITH SEVERAL INFORMATION BITS PER CELL	4/24/1989	07/342,476	10/16/1990	4,964,079	US
NMXS-0004.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY DEVICE WITH BALANCING CIRCUIT INCLUDING FOLLOWER AMPLIFIER COUPLED TO BIT LINE	7/23/1993	08/096,684	12/3/1996	5,581,511	US
NMXS-0004.01/US	Utility - CON	Issued	FOLLOWER AMPLIFIER COUPLED TO BIT LINE	6/7/1995	08/486,363	6/10/1997	5,638,332	US
NMXS-0004.02/US	Utility - DIV	Issued	INTEGRATED CIRCUIT MEMORY DEVICE WITH BALANCING CIRCUIT INCLUDING FOLLOWER AMPLIFIER COUPLED TO BIT LINE	6/7/1995	08/478,463	8/6/1996	5,544,114	US
NMXS-0005.00/US	Utility - ORG	Issued	DEVICE FOR THE PROTECTION OF AN INTEGRATED CIRCUIT AGAINST POWER SUPPLY CUTS	8/12/1993	08/100,867	12/5/1995	5,473,496	US
NMXS-0006.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER CIRCUIT OF THE CHARGE-PUMP TYPE WITH BOOTSTRAPPED OSCILLATOR	9/29/1993	08/128,871	12/31/1996	5,589,793	US
NMXS-0006.01/US	Utility - DIV	Issued	VOLTAGE BOOSTER CIRCUIT OF THE CHARGE-PUMP TYPE WITH A BOOTSTRAPPED OSCILLATOR	6/6/1995	08/467,297	1/7/1997	5,592,115	US
NMXS-0007.01/US	Utility - CON	Issued	POWER-ON-RESET CIRCUIT PROVIDING PROTECTION AGAINST POWER SUPPLY INTERRUPTIONS	8/8/1997	08/908,583	9/12/2000	6,118,315	US
NMXS-0008.00/US	Utility - ORG	Expired	SAFETY DEVICE FOR ELECTRICALLY PROGRAMMABLE READ-ONLY MEMORY	5/21/1987	07/052,068	3/7/1989	4,811,291	US
NMXS-0009.00/US	Utility - ORG	Expired	NON-VOLATILE SHADOW STORAGE CELL WITH REDUCED TUNNEL DEVICE COUNT FOR IMPROVED RELIABILITY	8/3/1987	07/081,032	1/31/1989	4,802,124	US
NMXS-0010.00/US	Utility - ORG	Expired	NON-VOLATILE SHADOW STORAGE CELL WITH IMPROVED LEVEL SHIFTING CIRCUIT AND REDUCED TUNNEL DEVICE COUNT FOR IMPROVED RELIABILITY	8/3/1987	07/081,037	11/22/1988	4,787,066	US
NMXS-0011.03/US	Utility - CON	Issued	CONTENT ADDRESSABLE MEMORY	6/7/1995	08/478,429	4/16/2002	6,373,737	US
NMXS-0012.00/US	Utility - ORG	Expired	REDUNDANCY FOR SERIAL MEMORY	1/12/1990	07/464,219	4/2/1991	5,005,158	US
NMXS-0013.00/RE	Utility - REIS	Expired	METHODS FOR FABRICATING MEMORY CELLS AND LOAD ELEMENTS	9/29/1994	08/316,035	9/29/1992	RE37,769	US
NMXS-0013.00/US	Utility - ORG	Abandoned	POLYCRYSTALLINE SILICON CONTACT STRUCTURE	4/30/1990	07/516,272	9/29/1992	5,151,387	US
NMXS-0013.01/US	Utility - DIV	Expired	POLYCRYSTALLINE SILICON CONTACT STRUCTURE	2/28/1992	07/843,818	1/18/1994	5,279,887	US
NMXS-0014.00/US	Utility - CIP	Expired	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	12/21/1989	07/454,408	6/9/1992	5,121,346	US
NMXS-0014.03/US	Utility - CON	Issued	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	4/1/1994	08/221,986	3/26/1996	5,502,655	US
NMXS-0015.00/US	Utility - ORG	Expired	PIPELINED CIRCUITRY FOR ALLOWING THE COMPARISON OF THE RELATIVE DIFFERENCE BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	10/18/1990	07/600,201	4/27/1993	5,206,817	US
NMXS-0016.00/US	Utility - ORG	Expired	TEST CIRCUIT FOR NON-VOLATILE STORAGE CELL	9/29/1989	07/414,775	7/9/1991	5,031,152	US
NMXS-0017.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH A CLOCKED ACCESS CODE FOR TEST MODE ENTRY	8/17/1990	07/569,009	12/10/1991	5,072,137	US
NMXS-0018.00/US	Utility - ORG	Expired	DUAL STATE MEMORY STORAGE CELL WITH IMPROVED DATA TRANSFER CIRCUITRY	2/26/1991	07/661,555	10/1/1991	5,053,996	US
NMXS-0019.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH IMPROVED TEST MODE	7/13/1990	07/552,567	11/23/1993	5,265,108	US
NMXS-0019.01/US	Utility - DIV	Expired	SEMICONDUCTOR MEMORY WITH IMPROVED TEST MODE	6/2/1993	08/071,448	5/10/1994	5,311,473	US
NMXS-0020.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH MULTIPLE CLOCKING FOR TEST MODE ENTRY	8/17/1990	07/568,968	11/3/1992	5,161,159	US

PATENT

REEL: 029186 FRAME: 0847

Exhibit A - US Issued Patents

WI# DctgNo	MatrType	MatrStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0021.01/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY WITH INHIBITED TEST MODE ENTRY DURING POWER-UP	11/20/1992	07/984,233	4/18/1995	5,408,435	US
NMMS-0021.02/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY WITH MULTIPLE CLOCKING FOR TEST MODE ENTRY	4/18/1995	08/424,722	1/6/1998	5,706,232	US
NMMS-0022.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY HAVING LATCHED REPEATERS FOR MEMORY ROW LINE SELECTION	9/26/1990	07/588,601	6/2/1992	5,119,340	US
NMMS-0023.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY HAVING IMPROVED LATCHED REPEATERS FOR MEMORY ROW LINE SELECTION	9/26/1990	07/588,577	7/7/1992	5,128,897	US
NMMS-0024.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH SEQUENTIAL CLOCKED ACCESS CODES FOR TEST MODE ENTRY	8/17/1990	07/570,203	12/10/1991	5,072,138	US
NMMS-0025.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT HAVING MULTIPLE DATA OUTPUTS SHARING A RESISTOR NETWORK	10/22/1990	07/601,288	3/24/1992	5,099,148	US
NMMS-0026.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH POWER-ON RESET CONTROLLED LATCHED ROW LINE REPEATERS	9/26/1990	07/588,609	6/9/1992	5,121,358	US
NMMS-0026.02/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY WITH POWER-ON RESET CONTROLLED LATCHED ROW LINE REPEATERS	1/19/1995	08/376,127	6/11/1996	5,526,318	US
NMMS-0027.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH SEQUENCED LATCHED ROW LINE REPEATERS	9/26/1990	07/588,600	6/23/1992	5,124,951	US
NMMS-0028.00/US	Utility - ORG	Expired	ADDRESS BUFFER CIRCUIT WITH TRANSITION-BASED LATCHING	10/22/1990	07/601,287	6/23/1992	5,124,584	US
NMMS-0029.00/US	Utility - ORG	Expired	VEHICLE SEAT ASSEMBLY WITH INTEGRATED CHILD SEAT	12/14/1990	07/827,403	11/23/1993	5,265,054	US
NMMS-0030.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH COLUMN DECODED BIT LINE EQUILIBRATE	12/13/1990	07/627,050	3/22/1994	5,297,090	US
NMMS-0031.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH SEPARATE TIME-OUT CONTROL FOR READ AND WRITE OPERATIONS	12/14/1990	07/627,236	11/2/1993	5,258,952	US
NMMS-0032.01/US	Utility - CON	Expired	SEMICONDUCTOR MEMORY WITH MULTIPLEXED REDUNDANCY	8/2/1993	08/101,347	10/11/1994	5,355,340	US
NMMS-0033.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT MEMORY DEVICE HAVING FLASH CLEAR	7/17/1991	07/731,803	5/10/1994	5,311,477	US
NMMS-0034.00/US	Utility - ORG	Expired	CONTROL CIRCUIT FOR RESETTNG A SNOOP VALID BIT IN A DUAL PORT CACHE TAG MEMORY	5/1/1991	07/694,451	6/7/1994	5,319,768	US
NMMS-0035.00/US	Utility - ORG	Expired	CONTROL CIRCUIT FOR DUAL PORT MEMORY	4/30/1991	07/693,670	6/27/1995	5,428,632	US
NMMS-0036.00/US	Utility - ORG	Expired	CACHE TAG PARTY DETECT CIRCUIT	3/29/1991	07/677,313	8/16/1994	5,339,322	US
NMMS-0037.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT MEMORY DEVICE WITH REDUNDANT ROWS	7/17/1991	07/731,487	3/22/1994	5,297,094	US
NMMS-0038.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT DUAL-PART MEMORY DEVICE HAVING REDUCED CAPACITANCE	7/17/1991	07/731,802	2/15/1994	5,287,322	US
NMMS-0039.01/US	Utility - CON	Expired	SIGNAL LINE PULSE ENHANCING CIRCUIT FOR INTEGRATED CIRCUITS	8/20/1993	08/109,697	6/14/1994	5,321,314	US
NMMS-0040.00/US	Utility - ORG	Issued	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	11/30/1992	07/982,988	12/27/1994	5,377,153	US
NMMS-0040.02/US	Utility - DIV	Issued	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	5/22/1996	08/651,233	3/31/1998	5,734,602	US
NMMS-0040.03/US	Utility - CON	Issued	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	8/26/1996	08/697,482	4/22/1997	5,623,438	US
NMMS-0041.00/US	Utility - ORG	Expired	READ/WRITE MEMORY WITH IMPROVED TEST MODE DATA COMPARE	12/17/1991	07/809,393	2/8/1994	5,285,419	US
NMMS-0042.01/US	Utility - CON	Issued	TRISTABLE DRIVER FOR INTERNAL DATA BUS LINES	12/31/1996	08/777,836	5/5/1998	5,748,556	US
NMMS-0043.00/US	Utility - ORG	Issued	MULTIPLEXING SENSE AMPLIFIER HAVING LEVEL SHIFTER CIRCUITS	3/31/1993	08/040,916	12/27/1994	5,377,143	US
NMMS-0043.01/US	Utility - DIV	Issued	MULTIPLEXING SENSE AMPLIFIER	9/16/1994	08/307,332	1/9/1996	5,483,489	US
NMMS-0044.00/US	Utility - ORG	Issued	MULTIPLEXING SENSE AMPLIFIER	3/31/1993	08/041,321	1/23/1996	5,487,048	US
NMMS-0045.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT WITH PRECHARGED INTERNAL DATA BUS	12/17/1991	07/809,395	3/15/1994	5,295,104	US
NMMS-0046.00/US	Utility - ORG	Issued	REDUNDANCY DECODER	1/31/1992	07/830,315	11/28/1995	5,471,426	US
NMMS-0047.00/US	Utility - ORG	Expired	COLUMN REDUNDANCY ARCHITECTURE FOR A READ/WRITE MEMORY	1/31/1992	07/830,314	10/26/1993	5,257,229	US
NMMS-0048.00/US	Utility - ORG	Issued	SELECTIVE BULK WRITE OPERATION	4/7/1992	07/864,481	5/10/1994	5,311,467	US
NMMS-0049.00/US	Utility - ORG	Issued	PARALLELIZED MAGNITUDE COMPARATOR	4/30/1992	07/876,959	10/18/1994	5,357,235	US
NMMS-0050.00/US	Utility - ORG	Issued	PARALLELIZED MAGNITUDE COMPARATOR FOR COMPARING A BINARY NUMBER TO A FIXED VALUE	4/30/1992	07/876,951	6/7/1994	5,319,347	US
NMMS-0051.01/US	Utility - CON	Issued	PARALLELIZED BORROW LOOK AHEAD SUBTRACTOR	9/16/1993	08/122,804	1/31/1995	5,386,377	US
NMMS-0052.00/US	Utility - ORG	Issued	PARALLELIZED DIFFERENCE FLAG LOGIC	5/29/1992	07/890,919	10/18/1994	5,357,236	US

PATENT

REEL: 029186 FRAME: 0848

Exhibit A - US Issued Patents

MTI-DefNo	MaterType	MaterStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0053.00/US	Utility - ORG	Issued	SERIALIZED DIFFERENCE FLAG CIRCUIT	6/22/1992	07/901,667	10/11/1994	5,355,113	US
NMMS-0054.00/US	Utility - ORG	Issued	PROGRAMMABLE DIFFERENCE FLAG LOGIC	7/31/1992	07/923,855	1/10/1995	5,381,126	US
NMMS-0054.01/US	Utility - CON	Issued	FIFO WITH ADAPTABLE FLAG FOR CHANGING SYSTEM SPEED REQUIREMENTS	9/8/1994	08/303,172	4/16/1996	5,508,679	US
NMMS-0055.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT WITH SELF-BIASED DIFFERENTIAL DATA LINES	12/17/1991	07/809,733	10/26/1993	5,257,226	US
NMMS-0056.00/US	Utility - ORG	Issued	PROCESS FOR REALIZING P-CHANNEL MOS TRANSISTORS HAVING A LOW THRESHOLD VOLTAGE IN SEMICONDUCTOR INTEGRATED CIRCUITS FOR ANALOG APPLICATIONS	7/28/1994	08/282,408	7/9/1996	5,534,448	US
NMMS-0056.01/US	Utility - DIV	Issued	PROCESS FOR REALIZING P-CHANNEL MOS TRANSISTORS HAVING A LOW THRESHOLD VOLTAGE IN SEMICONDUCTOR INTEGRATED CIRCUITS FOR ANALOG APPLICATIONS	6/7/1995	08/486,747	12/31/1996	5,589,701	US
NMMS-0057.02/US	Utility - CON	Issued	INPUT BUFFER CIRCUIT IMMUNE TO COMMON MODE POWER SUPPLY FLUCTUATIONS	6/7/1995	08/477,283	11/3/1998	5,831,457	US
NMMS-0058.00/US	Utility - ORG	Issued	STRUCTURE FOR USING A PORTION OF AN INTEGRATED CIRCUIT DIE	11/13/1992	07/975,628	10/11/1994	5,355,344	US
NMMS-0058.01/US	Utility - CON	Issued	STRUCTURE FOR USING A PORTION OF AN INTEGRATED CIRCUIT DIE	6/21/1994	08/263,048	6/11/1996	5,526,317	US
NMMS-0059.01/US	Utility - CON	Issued	INTEGRATED CIRCUIT OUTPUT DRIVER	12/30/1994	08/367,681	7/17/2001	6,262,617	US
NMMS-0060.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUIT WITH BODY BIAS CONTROL FOR MULTIPLE POWER SUPPLY OPERATION	1/3/1994	08/176,960	6/6/1995	5,422,591	US
NMMS-0061.01/US	Utility - CON	Issued	MULTIPLE CLOCKED DYNAMIC SENSE AMPLIFIER	9/15/1994	08/306,527	1/16/1996	5,485,430	US
NMMS-0062.00/US	Utility - ORG	Issued	DUAL DYNAMIC SENSE AMPLIFIERS FOR A MEMORY ARRAY	12/22/1992	07/995,581	10/3/1995	5,455,802	US
NMMS-0063.01/US	Utility - CON	Issued	LEADFRAME FOR INTEGRATED CIRCUITS	1/13/1994	08/180,692	2/14/1995	5,389,577	US
NMMS-0064.00/US	Utility - CIP	Issued	MULTIPLE LEVEL, PARALLEL MAGNITUDE COMPARATOR	2/19/1993	08/020,045	3/21/1995	5,400,007	US
NMMS-0065.01/US	Utility - ORG	Issued	STRESS TEST FOR MEMORY ARRAYS IN INTEGRATED CIRCUITS	9/30/1992	07/954,276	6/13/1995	5,424,988	US
NMMS-0065.02/US	Utility - CON	Issued	STRESS TEST FOR MEMORY ARRAYS IN INTEGRATED CIRCUITS	6/2/1995	08/460,409	7/1/1997	5,644,542	US
NMMS-0066.00/US	Utility - ORG	Issued	SLEW RATE LIMITED OUTPUT BUFFER WITH BYPASS CIRCUITRY	8/31/1992	07/938,399	4/5/1994	5,300,828	US
NMMS-0067.00/US	Utility - ORG	Issued	TEMPERATURE-COMPENSATED VOLTAGE LEVEL SENSE CIRCUIT	4/29/1993	08/055,080	11/15/1994	5,365,129	US
NMMS-0068.00/US	Utility - ORG	Issued	ADDRESS BUFFER	4/30/1993	08/056,078	8/16/1994	5,339,277	US
NMMS-0069.00/US	Utility - ORG	Issued	METHOD FOR STRESS TESTING DECODERS AND PERIPHERY CIRCUITS	4/30/1993	08/056,376	8/23/1994	5,341,336	US
NMMS-0070.00/US	Utility - ORG	Issued	INPUT BUFFER WITH HYSTERESIS CHARACTERISTICS	12/21/1992	07/995,666	9/20/1994	5,349,246	US
NMMS-0071.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUIT	7/31/1992	07/923,135	7/19/1994	5,331,228	US
NMMS-0072.00/US	Utility - ORG	Issued	REDUNDANCY ELEMENT CHECK IN IC MEMORY WITHOUT PROGRAMMING	7/30/1993	08/099,606	4/4/1995	5,404,331	US
NMMS-0073.00/US	Utility - ORG	Issued	SUBSTITUTION OF REDUNDANT ELEMENTS	5/31/1995	08/454,926	7/1/1997	5,644,216	US
NMMS-0074.01/US	Utility - DIV	Issued	PROCESS FOR IMPROVING THE INTERFACE UNION AMONG DIELECTRIC MATERIALS IN AN INTEGRATED CIRCUIT MANUFACTURE	6/6/1995	08/473,552	5/6/1997	5,627,403	US
NMMS-0074.02/US	Utility - CON	Issued	PROCESS FOR IMPROVING THE INTERFACE UNION AMONG DIELECTRIC MATERIALS IN AN INTEGRATED CIRCUIT MANUFACTURE	11/18/1996	08/751,244	8/18/1998	5,795,821	US
NMMS-0075.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR CIRCUIT PROVIDING POTENTIALS OF OPPOSITE POLARITY	9/26/1994	08/311,941	8/13/1996	5,546,044	US
NMMS-0076.00/US	Utility - ORG	Abandoned	PROCESS FOR THE MANUFACTURE OF AN INTEGRATED VOLTAGE LIMITER AND STABILIZER IN FLASH EEPROM MEMORY DEVICES	9/7/1994	08/301,792	1/23/1996	5,486,486	US
NMMS-0076.01/US	Utility - DIV	Issued	PROCESS FOR THE MANUFACTURE OF AN INTEGRATED VOLTAGE LIMITER AND STABILIZER IN FLASH EEPROM MEMORY DEVICES	6/7/1995	08/477,302	2/4/1997	5,600,590	US
NMMS-0077.00/US	Utility - ORG	Issued	MONOLITHICALLY INTEGRATED STORAGE DEVICE	11/30/1994	08/347,653	7/9/1996	5,535,157	US
NMMS-0078.00/US	Utility - ORG	Issued	CIRCUIT FOR COVERING INITIAL CONDITIONS WHEN STARTING-UP AN INTEGRATED CIRCUIT DEVICE	1/30/1995	08/380,309	3/18/1997	5,612,641	US
NMMS-0079.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR SUPPLYING NEGATIVE PROGRAMMING VOLTAGES TO NON-VOLATILE MEMORY CELLS IN A NON-VOLATILE MEMORY DEVICE	11/23/1994	08/344,232	6/18/1996	5,528,536	US
NMMS-0079.01/US	Utility - DIV	Issued	METHOD AND DEVICE FOR SUPPLYING NEGATIVE PROGRAMMING VOLTAGES TO NON-VOLATILE MEMORY CELLS IN A NON-VOLATILE MEMORY DEVICE	4/26/1996	08/639,931	8/19/1997	5,659,501	US

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Exhibit A - US Issued Patents

MTI-PatentNo	MasterType	MasterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0079.02/US	Utility - CON	Issued	METHOD AND DEVICE FOR SUPPLYING NEGATIVE PROGRAMMING VOLTAGES TO NON-VOLATILE MEMORY CELLS IN A NON-VOLATILE MEMORY DEVICE	6/9/1997	08/871,226	10/6/1998	5,818,760	US
NMMS-0080.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUITRY FOR A SEMICONDUCTOR MEMORY DEVICE	12/6/1994	08/349,783	10/15/1996	5,566,114	US
NMMS-0081.01/US	Utility - CON	Issued	INTEGRATED CIRCUIT FOR THE PROGRAMMING OF A MEMORY CELL IN A NON-VOLATILE MEMORY REGISTER	4/18/1996	08/635,455	7/1/1997	5,644,529	US
NMMS-0082.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING REDUNDANCY REGISTERS IN A ROW REDUNDANCY INTEGRATED CIRCUITRY FOR A SEMICONDUCTOR MEMORY DEVICE, AND ROW REDUNDANCY INTEGRATED CIRCUITRY	2/16/1995	08/391,999	8/19/1997	5,659,509	US
NMMS-0083.00/US	Utility - ORG	Issued	INTEGRATED CIRCUITRY FOR CHECKING THE UTILIZATION RATE OF REDUNDANCY MEMORY ELEMENTS IN A SEMICONDUCTOR MEMORY DEVICE	12/7/1994	08/350,961	2/20/1996	5,493,531	US
NMMS-0083.01/US	Utility - CIP	Issued	INTEGRATED CIRCUITRY FOR CHECKING THE UTILIZATION RATE OF REDUNDANCY MEMORY ELEMENTS IN A SEMICONDUCTOR MEMORY DEVICE	2/16/1996	08/602,237	1/13/1998	5,708,601	US
NMMS-0084.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUITRY LAYOUT FOR A SEMICONDUCTOR MEMORY DEVICE	3/29/1995	08/412,550	9/24/1996	5,559,743	US
NMMS-0085.00/US	Utility - ORG	Issued	CURRENT SOURCE HAVING VOLTAGE STABILIZING ELEMENT	1/20/1995	08/377,524	8/13/1996	5,546,054	US
NMMS-0086.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TIMING THE LOADING OF NONVOLATILE-MEMORY OUTPUT DATA	2/23/1995	08/391,160	5/7/1996	5,515,332	US
NMMS-0087.00/US	Utility - ORG	Issued	INTERNAL TIMING METHOD AND CIRCUIT FOR PROGRAMMABLE MEMORIES	2/23/1995	08/391,159	9/2/1997	5,663,921	US
NMMS-0088.01/US	Utility - CON	Issued	LOAD SIGNAL GENERATING METHOD AND CIRCUIT FOR NONVOLATILE MEMORIES	2/25/1997	08/803,915	2/10/1998	5,717,642	US
NMMS-0089.01/US	Utility - CON	Issued	COUNT UNIT FOR NONVOLATILE MEMORIES	8/20/1996	08/700,126	11/11/1997	5,687,135	US
NMMS-0090.00/US	Utility - ORG	Issued	END-OF-COUNT DETECTING DEVICE FOR NONVOLATILE MEMORIES	12/28/1994	08/365,510	1/14/1997	5,594,703	US
NMMS-0091.01/US	Utility - CON	Issued	VOLTAGE BOOSTER WITH AN ACCELERATION CIRCUIT	5/14/1997	08/855,922	6/16/1998	5,768,115	US
NMMS-0092.00/US	Utility - ORG	Issued	BIAS CIRCUIT FOR A MEMORY LINE DECODER DRIVER OF NONVOLATILE MEMORIES	12/2/1994	08/348,461	3/12/1996	5,499,217	US
NMMS-0093.00/US	Utility - ORG	Issued	READING CIRCUIT FOR AN INTEGRATED SEMICONDUCTOR MEMORY DEVICE	3/22/1995	08/408,589	5/6/1997	5,627,790	US
NMMS-0094.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR TESTING A NETWORK WITH A PROGRAMMABLE LOGIC MATRIX	11/28/1994	08/345,530	2/10/1998	5,717,698	US
NMMS-0095.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING AND TESTING A NONVOLATILE MEMORY	1/31/1995	08/381,530	2/4/1997	5,600,600	US
NMMS-0096.00/US	Utility - ORG	Issued	THRESHOLD VOLTAGE MEASURING DEVICE FOR MEMORY CELLS	3/31/1995	08/412,326	2/4/1997	5,600,594	US
NMMS-0097.00/US	Utility - ORG	Issued	CIRCUIT FOR IDENTIFYING A MEMORY CELL HAVING ERRONEOUS DATA STORED THEREIN	8/30/1995	08/521,304	11/11/1997	5,687,124	US
NMMS-0098.00/US	Utility - ORG	Issued	DEVICE FOR DETECTING A REDUCTION IN A SUPPLY VOLTAGE	12/29/1994	08/366,211	12/10/1996	5,583,820	US
NMMS-0098.01/US	Utility - DIV	Issued	DEVICE FOR DETECTING A REDUCTION IN A SUPPLY VOLTAGE	9/9/1996	08/711,132	5/5/1998	5,748,548	US
NMMS-0099.00/US	Utility - ORG	Issued	STABLE REFERENCE VOLTAGE GENERATOR CIRCUIT	11/30/1994	08/347,788	5/21/2002	6,392,469	US
NMMS-0100.00/US	Utility - ORG	Issued	REGULATION CIRCUIT AND METHOD FOR THE ERASING PHASE OF NON-VOLATILE MEMORY CELLS	2/23/1995	08/395,361	4/1/1997	5,617,356	US
NMMS-0101.00/US	Utility - ORG	Issued	CIRCUIT DEVICE AND CORRESPONDING METHOD FOR RESETTNG NON-VOLATILE AND ELECTRICALLY PROGRAMMABLE MEMORY DEVICES	12/29/1994	08/366,212	12/17/1996	5,586,077	US
NMMS-0102.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR PROGRAMMING NON-VOLATILE AND ELECTRICALLY PROGRAMMABLE MEMORY CELLS	12/29/1994	08/366,259	5/21/1996	5,519,656	US
NMMS-0103.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR NON-VOLATILE SEMICONDUCTOR ELECTRICALLY PROGRAMMABLE MEMORY DEVICES	1/3/1995	08/368,211	8/19/1997	5,659,516	US
NMMS-0103.01/US	Utility - DIV	Issued	VOLTAGE REGULATOR FOR NON-VOLATILE SEMICONDUCTOR ELECTRICALLY PROGRAMMABLE MEMORY DEVICES	4/1/1997	08/831,046	5/18/1999	5,905,677	US
NMMS-0104.00/US	Utility - ORG	Issued	INTEGRATED PROGRAMMING CIRCUITRY FOR AN ELECTRICALLY PROGRAMMABLE SEMICONDUCTOR MEMORY DEVICE WITH REDUNDANCY	12/28/1994	08/365,154	8/20/1996	5,548,554	US
NMMS-0105.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR SUPPRESSING DATA LOADING NOISE IN NONVOLATILE MEMORIES	2/21/1995	08/391,147	7/30/1996	5,541,884	US
NMMS-0106.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TIMING THE READING OF NONVOLATILE MEMORIES	2/21/1995	08/391,920	7/2/1996	5,533,972	US

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Exhibit A - US Issued Patents

INT-Defn	Matter Type	Matter Status	Title	Filed Date	Serial No	Issue Date	Patent No	Country
NMMS-0107.00/US	Utility - ORG	Issued	MEMORY ARRAY CELL READING CIRCUIT WITH EXTRA CURRENT BRANCH	4/17/1995	08/422,813	10/8/1996	5,565,826	US
NMMS-0108.00/US	Utility - ORG	Issued	FLASH-EEPROM MEMORY ARRAY AND METHOD FOR BIASING THE SAME	3/28/1995	08/412,162	6/10/1997	5,638,327	US
NMMS-0109.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING REDUNDANCY REGISTERS IN A COLUMN REDUNDANCY INTEGRATED CIRCUITRY FOR A SEMICONDUCTOR MEMORY DEVICE, AND COLUMN REDUNDANCY INTEGRATED CIRCUITRY	2/16/1995	08/389,599	2/11/1997	5,602,786	US
NMMS-0110.00/US	Utility - ORG	Issued	REFERENCE SIGNAL GENERATING METHOD AND CIRCUIT FOR DIFFERENTIAL EVALUATION OF THE CONTENT OF NONVOLATILE MEMORY CELLS	3/28/1995	08/411,904	7/30/1996	5,541,880	US
NMMS-0111.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR NON-VOLATILE SEMICONDUCTOR MEMORY DEVICES	1/3/1995	08/367,538	11/19/1996	5,576,990	US
NMMS-0112.00/RE	Utility - REIS	Issued	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	7/12/2000	09/616,821	7/14/1998	RE39,918	US
NMMS-0112.02/US	Utility - CON	Abandoned	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	9/18/1997	08/932,930	7/14/1998	5,781,043	US
NMMS-0113.00/US	Utility - ORG	Issued	STRUCTURE TO RECOVER A PORTION OF A PARTIALLY FUNCTIONAL EMBEDDED MEMORY	4/22/1993	08/051,184	11/28/1995	5,471,431	US
NMMS-0114.00/RE	Utility - REIS	Issued	STRUCTURE FOR DESELECTIVE BROKEN SELECT LINES IN MEMORY ARRAYS	11/7/1997	08/966,042	11/7/1995	RE36,319	US
NMMS-0114.00/US	Utility - ORG	Abandoned	STRUCTURE FOR DESELECTIVE BROKEN SELECT LINES IN MEMORY ARRAYS	5/28/1993	08/069,025	11/7/1995	5,465,233	US
NMMS-0115.00/US	Utility - ORG	Issued	LATCH CONTROLLED OUTPUT DRIVER	6/30/1993	08/085,760	9/20/1994	5,349,243	US
NMMS-0116.00/US	Utility - ORG	Issued	FUSE CIRCUITRY TO CONTROL THE PROPAGATION DELAY OF AN IC	6/30/1993	08/085,580	6/27/1995	5,428,311	US
NMMS-0117.01/US	Utility - CON	Issued	METHOD AND APPARATUS FOR PARALLEL TESTING OF MEMORY	3/27/1995	08/410,383	8/13/1996	5,546,537	US
NMMS-0118.01/US	Utility - CON	Issued	CLOCK GENERATION CIRCUIT HAVING COMPENSATION FOR SEMICONDUCTOR MANUFACTURING PROCESS VARIATIONS	3/30/1995	08/413,789	5/6/1997	5,627,793	US
NMMS-0119.00/US	Utility - ORG	Issued	CLOCK GENERATOR CIRCUIT WITH LOW CURRENT FREQUENCY DIVIDER	1/22/1994	08/188,153	11/21/1995	5,469,116	US
NMMS-0120.00/US	Utility - CIP	Issued	DISABLING SENSE AMPLIFIER	7/30/1993	08/099,947	12/27/1994	5,377,150	US
NMMS-0121.00/US	Utility - DIV	Issued	DISABLING SENSE AMPLIFIER	8/31/1994	08/298,766	12/5/1995	5,473,567	US
NMMS-0122.00/US	Utility - ORG	Issued	PRECHARGE DEVICE FOR AN INTEGRATED CIRCUIT INTERNAL BUS	8/31/1993	08/114,749	3/28/1995	5,402,379	US
NMMS-0122.01/US	Utility - ORG	Issued	APPARATUS FOR TESTING SIGNAL TIMING AND PROGRAMMING DELAY	1/31/1994	08/189,589	11/26/1996	5,579,326	US
NMMS-0123.00/US	Utility - DIV	Lapsed	DISTRIBUTED NOR TAG MATCH APPARATUS	5/9/1996	08/647,222	11/24/1998	5,841,789	US
NMMS-0124.02/US	Utility - CON	Issued	APPARATUS FOR TESTING SIGNAL TIMING AND PROGRAMMING DELAY	8/31/1993	08/114,747	11/5/1996	5,572,456	US
NMMS-0124.03/US	Utility - CON	Issued	VARIABLE IMPEDANCE DELAY ELEMENTS	2/18/1997	08/801,452	1/11/2000	6,014,050	US
NMMS-0125.00/US	Utility - DIV	Issued	VARIABLE IMPEDANCE DELAY ELEMENTS	8/25/1998	09/139,225	6/28/2001	6,252,447	US
NMMS-0126.00/US	Utility - ORG	Issued	TESTING CIRCUITRY OF INTERNAL PERIPHERAL BLOCKS IN A SEMICONDUCTOR MEMORY DEVICE AND METHOD OF TESTING THE SAME	1/27/1994	08/188,158	8/8/1995	5,440,516	US
NMMS-0127.00/US	Utility - ORG	Issued	STRESS TEST MODE	12/22/1993	08/172,854	11/10/1998	5,835,427	US
NMMS-0128.01/US	Utility - CON	Issued	METHOD AND DEVICE FOR ACQUIRING REDUNDANCY INFORMATION FROM A PACKAGED MEMORY CHIP	12/22/1993	08/172,848	8/8/2000	6,101,618	US
NMMS-0129.01/US	Utility - CON	Issued	REDUNDANT LINE DECODER MASTER ENABLE	6/19/1995	08/492,219	10/22/1996	5,568,061	US
NMMS-0130.00/US	Utility - ORG	Issued	STRUCTURE TO UTILIZE A PARTIALLY FUNCTIONAL CACHE MEMORY BY INVALIDATION OF FAULTY CACHE MEMORY LOCATIONS	9/20/1996	08/717,139	1/13/1998	5,708,789	US
NMMS-0131.00/US	Utility - ORG	Issued	LATCH CONTROLLED OUTPUT DRIVER	9/30/1993	08/129,257	3/7/1995	5,396,108	US
NMMS-0132.00/US	Utility - ORG	Issued	EDGE TRANSITION DETECTION DISABLE CIRCUIT TO ALTER MEMORY DEVICE OPERATING CHARACTERISTICS	9/30/1993	08/129,763	5/23/1995	5,448,756	US
NMMS-0133.00/US	Utility - ORG	Issued	DATA COMPARING SENSE AMPLIFIER	12/22/1993	08/172,853	10/22/1996	5,568,073	US
NMMS-0134.00/US	Utility - ORG	Issued	STATIC TEST MODE NOISE FILTER	11/30/1993	08/160,608	8/8/1995	5,440,178	US
NMMS-0135.00/US	Utility - ORG	Issued	FUSE CIRCUIT WITH FEEDBACK DISCONNECT	12/28/1993	08/174,139	2/13/1996	5,491,444	US
NMMS-0136.00/US	Utility - CIP	Issued	STATIC MEMORY LONG WRITE TEST	12/22/1993	08/173,197	11/19/1996	5,577,051	US
NMMS-0137.00/US	Utility - ORG	Issued	COMPENSATING DELAY ELEMENT FOR CLOCK GENERATION IN A MEMORY DEVICE	12/20/1993	08/170,612	6/13/1995	5,424,985	US
NMMS-0138.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH EDGE TRANSITION DETECTION PULSE DISABLE	2/28/1994	08/202,830	2/20/1996	5,493,537	US
NMMS-0139.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY WITH WRITE CHECKING	11/17/1994	08/340,940	4/21/1998	5,742,548	US

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Exhibit A - US Issued Patents

MTI-PartNo	Mater/Type	Mater/Status	Title	Filed/Date	Serial/No	Issue/Date	Patent/No	Country
NMXXS-0139.00/US	Utility - ORG	Issued	RESISTOR-CAPACITOR-TRANSISTOR TYPE INTEGRATED CIRCUIT, METHOD FOR THE MANUFACTURE OF SUCH A CIRCUIT AND APPLICATION TO AN OSCILLATOR	5/19/1994	08/246,070	7/4/1995	5,430,319	US
NMXXS-0139.01/US	Utility - CON	Issued	RELAXATION OSCILLATOR USING INTEGRATED RTC STRUCTURE	3/8/1995	08/399,900	8/26/1997	5,661,324	US
NMXXS-0140.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT CONTAINING A PROTECTED MEMORY AND SECURED SYSTEM USING SAID INTEGRATED CIRCUIT	10/27/1994	08/329,975	1/14/1997	5,594,793	US
NMXXS-0141.00/US	Utility - ORG	Issued	LINE DECODER CIRCUIT FOR A MEMORY WORKING AT LOW SUPPLY VOLTAGES	12/21/1994	08/361,313	9/10/1996	5,555,216	US
NMXXS-0142.00/US	Utility - ORG	Issued	MEMORY IN INTEGRATED CIRCUIT FORM WITH IMPROVED READING TIME	12/21/1994	08/361,996	7/16/1996	5,537,349	US
NMXXS-0143.00/US	Utility - ORG	Issued	METHOD FOR MAKING A BIPOLAR TRANSISTOR FOR THE PROTECTION OF AN INTEGRATED CIRCUIT AGAINST ELECTROSTATIC DISCHARGES	1/27/1995	08/379,337	3/23/1999	5,886,386	US
NMXXS-0143.01/US	Utility - DIV	Issued	METHOD FOR MAKING A BIPOLAR TRANSISTOR FOR THE PROTECTION OF AN INTEGRATED CIRCUIT AGAINST ELECTROSTATIC DISCHARGES	12/24/1998	09/220,635	7/24/2001	6,265,277	US
NMXXS-0144.02/US	Utility - CON	Issued	BIAS CIRCUIT FOR TRANSISTOR OF A STORAGE CELL	12/16/1996	08/767,521	5/4/1999	5,900,756	US
NMXXS-0145.00/US	Utility - ORG	Issued	MEMORY REDUNDANCY CIRCUIT	2/27/1995	08/393,464	11/26/1996	5,579,265	US
NMXXS-0146.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY WITH COLUMN VOLTAGE HOLDING CIRCUIT	3/10/1995	08/402,519	3/11/1997	5,610,860	US
NMXXS-0146.01/US	Utility - DIV	Issued	INTEGRATED CIRCUIT MEMORY WITH COLUMN VOLTAGE HOLDING CIRCUIT	3/4/1997	08/811,358	3/17/1998	5,729,162	US
NMXXS-0147.00/US	Utility - ORG	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR IN PREDEFINED INITIAL STATE FOR MEMORY REDUNDANCY CIRCUIT	1/30/1995	08/380,737	2/25/1997	5,606,523	US
NMXXS-0148.00/US	Utility - ORG	Issued	NON-VOLATILE-PROGRAMMABLE BISTABLE MULTIVIBRATOR, PROGRAMMABLE BY THE SOURCE, FOR MEMORY REDUNDANCY CIRCUIT	1/30/1995	08/380,738	1/7/1997	5,592,417	US
NMXXS-0148.01/US	Utility - DIV	Issued	NON-VOLATILE-PROGRAMMABLE BISTABLE MULTIVIBRATOR, PROGRAMMABLE BY THE SOURCE, FOR MEMORY REDUNDANCY CIRCUIT	11/21/1996	08/752,814	10/14/1997	5,677,870	US
NMXXS-0148.02/US	Utility - DIV	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR, PROGRAMMABLE BY THE SOURCE, FOR MEMORY REDUNDANCY CIRCUIT	11/21/1996	08/754,432	2/24/1998	5,721,706	US
NMXXS-0149.00/US	Utility - ORG	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR WITH REDUCED PARASITICS IN READING MODE NOTABLY FOR MEMORY REDUNDANCY CIRCUIT	1/31/1995	08/381,529	10/1/1996	5,561,621	US
NMXXS-0150.00/US	Utility - ORG	Issued	SWITCHING REGULATOR WITH DYSSYMMETRICAL DIFFERENTIAL INPUT STAGE	4/19/1995	08/423,398	3/18/1997	5,612,611	US
NMXXS-0151.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER CIRCUIT	4/19/1995	08/423,396	6/3/1997	5,636,115	US
NMXXS-0152.00/US	Utility - ORG	Abandoned	METHOD FOR THE ERASURE OF A MEMORY, AND CIRCUITS FOR THE IMPLEMENTATION THEREOF	4/12/1995	08/421,671	4/8/1997	5,619,451	US
NMXXS-0153.00/US	Utility - ORG	Issued	CIRCUIT FOR THE SELECTION OF REDUNDANT MEMORY ELEMENTS AND FLASH EEPROM MEMORY COMPRISING SAID CIRCUIT	2/22/1995	08/394,314	8/18/1998	5,796,633	US
NMXXS-0154.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY INCORPORATING TEST FUNCTIONS	4/7/1995	08/418,636	7/1/1997	5,644,530	US
NMXXS-0155.00/US	Utility - ORG	Issued	PROGRAMMABLE INTEGRATED CIRCUIT MEMORY COMPRISING EMULATION MEANS	6/7/1995	08/484,873	7/22/1997	5,651,128	US
NMXXS-0156.00/US	Utility - ORG	Issued	MATRIX DEVICE WITH REDUNDANCY FUSES FOR INTEGRATED MEMORY	3/21/1995	08/408,016	1/14/1997	5,594,693	US
NMXXS-0157.00/US	Utility - ORG	Issued	METHOD FOR TESTING AN ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY DEVICE	6/7/1995	08/479,081	12/31/1996	5,590,075	US
NMXXS-0158.01/US	Utility - CON	Issued	PROGRAMMABLE MEMORY ELEMENT	2/4/1997	08/794,965	12/9/1997	5,696,716	US
NMXXS-0159.00/US	Utility - ORG	Issued	SENSE AMPLIFIER WITH HYSTERESIS	5/2/1995	08/432,838	2/3/1998	5,715,204	US
NMXXS-0160.01/US	Utility - DIV	Issued	HIGHLY-PLANAR INTERLAYER DIELECTRIC THIN FILMS IN INTEGRATED CIRCUITS	6/6/1995	08/468,282	1/28/1997	5,598,028	US
NMXXS-0161.00/US	Utility - ORG	Issued	PROCESS FOR THE PRODUCTION OF A SEMICONDUCTOR DEVICE HAVING BETTER INTERFACE ADHESION BETWEEN DIELECTRIC LAYERS	12/22/1995	08/577,125	11/28/2000	6,153,537	US
NMXXS-0161.01/US	Utility - DIV	Issued	PROCESS FOR THE PRODUCTION OF A SEMICONDUCTOR DEVICE HAVING BETTER INTERFACE ADHESION BETWEEN DIELECTRIC LAYERS	12/14/1998	09/211,065	3/11/2003	6,531,714	US
NMXXS-0162.00/US	Utility - ORG	Issued	FAILURE TOLERANT MEMORY DEVICE, IN PARTICULAR OF THE FLASH EEPROM TYPE	5/31/1995	08/454,650	10/28/1997	5,682,349	US

PATENT

REEL: 029186 FRAME: 0852

Exhibit A - US Issued Patents

MT-DRNG	MatterType	MatterStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0163.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY CELL AND A METHOD FOR FORMING THE SAME	7/18/1995	08/503,303	1/27/1998	5,712,814	US
NMMS-0163.01/US	Utility - DIV	Issued	METHOD OF MAKING ASYMMETRIC NONVOLATILE MEMORY CELL	9/11/1996	08/712,373	7/6/1999	5,920,776	US
NMMS-0164.00/US	Utility - ORG	Issued	MEMORY DEVICE HAVING ERROR DETECTION AND CORRECTION FUNCTION, AND METHODS FOR READING, WRITING AND ERASING THE MEMORY DEVICE	10/2/1995	08/538,161	6/2/1998	5,764,222	US
NMMS-0165.00/US	Utility - ORG	Issued	SENSE AMPLIFIER HAVING CAPACITIVELY COUPLED INPUT FOR OFFSET COMPENSATION	4/26/1996	08/639,192	3/17/1998	5,729,492	US
NMMS-0165.01/US	Utility - CON	Issued	SENSE AMPLIFIER CIRCUIT FOR SEMICONDUCTOR MEMORY DEVICES	1/26/1998	09/013,141	11/9/1999	5,982,666	US
NMMS-0166.01/US	Utility - CON	Issued	MONOLITHICALLY INTEGRATED GENERATOR OF A PLURALITY OF VOLTAGE VALUES	9/10/1998	09/150,802	11/7/2000	6,144,588	US
NMMS-0167.00/US	Utility - ORG	Issued	HIGH-VOLTAGE N-CHANNEL MOS TRANSISTOR AND ASSOCIATED MANUFACTURING PROCESS	2/27/1996	08/607,779	12/15/1998	5,850,360	US
NMMS-0168.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH IMPROVED IMMUNITY TO LARGE METALLIZATION DEFECTS	10/2/1995	08/538,302	7/1/1997	5,644,526	US
NMMS-0169.00/US	Utility - ORG	Issued	READING CIRCUIT FOR MEMORY CELLS	6/6/1995	08/476,547	1/20/1998	5,710,739	US
NMMS-0170.00/US	Utility - ORG	Issued	SYNCHRONIZATION DEVICE FOR OUTPUT STAGES, PARTICULARLY FOR ELECTRONIC MEMORIES	11/17/1995	08/560,324	5/27/1997	5,633,834	US
NMMS-0171.00/US	Utility - ORG	Issued	PROGRAMMABLE MEMORY WITH SINGLE BIT ENCODING	4/4/1996	08/628,587	12/15/1998	5,850,361	US
NMMS-0172.00/US	Utility - ORG	Issued	DECODER WITH REDUCED ARCHITECTURE	11/17/1995	08/560,090	4/21/1998	5,742,187	US
NMMS-0173.00/US	Utility - ORG	Issued	OUTPUT STAGE FOR INTEGRATED CIRCUITS, PARTICULARLY FOR ELECTRONIC MEMORIES	5/17/1996	08/649,468	8/12/1997	5,657,276	US
NMMS-0174.00/US	Utility - ORG	Issued	PROGRAMMABLE MULTIBIT REGISTER FOR COINCIDENCE AND JUMP OPERATIONS AND COINCIDENCE FUSE CELL	1/26/1996	08/592,122	3/24/1998	5,731,716	US
NMMS-0176.00/US	Utility - ORG	Issued	EEPROM MEMORY WITH CONTACTLESS MEMORY CELLS	5/3/1996	08/642,325	2/10/1998	5,711,636	US
NMMS-0177.00/US	Utility - ORG	Issued	BYTE ERASABLE EEPROM FULLY COMPATIBLE WITH A SINGLE POWER SUPPLY FLASH-EPROM PROCESS	9/25/1995	08/533,631	3/18/1997	5,612,913	US
NMMS-0178.00/US	Utility - ORG	Issued	POWER-ON RESET CIRCUIT	8/31/1995	08/521,666	12/9/1997	5,696,461	US
NMMS-0179.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE HAVING SECTORS OF SELECTABLE SIZE AND NUMBER	5/3/1996	08/649,857	8/11/1998	5,793,676	US
NMMS-0179.01/US	Utility - CON	Issued	NONVOLATILE MEMORY DEVICE HAVING SECTORS OF SELECTABLE SIZE AND NUMBER	6/15/1998	09/094,916	9/7/1999	5,949,713	US
NMMS-0180.00/US	Utility - ORG	Issued	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON VOLATILE MEMORY CELLS AND SIDE TRANSISTORS AND CORRESPONDING IC	6/20/1996	08/667,097	12/21/1999	6,004,847	US
NMMS-0180.01/US	Utility - CON	Issued	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS AND CORRESPONDING IC	4/27/1999	09/300,029	6/19/2001	6,248,630	US
NMMS-0181.00/US	Utility - ORG	Issued	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS OF AT LEAST TWO DIFFERENT TYPES, AND CORRESPONDING IC	6/20/1996	08/670,179	1/5/1999	5,856,221	US
NMMS-0182.00/US	Utility - ORG	Issued	METHOD OF MANUFACTURING A MOS INTEGRATED CIRCUIT HAVING COMPONENTS WITH DIFFERENT DIELECTRICS	5/10/1996	08/644,892	9/5/2000	6,114,203	US
NMMS-0183.00/US	Utility - ORG	Issued	METHOD FOR IMPROVING THE PROFILE OF THE INTERMEDIATE DIELECTRIC, PARTICULARLY FOR NON VOLATILE MEMORIES	2/19/1997	08/802,619	4/13/1999	5,894,065	US
NMMS-0183.01/US	Utility - DIV	Issued	METHOD FOR IMPROVING THE INTERMEDIATE DIELECTRIC PROFILE, PARTICULARLY FOR NON-VOLATILE MEMORIES	7/22/1997	08/898,155	8/15/2000	6,104,058	US
NMMS-0184.00/US	Utility - ORG	Issued	CHARGE PUMP VOLTAGE MULTIPLIER CIRCUIT	12/5/1995	08/567,328	9/22/1998	5,812,017	US
NMMS-0185.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR-BOOSTER FOR SUPPLYING A PULSATING VOLTAGE HAVING APPROXIMATELY CONSTANT VOLTAGE LEVELS	4/12/1996	08/631,574	6/22/1999	5,914,867	US
NMMS-0186.00/US	Utility - ORG	Issued	CURRENT DETECTING CIRCUIT	8/2/1996	08/691,796	6/9/1998	5,764,570	US
NMMS-0187.00/US	Utility - ORG	Issued	HIERARCHIC MEMORY DEVICE HAVING AUXILIARY LINES CONNECTED TO WORD LINES	9/30/1996	08/724,495	11/24/1998	5,843,728	US

PATENT

REEL: 029186 FRAME: 0853

Exhibit A - US Issued Patents

Patent No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMMS-0188.04/US	Utility - CON	Issued	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	5/15/1997	08/940,045	5/18/1999	5,905,683	US
NMMS-0189.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH FUSE CIRCUITRY SIMULATING FUSE BLOWING	3/31/1994	08/220,976	5/14/1996	5,517,455	US
NMMS-0190.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH CENTRALIZED CONTROL OF EDGE TRANSITION DETECTION PULSE GENERATION	3/31/1994	08/220,834	11/28/1995	5,471,157	US
NMMS-0191.00/US	Utility - ORG	Issued	STRUCTURE CAPABLE OF SIMULTANEOUSLY TESTING REDUNDANT AND NON-REDUNDANT MEMORY ELEMENTS DURING STRESS TESTING OF AN INTEGRATED CIRCUIT MEMORY DEVICE	4/29/1994	08/235,161	6/25/1996	5,530,674	US
NMMS-0192.00/US	Utility - ORG	Issued	LONG WRITE TEST	6/29/1994	08/267,567	12/10/1996	5,583,816	US
NMMS-0193.00/US	Utility - ORG	Issued	CIRCUIT WHICH PROVIDES POWER ON RESET DISABLE DURING A TEST MODE	6/29/1994	08/267,566	10/3/1995	5,455,799	US
NMMS-0194.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY HAVING CONTROL CIRCUITRY FOR SHARED DATA BUS	5/31/1994	08/254,718	5/28/1996	5,521,880	US
NMMS-0195.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY WITH DISABLED EDGE TRANSITION PULSE GENERATION DURING SPECIAL TEST MODE	5/31/1994	08/251,337	2/20/1996	5,493,532	US
NMMS-0196.00/US	Utility - ORG	Issued	VARIABLE INPUT THRESHOLD ADJUSTMENT	7/29/1994	08/282,177	12/31/1996	5,589,783	US
NMMS-0197.00/US	Utility - ORG	Issued	TEST MODE CONTROL CIRCUIT OF AN INTEGRATED CIRCUIT DEVICE	7/29/1994	08/282,047	11/9/1999	5,982,188	US
NMMS-0198.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR ENABLING A BUS DRIVER WHEN A DATA SIGNAL IS VALID	8/10/1994	08/288,334	10/15/1996	5,566,112	US
NMMS-0198.01/US	Utility - DIV	Issued	APPARATUS AND METHOD FOR ENABLING A BUS DRIVER WHEN A DATA SIGNAL IS VALID	5/10/1995	08/438,276	10/29/1996	5,570,316	US
NMMS-0199.00/US	Utility - ORG	Issued	PRE-CHARGED EXCLUSIONARY WIRED-CONNECTED PROGRAMMED REDUNDANT SELECT	9/30/1994	08/316,087	2/27/1996	5,495,446	US
NMMS-0200.00/US	Utility - ORG	Issued	FULL MEMORY CHIP LONG WRITE TEST MODE	9/30/1994	08/315,337	3/26/1996	5,502,678	US
NMMS-0201.00/US	Utility - ORG	Issued	LOW CURRENT DIFFERENTIAL LEVEL SHIFTER	10/31/1994	08/331,892	5/14/1996	5,517,148	US
NMMS-0202.00/US	Utility - ORG	Issued	CIRCUIT STRUCTURE AND METHOD FOR STRESS TESTING OF BIT LINES	10/31/1994	08/331,699	3/11/1997	5,610,866	US
NMMS-0203.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR MAPPING A REDUNDANT MEMORY COLUMN TO A DEFECTIVE MEMORY COLUMN	5/10/1995	08/438,903	11/12/1996	5,574,688	US
NMMS-0204.00/US	Utility - ORG	Issued	POST-FABRICATION SELECTABLE REGISTERED AND NON-REGISTERED MEMORY	12/22/1994	08/362,187	11/26/1996	5,579,263	US
NMMS-0205.00/US	Utility - ORG	Issued	CIRCUIT FOR PROVIDING A COMPENSATED BIAS VOLTAGE	12/16/1994	08/357,664	10/22/1996	5,568,084	US
NMMS-0205.01/US	Utility - CON	Issued	CIRCUIT FOR PROVIDING A COMPENSATED BIAS VOLTAGE	4/12/1996	08/631,063	8/5/1997	5,654,663	US
NMMS-0206.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUITRY WITH LIMITED OUTPUT HIGH VOLTAGE	12/20/1994	08/360,228	1/21/1997	5,596,297	US
NMMS-0207.00/US	Utility - ORG	Issued	VOLTAGE REFERENCE CIRCUIT USING AN OFFSET COMPENSATING CURRENT SOURCE	12/20/1994	08/360,229	8/20/1996	5,548,241	US
NMMS-0208.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUITRY WITH SELECTIVE LIMITED OUTPUT HIGH VOLTAGE	12/20/1994	08/359,397	1/14/1997	5,594,373	US
NMMS-0209.00/US	Utility - ORG	Issued	ADJUSTABLE CURRENT SOURCE	12/20/1994	08/359,927	12/3/1996	5,581,209	US
NMMS-0210.00/US	Utility - ORG	Issued	VOLTAGE REFERENCE CIRCUIT HAVING A THRESHOLD VOLTAGE SHIFT	12/20/1994	08/359,926	1/28/1997	5,598,122	US
NMMS-0211.00/US	Utility - ORG	Issued	DYNAMICALLY CONTROLLED VOLTAGE REFERENCE CIRCUIT	12/20/1994	08/360,227	12/31/1996	5,589,794	US
NMMS-0212.01/US	Utility - CON	Issued	CLOCK PULSE EXTENDER MODE FOR Clocked MEMORY DEVICES HAVING PRECHARGED DATA PATHS	4/30/1997	08/848,342	6/29/1999	5,917,353	US
NMMS-0213.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR MAPPING A REDUNDANT MEMORY COLUMN TO A DEFECTIVE MEMORY COLUMN	5/10/1995	08/438,349	11/5/1996	5,572,470	US
NMMS-0214.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR TESTING AN INTEGRATED CIRCUIT DEVICE	8/25/1995	08/519,192	4/18/2000	6,052,806	US
NMMS-0215.00/US	Utility - ORG	Issued	RESIDUAL CHARGE ELIMINATION FOR A MEMORY DEVICE	12/29/1995	08/580,549	3/31/1998	5,734,608	US
NMMS-0216.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TESTING MEMORIES IN INTEGRATED CIRCUIT FORM	12/21/1995	08/575,953	10/7/1997	5,675,539	US
NMMS-0217.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR ADDRESS DECODING IN AN INTEGRATED CIRCUIT MEMORY	9/11/1995	08/526,500	4/21/1998	5,742,546	US
NMMS-0218.00/US	Utility - ORG	Issued	CURRENT DETECTION CIRCUIT FOR READING A MEMORY IN INTEGRATED CIRCUIT FORM	5/17/1996	08/649,282	12/16/1997	5,699,295	US

PATENT

REEL: 029186 FRAME: 0854

Exhibit A - US Issued Patents

MTI-DRGNO	MatterType	MatterStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0219.00/US	Utility - ORG	Issued	AN ELECTRICALLY PROGRAMMABLE MEMORY WITH IMPROVED RETENTION OF DATA AND A METHOD OF WRITING DATA IN SAID MEMORY	12/18/1995	08/573,897	7/29/1997	5,652,720	US
NMMS-0220.00/US	Utility - ORG	Issued	CHARGE PUMP TYPE OF NEGATIVE VOLTAGE GENERATOR CIRCUIT AND METHOD	6/13/1996	08/663,524	11/24/1998	5,841,314	US
NMMS-0221.00/US	Utility - ORG	Issued	RESISTANCE REFERENCE CIRCUIT	12/21/1995	08/576,881	2/10/1998	5,717,323	US
NMMS-0222.00/US	Utility - ORG	Issued	DIGITAL RAMP GENERATOR	5/30/1996	08/652,710	6/30/1998	5,774,390	US
NMMS-0223.00/US	Utility - ORG	Issued	SERIAL DICHOTOMIC METHOD FOR SENSING MULTIPLE-LEVEL NON-VOLATILE MEMORY CELLS, AND SENSING CIRCUIT IMPLEMENTING SUCH METHOD	1/29/1996	08/593,650	12/23/1997	5,701,265	US
NMMS-0224.01/US	Utility - CON	Issued	READING CIRCUIT FOR MULTILEVEL NON VOLATILE MEMORY CELL DEVICES	6/4/1997	08/869,072	11/17/1998	5,838,612	US
NMMS-0225.00/US	Utility - ORG	Issued	CIRCUIT FOR READING NON-VOLATILE MEMORIES	7/31/1996	08/690,530	3/31/1998	5,734,610	US
NMMS-0226.00/RE	Utility - REIS	Issued	CIRCUIT AND METHOD FOR READING A MEMORY CELL THAT CAN STORE MULTIPLE BITS OF DATA	9/30/1999	09/410,164	9/30/1997	RE38,166	US
NMMS-0226.00/US	Utility - ORG	Abandoned	SENSE CIRCUIT FOR SERIES BINARY SENSING OF MULTILEVEL NONVOLATILE STORAGE ELEMENT	1/29/1996	08/592,939	9/30/1997	5,673,221	US
NMMS-0227.00/US	Utility - ORG	Issued	READING CIRCUIT FOR MEMORY CELL DEVICES HAVING A LOW SUPPLY VOLTAGE	4/25/1996	08/638,976	12/2/1997	5,694,363	US
NMMS-0228.00/US	Utility - ORG	Issued	MODULATED SLOPE SIGNAL GENERATION CIRCUIT, PARTICULARLY FOR LATCH DATA SENSING ARRANGEMENTS	7/19/1996	08/684,431	4/7/1998	5,737,268	US
NMMS-0229.00/US	Utility - ORG	Issued	THRESHOLD DETECTING DEVICE	7/31/1996	08/688,956	12/8/1998	5,847,584	US
NMMS-0230.00/US	Utility - ORG	Issued	CHARGE PUMP CIRCUIT WITH MULTIPLE BOOST STAGES	7/19/1996	08/684,192	6/2/1998	5,760,497	US
NMMS-0231.00/US	Utility - ORG	Issued	FLASH EEPROM WITH INTEGRATED DEVICE FOR LIMITING THE ERASE SOURCE VOLTAGE	7/31/1996	08/692,936	1/14/2003	6,507,067	US
NMMS-0232.00/US	Utility - ORG	Issued	NEGATIVE WORD LINE VOLTAGE REGULATION CIRCUIT FOR ELECTRICALLY ERASABLE SEMICONDUCTOR MEMORY DEVICES	6/19/1996	08/665,862	8/19/1997	5,659,502	US
NMMS-0232.01/US	Utility - CON	Issued	NEGATIVE WORD LINE VOLTAGE REGULATION CIRCUIT FOR ELECTRICALLY ERASABLE SEMICONDUCTOR MEMORY DEVICES	6/23/1997	08/881,713	7/6/1999	5,920,505	US
NMMS-0233.00/US	Utility - ORG	Issued	PARALLEL PROGRAMMING METHOD OF MEMORY WORDS AND CORRESPONDING CIRCUIT	9/30/1996	08/722,378	7/14/1998	5,781,474	US
NMMS-0234.00/US	Utility - ORG	Issued	METHOD FOR SETTING THE THRESHOLD VOLTAGE OF A REFERENCE MEMORY CELL	7/12/1996	08/679,656	7/21/1998	5,784,314	US
NMMS-0235.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR SEMICONDUCTOR NON-VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY DEVICE	9/30/1996	08/720,491	12/1/1998	5,844,404	US
NMMS-0236.00/US	Utility - ORG	Issued	ROW DECODING CIRCUIT FOR SEMICONDUCTOR NON-VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY AND CORRESPONDING METHOD	3/27/1997	08/824,616	12/8/1998	5,848,013	US
NMMS-0236.01/US	Utility - DIV	Issued	ROW DECODING CIRCUIT FOR A SEMICONDUCTOR NON VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY AND CORRESPONDING METHOD	12/2/1998	09/203,937	10/24/2000	6,137,725	US
NMMS-0236.02/US	Utility - DIV	Issued	ROW DECODING CIRCUIT FOR A SEMICONDUCTOR NON VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY AND CORRESPONDING METHOD	8/7/2000	09/633,334	11/20/2001	6,320,792	US
NMMS-0237.00/US	Utility - ORG	Issued	DEVICE FOR GENERATING AND REGULATING A GATE VOLTAGE IN A NON-VOLATILE MEMORY	12/30/1996	08/775,111	10/13/1998	5,822,247	US
NMMS-0238.00/US	Utility - ORG	Issued	SYSTEM FOR DETERMINING THE PROGRAMMED/NON PROGRAMMED STATUS OF A MEMORY CELL	3/27/1997	08/827,409	1/26/1999	5,864,513	US
NMMS-0239.00/US	Utility - ORG	Issued	BOOST REGULATOR	1/21/1997	08/787,494	10/13/1998	5,821,806	US
NMMS-0240.00/US	Utility - ORG	Issued	UNBALANCED LATCH AND FUSE CIRCUIT INCLUDING THE SAME	7/19/1996	08/684,406	8/19/1997	5,659,498	US
NMMS-0241.00/US	Utility - ORG	Issued	REFERENCE WORD LINE AND DATA PROPAGATION REPRODUCTION CIRCUIT FOR MEMORIES PROVIDED WITH HIERARCHICAL DECODERS	3/27/1997	08/835,033	5/19/1998	5,754,483	US
NMMS-0242.00/US	Utility - ORG	Issued	TIMESHARING INTERNAL BUS, PARTICULARLY FOR NON-VOLATILE MEMORIES	3/7/1997	08/813,687	8/20/2002	6,438,669	US
NMMS-0243.00/US	Utility - ORG	Issued	PARALLEL-DICHOTOMIC SERIAL SENSING METHOD FOR SENSING MULTIPLE-LEVEL NON VOLATILE MEMORY CELLS, AND SENSING CIRCUIT FOR ACTUATING SUCH METHOD	7/31/1996	08/690,059	3/17/1998	5,729,490	US

PATENT

REEL: 029186 FRAME: 0855

Exhibit A - US Issued Patents

App. No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMKS-0244.01/US	Utility - CON	Issued	SUPPLY VOLTAGES SWITCHES CIRCUIT	7/2/1998	09/109,630	3/21/2000	6,040,734	US
NMKS-0245.00/US	Utility - ORG	Abandoned	METHOD AND PROGRAMMING DEVICE FOR DETECTING AN ERROR IN A MEMORY	12/24/1996	08/774,110	3/3/1998	5,724,290	US
NMKS-0246.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR FOR ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS	10/23/1996	08/735,709	8/11/1998	5,793,679	US
NMKS-0246.01/US	Utility - DIV	Issued	VOLTAGE GENERATOR FOR ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS	2/27/1998	09/032,179	12/5/2000	6,157,054	US
NMKS-0247.00/US	Utility - ORG	Issued	FLASH EEPROM WITH ON-CHIP ERASE SOURCE VOLTAGE GENERATOR	7/24/1996	08/687,145	2/27/2001	6,195,291	US
NMKS-0247.01/US	Utility - DIV	Issued	FLASH EEPROM WITH ON-CHIP ERASE SOURCE VOLTAGE GENERATOR	1/23/2001	09/768,744	11/19/2002	6,483,750	US
NMKS-0248.00/US	Utility - ORG	Issued	NEGATIVE CHARGE PUMP CIRCUIT FOR ELECTRICALLY ERASABLE SEMICONDUCTOR MEMORY DEVICES	10/31/1996	08/751,299	5/19/1998	5,754,476	US
NMKS-0249.00/US	Utility - ORG	Issued	ANALOG VOLTAGE-SIGNAL SELECTOR DEVICE	11/1/1996	08/742,978	5/18/1999	5,905,387	US
NMKS-0250.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH IMPROVED YIELD AND RELIABILITY	6/28/1996	08/671,848	7/7/1998	5,778,012	US
NMKS-0251.00/US	Utility - ORG	Issued	CIRCUIT FOR THE GENERATION OF A VOLTAGE AS A FUNCTION OF THE CONDUCTIVITY OF AN ELEMENTARY CELL OF A NON-VOLATILE MEMORY	3/27/1997	08/835,031	9/1/1998	5,801,988	US
NMKS-0252.00/US	Utility - ORG	Issued	ZERO CONSUMPTION POWER-ON-RESET	1/30/1997	08/790,832	10/13/1998	5,821,788	US
NMKS-0253.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR REDUNDANCY MANAGEMENT OF NON-VOLATILE MEMORIES	3/27/1997	08/828,039	11/17/1998	5,838,619	US
NMKS-0254.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING A MICROTPP CATHODE ASSEMBLY FOR A FIELD EMISSION DISPLAY PANEL	12/13/1996	08/807,113	12/14/1999	6,000,980	US
NMKS-0255.00/US	Utility - ORG	Issued	HIGH-VOLTAGE-RESISTANT MOS TRANSISTOR, AND CORRESPONDING MANUFACTURING PROCESS	3/18/1997	08/824,888	11/2/1999	5,977,591	US
NMKS-0256.01/US	Utility - CON	Issued	NONVOLATILE FLOATING-GATE MEMORY DEVICES, AND PROCESS OF FABRICATION	4/13/2000	09/548,782	9/10/2002	6,448,138	US
NMKS-0257.00/US	Utility - ORG	Issued	HIGH CAPACITY CAPACITOR AND CORRESPONDING MANUFACTURING PROCESS	10/30/1996	08/739,997	4/24/2001	6,222,245	US
NMKS-0258.00/US	Utility - ORG	Issued	ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS DEVICE FOR A REDUCED NUMBER OF PROGRAMMING CYCLES	2/27/1997	08/807,574	7/20/1999	5,926,416	US
NMKS-0259.00/US	Utility - ORG	Issued	PROCESS OF FABRICATING TUNNEL-OXIDE NONVOLATILE MEMORY DEVICES	1/31/1997	08/792,893	10/6/1998	5,817,557	US
NMKS-0260.00/US	Utility - ORG	Issued	FABRICATION OF NATURAL TRANSISTORS IN A NONVOLATILE MEMORY PROCESS	1/16/1997	08/784,967	7/13/1999	5,923,975	US
NMKS-0261.00/US	Utility - ORG	Issued	MULTI-LEVEL MEMORY CIRCUITS AND CORRESPONDING READING AND WRITING METHODS	1/30/1997	08/791,348	1/12/1999	5,859,795	US
NMKS-0262.00/US	Utility - NSPCT	Issued	MULTI-LEVEL MEMORY CIRCUIT WITH REGULATED READING VOLTAGE	10/30/1996	09/202,657	8/8/2000	6,101,121	US
NMKS-0263.00/US	Utility - ORG	Issued	CLOCK CIRCUIT FOR READING A MULTILEVEL NON VOLATILE MEMORY CELLS DEVICE	6/27/1997	08/883,822	11/16/1999	5,986,921	US
NMKS-0264.00/US	Utility - NSPCT	Issued	MULTI-LEVEL MEMORY CIRCUIT WITH REGULATED WRITING VOLTAGE	10/30/1996	09/202,656	8/1/2000	6,097,628	US
NMKS-0265.00/US	Utility - ORG	Issued	SECTIONIZED ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY DEVICE WITH REDUNDANCY	3/21/1997	08/821,804	12/29/1998	5,854,764	US
NMKS-0266.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY DEVICE WITH TESTABLE REDUNDANCY CIRCUITS	5/8/1997	08/853,756	12/7/1999	5,999,450	US
NMKS-0267.00/US	Utility - ORG	Issued	SENSING CIRCUITRY FOR READING AND VERIFYING THE CONTENTS OF ELECTRICALLY PROGRAMMABLE/ERASABLE NON-VOLATILE MEMORY CELLS	4/29/1997	08/845,916	6/29/1999	5,917,753	US
NMKS-0267.01/US	Utility - CON	Issued	SENSING CIRCUITRY FOR READING AND VERIFYING THE CONTENTS OF ELECTRICALLY PROGRAMMABLE/ERASABLE NON-VOLATILE MEMORY CELLS	12/9/1998	09/209,319	4/25/2000	6,055,187	US
NMKS-0268.00/US	Utility - ORG	Issued	ERASING METHOD FOR A NON-VOLATILE MEMORY	12/24/1996	08/774,860	10/6/1998	5,818,763	US
NMKS-0269.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR GENERATING A SYNCHRONIZING AND SIGNAL	11/26/1997	08/978,665	3/23/1999	5,886,949	US
NMKS-0270.01/US	Utility - DIV	Issued	METHOD OF AVOIDING DISTURBANCE DURING THE STEP OF PROGRAMMING AND ERASING AN ELECTRICALLY PROGRAMMABLE, SEMICONDUCTOR NON-VOLATILE STORAGE DEVICE	9/27/1999	09/407,135	2/27/2001	6,195,290	US

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NIM-DataNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0271.00/US	Utility - ORG	Issued	ERASE VOLTAGE CONTROL CIRCUIT FOR AN ELECTRICALLY ERASABLE NON-VOLATILE MEMORY CELL	1/23/1997	08/787,907	2/24/1998	5,721,707	US
NMMS-0272.00/US	Utility - ORG	Issued	METHOD FOR ERASING AN ELECTRICALLY PROGRAMMABLE AND ERASABLE NON-VOLATILE MEMORY CELL	1/24/1997	08/788,530	7/21/1998	5,784,319	US
NMMS-0273.00/US	Utility - ORG	Issued	MONOLITHICALLY INTEGRATED PROGRAMMABLE DEVICE HAVING ELEMENTARY MODULES CONNECTED ELECTRICALLY BY MEANS OF MEMORY CELLS OF THE FLASH TYPE	12/27/1996	08/777,296	12/21/1999	6,005,411	US
NMMS-0274.00/US	Utility - ORG	Issued	PAGE-MODE MEMORY DEVICE WITH MULTIPLE-LEVEL MEMORY CELLS	6/5/1997	08/869,708	5/26/1998	5,757,719	US
NMMS-0275.00/US	Utility - ORG	Issued	HIGH VOLTAGES DETECTOR CIRCUIT AND INTEGRATED CIRCUIT USING SAME	1/30/1997	08/791,700	8/18/1998	5,796,275	US
NMMS-0276.00/US	Utility - ORG	Issued	PROCESS FOR FORMING AN EDGE STRUCTURE TO SEAL INTEGRATED ELECTRONIC DEVICES, AND CORRESPONDING DEVICE	1/27/1998	09/014,437	5/2/2000	6,057,591	US
NMMS-0276.01/US	Utility - DIV	Issued	PROCESS FOR FORMING AN EDGE STRUCTURE TO SEAL INTEGRATED ELECTRONIC DEVICES	3/24/2000	09/534,675	4/3/2001	6,210,994	US
NMMS-0277.00/US	Utility - ORG	Issued	MEMORY ARCHITECTURE FOR FLEXIBLE READING MANAGEMENT, PARTICULARLY FOR NON-VOLATILE MEMORIES, HAVING NOISE-IMMUNITY FEATURES, MATCHING DEVICE PERFORMANCE, AND HAVING OPTIMIZED THROUGHOUT	4/24/1997	08/847,385	6/29/1999	5,917,768	US
NMMS-0278.00/US	Utility - ORG	Issued	CIRCUITS AND METHODS FOR READ-ENABLING MEMORY DEVICES SYNCHRONOUSLY WITH THE REACHING OF THE MINIMUM FUNCTIONALITY CONDITIONS OF THE MEMORY CELLS AND READING CIRCUITS, PARTICULARLY FOR NON-VOLATILE MEMORIES	4/24/1997	08/840,056	3/2/1999	5,878,049	US
NMMS-0279.00/US	Utility - ORG	Issued	COLUMN MULTIPLEXER	5/9/1997	08/853,732	7/7/1998	5,777,941	US
NMMS-0280.00/US	Utility - ORG	Issued	DRIVER DEVICE FOR SELECTION LINES FOR A MULTIPLEXER, TO BE USED IN A WIDE RANGE OF SUPPLY VOLTAGES, PARTICULARLY FOR NON-VOLATILE MEMORIES	3/27/1997	08/826,223	5/12/1998	5,751,654	US
NMMS-0281.00/US	Utility - ORG	Issued	A MEMORY UNDER TEST PROGRAMMING AND READING DEVICE	3/28/1997	08/835,030	11/14/2000	6,148,413	US
NMMS-0282.00/US	Utility - ORG	Issued	DECODING HIERARCHICAL ARCHITECTURE FOR HIGH INTEGRATION MEMORIES	1/30/1997	08/791,746	12/29/1998	5,854,770	US
NMMS-0283.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE CAPABLE OF READING DATA WITH AN APPROPRIATE SELF-TIMING AND A REDUCED NUMBER OF REFERENCE LINES	3/27/1997	08/826,489	8/31/1999	5,946,237	US
NMMS-0283.01/US	Utility - DIV	Issued	DATA READING PATH MANAGEMENT ARCHITECTURE FOR A MEMORY DEVICE, PARTICULARLY FOR NON-VOLATILE MEMORIES	7/12/1999	09/351,603	4/3/2001	6,212,096	US
NMMS-0284.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE HAVING OPTIMIZED MANAGEMENT OF DATA TRANSMISSION LINES	3/7/1997	08/812,595	11/3/1998	5,831,891	US
NMMS-0285.00/US	Utility - ORG	Issued	PULSE GENERATION CIRCUIT AND METHOD FOR SYNCHRONIZED DATA LOADING IN AN OUTPUT PRE-BUFFER	3/27/1997	08/826,009	1/12/1999	5,859,810	US
NMMS-0286.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH UNEQUALLY-SIZED, PAIRED MEMORY COUPLED TO ODD NUMBER OF INPUT/OUTPUT PADS	6/7/1995	08/477,244	4/29/1997	5,625,603	US
NMMS-0287.01/US	Utility - CON	Issued	CONSTANT CURRENT SOURCE WITH REDUCED SENSITIVITY TO SUPPLY VOLTAGE AND PROCESS VARIATION	6/24/1996	08/667,481	8/11/1998	5,793,247	US
NMMS-0288.00/US	Utility - ORG	Issued	CIRCUIT FOR PROVIDING A BIAS VOLTAGE COMPENSATED FOR P-CHANNEL TRANSISTOR VARIATIONS	6/5/1995	08/464,551	6/17/1997	5,640,122	US
NMMS-0289.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR AN OUTPUT DRIVER WITH REDUCED OUTPUT IMPEDANCE	3/31/1995	08/414,103	11/19/1996	5,576,656	US
NMMS-0290.00/RE	Utility - REIS	Issued	POWER SUPPLY SWITCH REFERENCE CIRCUITRY	5/5/2000	09/568,201	5/5/1998	RE37,876	US
NMMS-0290.00/US	Utility - ORG	Abandoned	POWER SUPPLY SWITCH REFERENCE CIRCUITRY	6/5/1995	08/464,082	5/5/1998	5,747,890	US
NMMS-0291.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR BIASING BIT LINES	6/7/1995	08/484,491	6/16/1998	5,768,206	US
NMMS-0292.00/US	Utility - ORG	Issued	REDUCED PIN COUNT STRESS TEST CIRCUIT FOR INTEGRATED MEMORY DEVICES AND METHOD THEREFOR	6/7/1995	08/476,658	1/7/1997	5,592,422	US
NMMS-0293.00/US	Utility - ORG	Issued	FAULT DETECTION FOR ENTIRE WAFER STRESS TEST	7/31/1995	08/509,158	4/8/1997	5,619,462	US

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Patent No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMMS-0294.01/US	Utility - CON	Issued	INTEGRATED MEMORY CIRCUIT WITH SEQUENCED BITLINES FOR STRESS TEST	4/7/1997	08/833,582	4/28/1998	5,745,420	US
NMMS-0295.01/US	Utility - CON	Issued	METHOD AND APPARATUS FOR TEST MODE ENTRY DURING POWER UP	9/12/1996	08/712,960	12/30/1997	5,703,512	US
NMMS-0296.00/US	Utility - ORG	Issued	COLUMN REDUNDANCY OF A MULTIPLE BLOCK MEMORY ARCHITECTURE	7/31/1995	08/509,351	3/4/1997	5,608,678	US
NMMS-0297.00/US	Utility - CIP	Issued	STRUCTURE FOR DESELECTING BROKEN SELECT LINES IN MEMORY ARRAYS	7/31/1995	08/509,211	7/15/1997	5,648,933	US
NMMS-0298.00/US	Utility - ORG	Issued	WRITE CONTROLLED ADDRESS BUFFER	8/31/1995	08/521,800	5/13/1997	5,629,896	US
NMMS-0299.00/US	Utility - ORG	Issued	REDUNDANCY ARCHITECTURE	12/29/1995	08/582,424	3/18/1997	5,612,918	US
NMMS-0300.00/US	Utility - ORG	Issued	TEST MODE ACTIVATION AND DATA OVERRIDE	1/19/1996	08/587,709	11/7/2000	6,144,594	US
NMMS-0300.02/US	Utility - CON	Issued	TEST MODE ACTIVATION AND DATA OVERRIDE	12/29/1995	09/457,558	10/2/2001	6,297,996	US
NMMS-0301.00/US	Utility - ORG	Issued	PIPELINED CHIP ENABLE CONTROL CIRCUITRY AND METHODOLOGY	1/19/1996	08/588,730	12/23/1997	5,701,275	US
NMMS-0301.01/US	Utility - DIV	Issued	PIPELINED CHIP ENABLE CONTROL CIRCUITRY AND METHODOLOGY	6/19/1997	08/878,612	8/25/1998	5,798,980	US
NMMS-0302.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUITRY HAVING A SINGLE SLEW RATE RESISTOR	1/19/1996	08/588,988	9/1/1998	5,801,563	US
NMMS-0303.00/US	Utility - ORG	Issued	REDUNDANCY CONTROL	12/29/1995	08/580,827	8/4/1998	5,790,462	US
NMMS-0304.00/US	Utility - ORG	Issued	WRITE PASS THROUGH CIRCUIT	1/19/1996	08/588,662	8/12/1997	5,657,292	US
NMMS-0305.00/US	Utility - ORG	Issued	DATA-INPUT DEVICE FOR GENERATING TEST SIGNALS ON BIT AND BIT-COMPLEMENT LINES	1/19/1996	08/588,762	12/1/1998	5,845,059	US
NMMS-0306.00/US	Utility - ORG	Issued	WRITE DRIVER HAVING A TEST FUNCTION	1/19/1996	08/589,141	4/28/1998	5,745,432	US
NMMS-0307.01/US	Utility - CON	Issued	CIRCUIT AND METHOD FOR TRACKING THE START OF A WRITE TO A MEMORY CELL	5/19/1997	08/858,295	9/15/1998	5,808,960	US
NMMS-0308.01/US	Utility - CON	Issued	CIRCUIT AND METHOD FOR TERMINATING A WRITE TO A MEMORY CELL	5/19/1997	08/858,788	10/20/1998	5,825,691	US
NMMS-0309.00/US	Utility - ORG	Issued	CLOCKED SENSE AMPLIFIER WITH WORDLINE TRACKING	1/19/1996	08/587,728	9/1/1998	5,802,004	US
NMMS-0309.01/US	Utility - DIV	Issued	CLOCKED SENSE AMPLIFIER WITH WORDLINE TRACKING	10/6/1997	08/944,649	10/27/1998	5,828,622	US
NMMS-0310.01/US	Utility - CON	Lapsed	MEMORY-ROW SELECTOR HAVING A TEST FUNCTION	9/10/1997	08/926,883	12/8/1998	5,848,018	US
NMMS-0311.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR SETTING THE TIME DURATION OF A WRITE TO A MEMORY CELL	1/19/1996	08/587,711	1/26/1999	5,864,696	US
NMMS-0311.01/US	Utility - DIV	Issued	CIRCUIT AND METHOD FOR SETTING THE TIME DURATION OF A WRITE TO A MEMORY CELL	2/9/1998	09/020,430	12/21/1999	6,006,339	US
NMMS-0312.00/US	Utility - ORG	Issued	LOW-POWER READ CIRCUIT AND METHOD FOR CONTROLLING A SENSE AMPLIFIER	1/19/1996	08/589,024	4/8/1997	5,619,466	US
NMMS-0313.01/US	Utility - DIV	Issued	DEVICE AND METHOD FOR DRIVING A CONDUCTIVE PATH WITH A SIGNAL	9/15/1997	08/929,987	3/16/1999	5,883,838	US
NMMS-0313.02/US	Utility - CON	Issued	DEVICE AND METHOD FOR DRIVING A CONDUCTIVE PATH WITH A SIGNAL	11/19/1997	08/974,747	4/20/1999	5,896,336	US
NMMS-0314.00/US	Utility - ORG	Issued	CIRCUITRY AND METHODOLOGY TO TEST SINGLE BIT FAILURES OF INTEGRATED CIRCUIT MEMORY DEVICES	8/24/1995	08/519,075	5/27/1997	5,633,828	US
NMMS-0315.00/US	Utility - ORG	Issued	ENTIRE WAFER STRESS TEST METHOD FOR INTEGRATED MEMORY DEVICES AND CIRCUIT THEREFOR	8/21/1995	08/484,609	9/17/1996	5,557,573	US
NMMS-0316.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR THE PROTECTION OF NON-VOLATILE MEMORY ZONES	3/28/1996	08/623,044	9/22/1998	5,812,446	US
NMMS-0317.00/US	Utility - ORG	Issued	PHASE GENERATOR CIRCUIT FOR CHARGE PUMP TYPE OF NEGATIVE SUPPLY CIRCUIT	6/13/1996	08/664,083	6/2/1998	5,760,638	US
NMMS-0318.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR THE PROGRAMMING AND ERASURE OF A MEMORY	8/27/1996	08/703,811	3/16/1999	5,883,833	US
NMMS-0318.01/US	Utility - DIV	Issued	METHOD AND CIRCUIT FOR THE PROGRAMMING AND ERASURE OF A MEMORY	11/24/1998	09/198,431	3/7/2000	6,034,895	US
NMMS-0319.00/US	Utility - ORG	Issued	HIGH VOLTAGE GENERATOR	12/11/1996	08/762,677	9/1/1998	5,801,577	US
NMMS-0320.00/US	Utility - ORG	Issued	METHODS AND APPARATUS FOR PROGRAMMING CONTENT-ADDRESSABLE MEMORIES USING FLOATING-GATE MEMORY CELLS	10/25/1996	08/738,231	4/7/1998	5,737,266	US
NMMS-0321.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	2/12/1997	08/797,948	9/7/1999	5,950,224	US
NMMS-0322.00/US	Utility - ORG	Issued	DEVICE TO NEUTRALIZE AN ELECTRONIC CIRCUIT WHEN IT IS BEING POWERED OR DISCONNECTED	1/24/1997	08/792,962	3/23/1999	5,886,549	US

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Patent No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMKS-0322.00/US	Utility - CON	Issued	DISCONNECT TO NEUTRALIZE AN ELECTRONIC CIRCUIT WHEN IT IS BEING POWERED OR	11/17/1998	09/199,735	9/26/2000	6,125,022	US
NMKS-0323.00/US	Utility - ORG	Issued	FLASH-EEPROM WITH EMBEDDED EEPROM	4/10/1997	08/833,925	12/15/1998	5,850,092	US
NMKS-0323.01/US	Utility - DIV	Issued	FLASH-EEPROM WITH EMBEDDED EEPROM	10/1/1998	09/164,761	6/13/2000	6,074,916	US
NMKS-0324.00/US	Utility - ORG	Issued	DATA SENSING TIMING MODULATING CIRCUIT, PARTICULARLY FOR NON-VOLATILE MEMORIES	3/27/1997	08/825,098	3/9/1999	5,881,001	US
NMKS-0325.00/US	Utility - ORG	Issued	SYNCHRONIZATION SIGNAL GENERATION CIRCUIT AND METHOD	5/30/1997	08/865,748	9/28/1999	5,959,935	US
NMKS-0326.00/US	Utility - ORG	Issued	POWER ON RESET CIRCUIT WITH AUTO TURN OFF	4/30/1997	08/846,757	7/27/1999	5,929,674	US
NMKS-0327.00/US	Utility - ORG	Issued	UPROM CELL FOR LOW VOLTAGE SUPPLY	4/30/1997	08/846,755	10/13/1998	5,822,259	US
NMKS-0328.00/US	Utility - ORG	Issued	BIASING CIRCUIT FOR UPROM CELLS WITH LOW VOLTAGE SUPPLY	4/30/1997	08/846,753	1/12/1999	5,859,797	US
NMKS-0329.00/US	Utility - ORG	Issued	LINE DECODER FOR MEMORY DEVICES	5/23/1997	08/862,563	1/25/2000	6,018,255	US
NMKS-0329.01/US	Utility - DIV	Issued	LINE DECODER FOR MEMORY DEVICES	11/2/1999	09/433,642	7/25/2000	6,094,073	US
NMKS-0330.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR READING LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY CELLS	6/18/1997	08/879,017	10/3/2000	6,128,225	US
NMKS-0331.00/US	Utility - ORG	Issued	LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY DEVICE WITH VOLTAGE BOOSTING	6/18/1997	08/877,927	5/11/1999	5,903,498	US
NMKS-0332.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR GENERATING A READ REFERENCE SIGNAL FOR NONVOLATILE MEMORY CELLS	6/18/1997	08/877,921	9/8/1998	5,805,500	US
NMKS-0333.01/US	Utility - CON	Issued	MOS CAPACITOR WITH WIDE VOLTAGE AND FREQUENCY OPERATING RANGES	7/27/2001	09/916,954	7/8/2003	6,590,247	US
NMKS-0334.00/US	Utility - ORG	Issued	BIDIRECTIONAL CHARGE PUMP GENERATING EITHER A POSITIVE OR NEGATIVE VOLTAGE	7/28/1997	08/900,165	2/6/2001	6,184,741	US
NMKS-0335.00/US	Utility - ORG	Issued	BICMOS NEGATIVE CHARGE PUMP	11/5/1997	08/965,068	1/18/2000	6,016,073	US
NMKS-0336.00/US	Utility - ORG	Issued	POSITIVE CHARGE PUMP	10/8/1997	08/946,727	6/13/2000	6,075,402	US
NMKS-0337.00/US	Utility - ORG	Issued	OUTPUT STAGE FOR A MEMORY DEVICE AND FOR LOW VOLTAGE APPLICATIONS	7/23/1997	08/899,228	4/10/2001	6,215,329	US
NMKS-0338.00/US	Utility - ORG	Issued	LOW NOISE OUTPUT BUFFER FOR SEMICONDUCTOR ELECTRONIC CIRCUITS	7/8/1997	08/889,653	5/9/2000	6,060,753	US
NMKS-0339.00/US	Utility - ORG	Issued	HIGH VOLTAGE TOLERANCE OUTPUT STAGE	7/23/1997	08/898,811	11/21/2000	6,150,844	US
NMKS-0340.00/US	Utility - CON	Issued	SINGLE-CELL REFERENCE SIGNAL GENERATING CIRCUIT FOR READING NONVOLATILE MEMORY	6/17/1997	08/877,066	8/31/1999	5,946,238	US
NMKS-0341.00/US	Utility - ORG	Issued	INTEGRATED DEVICE WITH PADS	3/5/1997	08/811,577	7/13/1999	5,923,076	US
NMKS-0342.00/US	Utility - ORG	Issued	NMOS NEGATIVE CHARGE PUMP	1/23/1998	09/012,331	10/10/2000	6,130,572	US
NMKS-0343.00/US	Utility - ORG	Issued	ADDRESS TRANSITION DETECTION CIRCUIT	3/5/1997	08/811,869	9/29/1998	5,815,464	US
NMKS-0344.00/US	Utility - ORG	Issued	METHOD FOR RECOVERING FAILED MEMORY DEVICES	3/18/1997	08/816,766	4/25/2000	6,055,665	US
NMKS-0345.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR GENERATING A POWER-ON RESET SIGNAL	3/27/1997	08/828,791	9/28/1999	5,959,476	US
NMKS-0346.01/US	Utility - ORG	Issued	GAIN MODULATED SENSE AMPLIFIER	3/27/1997	08/828,790	5/4/1999	5,901,087	US
NMKS-0347.00/US	Utility - DIV	Issued	GAIN MODULATED SENSE AMPLIFIER, PARTICULARLY FOR MEMORY DEVICES	3/28/1998	09/048,941	6/20/2000	6,078,523	US
NMKS-0348.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD TO ADJUST MEMORY TIMING	3/28/1997	08/825,138	3/23/1999	5,886,945	US
NMKS-0349.00/US	Utility - ORG	Issued	STANDBY VOLTAGE BOOSTING STAGE AND METHOD FOR A MEMORY DEVICE	3/27/1997	08/826,008	3/30/1999	5,889,723	US
NMKS-0350.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER FOR MEMORY DEVICES	3/27/1997	08/824,958	9/8/1998	5,805,435	US
NMKS-0351.00/US	Utility - ORG	Issued	CIRCUIT FOR THE SWITCHING OF SUPPLY VOLTAGES IN ELECTRICALLY PROGRAMMABLE AND CANCELABLE NON-VOLATILE SEMICONDUCTOR MEMORY DEVICES	4/7/1997	08/835,294	5/19/1998	5,754,473	US
NMKS-0352.00/US	Utility - ORG	Issued	AUTO-SAVING CIRCUIT FOR PROGRAMMING CONFIGURATION ELEMENTS IN NON-VOLATILE MEMORY DEVICES	4/7/1997	08/835,296	1/26/1999	5,864,500	US
NMKS-0353.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR PROGRAMMING ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS IN A CELL MATRIX	4/4/1997	08/833,336	1/16/2001	6,175,521	US
NMKS-0354.00/US	Utility - ORG	Issued	CIRCUIT FOR DETECTING THE COINCIDENCE BETWEEN A BINARY INFORMATION UNIT STORED THEREIN AND AN EXTERNAL DATUM	3/17/1997	08/819,519	9/28/1999	5,959,917	US
NMKS-0354.00/US	Utility - ORG	Issued	METHOD FOR DETECTING REDUNDANT DEFECTIVE ADDRESSES IN A MEMORY DEVICE WITH REDUNDANCY	4/17/1997	08/841,904	11/17/1998	5,838,623	US

PATENT

REEL: 029186 FRAME: 0859

Exhibit A - US Issued Patents

NIM-DK4NO	MatterType	MatterStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0364.01/US	Utility - CON	Issued	METHOD FOR DETECTING REDUNDED DEFECTIVE ADDRESSES IN A MEMORY DEVICE WITH REDUNDANCY	10/30/1998	09/183,469	8/10/1999	5,936,907	US
NMMS-0355.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH ROW REDUNDANCY	4/17/1997	08/842,835	3/30/1999	5,889,710	US
NMMS-0356.00/US	Utility - ORG	Issued	REDUNDANCY MEMORY REGISTER	4/17/1997	08/844,903	9/22/1998	5,812,467	US
NMMS-0357.00/US	Utility - ORG	Issued	CIRCUIT FOR TRANSFERRING REDUNDANCY DATA OF A REDUNDANCY CIRCUIT INSIDE A MEMORY DEVICE BY MEANS OF A TIME-SHARED APPROACH	6/5/1997	08/860,859	1/26/1999	5,864,562	US
NMMS-0358.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH ROW AND COLUMN REDUNDANCY CIRCUITS AND A TIME-SHARED REDUNDANCY CIRCUIT TEST ARCHITECTURE	6/5/1997	08/860,367	2/2/1999	5,867,504	US
NMMS-0359.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH CLOCKED COLUMN REDUNDANCY AND TIME-SHARED REDUNDANCY DATA TRANSFER APPROACH	6/3/1997	08/868,214	10/19/1999	5,968,183	US
NMMS-0360.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH CLOCKED COLUMN REDUNDANCY	6/3/1997	08/868,213	11/3/1998	5,831,915	US
NMMS-0361.00/US	Utility - ORG	Issued	PRE-CHARGE STEP DETERMINING CIRCUIT OF A GENERIC BIT LINE PARTICULARLY FOR NON-VOLATILE MEMORIES	4/7/1997	08/835,347	5/9/2000	6,061,273	US
NMMS-0362.00/US	Utility - ORG	Issued	READ CIRCUIT AND METHOD FOR NONVOLATILE MEMORY CELLS WITH AN EQUALIZING STRUCTURE	6/18/1997	08/877,922	3/23/1999	5,886,925	US
NMMS-0363.00/US	Utility - ORG	Issued	MULTILEVEL NON VOLATILE MEMORY DEVICE	8/22/1997	08/916,874	12/7/1999	5,999,445	US
NMMS-0364.00/US	Utility - ORG	Issued	FLOATING GATE MOS TRANSISTOR CHARGE INJECTION CIRCUIT AND COMPUTATION DEVICES INCORPORATING IT	9/30/1997	08/940,278	8/31/1999	5,946,235	US
NMMS-0364.01/US	Utility - CON	Issued	FLOATING GATE MOS TRANSISTOR CHARGE INJECTION CIRCUIT AND COMPUTATION DEVICES INCORPORATING IT	8/13/1999	09/373,813	5/22/2001	6,236,592	US
NMMS-0364.02/US	Utility - CON	Issued	FLOATING GATE MOS TRANSISTOR CHARGE INJECTION CIRCUIT AND COMPUTATION DEVICES INCORPORATING IT	8/9/2000	09/636,397	6/26/2001	6,252,802	US
NMMS-0365.00/US	Utility - ORG	Issued	ASYMMETRICAL PULSIVE DELAY NETWORK	7/25/1997	08/900,424	4/4/2000	6,046,619	US
NMMS-0366.00/US	Utility - ORG	Issued	CONTROL CIRCUIT OF AN OUTPUT BUFFER	9/19/1997	08/934,499	5/18/1999	5,905,678	US
NMMS-0367.00/US	Utility - ORG	Issued	PROTECTION CIRCUIT FOR REDUNDANCY REGISTER SET-UP CELLS OF ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY DEVICES	10/30/1997	08/961,368	12/29/1998	5,854,762	US
NMMS-0368.00/US	Utility - ORG	Issued	PROCESS FOR REALIZING AN INTERMEDIATE DIELECTRIC LAYER FOR ENHANCING THE PLANARITY IN SEMICONDUCTOR ELECTRONIC DEVICES	12/9/1997	08/987,454	5/29/2001	6,239,042	US
NMMS-0368.01/US	Utility - DIV	Issued	PROCESS FOR REALIZING AN INTERMEDIATE DIELECTRIC LAYER FOR ENHANCING THE PLANARITY IN SEMICONDUCTOR ELECTRONIC DEVICES	8/24/2000	09/645,711	10/29/2002	6,472,750	US
NMMS-0369.00/US	Utility - ORG	Issued	FLOATING GATE NON-VOLATILE MEMORY CELL WITH LOW ERASING VOLTAGE AND MANUFACTURING METHOD	9/29/1997	08/940,856	4/25/2000	6,054,731	US
NMMS-0369.01/US	Utility - DIV	Issued	Floating gate non-volatile memory cell with low erasing voltage and having different potential barriers	3/8/2000	09/521,867	11/14/2000	6,147,380	US
NMMS-0369.02/US	Utility - DIV	Issued	METHOD OF MAKING FLOATING GATE NON-VOLATILE MEMORY CELL WITH LOW ERASING VOLTAGE	3/8/2000	09/521,876	6/4/2002	6,399,444	US
NMMS-0369.03/US	Utility - DIV	Issued	METHOD OF MAKING FLOATING GATE NON-VOLATILE MEMORY CELL WITH LOW ERASING VOLTAGE HAVING DOUBLE LAYER GATE DIELECTRIC	5/30/2002	10/158,706	3/23/2004	6,710,394	US
NMMS-0369.04/US	Utility - DIV	Issued	METHOD OF MAKING FLOATING GATE NON-VOLATILE MEMORY CELL WITH LOW ERASING VOLTAGE HAVING DOUBLE LAYER GATE DIELECTRIC	3/3/2004	10/792,308	1/11/2005	6,841,445	US
NMMS-0370.00/US	Utility - ORG	Issued	ERASING VOLTAGE HAVING DOUBLE LAYER GATE DIELECTRIC	12/23/1997	08/996,920	11/30/1999	5,994,231	US
NMMS-0371.00/US	Utility - ORG	Abandoned	METHOD OF PLANARIZING A SEMICONDUCTOR DEVICE BY DEPOSITING A DIELECTRIC FILM STRUCTURE	12/23/1997	08/997,403	12/5/2000	6,156,637	US
NMMS-0371.01/US	Utility - DIV	Abandoned	PLANARIZATION STRUCTURE AND METHOD FOR DIELECTRIC LAYERS	10/13/2000	09/687,217	10/7/2003	6,630,739	US
NMMS-0372.00/US	Utility - ORG	Issued	READING CIRCUIT FOR SEMICONDUCTOR MEMORY CELLS	9/29/1997	08/940,115	3/16/1999	5,883,837	US
NMMS-0372.01/US	Utility - DIV	Issued	READING CIRCUIT FOR SEMICONDUCTOR MEMORY CELLS	12/1/1998	09/203,798	10/26/1999	5,973,966	US
NMMS-0373.00/US	Utility - ORG	Issued	STARCASE ADAPTIVE VOLTAGE GENERATOR CIRCUIT	2/26/1998	09/032,282	9/7/1999	5,949,666	US
NMMS-0374.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT ARCHITECTURE FOR TESTING A NON-VOLATILE MEMORY DEVICE	5/30/1997	08/865,642	6/27/2000	6,081,911	US

PATENT

REEL: 029186 FRAME: 0860

Exhibit A - US Issued Patents

MTI-DC-NO	MatterType	MatterStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0375.00/US	Utility - ORG	Issued	SELF-REGULATED EQUALIZER, PARTICULARLY FOR SENSE AMPLIFIERS	10/30/1997	08/960,926	11/16/1999	5,986,954	US
NMMS-0376.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT FOR GENERATING INITIALIZATION SIGNALS FOR MEMORY CELL SENSING CIRCUITS	5/30/1997	08/866,283	6/22/1999	5,914,901	US
NMMS-0377.00/US	Utility - ORG	Issued	CIRCUIT FOR IMMUNIZING AN INTEGRATED CIRCUIT FROM NOISE AFFECTING ENABLE SIGNALS OF THE INTEGRATED CIRCUIT	3/4/1997	08/811,548	5/11/1999	5,903,166	US
NMMS-0378.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH A MEMORY CELL ARRAY IN TRIPLE WELL, AND RELATED MANUFACTURING PROCESS	2/20/1998	09/027,343	11/23/1999	5,990,526	US
NMMS-0378.01/US	Utility - DIV	Issued	MEMORY DEVICE WITH A MEMORY CELL ARRAY IN TRIPLE WELL, AND RELATED MANUFACTURING PROCESS	9/3/1999	09/389,955	6/6/2000	6,071,778	US
NMMS-0379.00/US	Utility - ORG	Issued	HIGH VOLTAGE CAPACITOR	7/20/1998	09/119,115	2/13/2001	6,188,121	US
NMMS-0380.00/US	Utility - ORG	Issued	CIRCUIT FOR THE GENERATION AND RESET OF TIMING SIGNAL USED FOR READING A MEMORY DEVICE	3/4/1997	08/811,386	8/11/1998	5,793,699	US
NMMS-0381.00/US	Utility - ORG	Issued	CIRCUIT FOR SELECTIVELY ENABLING ONE AMONG A PLURALITY OF CIRCUIT ALTERNATIVES OF AN INTEGRATED CIRCUIT	10/24/1997	08/957,685	8/8/2000	6,100,740	US
NMMS-0381.01/US	Utility - CON	Issued	CIRCUIT FOR SELECTIVELY ENABLING ONE AMONG A PLURALITY OF CIRCUIT ALTERNATIVES OF AN INTEGRATED CIRCUIT	4/25/2000	09/558,956	2/25/2003	6,525,591	US
NMMS-0381.02/US	Utility - DIV	Issued	METHOD FOR SELECTIVELY ENABLING ALTERNATIVE CIRCUITS	7/6/2000	09/611,258	11/20/2001	6,320,440	US
NMMS-0382.00/US	Utility - ORG	Issued	METHOD FOR VERIFYING ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY DEVICE AFTER PROGRAMMING	5/30/1997	08/866,531	1/26/1999	5,864,503	US
NMMS-0383.00/US	Utility - ORG	Issued	AUTOALIGNED ETCHING PROCESS FOR REALIZING WORD LINES IN MEMORY DEVICES	12/29/1997	08/997,499	10/10/2000	6,130,165	US
NMMS-0383.01/US	Utility - DIV	Issued	AUTOALIGNED ETCHING PROCESS FOR REALIZING WORD LINES IN MEMORY DEVICES	3/17/2000	09/528,406	4/30/2002	6,380,582	US
NMMS-0384.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY DEVICE HAVING BIT LINES WITH BLOCK SELECTOR SWITCHES	12/29/1997	08/998,854	10/19/1999	5,969,977	US
NMMS-0384.01/US	Utility - CON	Issued	MEMORY BLOCK FOR REALIZING SEMICONDUCTOR MEMORY DEVICES AND CORRESPONDING MANUFACTURING PROCESS	6/7/1999	09/327,111	8/8/2000	6,101,124	US
NMMS-0384.02/US	Utility - DIV	Issued	ELECTRONIC MEMORY DEVICE HAVING BIT LINES WITH BLOCK SELECTOR SWITCHES	8/13/1999	09/387,103	9/4/2001	6,284,585	US
NMMS-0385.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING AN INTEGRATED CIRCUIT COMPRISING AN ARRAY OF MEMORY CELLS	7/21/1997	08/897,799	11/2/1999	5,976,933	US
NMMS-0385.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING AN INTEGRATED CIRCUIT COMPRISING AN ARRAY OF MEMORY CELLS	7/16/1999	09/356,080	3/5/2002	6,353,243	US
NMMS-0386.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR INCREASING THE INTERNAL ADDRESS OF A MEMORY	3/27/1998	09/049,858	9/5/2000	6,115,801	US
NMMS-0387.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR CHECKING MULTILEVEL PROGRAMMING OF FLOATING-GATE NONVOLATILE MEMORY CELLS PARTICULARLY FLASH CELLS	9/30/1997	08/941,882	3/9/1999	5,880,993	US
NMMS-0388.00/US	Utility - ORG	Abandoned	METHOD OF PRODUCING MOSFET TRANSISTORS BY MEANS OF TILTED IMPLANTS	4/20/1998	09/062,859	6/22/1999	5,915,185	US
NMMS-0389.00/US	Utility - ORG	Issued	METHOD FOR SAVING DATA IN THE EVENT OF UNWANTED INTERRUPTIONS IN THE PROGRAMMING CYCLE OF A NONVOLATILE MEMORY, AND A NONVOLATILE MEMORY	4/29/1999	09/302,231	9/5/2000	6,115,313	US
NMMS-0390.00/US	Utility - ORG	Issued	PROCESS FOR THE REPAIR OF FLOATING-GATE NON-VOLATILE MEMORIES DAMAGED BY PLASMA TREATMENT	12/15/1997	08/990,328	3/30/1999	5,888,836	US
NMMS-0391.00/US	Utility - ORG	Issued	VOLTAGE LEVEL SHIFTER DEVICE, PARTICULARLY FOR A NONVOLATILE MEMORY	2/25/1998	09/030,604	9/28/1999	5,959,902	US
NMMS-0392.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING A NATIVE MOS P-CHANNEL TRANSISTOR WITH A PROCESS FOR MANUFACTURING NON-VOLATILE MEMORIES	8/26/1998	09/139,909	5/16/2000	6,063,663	US

PATENT

REEL: 029186 FRAME: 0861

Exhibit A - US issued Patents

Patent No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMXS-0394.00/US	Utility - ORG	Issued	METHOD OF MANUFACTURING AN EPROM MEMORY DEVICE HAVING MEMORY CELLS ORGANIZED IN A TABLECLOTH MATRIX	8/27/1998	09/144,849	12/4/2001	6,326,266	US
NMXS-0395.00/US	Utility - ORG	Issued	SWITCHING MASTER SLAVE CIRCUIT	1/19/1996	08/588,648	7/21/1998	5,783,958	US
NMXS-0396.00/US	Utility - ORG	Issued	MULTIPLE ACCESS MEMORY DEVICE	12/31/1996	08/775,664	7/21/1998	5,784,331	US
NMXS-0397.00/US	Utility - CIP	Issued	LOW CURRENT CRYSTAL OSCILLATOR WITH FAST START-UP TIME	5/3/1996	08/642,271	9/8/1998	5,805,027	US
NMXS-0398.00/US	Utility - ORG	Issued	EXTERNAL WRITE PULSE CONTROL METHOD AND STRUCTURE	12/21/1996	08/771,642	6/9/1998	5,764,592	US
NMXS-0399.00/US	Utility - ORG	Issued	BURN-IN STRESS TEST MODE	12/21/1996	08/771,645	3/14/2000	6,037,792	US
NMXS-0399.01/US	Utility - Div	Issued	INTEGRATED CIRCUIT DEVICE HAVING A BURN-IN MODE FOR WHICH ENTRY INTO AND EXIT FROM CAN BE CONTROLLED	1/27/2000	09/491,664	10/30/2001	6,310,485	US
NMXS-0399.02/US	Utility - Div	Issued	INTEGRATED CIRCUIT DEVICE HAVING A BURN-IN MODE FOR WHICH ENTRY INTO AND EXIT FROM CAN BE CONTROLLED	8/17/2001	09/931,820	2/11/2003	6,518,746	US
NMXS-0400.00/US	Utility - ORG	Issued	INPUT DELAY CONTROL	12/23/1996	08/780,053	2/17/1998	5,719,445	US
NMXS-0401.00/US	Utility - ORG	Issued	SENSE AMPLIFIER CONTROL OF A MEMORY DEVICE	4/25/1997	08/844,696	9/28/1999	5,959,910	US
NMXS-0402.00/US	Utility - CIP	Issued	CIRCUIT AND METHOD FOR SELECTING A SIGNAL	11/27/1996	08/758,587	3/14/2000	6,037,799	US
NMXS-0403.00/RE	Utility - REIS	Issued	EDGE TRANSITION DETECTION CIRCUITRY FOR USE WITH TEST MODE OPERATION OF AN INTEGRATED CIRCUIT MEMORY DEVICE	5/9/2002	10/145,606	5/9/2000	RE40,282	US
NMXS-0403.00/US	Utility - ORG	Issued	AN INTEGRATED DETECTION CIRCUITRY FOR USE WITH TEST MODE OPERATION OF AN INTEGRATED CIRCUIT MEMORY DEVICE	12/21/1996	08/771,643	5/9/2000	6,059,450	US
NMXS-0404.00/US	Utility - CIP	Issued	INTEGRATED CIRCUIT THAT SUPPORTS AND METHOD FOR WAFER-LEVEL TESTING	9/17/1996	08/710,357	9/15/1998	5,808,947	US
NMXS-0405.00/US	Utility - ORG	Issued	DEVICE FOR READING CELLS OF A MEMORY	6/12/1997	08/873,502	7/13/1999	5,923,590	US
NMXS-0406.00/US	Utility - ORG	Issued	PARALLEL-ACCESS MEMORY AND METHOD	5/9/1997	08/853,526	7/4/2000	6,085,280	US
NMXS-0407.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE MULTILEVEL NON-VOLATILE MEMORY COMPRISING INTERNAL REFRESH MEANS	10/24/1997	08/957,715	4/11/2000	6,049,497	US
NMXS-0408.00/US	Utility - ORG	Issued	READ CIRCUIT FOR NON-VOLATILE MEMORY WORKING WITH A LOW SUPPLY VOLTAGE	9/22/1997	08/934,696	1/12/1999	5,859,798	US
NMXS-0409.00/US	Utility - ORG	Issued	DEVICE FOR THE PROTECTION OF STORED DATA	8/21/1997	08/916,054	7/28/1998	5,787,040	US
NMXS-0410.00/US	Utility - ORG	Issued	CURRENT REFERENCE DEVICE IN INTEGRATED CIRCUIT FORM	1/30/1997	08/791,383	5/11/1999	5,903,144	US
NMXS-0411.00/US	Utility - ORG	Issued	DEVICE FOR THE PROTECTION OF STORED DATA USING A TIME DELAY CIRCUIT	8/21/1997	08/915,855	9/12/2000	6,119,210	US
NMXS-0412.00/US	Utility - ORG	Issued	MEMORY WITH IMPROVED READING TIME	10/24/1997	08/957,666	2/9/1999	5,870,336	US
NMXS-0413.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR POWER-ON/POWER-OFF CHECKING OF AN INTEGRATED CIRCUIT	12/18/1997	08/993,071	8/28/2001	6,281,723	US
NMXS-0414.00/US	Utility - ORG	Issued	OSCILLATOR AND SWITCH-OVER CONTROL CIRCUIT FOR A HIGH-VOLTAGE GENERATOR	12/22/1997	08/995,667	11/14/2000	6,147,566	US
NMXS-0414.01/US	Utility - Div	Issued	OSCILLATOR AND SWITCH-OVER CONTROL CIRCUIT FOR A HIGH-VOLTAGE GENERATOR.	5/25/2000	09/578,778	4/16/2002	6,373,311	US
NMXS-0415.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE FLOATING-GATE MEMORY ORGANIZED IN WORDS	3/24/1999	09/275,327	8/1/2000	6,097,651	US
NMXS-0416.00/US	Utility - ORG	Abandoned	METHOD AND DEVICE FOR THE PREDICTIVE READING OF A MEMORY	12/16/1997	08/991,693	10/24/2000	6,138,220	US
NMXS-0417.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR THE INCREMENTAL READING OF A MEMORY	12/16/1997	08/991,428	1/23/2001	6,178,490	US
NMXS-0418.00/US	Utility - ORG	Issued	MEMORY WITH READ PROTECTED ZONES	12/19/1997	08/994,889	12/14/1999	6,002,619	US
NMXS-0419.00/US	Utility - ORG	Issued	A READ CIRCUIT FOR MEMORY ADAPTED TO THE MEASUREMENT OF LEAKAGE CURRENTS	2/27/1998	09/031,748	3/30/1999	5,889,702	US
NMXS-0420.00/US	Utility - ORG	Issued	LOAD PUMP TYPE OF VOLTAGE GENERATOR CIRCUIT	4/13/1998	09/059,088	10/19/1999	5,969,961	US
NMXS-0421.00/US	Utility - ORG	Issued	DIFFERENTIAL AMPLIFIER IN A MOS TRANSISTORS STRUCTURE	11/6/1998	09/188,071	10/31/2000	6,140,876	US
NMXS-0422.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR THE FILTERING OF A PULSE SIGNAL	5/29/1998	09/087,478	10/12/1999	5,966,034	US
NMXS-0423.00/US	Utility - ORG	Issued	RING OSCILLATOR USING CMOS TECHNOLOGY	1/12/1998	09/005,722	10/3/2000	6,127,898	US
NMXS-0424.00/US	Utility - ORG	Issued	PAGE-WRITE INDICATOR FOR NON-VOLATILE MEMORY	1/20/1998	09/009,088	9/28/1999	5,959,886	US
NMXS-0425.00/US	Utility - ORG	Issued	WORD ADDRESSABLE FLOATING-GATE MEMORY COMPRISING A REFERENCE VOLTAGE GENERATOR CIRCUIT FOR THE VERIFICATION OF THE CONTENTS OF A WORD	3/6/1998	09/035,654	9/14/1999	5,953,253	US

PATENT

REEL: 029186 FRAME: 0862

Exhibit A - US Issued Patents

Patent No.	Patent Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMMS-0426.00/US	Utility - ORG Issued CIRCUIT AND METHOD FOR THE ERASURE OF A NON-VOLATILE AND ELECTRICALLY ERASABLE MEMORY	11/26/1997	08/980,195	1/4/2000	6,011,724	US
NMMS-0427.00/US	Utility - ORG Issued DEVICE FOR PROTECTION AFTER A PAGE-WRITE OPERATION IN AN ELECTRICALLY PROGRAMMABLE MEMORY	11/14/1997	08/970,445	1/19/1999	5,862,075	US
NMMS-0428.00/US	Utility - ORG Issued CIRCUIT FOR THE DETECTION OF CHANGES OF ADDRESS	1/27/1998	09/013,966	8/15/2000	6,104,644	US
NMMS-0429.00/US	Utility - ORG Abandoned SET OF TWO MEMORIES ON THE SAME MONOLITHIC INTEGRATED CIRCUIT	4/8/1998	09/056,921	3/20/2001	6,205,512	US
NMMS-0429.01/US	Utility - DIV Abandoned SET OF TWO MEMORIES ON THE SAME MONOLITHIC INTEGRATED CIRCUIT	12/21/2000	09/746,473	8/13/2002	6,434,055	US
NMMS-0429.02/US	Utility - DIV Abandoned SET OF TWO MEMORIES ON THE SAME MONOLITHIC INTEGRATED CIRCUIT	12/21/2000	09/746,472	8/13/2001	6,279,068	US
NMMS-0430.00/US	Utility - ORG Issued DEVICE AND METHOD FOR THE PROGRAMMING OF A MEMORY	1/22/1998	09/012,938	11/23/1999	5,991,199	US
NMMS-0430.02/US	Utility - NSPCT Issued METHOD FOR PROGRAMMING AN EPROM-FLASH TYPE MEMORY	1/22/1998	09/355,064	10/31/2000	6,141,254	US
NMMS-0431.00/US	Utility - ORG Issued VARIABLE FREQUENCY CHARGE PUMP	7/11/1998	09/118,953	5/9/2000	6,060,932	US
NMMS-0432.02/US	Utility - CON Issued PROCESS OF FINAL PASSIVATION OF AN INTEGRATED CIRCUIT DEVICE	12/18/2002	10/323,961	5/3/2005	6,888,225	US
NMMS-0433.00/US	Utility - ORG Issued MEMORY DEVICE WITH REDUCED POWER DISSIPATION	4/1/1998	09/053,720	5/9/2000	6,061,286	US
NMMS-0434.00/US	Utility - ORG Issued METHOD AND DEVICE FOR READING A NON-ERASABLE MEMORY CELL	3/16/1998	09/039,588	6/13/2000	6,075,718	US
NMMS-0435.00/US	Utility - ORG Issued SEMICONDUCTOR INTEGRATED CIRCUIT	10/6/1999	09/093,382	4/3/2001	6,212,128	US
NMMS-0436.00/US	Utility - ORG Issued METHOD AND CIRCUIT FOR GENERATING A GATE VOLTAGE IN NON-VOLATILE MEMORY DEVICES	8/27/1998	09/141,250	1/18/2000	6,016,271	US
NMMS-0437.00/US	Utility - ORG Issued PROCESS FOR THE MANUFACTURING OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY DEVICE	8/6/1998	09/130,720	2/27/2001	6,194,270	US
NMMS-0437.01/US	Utility - DIV Issued PROCESS FOR THE MANUFACTURING OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY DEVICE	2/1/2001	09/776,105	8/20/2002	6,437,395	US
NMMS-0438.00/US	Utility - ORG Issued METHOD FOR FINAL PASSIVATION OF INTEGRATED CIRCUIT	4/14/1998	09/059,740	2/13/2001	6,187,683	US
NMMS-0439.00/US	Utility - ORG Issued METHOD AND DEVICE FOR ANALOG PROGRAMMING OF NON-VOLATILE MEMORY CELLS	5/11/1998	09/076,013	2/27/2001	6,195,283	US
NMMS-0440.00/US	Utility - ORG Issued MEMORY DEVICE GENERATOR FOR GENERATING MEMORY DEVICES WITH REDUNDANCY	10/19/1998	09/175,220	7/22/2003	6,598,190	US
NMMS-0441.00/US	Utility - ORG Issued METHOD FOR FORMING NON VOLATILE MEMORY STRUCTURES ON A SEMICONDUCTOR SUBSTRATE	12/10/1999	09/459,754	4/23/2002	6,376,306	US
NMMS-0442.00/US	Utility - ORG Issued CIRCUIT AND METHOD FOR READING A NON-VOLATILE MEMORY	7/14/1999	09/353,756	2/27/2001	6,195,286	US
NMMS-0443.00/US	Utility - ORG Issued METHOD AND DEVICE FOR ANALOG PROGRAMMING OF FLASH EPROM MEMORY CELLS WITH AUTOVERITY	9/28/1998	09/162,639	6/27/2000	6,081,448	US
NMMS-0444.00/US	Utility - ORG Abandoned METHOD FOR LOCALIZING POINT DEFECTS CAUSING LEAKAGE CURRENTS IN A NON-VOLATILE MEMORY DEVICE	5/13/1999	09/311,258	4/9/2002	6,369,406	US
NMMS-0445.00/US	Utility - ORG Issued DATA READ CIRCUIT FOR NON VOLATILE MEMORY CELLS	10/6/1999	09/413,383	12/11/2001	6,330,188	US
NMMS-0446.00/US	Utility - ORG Issued ROW DECODER FOR A FLASH-EEPROM MEMORY DEVICE WITH THE POSSIBILITY OF SELECTIVE ERASING OF A SUB-GROUP OF ROWS OF A SECTOR	11/25/1998	09/200,002	9/19/2000	6,122,200	US
NMMS-0447.00/US	Utility - ORG Issued CIRCUIT AND METHOD OF READING CELLS OF AN ANALOG MEMORY ARRAY, IN PARTICULAR OF THE FLASH TYPE	7/22/1998	09/124,024	10/26/1999	5,973,959	US
NMMS-0448.00/US	Utility - ORG Issued DECODER FOR MEMORIES HAVING OPTIMIZED CONFIGURATION	11/20/2000	09/716,747	3/26/2002	6,362,658	US
NMMS-0449.00/US	Utility - ORG Issued METHOD FOR PARALLEL PROGRAMMING OF NONVOLATILE MEMORY DEVICES, IN PARTICULAR FLASH MEMORIES AND EEPROMS	10/27/1998	09/181,230	5/30/2000	6,069,822	US
NMMS-0450.00/US	Utility - ORG Issued METHOD OF MEASURING THE THICKNESS OF A LAYER OF SILICON DAMAGED BY PLASMA ETCHING	6/29/1999	09/343,207	5/15/2001	6,233,046	US
NMMS-0451.00/US	Utility - ORG Issued SECTORED SEMICONDUCTOR MEMORY DEVICE WITH CONFIGURABLE MEMORY SECTOR ADDRESSES	9/23/1998	09/159,322	6/4/2002	6,401,164	US
NMMS-0452.00/US	Utility - ORG Issued STACKED CHARGE PUMP CIRCUIT	8/27/1997	08/927,391	7/20/1999	5,926,059	US
NMMS-0453.00/US	Utility - ORG Issued CMOS TWIN-TUB NEGATIVE VOLTAGE SWITCHING ARCHITECTURE	8/27/1997	08/921,930	11/30/1999	5,994,948	US
NMMS-0454.00/US	Utility - ORG Abandoned DETERMINATION OF THE THICKNESS OF A DENUDED ZONE IN A SILICON WAFER	7/1/1998	09/108,439	3/6/2001	6,197,606	US

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Exhibit A - US Issued Patents

MTI-Device No	Material Type	Material Status	Title	Filed Date	Serial No	Issued Date	Patent No	Country
NMMS-0481.00/US	Utility - NSPCT	Issued	MONOLITHICALLY INTEGRATED SELECTOR FOR ELECTRICALLY PROGRAMMABLE MEMORY CELLS DEVICES	6/30/1999	09/674,395	9/11/2001	6,288,594	US
NMMS-0482.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY CAPABLE OF AUTONOMOUSLY EXECUTING A PROGRAM	7/8/1999	09/349,702	7/1/2003	6,587,914	US
NMMS-0483.00/US	Utility - ORG	Issued	OPERATING VOLTAGE SELECTION CIRCUIT FOR NON-VOLATILE SEMICONDUCTOR MEMORIES	1/13/1999	09/229,474	8/8/2000	6,103,127	US
NMMS-0484.00/US	Utility - ORG	Issued	METHOD FOR CONTROLLED ERASING MEMORY DEVICES, IN PARTICULAR ANALOG AND MULTIT-LEVEL FLASH-EEPROM DEVICES	1/22/1999	09/234,942	7/18/2000	6,091,642	US
NMMS-0485.00/US	Utility - ORG	Issued	ESD PROTECTION NETWORK FOR CIRCUIT STRUCTURES FORMED IN A SEMICONDUCTOR	12/30/1998	09/223,621	7/24/2001	6,266,222	US
NMMS-0486.00/US	Utility - ORG	Issued	METHOD AND A CIRCUIT FOR IMPROVING THE EFFECTIVENESS OF ESD PROTECTION IN CIRCUIT STRUCTURES FORMED IN A SEMICONDUCTOR	12/30/1998	09/231,129	6/5/2001	6,242,793	US
NMMS-0487.00/US	Utility - DIV	Issued	METHOD FOR IMPROVING THE EFFECTIVENESS OF ESD PROTECTION IN CIRCUIT STRUCTURES FORMED IN A SEMICONDUCTOR SUBSTRATE	4/17/2001	09/837,137	4/16/2002	6,372,597	US
NMMS-0488.00/US	Utility - ORG	Issued	HIGH STORAGE CAPACITY NON-VOLATILE MEMORY	5/26/1999	09/320,315	11/14/2000	6,147,902	US
NMMS-0489.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING A SEMICONDUCTOR NON-VOLATILE MEMORY DEVICE WITH SHALLOW TRENCH ISOLATION (STI)	6/18/1999	09/336,089	4/17/2001	6,218,265	US
NMMS-0490.00/US	Utility - ORG	Issued	CIRCUIT DEVICE AND CORRESPONDING METHOD FOR PROGRAMMING A NONVOLATILE MEMORY CELL HAVING A SINGLE VOLTAGE SUPPLY	5/28/1999	09/322,644	8/29/2000	6,111,791	US
NMMS-0491.00/US	Utility - ORG	Issued	ELECTRONIC COUNTER FOR A NON-VOLATILE MEMORY DEVICE INTEGRATED ON A SEMICONDUCTOR	3/4/1999	09/762,500	3/27/2001	6,208,705	US
NMMS-0492.01/US	Utility - CON	Abandoned	EDGE TRANSITION DETECTION CONTROL OF A MEMORY DEVICE	12/30/1997	08/000,547	11/30/1999	5,995,444	US
NMMS-0493.00/US	Utility - ORG	Issued	VSS SWITCHING SCHEME FOR BATTERY BACKED-UP SEMICONDUCTOR DEVICES	10/29/1999	09/429,964	8/22/2000	6,107,865	US
NMMS-0494.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR READING A MEMORY CELL	5/29/1998	09/087,399	11/30/1999	5,995,421	US
NMMS-0495.00/US	Utility - ORG	Issued	READ CIRCUIT FOR NON VOLATILE MEMORIES	10/29/1998	09/182,843	8/1/2000	6,097,633	US
NMMS-0496.00/US	Utility - ORG	Issued	IN-SITU DEPOSITION AND DOPING PROCESS FOR POLYCRYSTALLINE SILICON LAYERS AND THE RESULTING DEVICE	11/13/1998	09/191,743	3/15/2005	6,867,113	US
NMMS-0497.00/US	Utility - ORG	Issued	TEMPERATURE-STABLE CURRENT GENERATION	2/5/1999	09/246,029	10/17/2000	6,133,718	US
NMMS-0498.00/US	Utility - ORG	Issued	BITLEAK BIAS CIRCUIT FOR NON-VOLATILE MEMORY DEVICES	2/23/1999	09/256,648	4/11/2000	6,049,491	US
NMMS-0499.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR NON-VOLATILE MEMORY DEVICES	2/23/1999	09/256,603	7/25/2000	6,094,394	US
NMMS-0500.00/US	Utility - ORG	Issued	UPROM MEMORY CELLS FOR NON-VOLATILE MEMORY DEVICES INTEGRATED ON SEMICONDUCTORS	3/25/1999	09/276,263	8/7/2001	6,271,571	US
NMMS-0501.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR LOW VOLTAGE MEMORIES	2/12/1999	09/249,834	10/15/2002	6,466,059	US
NMMS-0502.00/US	Utility - ORG	Issued	LOW CONSUMPTION BOOSTED VOLTAGE DRIVING CIRCUIT	2/26/1999	09/257,682	10/10/2000	6,130,844	US
NMMS-0503.00/US	Utility - ORG	Issued	VOLTAGE BOOSTING CIRCUIT FOR GENERATING BOOSTED VOLTAGE PHASES	2/26/1999	09/258,224	5/16/2000	6,064,594	US
NMMS-0504.00/US	Utility - ORG	Issued	VOLTAGE PHASE GENERATOR WITH INCREASED DRIVING CAPACITY	2/26/1999	09/257,684	3/6/2001	6,198,672	US
NMMS-0505.00/US	Utility - ORG	Issued	PIPELINED NON-BLOCKING LEVEL TWO CACHE SYSTEM WITH INHERENT TRANSACTION COLLISION-AVOIDANCE	12/4/1998	09/205,598	2/11/2003	6,519,682	US
NMMS-0506.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY HAVING A PROTECTABLE ZONE AND AN ELECTRONIC SYSTEM INCLUDING THE MEMORY	10/23/1998	09/177,899	3/7/2000	6,034,889	US
NMMS-0507.00/US	Utility - ORG	Issued	METHOD FOR WRITING IN AN ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY	10/27/1998	09/179,621	3/21/2000	6,040,994	US
NMMS-0508.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR THE GENERATION OF PROGRAMMING AND ERASURE VOLTAGE IN A NON-VOLATILE MEMORY	9/18/1998	09/156,945	11/30/1999	5,995,416	US
NMMS-0509.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORIES PROGRAMMABLE BY "HOT CARRIER" TUNNEL EFFECT AND ERASABLE BY TUNNEL EFFECT	10/15/1998	09/173,136	10/3/2000	6,128,222	US
NMMS-0510.00/US	Utility - ORG	Issued	NON-VOLATILE ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY DETECTOR OF RADIO FREQUENCY SIGNALS FOR CONTACTLESS CHIP CARDS	11/25/1998	09/199,671	12/7/1999	5,999,447	US
NMMS-0511.00/US	Utility - ORG	Issued	NON-VOLATILE ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY DETECTOR OF RADIO FREQUENCY SIGNALS FOR CONTACTLESS CHIP CARDS	9/18/1998	09/157,060	11/28/2000	6,152,373	US

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Exhibit A - US Issued Patents

MTI-Design	Mattertype	MatterStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0511.00/US	Utility - ORG	Issued	CIRCUIT FOR THE GENERATION OF VOLTAGE FOR THE PROGRAMMING OR ERASURE OF A MEMORY THAT USES FLOATING-GATE TRANSISTORS	10/27/1998	09/179,635	11/2/1999	5,978,268	US
NMMS-0512.00/US	Utility - ORG	Issued	DEVICE FOR DEMODULATING A BINARY PHASE-SHIFT KEYSIGNAL	1/20/1999	09/234,194	10/31/2000	6,140,869	US
NMMS-0513.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE AND METHOD FOR THE PROGRAMMING OF THE SAME	11/18/1998	09/195,276	7/18/2000	6,093,641	US
NMMS-0514.00/US	Utility - ORG	Abandoned	DEVICE AND METHOD FOR THE READING OF EEPROM CELLS	4/27/1999	09/300,527	4/17/2001	6,219,277	US
NMMS-0515.00/US	Utility - ORG	Issued	LINE DECODER FOR A LOW SUPPLY VOLTAGE MEMORY DEVICE	6/1/1999	09/324,087	8/79/2000	6,111,809	US
NMMS-0516.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR READING NONVOLATILE MEMORY CELLS	5/28/1999	09/322,460	1/30/2001	6,181,602	US
NMMS-0517.00/US	Utility - ORG	Issued	CIRCUIT FOR PROVIDING A READING PHASE AFTER POWER-ON-RESET	4/20/1999	09/295,263	12/5/2000	6,157,579	US
NMMS-0519.00/US	Utility - ORG	Issued	SWITCHING CIRCUIT WITH AN OUTPUT VOLTAGE CHANGING AMONG FOUR POSSIBLE VALUES	3/24/1999	09/275,691	8/1/2000	6,097,213	US
NMMS-0520.00/US	Utility - ORG	Issued	PROCESS FOR THE MANUFACTURE OF INTEGRATED DEVICES WITH GATE OXIDE PROTECTION FROM MANUFACTURING PROCESS DAMAGE, AND PROTECTION STRUCTURE THEREFOR	6/25/1999	09/344,426	8/21/2001	6,278,159	US
NMMS-0520.01/US	Utility - DIV	Issued	PROCESS FOR THE MANUFACTURE OF INTEGRATED DEVICES WITH GATE OXIDE PROTECTION FROM MANUFACTURING PROCESS DAMAGE, AND PROTECTION STRUCTURE THEREFOR	6/25/2001	09/891,438	4/22/2003	6,551,892	US
NMMS-0521.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY CIRCUIT AND RELATED MANUFACTURING METHOD	7/30/1999	09/364,766	4/10/2001	6,215,688	US
NMMS-0521.01/US	Utility - DIV	Issued	ELECTRONIC MEMORY CIRCUIT AND RELATED MANUFACTURING METHOD	2/9/2001	09/779,956	2/8/2005	6,852,596	US
NMMS-0521.02/US	Utility - DIV	Issued	ELECTRONIC MEMORY CIRCUIT AND RELATED MANUFACTURING METHOD	1/12/2005	11/033,776	10/13/2009	7,601,590	US
NMMS-0522.00/US	Utility - ORG	Issued	METHOD FOR READING A MULTIPLE-LEVEL MEMORY CELL	3/25/1999	09/276,214	7/4/2000	6,084,797	US
NMMS-0523.01/US	Utility - CON	Issued	CIRCUIT STRUCTURE FOR PROVIDING A HIERARCHICAL DECODING IN SEMICONDUCTOR MEMORY DEVICES	6/27/2001	09/894,975	2/4/2003	6,515,911	US
NMMS-0524.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES HAVING HV TRANSISTORS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	7/22/1999	09/359,923	6/26/2001	6,251,728	US
NMMS-0524.01/US	Utility - DIV	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES HAVING HV TRANSISTORS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	5/18/2001	09/861,404	7/16/2002	6,420,789	US
NMMS-0525.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING AN ELECTRONIC DEVICE INCLUDING MOS TRANSISTORS WITH SALICIDED JUNCTIONS AND NON-SALICIDED RESISTORS	7/21/1999	09/358,075	10/9/2001	6,300,181	US
NMMS-0526.00/US	Utility - ORG	Issued	METHOD FOR THE INTEGRATION OF RESISTORS AND ESD SELF-PROTECTED TRANSISTORS IN AN INTEGRATED DEVICE WITH A MEMORY MATRIX MANUFACTURED BY MEANS OF A PROCESS FEATURING SELF-ALIGNED SOURCE (SAS) FORMATION AND JUNCTION SALICIDATION	12/15/1999	09/464,066	10/8/2002	6,461,922	US
NMMS-0527.01/US	Utility - DIV	Issued	METHOD FOR ENHANCING SELECTIVITY BETWEEN A FILM OF A LIGHT-SENSITIVE MATERIAL AND A LAYER TO BE ETCHED IN ELECTRONIC SEMICONDUCTOR DEVICE FABRICATION PROCESSES	4/17/2001	09/836,937	12/17/2002	6,495,455	US
NMMS-0528.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SELECTION TRANSISTORS FOR NONVOLATILE SERIAL-FLASH, EPROM, EEPROM AND FLASH-EEPROM MEMORIES IN STANDARD OR AMG CONFIGURATION	6/11/1999	09/330,540	7/3/2001	6,255,163	US
NMMS-0529.00/US	Utility - ORG	Issued	METHOD OF MAKING HIGH-VOLTAGE HV TRANSISTORS WITH DRAIN EXTENSION IN A CMOS PROCESS OF THE DUAL GATE TYPE WITH SILICIDE	10/1/1999	09/411,134	9/4/2001	6,284,607	US
NMMS-0530.00/US	Utility - ORG	Issued	METHOD FOR FAST PROGRAMMING FLOATING GATE MEMORIES BY TUNNEL EFFECT	6/25/1999	09/344,425	1/9/2001	6,172,913	US
NMMS-0531.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING OF A NON VOLATILE MEMORY WITH REDUCED RESISTANCE OF THE COMMON SOURCE LINES	6/21/1999	09/337,051	1/30/2001	6,180,460	US
NMMS-0532.00/US	Utility - ORG	Issued	SIMPLIFIED PROCESS FOR DEFINING THE TUNNEL AREA IN NON-ALIGNED, NON-VOLATILE SEMICONDUCTOR MEMORY CELLS	10/14/1999	09/419,403	9/3/2002	6,444,526	US
NMMS-0533.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH ROW REDUNDANCY	5/12/2000	09/570,332	10/9/2001	6,301,152	US
NMMS-0534.00/US	Utility - ORG	Issued	METHOD FOR FORMING CONTACTLESS MOS TRANSISTORS AND RESULTING DEVICES, ESPECIALLY FOR USE IN NON-VOLATILE MEMORY ARRAYS	12/28/1999	09/473,368	6/26/2001	6,251,736	US

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Exhibit A - US Issued Patents

MTI-DataNo	MaterType	MaterStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMKS-0535.00/US	Utility - ORG	Abandoned	PROCESS FOR THE DEFINITION OF OPENINGS IN A DIELECTRIC LAYER	9/28/1999	09/406,903	11/6/2001	6,313,040	US
NMKS-0536.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NON-VOLATILE MEMORY CELLS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	9/9/1999	09/392,937	8/28/2001	6,281,077	US
NMKS-0536.01/US	Utility - DIV	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NON-VOLATILE MEMORY CELLS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	4/16/2001	09/836,590	5/28/2002	6,396,101	US
NMKS-0537.00/US	Utility - ORG	Issued	SELECTIVE SALICIDATION PROCESS FOR ELECTRONIC DEVICES INTEGRATED IN A SEMICONDUCTOR SUBSTRATE	12/28/1999	09/473,367	8/5/2003	6,602,774	US
NMKS-0538.00/US	Utility - ORG	Issued	METHOD FOR CORRECTION OF ERRORS IN A BINARY WORD STORED IN MULTILEVEL MEMORY CELLS, NOT REQUIRING ADDITIONAL CELLS	2/24/2000	09/513,286	3/4/2003	6,530,058	US
NMKS-0539.00/US	Utility - ORG	Issued	NONVOLATILE MULTILEVEL MEMORY AND READING METHOD THEREFORE	2/9/2000	09/501,131	9/24/2002	6,456,527	US
NMKS-0540.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TESTING MEMORY CELLS IN A MULTILEVEL MEMORY DEVICE	10/7/1999	09/415,024	10/9/2001	6,301,157	US
NMKS-0541.00/US	Utility - ORG	Issued	DEVICE FOR READING NON-VOLATILE MEMORY CELLS, IN PARTICULAR ANALOG FLASH MEMORY CELLS	10/19/1999	09/421,616	11/27/2001	6,323,799	US
NMKS-0542.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR PROGRAMMING NONVOLATILE MEMORY CELLS WITH AUTOMATIC GENERATION OF PROGRAMMING VOLTAGE	11/12/1999	09/438,232	10/15/2002	6,466,481	US
NMKS-0543.00/US	Utility - ORG	Issued	CIRCUIT FOR PARALLEL PROGRAMMING NONVOLATILE MEMORY CELLS, WITH ADJUSTABLE PROGRAMMING SPEED	11/23/1999	09/447,531	12/19/2000	6,163,483	US
NMKS-0544.00/US	Utility - ORG	Issued	CIRCUIT FOR HIGH-PRECISION ANALOG READING OF NONVOLATILE MEMORY CELLS, IN PARTICULAR ANALOG OR MULTILEVEL FLASH OR EEPROM MEMORY CELLS	11/12/1999	09/438,823	10/3/2000	6,128,228	US
NMKS-0545.00/US	Utility - ORG	Issued	PROCESS FOR INTEGRATING IN A SAME CHIP A NON-VOLATILE MEMORY AND A HIGH-PERFORMANCE LOGIC CIRCUITRY	11/24/1999	09/449,044	6/25/2002	6,410,387	US
NMKS-0545.01/US	Utility - DIV	Issued	PROCESS FOR INTEGRATING IN A SAME CHIP A NON-VOLATILE MEMORY AND A HIGH-PERFORMANCE LOGIC CIRCUITRY	5/29/2002	10/158,424	3/30/2004	6,713,347	US
NMKS-0546.00/US	Utility - ORG	Issued	METHOD FOR MAINTAINING THE MEMORY CONTENT OF NON-VOLATILE MEMORY CELLS	9/15/1999	09/397,387	1/2/2001	6,169,691	US
NMKS-0547.00/US	Utility - ORG	Issued	METHOD OF FABRICATION OF A NO-FIELD MOS TRANSISTOR	4/5/2000	09/543,400	2/26/2002	6,350,637	US
NMKS-0548.00/US	Utility - CIP	Issued	DEVICE FOR READING ANALOG NONVOLATILE MEMORY CELLS, IN PARTICULAR FLASH CELLS	10/22/1999	09/425,446	2/27/2001	6,195,289	US
NMKS-0549.00/US	Utility - ORG	Issued	CAPACITIVE BOOSTING CIRCUIT FOR THE REGULATION OF THE WORD LINE READING VOLTAGE IN NON-VOLATILE MEMORIES	1/19/2000	09/491,476	7/10/2001	6,259,635	US
NMKS-0550.00/US	Utility - ORG	Issued	CAPACITIVE COMPENSATION CIRCUIT FOR THE REGULATION OF THE WORD LINE READING VOLTAGE IN NON-VOLATILE MEMORIES	1/19/2000	09/491,475	7/10/2001	6,259,632	US
NMKS-0551.00/US	Utility - ORG	Issued	CHIP OUTLINE BAND (COB) STRUCTURE FOR INTEGRATED CIRCUITS	1/14/2000	09/483,656	10/8/2002	6,462,400	US
NMKS-0552.00/US	Utility - ORG	Issued	IMPROVED FIELD-EFFECT TRANSISTOR AND CORRESPONDING MANUFACTURING METHOD	11/18/1999	09/442,834	5/14/2002	6,387,763	US
NMKS-0552.01/US	Utility - DIV	Issued	FIELD-EFFECT TRANSISTOR AND CORRESPONDING MANUFACTURING METHOD	3/4/2002	10/090,978	5/18/2004	6,737,715	US
NMKS-0553.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR DRIVING PLURAL LOADS BASED ON THE NUMBER OF LOADS BEING DRIVEN	12/20/1999	09/467,726	5/15/2001	6,232,753	US
NMKS-0554.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES, COMPRISING NON-SALICIDED NON-VOLATILE MEMORY CELLS, NON-SALICIDED HV TRANSISTORS, AND LV TRANSISTORS WITH SALICIDED JUNCTIONS WITH FEW MASKS	12/21/1999	09/469,849	8/14/2001	6,274,411	US
NMKS-0555.00/US	Utility - ORG	Issued	METHOD FOR HIGH PRECISION PROGRAMMING NONVOLATILE MEMORY CELLS, WITH OPTIMIZED PROGRAMMING SPEED	11/24/1999	09/449,168	5/21/2002	6,392,931	US
NMKS-0556.00/US	Utility - ORG	Issued	PROCESS OF MANUFACTURE OF A NON VOLATILE MEMORY WITH ELECTRIC CONTINUITY OF THE COMMON SOURCE LINES	4/12/2000	09/547,520	9/25/2001	6,294,431	US
NMKS-0557.00/US	Utility - ORG	Issued	FLASH COMPATIBLE EEPROM	6/21/2000	09/599,356	4/24/2001	6,222,775	US

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Exhibit A - US Issued Patents

Patent No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMXS-0558.00/US	Utility - ORG	Issued	METHOD FOR READING A MEMORY, PARTICULARLY A NON-VOLATILE MEMORY	12/29/1999	09/474,932	11/5/2002	6,477,625	US
NMXS-0559.00/US	Utility - ORG	Issued	SYNCHRONOUS OUTPUT BUFFER, PARTICULARLY FOR NON-VOLATILE MEMORIES	11/20/2000	09/716,759	7/23/2002	6,424,575	US
NMXS-0560.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SEMICONDUCTOR INTEGRATED MEMORY DEVICES WITH CELLS MATRIX HAVING VIRTUAL GROUND	2/18/2000	09/507,777	4/2/2002	6,365,456	US
NMXS-0561.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SEMICONDUCTOR INTEGRATED MEMORY DEVICES WITH CELLS MATRIX HAVING VIRTUAL GROUND	2/25/2000	09/512,900	10/9/2001	6,300,195	US
NMXS-0562.00/US	Utility - ORG	Abandoned	BIT COUNTER STAGE, PARTICULARLY FOR MEMORIES	12/15/1999	09/464,631	11/27/2001	6,324,238	US
NMXS-0563.00/US	Utility - ORG	Issued	PULSE GENERATOR CIRCUIT, PARTICULARLY FOR NON-VOLATILE MEMORIES	12/27/1999	09/472,496	8/14/2001	6,275,416	US
NMXS-0564.00/US	Utility - ORG	Issued	PRIORITY DETERMINING CIRCUIT FOR NON-VOLATILE MEMORY	2/8/2000	09/499,799	4/16/2002	6,373,781	US
NMXS-0565.00/US	Utility - ORG	Issued	METHOD FOR AUTOALIGNING OVERLAPPED LINES OF A CONDUCTIVE MATERIAL PROVIDED IN INTEGRATED ELECTRONIC CIRCUITS	5/26/2000	09/579,778	2/26/2002	6,350,671	US
NMXS-0566.00/US	Utility - ORG	Issued	METHOD OF ADJUSTING PROGRAM VOLTAGE IN NON-VOLATILE MEMORIES, AND PROCESS FOR FABRICATING A NON-VOLATILE MEMORY DEVICE	4/20/2000	09/552,933	3/18/2003	6,535,431	US
NMXS-0566.01/US	Utility - DIV	Issued	METHOD OF ADJUSTING PROGRAM VOLTAGE IN NON-VOLATILE MEMORIES, AND PROCESS FOR FABRICATING A NON-VOLATILE MEMORY DEVICE	1/24/2003	10/350,745	10/12/2004	6,803,630	US
NMXS-0567.00/US	Utility - ORG	Issued	ADDRESS LATCH ENABLE SIGNAL CONTROL CIRCUIT FOR ELECTRONIC MEMORIES	12/8/1999	09/457,500	6/19/2001	6,249,463	US
NMXS-0569.00/US	Utility - ORG	Issued	SEMICONDUCTOR DEVICE WITH IMPROVED INTERCONNECTIONS BETWEEN THE CHIP AND THE TERMINALS, AND PROCESS FOR ITS MANUFACTURE	2/10/2000	09/502,070	5/28/2002	6,396,132	US
NMXS-0570.00/US	Utility - ORG	Issued	Method for reading a multilevel nonvolatile memory and multilevel nonvolatile memory	2/25/2000	09/513,598	10/9/2001	6,301,149	US
NMXS-0571.00/US	Utility - ORG	Issued	VOLTAGE REGULATING CIRCUIT FOR A CAPACITIVE LOAD	6/29/2000	09/608,445	6/19/2001	6,249,112	US
NMXS-0572.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR SWITCHING BETWEEN TWO POWER SUPPLIES OF AN INTEGRATED CIRCUIT	12/21/1998	09/217,321	9/12/2000	6,118,188	US
NMXS-0573.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR INCREASING COMPARATOR GAIN WITHOUT AFFECTING STANDBY CURRENT	12/21/1998	09/217,323	7/4/2000	6,084,390	US
NMXS-0574.00/US	Utility - ORG	Issued	METHOD AND STRUCTURE FOR ENHANCING THE ACCESS TIME OF INTEGRATED CIRCUIT MEMORY DEVICES	10/30/1998	09/183,231	8/31/1999	5,946,264	US
NMXS-0575.00/US	Utility - ORG	Issued	CONTROL CIRCUIT FOR TERMINATING A MEMORY ACCESS CYCLE IN A MEMORY BLOCK OF AN ELECTRONIC STORAGE DEVICE	10/30/1998	09/183,589	3/7/2000	6,034,917	US
NMXS-0577.00/US	Utility - ORG	Issued	STRESS TEST MODE ENTRY AT POWER UP FOR LOW/ZERO POWER MEMORIES	10/30/1998	09/183,451	6/27/2000	6,081,466	US
NMXS-0578.00/US	Utility - ORG	Issued	INITIALIZATION FOR FUSE CONTROL	10/30/1998	09/183,840	3/21/2000	6,041,009	US
NMXS-0578.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT STRUCTURE COMPRISING CAPACITOR ELEMENT AND CORRESPONDING MANUFACTURING PROCESS	10/12/1999	09/415,992	9/25/2001	6,294,798	US
NMXS-0578.01/US	Utility - DIV	Issued	INTEGRATED CIRCUIT STRUCTURE COMPRISING CAPACITOR ELEMENT AND CORRESPONDING MANUFACTURING PROCESS	7/24/2001	09/912,638	1/28/2003	6,511,874	US
NMXS-0580.00/US	Utility - ORG	Issued	CONTENT ADDRESSABLE MEMORY PROTECTION CIRCUIT AND METHOD	4/28/2000	09/561,271	5/29/2001	6,240,002	US
NMXS-0581.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR FAST ADDRESSING REDUNDANT COLUMNS IN A NON VOLATILE MEMORY	4/13/2000	09/548,783	10/30/2001	6,310,801	US
NMXS-0582.00/US	Utility - ORG	Issued	READING CIRCUIT FOR NONVOLATILE MEMORY CELLS WITHOUT LIMITATION OF THE SUPPLY VOLTAGE	4/11/2000	09/546,957	11/27/2001	6,324,098	US
NMXS-0583.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR AUTOMATICALLY REGULATING THE EQUALIZATION DURATION WHEN READING A NONVOLATILE MEMORY	4/11/2000	09/546,975	6/5/2001	6,243,310	US
NMXS-0583.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SOI INTEGRATED CIRCUITS AND CIRCUITS MADE THEREBY	4/28/2000	09/561,272	6/25/2002	6,410,404	US
NMXS-0584.00/US	Utility - ORG	Issued	COMMUNICATIONS SYSTEM AND METHOD FOR REDUCING THE EFFECTS OF TRANSMITTER NON-LINEAR DISTORTION ON A RECEIVED SIGNAL	6/17/1999	09/335,024	8/13/2002	6,434,189	US
NMXS-0585.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT COMPRISING AT LEAST TWO MEMORIES	5/18/1999	09/313,926	7/23/2002	6,425,066	US

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Exhibit A - US Issued Patents

MTI-DC-NO	MaterType	MaterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0586.00/US	Utility - ORG	Issued	MULTIPLE LEVEL FLOATING-GATE MEMORY	12/2/1999	09/453,753	6/11/2002	6,404,679	US
NMXS-0587.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR GENERATING A HIGH VOLTAGE	8/10/1999	09/371,549	12/5/2000	6,157,243	US
NMXS-0588.00/US	Utility - ORG	Abandoned	METHOD FOR VERIFYING THE MEMORY CIRCUIT INTEGRITY	12/2/1999	09/452,446	4/3/2001	6,212,112	US
NMXS-0589.00/US	Utility - ORG	Issued	SECURED EEPROM MEMORY COMPRISING MEANS FOR THE DETECTION OF ERASURE BY ULTRAVIOLET RADIATION	11/24/1999	09/448,632	6/19/2001	6,249,456	US
NMXS-0590.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR THE PROGRAMMING OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY FOR IMPROVING RELIABILITY	4/21/2000	09/556,476	9/18/2001	6,292,396	US
NMXS-0591.00/US	Utility - ORG	Issued	METHOD FOR ERASING AND REWRITING NON VOLATILE MEMORY CELLS AND PARTICULARLY FLASH CELLS	4/20/2000	09/553,526	8/28/2001	6,282,125	US
NMXS-0592.00/US	Utility - ORG	Issued	READING METHOD FOR NON-VOLATILE MEMORIES WITH SENSING RATIO VARIABLE WITH THE READING VOLTAGE, AND DEVICE TO REALIZE SAID METHOD	6/8/2000	09/589,723	3/26/2002	6,363,015	US
NMXS-0593.00/US	Utility - ORG	Issued	ERASING AND PARALLEL REWRITING CIRCUIT FOR MEMORY CELL BLOCKS, PARTICULARLY FOR ANALOG FLASH CELLS, AND RELATED OPERATING METHOD	6/13/2000	09/592,796	11/6/2001	6,314,043	US
NMXS-0594.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE, IN PARTICULAR A FLASH-EEPROM	4/20/2000	09/552,945	2/26/2002	6,351,413	US
NMXS-0595.00/US	Utility - ORG	Issued	METHOD FOR THE IN-WRITING VERIFICATION OF THE THRESHOLD VALUE IN NON-VOLATILE MEMORIES	5/11/2000	09/569,232	9/18/2001	6,292,398	US
NMXS-0596.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE WITH HIERARCHICAL SECTOR DECODING	6/26/2000	09/602,660	9/24/2002	6,456,530	US
NMXS-0597.00/US	Utility - ORG	Issued	SYNCHRONOUS MULTILEVEL NON-VOLATILE MEMORY AND RELATED READING METHOD	5/17/2000	09/572,127	3/6/2001	6,198,660	US
NMXS-0598.00/US	Utility - ORG	Issued	CIRCUIT FOR READING A SEMICONDUCTOR MEMORY	6/26/2000	09/603,275	12/26/2001	6,333,885	US
NMXS-0599.00/US	Utility - ORG	Issued	BIASING STAGE FOR BIASING THE DRAIN TERMINAL OF A NONVOLATILE MEMORY CELL DURING THE READ PHASE	6/2/2000	09/586,399	11/20/2001	6,320,790	US
NMXS-0600.00/US	Utility - ORG	Issued	METHOD AND A DEVICE FOR MEASURING AN ANALOG VOLTAGE IN A NON-VOLATILE MEMORY	6/29/2000	09/608,847	1/14/2003	6,507,183	US
NMXS-0601.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR GENERATING FROM A SINGLE SUPPLY LINE VOLTAGES INTERNAL TO A FLASH MEMORY WITH REDUCED SETTLING TIMES	6/30/2000	09/608,239	5/21/2002	6,392,936	US
NMXS-0602.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH LOW POWER CONSUMPTION AND AND RELATIVE WRITING, READING AND ERASING METHODS	7/21/2000	09/620,765	9/18/2001	6,292,400	US
NMXS-0603.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING MULTI-LEVEL NON-VOLATILE MEMORIES BY CONTROLLING THE GATE VOLTAGE	8/2/2000	09/631,187	4/2/2002	6,366,496	US
NMXS-0604.00/US	Utility - ORG	Issued	CMOS SWITCH CIRCUIT FOR TRANSFERRING HIGH VOLTAGES, IN PARTICULAR FOR LINE ROW DECODER FOR SELECTIVELY BIASING A WORDLINE TO A POSITIVE OR NEGATIVE VOLTAGE	6/2/2000	09/585,916	8/13/2002	6,433,583	US
NMXS-0605.00/US	Utility - ORG	Abandoned		6/16/2000	09/595,054	3/12/2002	6,356,481	US
NMXS-0606.00/US	Utility - ORG	Issued	METHOD FOR READING DATA FROM A NON-VOLATILE MEMORY DEVICE WITH AUTODETECT BURST MODE READING AND CORRESPONDING READING CIRCUIT	11/20/2000	09/716,746	2/19/2002	6,349,059	US
NMXS-0607.00/US	Utility - ORG	Issued	SOFT PROGRAMMING METHOD FOR NON-VOLATILE MEMORY CELLS	12/14/2000	09/738,253	10/22/2002	6,469,934	US
NMXS-0608.01/US	Utility - Div	Issued	BIPOLAR TRANSISTOR PRODUCED USING PROCESSES COMPATIBLE WITH THOSE EMPLOYED IN THE MANUFACTURE OF MOS DEVICES	2/15/2002	10/077,288	12/30/2003	6,570,229	US
NMXS-0610.00/US	Utility - ORG	Issued	BUFFER DEVICE WITH DUAL SUPPLY VOLTAGE FOR LOW SUPPLY VOLTAGE APPLICATIONS	12/13/2000	09/736,984	11/20/2001	6,320,361	US
NMXS-0611.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH FUNCTIONAL CAPABILITY OF SIMULTANEOUS MODIFICATION OF THE CONTENT AND BURST MODE READ OR PAGE MODE READ	7/28/2000	09/627,703	6/28/2005	6,912,598	US
NMXS-0612.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH FUNCTIONAL CAPABILITY OF BURST MODE READ AND PAGE MODE READ DURING SUSPENSION OF AN OPERATION OF ELECTRICAL ALTERATION	7/19/2000	09/619,589	8/27/2002	6,442,068	US

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Exhibit A - US Issued Patents

MT-Id-No	Material-type	Material-Status	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0613.00/US	Utility - ORG	Issued	METHOD FOR CONTROLLED SOFT PROGRAMMING OF NON-VOLATILE MEMORY CELLS, IN PARTICULAR OF THE FLASH EEPROM AND EPROM TYPE	10/27/2000	09/699,309	4/30/2002	6,383,177	US
NMMS-0614.00/US	Utility - ORG	Issued	MANUFACTURE OF AN HV-MOS TRANSISTOR WITH LOW MULTIPLICATION FACTOR IN A DUAL-GATE CMOS PROCESS	11/14/2000	09/713,144	12/31/2002	6,501,147	US
NMMS-0615.00/US	Utility - ORG	Issued	READ CIRCUIT FOR A NONVOLATILE MEMORY	7/22/2000	09/621,019	12/4/2001	6,327,184	US
NMMS-0617.00/US	Utility - ORG	Issued	ENABLING CIRCUIT FOR OUTPUT DEVICES IN ELECTRONIC MEMORIES	7/31/2000	09/629,229	8/20/2002	6,438,040	US
NMMS-0618.00/US	Utility - ORG	Issued	PROCESS FOR THE FABRICATION OF INTEGRATED DEVICES WITH REDUCTION OF DAMAGE FROM PLASMA	3/9/2001	09/804,693	10/28/2003	6,686,833	US
NMMS-0619.00/US	Utility - ORG	Issued	NON VOLATILE MEMORY WITH DETECTION OF SHORT CIRCUITS BETWEEN WORD LINES	9/8/2000	09/658,236	10/23/2001	6,307,778	US
NMMS-0620.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR TIMING MULTI-LEVEL NON-VOLATILE MEMORIES	12/15/2000	09/738,889	6/3/2003	6,574,146	US
NMMS-0621.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH BURST MODE READING AND CORRESPONDING READING METHOD	11/21/2000	09/717,938	2/8/2005	6,854,040	US
NMMS-0622.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR SWITCHING BETWEEN ALTERNATING, FIRST AND SECOND VOLTAGE VALUES, IN PARTICULAR FOR PROGRAMMING MULTILEVEL CELLS	11/15/2000	09/714,837	7/23/2002	6,424,121	US
NMMS-0623.00/US	Utility - ORG	Lapsed	ANTI-DECIPHERING CONTACTS	10/1/2001	09/668,682	3/4/2003	6,528,885	US
NMMS-0624.00/US	Utility - ORG	Issued	READING CIRCUIT FOR A NON-VOLATILE MEMORY	10/27/2000	09/699,304	6/4/2002	6,400,607	US
NMMS-0625.00/US	Utility - ORG	Issued	READING METHOD AND CIRCUIT FOR A NON-VOLATILE MEMORY	10/27/2000	09/699,043	10/29/2002	6,473,340	US
NMMS-0626.00/US	Utility - ORG	Issued	COLUMN DECODER CIRCUIT FOR PAGE READING OF A SEMICONDUCTOR MEMORY	2/27/2001	09/797,171	1/14/2003	6,507,534	US
NMMS-0627.00/US	Utility - ORG	Issued	FLOW PROCESS FOR PRODUCING NON-VOLATILE MEMORIES WITH DIFFERENTIATED REMOVAL OF SACRIFICIAL OXIDE	8/2/2000	09/630,933	3/26/2002	6,362,053	US
NMMS-0628.00/US	Utility - ORG	Issued	ARCHITECTURE FOR HANDLING INTERNAL VOLTAGES IN A NON-VOLATILE MEMORY, PARTICULARLY IN A SINGLE-VOLTAGE SUPPLY TYPE OF DUAL-WORK FLASH MEMORY	11/9/2000	09/710,067	5/7/2002	6,385,107	US
NMMS-0629.00/US	Utility - ORG	Issued	CONTROL AND TIMING STRUCTURE FOR A MEMORY	10/5/2001	09/972,753	4/15/2003	6,549,485	US
NMMS-0630.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NONVOLATILE MEMORY CELLS OF REDUCED DIMENSIONS	11/22/2000	09/718,971	1/21/2003	6,509,222	US
NMMS-0630.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NONVOLATILE MEMORY CELLS OF REDUCED DIMENSIONS	8/20/2002	10/725,315	8/5/2003	6,603,171	US
NMMS-0631.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR NON-VOLATILE FLOATING GATE MEMORY CELLS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND COMPRISED IN A CELL MATRIX WITH AN ASSOCIATED CONTROL CIRCUITRY	12/5/2000	09/730,518	7/16/2002	6,420,223	US
NMMS-0632.00/US	Utility - ORG	Issued	SINGLE SUPPLY VOLTAGE NONVOLATILE MEMORY DEVICE WITH ROW DECODING	7/28/2000	09/627,273	4/16/2002	6,373,780	US
NMMS-0633.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY TEST STRUCTURE AND NONVOLATILE MEMORY RELIABILITY TEST METHOD	10/27/1999	09/428,683	10/3/2000	6,128,219	US
NMMS-0633.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR PROGRAMMING A MULTILEVEL NON-VOLATILE MEMORY	9/12/2001	09/952,957	2/25/2003	6,525,961	US
NMMS-0637.00/US	Utility - ORG	Issued	SERIAL-FLASH, EPROM, EEPROM AND FLASH EEPROM NONVOLATILE MEMORY IN AMG CONFIGURATION	10/10/2000	09/686,362	4/30/2002	6,381,173	US
NMMS-0638.00/US	Utility - ORG	Issued	LOW CONSUMPTION VOLTAGE BOOST DEVICE	12/22/2000	09/747,312	8/20/2002	6,437,636	US
NMMS-0639.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH CONFIGURABLE ROW REDUNDANCY	2/14/2001	09/785,079	7/9/2002	6,418,051	US
NMMS-0640.00/US	Utility - ORG	Abandoned	INTEGRATED DEVICE WITH OPERATIVITY TESTING	3/2/2001	09/798,347	5/24/2005	6,898,745	US
NMMS-0641.00/US	Utility - ORG	Issued	VOLTAGE BOOSTING DEVICE, IN PARTICULAR FOR SPEEDING POWER-UP OF MULTILEVEL NONVOLATILE MEMORIES	2/6/2001	09/778,330	8/6/2002	6,429,634	US
NMMS-0642.00/US	Utility - ORG	Issued	NON-VOLATILE HIGH-PERFORMANCE MEMORY DEVICE AND RELATIVE MANUFACTURING PROCESS	12/19/2000	09/740,407	1/13/2004	6,677,206	US
NMMS-0643.00/US	Utility - ORG	Issued	SYNCHRONOUS COUNTER FOR ELECTRONIC MEMORIES	1/23/2001	09/767,762	2/26/2002	6,351,434	US

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Exhibit A - US Issued Patents

MT-DataNo	MetType	MetStatus	Title	FiledDate	SerialNo	IssuedDate	PatentNo	Country
NMMS-0644.00/US	Utility - ORG	Issued	SYNCHRONIZATION CIRCUIT FOR READ PATHS OF AN ELECTRONIC MEMORY	4/5/2001	09/827,369	10/12/2004	6,804,756	US
NMMS-0645.00/US	Utility - ORG	Issued	INTERNAL ADDRESSING STRUCTURE OF A SEMICONDUCTOR MEMORY	10/9/2001	09/974,737	12/31/2002	6,501,700	US
NMMS-0646.00/US	Utility - ORG	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	3/20/2001	09/817,363	7/2/2002	6,414,875	US
NMMS-0646.01/US	Utility - DIV	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	6/24/2002	10/179,553	1/27/2004	6,683,808	US
NMMS-0646.02/US	Utility - DIV	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	12/19/2003	10/742,429	7/4/2006	7,072,212	US
NMMS-0646.03/US	Utility - DIV	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	12/19/2003	10/742,481	10/11/2005	6,954,395	US
NMMS-0647.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR THE INTEGRATION IN A SEMICONDUCTOR CHIP OF AN INTEGRATED CIRCUIT INCLUDING A HIGH-DENSITY INTEGRATED CIRCUIT COMPONENTS PORTION AND A HIGH-PERFORMANCE LOGIC INTEGRATED CIRCUIT COMPONENTS PORTION	5/15/2001	09/858,335	9/17/2002	6,451,653	US
NMMS-0648.00/US	Utility - ORG	Issued	LOGIC PARTITIONING OF A NONVOLATILE MEMORY ARRAY	3/26/2001	09/817,804	6/17/2003	6,581,134	US
NMMS-0649.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY MATRIX ARCHITECTURE WITH VERTICAL INSULATION STRIPS BETWEEN ADJACENT MEMORY BLOCKS	7/3/2001	09/898,744	10/15/2002	6,466,479	US
NMMS-0650.00/US	Utility - ORG	Issued	PROGRAMMABLE VOLTAGE GENERATOR	11/15/2000	09/714,852	11/18/2003	6,650,173	US
NMMS-0651.00/US	Utility - ORG	Issued	METHOD OF FORMING LOW-RESISTIVITY CONNECTIONS IN NON-VOLATILE MEMORIES	3/2/2001	09/798,778	2/3/2004	6,686,241	US
NMMS-0652.00/US	Utility - ORG	Issued	METHOD AND A DEVICE FOR TESTING ELECTRONIC MEMORY DEVICES	3/30/2001	09/823,826	1/23/2007	7,168,016	US
NMMS-0653.00/US	Utility - ORG	Issued	CIRCUITAL STRUCTURE FOR PROGRAMMING DATA IN A NON-VOLATILE MEMORY DEVICE	5/30/2001	09/871,235	1/14/2003	6,507,517	US
NMMS-0654.00/US	Utility - ORG	Issued	CIRCUITAL STRUCTURE FOR READING DATA IN A NON-VOLATILE MEMORY DEVICE	5/30/2001	09/871,234	4/15/2003	6,549,473	US
NMMS-0655.00/US	Utility - ORG	Abandoned	INTEGRATED DEVICE WITH VOLTAGE SELECTOR	3/29/2001	09/823,262	11/5/2002	6,476,664	US
NMMS-0656.00/US	Utility - ORG	Abandoned	LOW-CONSUMPTION CHARGE PUMP FOR A NONVOLATILE MEMORY	3/28/2001	09/821,827	5/6/2003	6,559,709	US
NMMS-0657.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING A NON-VOLATILE MEMORY CELL WITH A FLOATING GATE REGION AUTOALIGNED TO THE ISOLATION AND WITH A HIGH COUPLING COEFFICIENT	7/6/2001	09/900,501	3/25/2003	6,537,879	US
NMMS-0657.01/US	Utility - DIV	Issued	NON-VOLATILE MEMORY CELL WITH A FLOATING GATE REGION AUTOALIGNED TO THE ISOLATION AND WITH A HIGH COUPLING COEFFICIENT	1/7/2003	10/337,556	6/15/2004	6,750,505	US
NMMS-0658.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY ARCHITECTURE	10/5/2001	09/972,769	6/17/2003	6,580,637	US
NMMS-0659.00/US	Utility - ORG	Issued	REFERENCE VOLTAGE ADJUSTMENT	12/31/1999	09/476,036	8/28/2001	6,281,734	US
NMMS-0659.01/US	Utility - DIV	Issued	REFERENCE VOLTAGE ADJUSTMENT	7/10/2001	09/902,206	11/5/2002	6,476,669	US
NMMS-0660.00/US	Utility - ORG	Issued	INTEGRATED VOLATILE AND NON-VOLATILE MEMORY	8/31/2001	09/653,495	7/15/2003	6,594,192	US
NMMS-0660.01/US	Utility - DIV	Issued	INTEGRATED VOLATILE AND NON-VOLATILE MEMORY	5/20/2003	10/442,844	8/24/2004	6,781,916	US
NMMS-0661.00/US	Utility - ORG	Issued	CMOS TECHNOLOGY VOLTAGE BOOSTER	12/15/2000	09/738,890	7/16/2002	6,420,926	US
NMMS-0662.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY AND HIGH SPEED MEMORY TEST METHOD	9/26/2000	09/670,248	8/20/2002	6,438,048	US
NMMS-0663.00/US	Utility - ORG	Issued	Memory test method and nonvolatile memory with low error masking probability	9/26/2000	09/670,471	8/28/2001	6,282,134	US
NMMS-0664.00/US	Utility - ORG	Issued	ROW SELECTION CIRCUIT FOR FAST MEMORY DEVICES	12/21/2000	09/745,286	8/27/2002	6,442,072	US
NMMS-0665.00/US	Utility - ORG	Issued	INTERLEAVED DATA PATH AND OUTPUT MANAGEMENT ARCHITECTURE FOR AN INTERLEAVED MEMORY AND LOAD PULSER CIRCUIT FOR OUTPUTTING THE READ DATA	1/31/2001	09/774,542	10/22/2002	6,470,431	US
NMMS-0666.00/US	Utility - ORG	Issued	INTERLEAVED MEMORY DEVICE FOR SEQUENTIAL ACCESS SYNCHRONOUS READING WITH SIMPLIFIED ADDRESS COUNTERS	1/31/2001	09/774,539	9/17/2002	6,452,864	US
NMMS-0667.00/US	Utility - ORG	Issued	REDUNDANCY ARCHITECTURE FOR AN INTERLEAVED MEMORY	1/31/2001	09/773,272	10/29/2002	6,473,339	US
NMMS-0668.00/US	Utility - ORG	Issued	ACCELERATED CARRY GENERATION	1/31/2001	09/774,878	4/2/2002	6,366,634	US
NMMS-0669.00/US	Utility - ORG	Issued	ATD GENERATION IN A SYNCHRONOUS MEMORY	1/31/2001	09/773,760	9/23/2003	6,625,706	US
NMMS-0670.00/US	Utility - ORG	Issued	INTERLEAVED MEMORY DEVICE FOR BURST TYPE ACCESS IN SYNCHRONOUS READ MODE WITH THE TWO SEMI-ARRAYS INDEPENDENTLY READABLE IN RANDOM ACCESS ASYNCHRONOUS MODE	1/31/2001	09/773,300	7/1/2003	6,587,913	US
NMMS-0671.00/US	Utility - ORG	Issued	COLOR IMAGE RESTORATION WITH ANTIALIAS	11/2/2000	09/705,145	1/11/2005	6,842,191	US

PATENT

REEL: 029186 FRAME: 0871

Exhibit A - US Issued Patents

MTI-DrkNo	MatrType	MatrStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0672.00/US	Utility - NSPCT	Issued	VOLTAGE PRODUCTION CIRCUIT	6/26/2000	10/019,771	9/16/2003	6,621,720	US
NMMS-0673.00/US	Utility - ORG	Issued	METHOD FOR PAGE MODE WRITING IN AN ELECTRICALLY ERASABLE/PROGRAMMABLE NON-VOLATILE MEMORY AND CORRESPONDING ARCHITECTURE	9/12/2000	09/660,303	1/7/2003	6,504,791	US
NMMS-0674.00/US	Utility - ORG	Issued	MEMORY INCORPORATING COLUMN REGISTER AND METHOD OF WRITING IN SAID MEMORY	9/29/2000	09/675,366	10/23/2001	6,307,792	US
NMMS-0674.01/US	Utility - DIV	Issued	MEMORY INCORPORATING COLUMN REGISTER AND METHOD OF WRITING IN SAID MEMORY	9/13/2001	09/952,904	5/7/2002	6,385,096	US
NMMS-0675.00/US	Utility - ORG	Issued	ARCHITECTURE OF A NON-VOLATILE ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY	9/7/2000	09/657,319	8/14/2001	6,275,413	US
NMMS-0677.00/US	Utility - ORG	Issued	PAGE BY PAGE PROGRAMMABLE FLASH MEMORY	12/14/2000	09/737,170	1/4/2005	6,839,285	US
NMMS-0678.00/US	Utility - ORG	Issued	METHOD FOR THE CORRECTION OF A BIT IN A STRING OF BITS	12/15/2000	09/737,827	5/11/2004	6,735,733	US
NMMS-0679.00/US	Utility - ORG	Issued	HIGH-EFFICIENCY BIDIRECTIONAL VOLTAGE BOOSTING DEVICE	2/13/2001	09/788,623	9/10/2002	6,448,842	US
NMMS-0680.00/US	Utility - ORG	Issued	CHARGE PUMP BOOSTER DEVICE WITH TRANSFER AND RECOVERY OF THE CHARGE	2/13/2001	09/785,042	6/11/2002	6,404,272	US
NMMS-0681.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY CELL WITH HIGH PROGRAMMING EFFICIENCY	7/30/2001	09/919,341	5/11/2004	6,734,490	US
NMMS-0682.00/US	Utility - ORG	Issued	METHOD AND A CIRCUIT STRUCTURE FOR MODIFYING THE THRESHOLD VOLTAGES OF NON-VOLATILE MEMORY CELLS	7/18/2001	09/908,986	2/11/2003	6,519,183	US
NMMS-0683.00/US	Utility - ORG	Issued	CONTROL CIRCUIT FOR A VARIABLE-VOLTAGE REGULATOR OF A NONVOLATILE MEMORY WITH HIERARCHICAL ROW DECODING	9/21/2001	09/960,851	1/7/2003	6,504,758	US
NMMS-0684.00/US	Utility - ORG	Issued	CIRCUIT ARRANGEMENT FOR THE LOWERING OF THE THRESHOLD VOLTAGE OF A DIODE CONFIGURED TRANSISTOR	6/13/2001	09/881,661	12/31/2002	6,501,673	US
NMMS-0685.00/US	Utility - ORG	Issued	DIRECT-COMPARISON READING CIRCUIT FOR A NONVOLATILE MEMORY ARRAY	8/15/2001	09/930,875	10/8/2002	6,462,987	US
NMMS-0686.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH A CHARGE PUMP WITH REGULATED VOLTAGE	7/19/2001	09/909,467	11/12/2002	6,480,436	US
NMMS-0687.00/US	Utility - ORG	Issued	CHARGE PUMP WITH EFFICIENT SWITCHING TECHNIQUES	12/11/2000	09/735,277	3/19/2002	6,359,509	US
NMMS-0688.00/US	Utility - ORG	Issued	High-voltage bidirectional switch made using high-voltage MOS transistors	6/16/2000	09/595,680	9/11/2001	6,288,603	US
NMMS-0689.00/US	Utility - ORG	Issued	READING CIRCUIT FOR SEMICONDUCTOR NON-VOLATILE MEMORIES	9/13/2001	09/953,070	5/13/2003	6,563,737	US
NMMS-0691.00/US	Utility - ORG	Issued	LATERAL DMOS TRANSISTOR WITH FIRST AND SECOND DRAIN ELECTRODES IN RESPECTIVE CONTACT WITH HIGH-AND LOW-CONCENTRATION PORTIONS OF A DRAIN REGION	9/20/2001	09/960,254	9/23/2003	6,624,471	US
NMMS-0692.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER WITH A LOW OUTPUT RESISTANCE	2/13/2001	09/785,083	6/11/2002	6,404,273	US
NMMS-0693.00/US	Utility - ORG	Issued	MULTIPURPOSE METHOD FOR CONSTRUCTING AN ERROR-CONTROL CODE FOR MULTILEVEL MEMORY CELLS OPERATING WITH A VARIABLE NUMBER OF STORAGE LEVELS, AND MULTIPURPOSE ERROR-CONTROL METHOD USING SAID ERROR-CONTROL CODE	11/2/2001	10/015,949	5/16/2006	7,047,478	US
NMMS-0694.00/US	Utility - ORG	Issued	FOLDING TOP FOR A MOTOR VEHICLE	10/31/2001	09/002,599	8/5/2003	6,603,681	US
NMMS-0695.00/US	Utility - ORG	Issued	NONVOLATILE SEMICONDUCTOR MEMORY CAPABLE OF SELECTIVELY ERASING A PLURALITY OF ELEMENTAL MEMORY UNITS	7/31/2001	09/919,789	3/11/2003	6,532,171	US
NMMS-0696.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE, NON-VOLATILE, SEMICONDUCTOR MEMORY WHICH CAN KEEP A DATUM STORED UNTIL AN OPERATION TO MODIFY THE DATUM IS COMPLETED	12/28/2001	10/036,088	1/4/2005	6,839,818	US
NMMS-0697.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR LOW-CONSUMPTION CIRCUITS	11/7/2001	09/008,540	5/6/2003	6,559,627	US
NMMS-0698.00/US	Utility - ORG	Issued	SMALL SIZE, LOW CONSUMPTION, MULTILEVEL NONVOLATILE MEMORY	10/4/2001	09/972,726	4/1/2003	6,542,404	US
NMMS-0699.00/US	Utility - ORG	Issued	METHOD FOR STORING AND READING DATA IN A MULTILEVEL NONVOLATILE MEMORY	10/11/2001	09/976,473	11/11/2003	6,646,913	US
NMMS-0700.00/US	Utility - ORG	Abandoned	NONVOLATILE MEMORY DEVICE, HAVING PARTS WITH DIFFERENT ACCESS TIME, RELIABILITY, AND CAPACITY	9/19/2001	09/957,628	12/10/2002	6,493,260	US
NMMS-0701.00/US	Utility - ORG	Issued	CONTROL CIRCUIT FOR AN OUTPUT DRIVING STAGE OF AN INTEGRATED CIRCUIT	11/21/2001	09/991,493	5/20/2003	6,567,318	US

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REEL: 029186 FRAME: 0872

Exhibit A - US Issued Patents

MT-Id-No	MasterType	MasterStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0702.00/US	Utility - ORG	Issued	BIASING CIRCUIT FOR MULTI-LEVEL MEMORY CELLS	6/14/2001	09/882,535	3/18/2003	6,533,428	US
NMMS-0703.00/US	Utility - ORG	Issued	CIRCUIT DEVICE FOR PERFORMING HIERARCHIC ROW DECODING IN NON-VOLATILE MEMORY DEVICES	7/11/2001	09/905,163	12/10/2002	6,493,268	US
NMMS-0704.01/US	Utility - CON	Issued	ANALOG-TO-DIGITAL CONVERSION METHOD AND DEVICE, IN HIGH-DENSITY MULTILEVEL, NON-VOLATILE MEMORY DEVICES	1/29/2002	10/060,076	1/6/2004	6,674,385	US
NMMS-0705.00/US	Utility - ORG	Issued	POWER-ON RESET CIRCUIT FOR DUAL SUPPLY VOLTAGES	10/25/2001	09/003,474	11/12/2002	6,480,421	US
NMMS-0706.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING NONVOLATILE MEMORY CELLS WITH PROGRAM AND VERIFY ALGORITHM USING A STAIRCASE VOLTAGE WITH VARYING STEP AMPLITUDE	4/9/2002	10/119,523	9/7/2004	6,788,579	US
NMMS-0707.00/US	Utility - ORG	Issued	CONDUCTION LINE DECOUPLING CIRCUIT	7/26/2001	09/917,613	9/7/2004	6,788,517	US
NMMS-0708.00/US	Utility - ORG	Issued	MULTI-EMITTER BIPOLAR TRANSISTOR FOR BANDGAP REFERENCE CIRCUITS	12/27/2001	10/035,006	3/2/2004	6,700,226	US
NMMS-0710.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY SYSTEM	5/30/2002	10/158,554	7/15/2003	6,594,180	US
NMMS-0711.00/US	Utility - ORG	Issued	OUTPUT BUFFER FOR A NONVOLATILE MEMORY WITH OUTPUT SIGNAL SWITCHING NOISE REDUCTION, AND NONVOLATILE MEMORY COMPRISING THE SAME	5/30/2002	10/161,053	9/7/2004	6,788,586	US
NMMS-0712.00/US	Utility - ORG	Issued	OUTPUT BUFFER FOR A NONVOLATILE MEMORY WITH OPTIMIZED SLEW-RATE CONTROL	5/30/2002	10/161,055	12/7/2004	6,829,177	US
NMMS-0713.00/US	Utility - ORG	Issued	EEPROM FLASH MEMORY ERASABLE LINE BY LINE	8/20/2002	10/225,513	2/3/2004	6,687,167	US
NMMS-0714.00/US	Utility - ORG	Issued	METHOD OF PROGRAMMING A PLURALITY OF MEMORY CELLS CONNECTED IN PARALLEL, AND A PROGRAMMING CIRCUIT THEREFOR	12/19/2001	10/036,337	2/3/2004	6,687,159	US
NMMS-0715.00/US	Utility - ORG	Issued	COLUMN MULTIPLEXER FOR SEMICONDUCTOR MEMORIES	5/30/2002	10/158,553	4/22/2003	6,552,952	US
NMMS-0716.00/US	Utility - ORG	Issued	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	12/14/2001	10/017,502	12/23/2003	6,667,903	US
NMMS-0717.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR DYNAMIC READING OF A MEMORY CELL, IN PARTICULAR A MULTI-LEVEL, NONVOLATILE MEMORY CELL	1/14/2002	10/047,918	11/4/2003	6,643,179	US
NMMS-0718.00/US	Utility - ORG	Issued	METHOD FOR STORING DATA IN A NONVOLATILE MEMORY	12/19/2001	10/035,909	12/2/2003	6,655,758	US
NMMS-0720.00/US	Utility - ORG	Issued	AUTOTESTING METHOD OF A MEMORY CELL MATRIX, PARTICULARLY OF THE NON-VOLATILE TYPE	12/23/2002	10/328,721	11/8/2005	6,963,512	US
NMMS-0721.00/US	Utility - ORG	Issued	READING CIRCUIT AND METHOD FOR A MULTILEVEL NON-VOLATILE MEMORY	4/8/2002	10/118,660	12/2/2003	6,657,895	US
NMMS-0722.00/US	Utility - ORG	Abandoned	METHOD OF REFRESHING AN ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY	6/20/2002	10/176,387	5/4/2004	6,731,557	US
NMMS-0723.00/US	Utility - ORG	Issued	HIGH-EFFICIENCY POWER CHARGE PUMP SUPPLYING HIGH DC OUTPUT CURRENTS	6/3/2002	10/162,135	8/12/2003	6,605,985	US
NMMS-0724.00/US	Utility - ORG	Issued	NON-VOLATILE, ELECTRICALLY ALTERABLE SEMICONDUCTOR MEMORY	1/24/2002	10/057,769	9/9/2003	6,618,315	US
NMMS-0725.00/US	Utility - ORG	Issued	METHOD FOR ERASING AN ELECTRICALLY ERASABLE NONVOLATILE MEMORY DEVICE, IN PARTICULAR AN EEPROM-FLASH MEMORY DEVICE, IN PARTICULAR AN EEPROM-FLASH MEMORY DEVICE	5/30/2002	10/159,780	3/22/2005	6,871,258	US
NMMS-0726.00/US	Utility - ORG	Issued	METHOD FOR REFRESHING STORED DATA IN AN ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY	1/24/2002	10/057,768	12/23/2003	6,668,303	US
NMMS-0727.00/US	Utility - ORG	Issued	MEMORY WITH IMPROVED DIFFERENTIAL READING SYSTEM	6/20/2002	10/176,954	3/2/2004	6,700,819	US
NMMS-0728.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TIMING DYNAMIC READING OF A MEMORY CELL WITH CONTROL OF THE INTEGRATION TIME	4/18/2002	10/123,874	4/27/2004	6,728,141	US
NMMS-0729.00/US	Utility - CIP	Issued	METHOD AND CIRCUIT FOR DYNAMIC READING OF A MEMORY CELL AT LOW SUPPLY VOLTAGE AND WITH LOW OUTPUT DYNAMICS	2/13/2002	10/076,023	10/28/2003	6,639,833	US
NMMS-0730.00/US	Utility - CIP	Issued	METHOD AND CIRCUIT FOR GENERATING REFERENCE VOLTAGES FOR READING A MULTILEVEL MEMORY CELL	4/26/2002	10/133,231	4/20/2004	6,724,658	US
NMMS-0731.00/US	Utility - ORG	Issued	METHOD FOR ERROR CONTROL IN MULTILEVEL CELLS WITH CONFIGURABLE NUMBER OF STORED BITS	5/30/2002	10/159,782	3/21/2006	7,017,099	US
NMMS-0732.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS OF AN INTERPOLY DIELECTRIC STRUCTURE FOR NON-VOLATILE SEMICONDUCTOR INTEGRATED MEMORIES	1/30/2003	10/356,351	8/1/2006	7,084,032	US

PATENT

REEL: 029186 FRAME: 0873

Exhibit A - US Issued Patents

MTLDCARDNO	MatterType	MatterStatus	Title	FiledDate	SerialNo	IssuedDate	PatentNo	Country
NMMS-0733.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS OF A SEMICONDUCTOR NON-VOLATILE MEMORY CELL	12/18/2002	10/323,615	8/28/2007	7,262,098	US
NMMS-0734.00/US	Utility - ORG	Issued	PROCESS FOR REMOVING POLYMERS DURING THE FABRICATION OF SEMICONDUCTOR DEVICES	7/2/2002	10/189,152	4/13/2004	6,720,271	US
NMMS-0735.00/US	Utility - ORG	Issued	LOW POWER CHARGE PUMP CIRCUIT	11/7/2002	10/290,030	11/9/2004	6,816,001	US
NMMS-0736.00/US	Utility - ORG	Issued	CONTENT ADDRESSABLE MEMORY (CAM) WITH BATTERY BACK-UP	6/29/2001	09/895,491	12/17/2002	6,496,439	US
NMMS-0737.00/US	Utility - ORG	Issued	Data output buffer with precharge	8/11/2000	09/636,363	9/18/2001	6,292,405	US
NMMS-0738.00/US	Utility - ORG	Issued	INTERLACED MEMORY DEVICE WITH RANDOM OR SEQUENTIAL ACCESS	10/15/2001	09/977,561	3/2/2004	6,701,419	US
NMMS-0739.00/US	Utility - ORG	Issued	OUTPUT BUFFER AND METHOD OF DRIVING	8/8/2001	09/925,842	12/3/2002	6,489,807	US
NMMS-0740.00/US	Utility - ORG	Issued	GENERATOR CIRCUIT FOR VOLTAGE RAMPS AND CORRESPONDING VOLTAGE GENERATION METHOD	12/28/2001	10/033,711	11/18/2003	6,650,133	US
NMMS-0741.00/US	Utility - ORG	Issued	BOOST DEVICE FOR NONVOLATILE MEMORIES WITH AN INTEGRATED STAND-BY CHARGE PUMP	2/13/2002	10/076,134	11/18/2003	6,650,569	US
NMMS-0742.00/US	Utility - ORG	Abandoned	CHARGE PUMP FOR A NONVOLATILE MEMORY WITH READ VOLTAGE REGULATION IN THE PRESENCE OF ADDRESS SKEW, AND NONVOLATILE MEMORY COMPRISING SUCH A CHARGE PUMP	2/5/2002	10/068,560	5/6/2003	6,560,145	US
NMMS-0743.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR TIMING THE READING OF A NONVOLATILE MEMORY WITH REDUCED SWITCHING NOISE	2/15/2002	10/077,687	1/6/2004	6,674,666	US
NMMS-0744.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR AUTOMATICALLY GENERATING AN APPROPRIATE NUMBER OF WAIT CYCLES WHILE READING A NONVOLATILE MEMORY	4/3/2002	10/115,888	7/12/2005	6,917,994	US
NMMS-0745.00/US	Utility - ORG	Issued	CIRCUIT FOR GENERATING A PULSE SIGNAL INDEPENDENT OF VOLTAGE AND TEMPERATURE VARIATIONS	10/31/2001	09/998,903	4/15/2003	6,549,486	US
NMMS-0746.00/US	Utility - ORG	Issued	NEGATIVE CHARGE PUMP ARCHITECTURE WITH SELF-GENERATED BOOSTED PHASES	10/31/2001	09/998,902	4/13/2004	6,720,822	US
NMMS-0748.00/US	Utility - ORG	Issued	THRESHOLD AMPLIFIER	8/9/2001	09/925,979	4/15/2003	6,549,048	US
NMMS-0749.00/US	Utility - NSPCT	Issued	PAGE-ERASABLE FLASH MEMORY	11/14/2001	10/438,733	10/19/2004	6,807,103	US
NMMS-0750.00/US	Utility - ORG	Issued	CIRCUIT FOR THE DETECTION OF A DEFECTIVE POWER SUPPLY CONNECTION	1/29/2002	10/060,105	8/10/2004	6,774,663	US
NMMS-0751.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR SEQUENTIAL READOUT OF A MEMORY WITH ADDRESS JUMP	2/22/2002	10/081,740	8/9/2005	6,928,530	US
NMMS-0752.00/US	Utility - ORG	Issued	FLASH MEMORY INCLUDING MEANS OF CHECKING MEMORY CELL THRESHOLD VOLTAGES	11/15/2001	09/997,214	5/27/2003	6,568,510	US
NMMS-0752.01/US	Utility - DIV	Issued	FLASH MEMORY INCLUDING MEANS OF CHECKING MEMORY CELL THRESHOLD VOLTAGES	1/28/2003	10/352,581	3/30/2004	6,714,453	US
NMMS-0753.00/US	Utility - NSPCT	Issued	CONTACT LESS IC CARD WITH OPERATING SYSTEM USED IN CONTACT TYPE CARDS AND READER FOR SUCH CONTACTLESS CARDS	2/14/2002	10/471,883	1/16/2007	7,163,154	US
NMMS-0754.00/US	Utility - ORG	Issued	FLASH EEPROM AND EPROM ARRAYS WITH SELECT TRANSISTORS WITHIN THE BIT LINE PITCH	3/11/1994	08/212,176	9/17/1996	5,557,124	US
NMMS-0755.00/US	Utility - ORG	Issued	METHOD OF RE-PROGRAMMING AN ARRAY OF NON-VOLATILE MEMORY CELLS, IN PARTICULAR OF THE NOR ARCHITECTURE FLASH TYPE, AFTER AN ERASE OPERATION, AND A CORRESPONDING MEMORY DEVICE	6/12/2002	10/171,078	11/25/2003	6,654,287	US
NMMS-0756.00/US	Utility - ORG	Issued	MEMORY DEVICE	10/24/2001	10/041,684	5/20/2003	6,567,296	US
NMMS-0757.00/US	Utility - ORG	Issued	WORD LINE SELECTOR FOR A SEMICONDUCTOR MEMORY	2/20/2003	10/372,626	3/8/2005	6,865,114	US
NMMS-0758.00/US	Utility - ORG	Issued	DESIGN FAILURE MODE EFFECT ANALYSIS (DFMEA)	12/26/2002	10/330,485	4/25/2006	7,055,769	US
NMMS-0759.00/US	Utility - ORG	Issued	POWER SUPPLY CIRCUIT STRUCTURE FOR A ROW DECODER OF A MULTILEVEL NON-VOLATILE MEMORY DEVICE	12/30/2002	10/334,126	12/7/2004	6,829,168	US
NMMS-0760.00/US	Utility - ORG	Issued	BINARY ENCODING CIRCUIT	12/20/2002	10/325,707	2/24/2004	6,696,990	US
NMMS-0761.00/US	Utility - ORG	Issued	REGULATION METHOD FOR THE DRAIN, BODY AND SOURCE TERMINALS VOLTAGES IN A NON-VOLATILE MEMORY CELL DURING A PROGRAM PHASE AND CORRESPONDING PROGRAM CIRCUIT	12/27/2002	10/331,116	10/26/2004	6,809,961	US

PATENT

REEL: 029186 FRAME: 0874

Exhibit A - US Issued Patents

MTI-DC#NO	MaterType	MaterStatus	Title	FiledDate	SerialNo	IssuedDate	PatentNo	Country
NMXXS-0762.00/US	Utility - ORG	Issued	METHOD FOR STORING AND READING DATA IN A MULTILEVEL NONVOLATILE MEMORY, AND ARCHITECTURE THEREOF	9/26/2002	10/259,252	11/9/2004	6,816,407	US
NMXXS-0762.01/US	Utility - DIV	Issued	MULTILEVEL MEMORY DEVICE WITH MEMORY CELLS STORING NON-POWER OF TWO VOLTAGE LEVELS	10/12/2004	10/964,160	11/15/2005	6,965,523	US
NMXXS-0763.00/US	Utility - ORG	Issued	ELECTRICALLY-PROGRAMMABLE NON-VOLATILE MEMORY CELL	2/20/2003	10/372,044	4/19/2005	6,882,001	US
NMXXS-0764.00/US	Utility - ORG	Issued	DECODING STRUCTURE FOR A MEMORY DEVICE WITH A CONTROL CODE	12/27/2002	10/331,177	10/25/2005	6,958,949	US
NMXXS-0765.00/US	Utility - ORG	Issued	SENSE AMPLIFIER STRUCTURE FOR MULTILEVEL NON-VOLATILE MEMORY DEVICES AND CORRESPONDING READING METHOD	12/26/2002	10/331,158	11/23/2004	6,822,906	US
NMXXS-0766.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING A DUAL CHARGE STORAGE LOCATION MEMORY CELL	11/14/2002	10/294,999	10/25/2005	6,958,510	US
NMXXS-0767.00/US	Utility - ORG	Issued	FINITE STATE MACHINE INTERFACE HAS ARBITRATION STRUCTURE TO STORE COMMAND GENERATED BY INTERNAL CIRCUITS DURING EVALUATION PHASE OF STATE MACHINE FOR FLASH EEPROM DEVICE	12/4/2002	10/309,759	10/16/2007	7,284,144	US
NMXXS-0768.00/US	Utility - ORG	Issued	SINGLE SUPPLY VOLTAGE, NONVOLATILE PHASE CHANGE MEMORY DEVICE WITH CASCODED COLUMN SELECTION AND SIMULTANEOUS WORD READ/WRITE OPERATIONS	12/27/2002	10/331,185	6/22/2004	6,754,107	US
NMXXS-0769.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING A DUAL CHARGE STORAGE LOCATION MEMORY CELL	10/7/2002	10/267,033	11/30/2004	6,825,523	US
NMXXS-0769.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING A DUAL CHARGE STORAGE LOCATION MEMORY CELL	10/12/2004	10/964,049	10/3/2006	7,115,472	US
NMXXS-0770.00/US	Utility - ORG	Issued	ARCHITECTURE FOR A FLASH-EEPROM SIMULTANEOUSLY READABLE IN OTHER SECTORS WHILE ERASING AND/OR PROGRAMMING ONE OR MORE SECTORS	1/10/2003	10/340,207	5/10/2005	6,891,755	US
NMXXS-0771.00/US	Utility - ORG	Issued	FAST PROGRAMMING METHOD FOR NONVOLATILE MEMORIES, IN PARTICULAR FLASH MEMORIES, AND RELATIVE MEMORY ARCHITECTURE	10/24/2002	10/281,078	12/27/2005	6,981,107	US
NMXXS-0772.00/US	Utility - ORG	Issued	ARCHITECTURE OF A PHASE-CHANGE NONVOLATILE MEMORY ARRAY	12/12/2002	10/319,439	11/9/2004	6,816,404	US
NMXXS-0773.00/US	Utility - ORG	Issued	MEMORY SYSTEM COMPRISING A SEMICONDUCTOR MEMORY	12/12/2003	10/735,250	5/22/2007	7,221,602	US
NMXXS-0774.00/US	Utility - ORG	Issued	SELF-REPAIR METHOD FOR NONVOLATILE MEMORY DEVICES WITH ERASING/PROGRAMMING FAILURE, AND RELATIVE NONVOLATILE MEMORY DEVICE	5/15/2003	10/440,043	9/13/2005	6,944,072	US
NMXXS-0775.00/US	Utility - ORG	Issued	INTEGRATED RESISTOR, PHASE-CHANGE MEMORY ELEMENT INCLUDING THIS RESISTOR, AND PROCESS FOR THE FABRICATION THEREOF	1/14/2003	10/345,129	9/20/2005	6,946,673	US
NMXXS-0776.00/US	Utility - ORG	Issued	METHOD FOR AVOIDING THE EFFECTS OF LACK OF UNIFORMITY IN TRENCH ISOLATED INTEGRATED CIRCUITS	11/29/2001	09/997,227	7/1/2003	6,586,313	US
NMXXS-0777.00/US	Utility - ORG	Issued	SMALL AREA CONTACT REGION, HIGH EFFICIENCY PHASE CHANGE MEMORY CELL AND FABRICATING METHOD THEREOF	12/5/2002	10/313,991	6/5/2007	7,227,171	US
NMXXS-0778.00/US	Utility - ORG	Issued	PROGRAMMING METHOD OF THE MEMORY CELLS IN A MULTILEVEL NON-VOLATILE MEMORY DEVICE	5/13/2003	10/438,175	7/19/2005	6,920,066	US
NMXXS-0779.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE	3/14/2003	10/390,556	12/12/2006	7,149,844	US
NMXXS-0780.00/US	Utility - CIP	Issued	PHASE CHANGE MEMORY CELL AND MANUFACTURING METHOD THEREOF USING MINITRENCHES	2/20/2003	10/372,761	5/10/2005	6,891,747	US
NMXXS-0780.01/US	Utility - DIV	Issued	PHASE CHANGE MEMORY CELL AND MANUFACTURING METHOD THEREOF USING MINITRENCHES	1/27/2005	11/045,170	8/9/2011	7,993,957	US
NMXXS-0782.00/US	Utility - CIP	Issued	SUBLITHOGRAPHIC CONTACT STRUCTURE, PHASE CHANGE MEMORY CELL WITH OPTIMIZED HEATER SHAPE, AND MANUFACTURING METHOD THEREOF	2/20/2003	10/371,154	12/6/2005	6,972,430	US
NMXXS-0782.01/US	Utility - DIV	Issued	SUBLITHOGRAPHIC CONTACT STRUCTURE, PHASE CHANGE MEMORY CELL WITH OPTIMIZED HEATER SHAPE, AND MANUFACTURING METHOD THEREOF	10/24/2005	11/258,340	5/13/2008	7,372,166	US
NMXXS-0783.00/US	Utility - CIP	Issued	CONTACT STRUCTURE, PHASE CHANGE MEMORY CELL, AND MANUFACTURING METHOD THEREOF WITH ELIMINATION OF DOUBLE CONTACTS	2/20/2003	10/372,639	8/16/2005	6,930,913	US

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Exhibit A - US Issued Patents

Patent No.	Material Type	Material Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMMS-0783.00/US	Utility - DIV	Issued	MANUFACTURING METHOD OF A CONTACT STRUCTURE AND PHASE CHANGE MEMORY CELL WITH ELIMINATION OF DOUBLE CONTACTS	6/20/2005	11/156,989	7/22/2008	7,402,455	US
NMMS-0784.00/US	Utility - ORG	Issued	BASIC STAGE FOR A CHARGE PUMP CIRCUIT	3/28/2003	10/402,852	2/21/2006	7,002,399	US
NMMS-0785.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY CELL	6/25/2003	10/606,164	4/5/2005	6,876,033	US
NMMS-0786.00/US	Utility - ORG	Issued	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	12/26/2002	10/331,161	8/8/2006	7,088,614	US
NMMS-0787.00/US	Utility - ORG	Issued	ADJUSTABLE FREQUENCY OSCILLATOR WITH PROGRAMMABLE CALIBRATING CIRCUIT AND RELATED SYSTEM AND METHOD	4/2/2003	10/406,628	4/19/2005	6,882,231	US
NMMS-0788.00/US	Utility - ORG	Issued	TEST STRUCTURE FOR THE MEASUREMENT OF CONTACT TO GATE DISTANCE IN NON-VOLATILE MEMORY DEVICES AND CORRESPONDING TEST METHOD	5/30/2003	10/449,761	6/7/2005	6,903,995	US
NMMS-0789.00/US	Utility - ORG	Abandoned	METHOD FOR MANUFACTURING ELECTRONIC CIRCUITS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE	5/1/2003	10/428,338	2/14/2006	6,998,348	US
NMMS-0790.00/US	Utility - ORG	Issued	SINGLE ENDED OUTPUT SENSE AMPLIFIER CIRCUIT WITH REDUCED POWER CONSUMPTION AND NOISE	7/8/2002	10/190,917	11/23/2004	6,822,919	US
NMMS-0791.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUIT AND METHOD FOR SEMICONDUCTOR MEMORY DEVICES	5/31/2002	10/161,501	5/4/2004	6,731,550	US
NMMS-0792.00/US	Utility - ORG	Issued	CIRCUIT FOR CONTROLLING A REFERENCE NODE IN A SENSE AMPLIFIER	12/27/2002	10/331,147	10/5/2004	6,801,466	US
NMMS-0793.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE WITH DOUBLE SERIAL/PARALLEL COMMUNICATION INTERFACE	10/15/2002	10/271,352	5/10/2005	6,892,269	US
NMMS-0794.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH SELF-REFRESH CAPABILITY	3/11/2003	10/387,141	5/8/2007	7,216,212	US
NMMS-0796.00/US	Utility - ORG	Issued	MEMORY DEVICE OUTPUTTING READ DATA IN A TIME STARTING FROM A RISING EDGE OF AN EXTERNAL CLOCK THAT IS SHORTER THAN THAT OF KNOWN DEVICES	12/19/2002	10/325,486	1/24/2006	6,990,596	US
NMMS-0797.00/US	Utility - ORG	Issued	DEVICE INTEGRATING A NONVOLATILE MEMORY ARRAY AND A VOLATILE MEMORY ARRAY	2/7/2003	10/360,840	5/23/2006	7,050,322	US
NMMS-0798.00/US	Utility - ORG	Issued	METHOD OF WRITING A GROUP OF DATA BYTES IN A MEMORY AND MEMORY DEVICE	2/21/2003	10/371,221	2/7/2006	6,996,697	US
NMMS-0799.00/US	Utility - ORG	Issued	NONVOLATILE STORAGE DEVICE AND SELF-REDUNDANCY METHOD FOR THE SAME	8/11/2003	10/639,240	4/8/2008	7,355,908	US
NMMS-0800.00/US	Utility - ORG	Issued	BUILT-IN TESTING METHODOLOGY IN FLASH MEMORY	2/27/2004	10/789,443	5/23/2006	7,050,343	US
NMMS-0801.00/US	Utility - ORG	Issued	SECTOR PROTECTION CIRCUIT AND METHOD FOR FLASH MEMORY DEVICES	8/2/2001	09/922,043	12/3/2002	6,490,197	US
NMMS-0802.00/US	Utility - ORG	Issued	DUAL BANK FLASH MEMORY DEVICE AND METHOD	8/2/2001	09/922,044	4/22/2003	6,552,935	US
NMMS-0803.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUIT AND METHOD FOR FLASH MEMORY DEVICES	8/2/2001	09/922,068	5/13/2003	6,563,732	US
NMMS-0804.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUIT AND METHOD FOR REPLACING DEFECTIVE MEMORY CELLS IN A FLASH MEMORY DEVICE	8/2/2001	09/922,176	7/15/2003	6,594,177	US
NMMS-0805.00/US	Utility - ORG	Issued	CIRCUIT AND ASSOCIATED METHOD FOR THE ERASURE OR PROGRAMMING OF A MEMORY CELL	3/11/2002	10/096,531	9/16/2003	6,621,737	US
NMMS-0806.00/US	Utility - ORG	Issued	EEPROM MEMORY COMPRISING MEANS FOR SIMULTANEOUS READING OF SPECIAL BITS OF A FIRST AND SECOND TYPE	10/21/2002	10/277,183	5/18/2004	6,738,286	US
NMMS-0807.00/US	Utility - ORG	Issued	EEPROM MEMORY PROTECTED AGAINST THE EFFECTS FROM A BREAKDOWN OF AN ACCESS TRANSISTOR	6/24/2002	10/178,796	8/23/2005	6,934,192	US
NMMS-0808.00/US	Utility - ORG	Issued	READ AMPLIFIER WITH A LOW CURRENT CONSUMPTION DIFFERENTIAL OUTPUT STAGE	11/19/2002	10/299,965	7/6/2004	6,760,265	US
NMMS-0809.00/US	Utility - ORG	Issued	METHOD FOR ERASING NON-VOLATILE MEMORY CELLS AND CORRESPONDING MEMORY DEVICE	9/30/2003	10/675,221	2/27/2007	7,184,319	US
NMMS-0810.00/US	Utility - ORG	Issued	NON-VOLATILE LATCH CIRCUIT	5/7/2003	10/434,395	4/18/2006	7,031,189	US
NMMS-0811.00/US	Utility - ORG	Issued	METHOD FOR REDUCING SPURIOUS ERASING DURING PROGRAMMING OF A NONVOLATILE NROM	4/29/2003	10/426,924	11/30/2004	6,826,083	US
NMMS-0812.00/US	Utility - CIP	Issued	METHOD FOR POST ETCH CLEANS	5/13/2002	10/137,096	6/24/2008	7,390,755	US

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Exhibit A - US Issued Patents

Patent No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Issued Date	Patent No.	Country
NMMS-0813.00/US	Utility - ORG	Issued	REGULATION METHOD FOR THE SOURCE TERMINAL VOLTAGE IN A NON-VOLATILE MEMORY CELL DURING A PROGRAM PHASE AND CORRESPONDING PROGRAM CIRCUIT	12/27/2002	10/331,106	11/23/2004	6,822,905	US
NMMS-0815.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT STRUCTURES	7/30/2003	10/631,463	1/16/2007	7,163,898	US
NMMS-0816.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR WITH VERY QUICK RESPONSE	6/26/2003	10/608,998	6/21/2005	6,909,264	US
NMMS-0817.00/US	Utility - ORG	Issued	ARRAY OF CELLS INCLUDING A SELECTION BIPOLAR TRANSISTOR AND FABRICATION METHOD THEREOF	10/7/2003	10/680,727	11/14/2006	7,135,756	US
NMMS-0817.01/US	Utility - DIV	Issued	ARRAY OF CELLS INCLUDING A SELECTION BIPOLAR TRANSISTOR AND FABRICATION METHOD THEREOF	10/19/2006	11/551,170	11/14/2008	7,446,011	US
NMMS-0818.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING AN ARRAY OF CELLS INCLUDING SELECTION BIPOLAR JUNCTION TRANSISTORS	10/7/2003	10/680,721	1/24/2006	6,989,580	US
NMMS-0818.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING AN ARRAY OF CELLS INCLUDING SELECTION BIPOLAR JUNCTION TRANSISTORS	11/1/2005	11/264,084	7/21/2009	7,563,684	US
NMMS-0819.00/US	Utility - ORG	Issued	SELF-REPAIR METHOD VIA ECC FOR NONVOLATILE MEMORY DEVICES, AND RELATIVE NONVOLATILE MEMORY DEVICE	4/15/2003	10/417,416	5/31/2005	6,901,011	US
NMMS-0820.00/US	Utility - ORG	Issued	SELF-REPAIR METHOD FOR NONVOLATILE MEMORY DEVICES USING A SUPERSECURE ARCHITECTURE, AND NONVOLATILE MEMORY DEVICE	4/24/2003	10/423,845	7/26/2005	6,922,366	US
NMMS-0821.00/US	Utility - ORG	Issued	LINE SELECTOR FOR A MATRIX OF MEMORY ELEMENTS	7/8/2003	10/616,414	8/16/2005	6,930,927	US
NMMS-0822.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR A HIGH VOLTAGE TRANSISTOR INTEGRATED ON A SEMICONDUCTING SUBSTRATE WITH NON-VOLATILE MEMORY CELLS AND CORRESPONDING TRANSISTOR	9/29/2003	10/675,245	9/27/2005	6,949,803	US
NMMS-0823.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING A BYTE SELECTION TRANSISTOR FOR A MATRIX OF NON VOLATILE MEMORY CELLS AND CORRESPONDING STRUCTURE	11/18/2003	10/715,887	12/6/2005	6,972,454	US
NMMS-0823.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING A BYTE SELECTION TRANSISTOR FOR A MATRIX OF NON VOLATILE MEMORY CELLS AND CORRESPONDING STRUCTURE	10/25/2005	11/258,675	11/25/2008	7,456,467	US
NMMS-0824.00/US	Utility - ORG	Issued	ION-IMPLANTATION MACHINE, CONTROL METHOD THEREOF, AND PROCESS FOR MANUFACTURING INTEGRATED DEVICES	9/8/2003	10/657,801	6/13/2006	7,060,995	US
NMMS-0826.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR PROGRAMMING AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE SEMICONDUCTOR MEMORY	12/5/2003	10/729,875	4/18/2006	7,031,193	US
NMMS-0827.00/US	Utility - ORG	Issued	CIRCUIT FOR PROGRAMMING A NON-VOLATILE MEMORY DEVICE WITH ADAPTIVE PROGRAM LOAD CONTROL	11/12/2003	10/706,306	10/18/2005	6,956,773	US
NMMS-0828.00/US	Utility - ORG	Issued	SINGLE CELL ERASING METHOD FOR RECOVERING MEMORY CELLS UNDER PROGRAMMING DISTURBS IN NON VOLATILE SEMICONDUCTOR MEMORY DEVICES	11/26/2003	10/724,022	9/13/2005	6,944,061	US
NMMS-0829.00/US	Utility - ORG	Issued	CIRCUIT FOR BIASING AN INPUT NODE OF A SENSE AMPLIFIER WITH A PRE-CHARGE STAGE	9/16/2003	10/664,606	2/15/2005	6,856,547	US
NMMS-0830.00/US	Utility - ORG	Issued	INTEGRATED RESISTIVE ELEMENTS WITH SILICIDATION PROTECTION	9/26/2003	10/672,293	2/13/2007	7,176,553	US
NMMS-0830.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING INTEGRATED RESISTIVE ELEMENTS WITH SILICIDATION PROTECTION	1/30/2006	11/343,593	7/28/2009	7,566,610	US
NMMS-0831.00/US	Utility - ORG	Issued	SELF-ALIGNED INTEGRATED ELECTRONIC DEVICES	11/14/2003	10/713,538	12/23/2008	7,468,535	US
NMMS-0831.01/US	Utility - DIV	Issued	PROCESS FOR SELF-ALIGNED MANUFACTURE OF INTEGRATED ELECTRONIC DEVICES	1/4/2008	12/006,706	8/10/2010	7,772,084	US
NMMS-0832.00/US	Utility - ORG	Issued	VOLTAGE BOOST DEVICE AND MEMORY SYSTEM	7/7/2003	10/614,693	12/28/2004	6,836,442	US
NMMS-0833.00/US	Utility - ORG	Issued	METHOD OF PROGRAMMING A MULTI-LEVEL, ELECTRICALLY PROGRAMMABLE NON-VOLATILE SEMICONDUCTOR MEMORY	2/19/2004	10/782,725	9/19/2006	7,110,300	US
NMMS-0834.00/US	Utility - ORG	Issued	STRUCTURE AND METHOD FOR DETECTING ERRORS IN A MULTILEVEL MEMORY DEVICE WITH IMPROVED PROGRAMMING GRANULARITY	12/18/2003	10/741,815	1/13/2009	7,478,292	US

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Exhibit A - US Issued Patents

Patent No	Metatype	Metatype	Metatype	Title	Filed Date	Serial No	Issue Date	Patent No	Country
NMMS-0834.01/US	Utility - DIV	Issued		STRUCTURE AND METHOD FOR DETECTING ERRORS IN A MULTILEVEL MEMORY DEVICE WITH IMPROVED PROGRAMMING GRANULARITY	10/14/2008	12/251,289	6/1/2010	7,730,383	US
NMMS-0835.00/US	Utility - ORG	Issued		METHOD FOR CONTROLLING PROGRAMMING VOLTAGE LEVELS OF NON-VOLATILE MEMORY CELLS, THE METHOD TRACKING THE CELL FEATURES, AND CORRESPONDING VOLTAGE REGULATOR	8/28/2003	10/651,019	11/22/2005	6,967,876	US
NMMS-0836.00/US	Utility - ORG	Issued		SEMICONDUCTOR MEMORY SYSTEM INCLUDING SELECTION TRANSISTORS	12/24/2003	10/746,555	4/4/2006	7,023,728	US
NMMS-0837.00/US	Utility - ORG	Issued		METHOD AND DEVICE FOR TIMING RANDOM READING OF A MEMORY DEVICE	11/3/2003	10/700,322	10/18/2005	6,956,787	US
NMMS-0838.00/US	Utility - ORG	Issued		NON-VOLATILE MEMORY DEVICE WITH IMPROVED SEQUENTIAL PROGRAMMING SPEED	12/18/2003	10/739,928	9/6/2005	6,940,756	US
NMMS-0839.00/US	Utility - ORG	Issued		MEMORY DEVICE COMPOSED OF A PLURALITY OF MEMORY CHIPS IN A SINGLE PACKAGE	12/3/2003	10/727,150	9/27/2005	6,950,324	US
NMMS-0840.00/US	Utility - ORG	Issued		NONVOLATILE MEMORY DEVICE WITH SIMULTANEOUS READ/WRITE	11/21/2003	10/719,650	9/27/2005	6,950,337	US
NMMS-0842.00/US	Utility - ORG	Issued		NON VOLATILE MEMORY DEVICE INCLUDING A PREDETERMINED NUMBER OF SECTORS	12/30/2003	10/748,696	4/25/2006	7,035,142	US
NMMS-0843.00/US	Utility - ORG	Issued		PROGRAMMING METHOD FOR NON VOLATILE MULTILEVEL MEMORY CELLS AND CORRESPONDING PROGRAMMING CIRCUIT	11/26/2003	10/724,023	8/23/2005	6,934,185	US
NMMS-0844.00/US	Utility - ORG	Issued		STABILIZATION METHOD FOR DRAIN VOLTAGE IN NON-VOLATILE MULTI-LEVEL MEMORY CELLS AND RELATED MEMORY DEVICE	12/30/2003	10/748,701	4/18/2006	7,031,191	US
NMMS-0845.00/US	Utility - ORG	Issued		SEMICONDUCTOR MEMORY WITH ACCESS PROTECTION SCHEME	2/18/2004	10/781,974	7/24/2007	7,249,231	US
NMMS-0849.00/US	Utility - ORG	Issued		ANALYSIS OF THE QUALITY OF CONTACTS AND VIAS IN MULTI-METAL FABRICATION PROCESSES OF SEMICONDUCTOR DEVICES, METHOD AND TEST CHIP ARCHITECTURE	5/21/2004	10/850,834	12/4/2007	7,304,485	US
NMMS-0850.00/US	Utility - ORG	Issued		METHOD FOR MANUFACTURING NON-VOLATILE MEMORY CELLS ON A SEMICONDUCTIVE SUBSTRATE	12/29/2003	10/749,020	10/24/2006	7,125,808	US
NMMS-0851.00/US	Utility - ORG	Issued		METHOD FOR MANUFACTURING NON-VOLATILE MEMORY CELLS ON A SEMICONDUCTOR SUBSTRATE	12/23/2003	10/746,878	10/24/2006	7,125,807	US
NMMS-0853.00/US	Utility - ORG	Issued		METHOD FOR DETECTING A RESISTIVE PATH OR A PREDETERMINED POTENTIAL IN NON-VOLATILE MEMORY ELECTRONIC DEVICES	9/30/2003	10/675,805	9/20/2005	6,947,329	US
NMMS-0854.00/US	Utility - ORG	Issued		NON-VOLATILE MEMORY CELL SENSING CIRCUIT, PARTICULARLY FOR LOW POWER SUPPLY VOLTAGES AND HIGH CAPACITIVE LOAD VALUES	12/4/2003	10/728,372	5/17/2005	6,894,934	US
NMMS-0855.00/US	Utility - ORG	Issued		SEMICONDUCTOR MEMORY WITH EMBEDDED DRAM	11/20/2003	10/720,013	4/11/2006	7,027,317	US
NMMS-0857.00/US	Utility - ORG	Issued		REUNDANCY SCHEME FOR A MEMORY INTEGRATED CIRCUIT	7/16/2004	10/893,760	12/26/2006	7,154,803	US
NMMS-0858.00/US	Utility - ORG	Issued		FULL-SWING WORDLINE DRIVING CIRCUIT	4/29/2004	10/835,538	4/4/2006	7,023,738	US
NMMS-0859.00/US	Utility - ORG	Issued		METHOD FOR GENERATING A REFERENCE CURRENT FOR SENSE AMPLIFIERS AND CORRESPONDING GENERATOR	6/4/2004	10/861,340	2/28/2006	7,006,025	US
NMMS-0860.00/US	Utility - ORG	Issued		METHOD AND DEVICE FOR PROGRAMMING AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE SEMICONDUCTOR MEMORY	12/5/2003	10/729,829	6/27/2006	7,068,540	US
NMMS-0861.00/US	Utility - ORG	Issued		HIGH-DENSITY PLASMA PROCESS FOR DEPOSITING A LAYER OF SILICON NITRIDE	10/14/2003	10/686,556	10/11/2005	6,953,609	US
NMMS-0862.00/US	Utility - ORG	Issued		SENSE AMPLIFIER FOR LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY CELLS	2/12/2004	10/777,457	3/24/2009	7,508,716	US
NMMS-0862.01/US	Utility - CON	Issued		SENSE AMPLIFIER FOR LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY CELLS	2/9/2009	12/568,271	2/8/2011	7,885,116	US
NMMS-0863.00/US	Utility - ORG	Issued		VOLTAGE SUPPLY DISTRIBUTION ARCHITECTURE FOR A PLURALITY OF MEMORY MODULES	12/30/2003	10/748,697	5/24/2005	6,897,710	US
NMMS-0864.00/US	Utility - ORG	Issued		NONLITHOGRAPHIC METHOD OF DEFINING GEOMETRIES FOR PLASMA AND/OR ION IMPLANTATION TREATMENTS ON A SEMICONDUCTOR WAFER	3/25/2005	11/089,942	10/30/2007	7,288,008	US
NMMS-0865.00/US	Utility - ORG	Issued		PARALLEL SENSE AMPLIFIER WITH MIRRORING OF THE CURRENT TO BE MEASURED INTO EACH REFERENCE BRANCH	1/20/2004	10/762,195	7/11/2006	7,075,844	US
NMMS-0866.00/US	Utility - ORG	Issued		METHOD FOR THE FABRICATION OF ISOLATION STRUCTURES	8/21/2004	10/909,749	5/8/2007	7,214,596	US
NMMS-0869.00/US	Utility - ORG	Issued		MOS DEVICE AND PROCESS FOR MANUFACTURING MOS DEVICES USING DUAL-POLYSILICON LAYER TECHNOLOGY	12/23/2003	10/745,295	4/4/2006	7,023,047	US

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Exhibit A - US Issued Patents

MTI-Device	Material Type	Material Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMMS-0870.00/US	Utility - ORG	Issued	MOS DEVICE AND A PROCESS FOR MANUFACTURING MOS DEVICES USING A DUAL-POLYSILICON LAYER TECHNOLOGY WITH SIDE CONTACT	12/23/2003	10/745,297	8/15/2006	7,091,570	US
NMMS-0871.00/US	Utility - ORG	Issued	FAST READING, LOW CONSUMPTION MEMORY DEVICE AND READING METHOD THEREOF	12/20/2004	11/018,550	4/10/2007	7,203,087	US
NMMS-0872.00/US	Utility - ORG	Issued	METHOD FOR READING A NONVOLATILE MEMORY DEVICE AND NONVOLATILE MEMORY DEVICE IMPLEMENTING THE READING METHOD	4/8/2004	10/820,458	5/30/2006	7,054,197	US
NMMS-0873.00/US	Utility - ORG	Issued	TESTING METHOD AND DEVICE FOR NON-VOLATILE MEMORIES HAVING A LPC (LOW PIN COUNT) COMMUNICATION SERIAL INTERFACE	5/28/2003	10/447,293	8/31/2004	6,785,174	US
NMMS-0874.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR READING NON-VOLATILE MEMORIES HAVING AT LEAST ONE PSEUDO-PARALLEL COMMUNICATION INTERFACE	5/30/2003	10/452,762	12/13/2005	6,975,559	US
NMMS-0875.00/US	Utility - ORG	Issued	AUTOMATIC DECODING METHOD FOR MAPPING AND SELECTING A NON-VOLATILE MEMORY DEVICE HAVING A LPC SERIAL COMMUNICATION INTERFACE IN THE AVAILABLE ADDRESSING AREA ON MOTHERBOARDS	7/18/2003	10/623,474	6/12/2007	7,231,487	US
NMMS-0876.00/US	Utility - ORG	Issued	STRUCTURE FOR UPDATING A BLOCK OF MEMORY CELLS IN A FLASH MEMORY DEVICE WITH ERASE AND PROGRAM OPERATION REDUCTION	10/15/2003	10/686,552	7/26/2005	6,922,362	US
NMMS-0877.00/US	Utility - ORG	Issued	VOLTAGE REGULATION SYSTEM FOR A MULTIWORD PROGRAMMING OF A LOW INTEGRATION AREA NON VOLATILE MEMORY	2/26/2004	10/789,351	11/21/2006	7,139,197	US
NMMS-0878.00/US	Utility - ORG	Issued	GATE VOLTAGE REGULATION SYSTEM FOR A NON VOLATILE MEMORY CELLS PROGRAMMING AND/OR SOFT PROGRAMMING PHASE	2/26/2004	10/788,525	8/1/2006	7,085,163	US
NMMS-0879.00/US	Utility - ORG	Issued	METHOD FOR SOFT-PROGRAMMING AN ELECTRICALLY ERASABLE NONVOLATILE MEMORY DEVICE, AND AN ELECTRICALLY ERASABLE NONVOLATILE MEMORY DEVICE IMPLEMENTING THE SOFT-PROGRAMMING METHOD	2/17/2004	10/779,856	10/10/2006	7,120,062	US
NMMS-0880.00/US	Utility - ORG	Issued	MEMORY DEVICE ACCESSIBLE WITH DIFFERENT COMMUNICATION PROTOCOLS	9/2/2003	10/653,459	8/9/2005	6,927,991	US
NMMS-0881.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE ARCHITECTURE, FOR INSTANCE A FLASH KIND, HAVING A SERIAL COMMUNICATION INTERFACE	11/26/2003	10/727,341	12/19/2006	7,151,705	US
NMMS-0882.00/US	Utility - ORG	Issued	FAST PAGE PROGRAMMING ARCHITECTURE AND METHOD IN A NON-VOLATILE MEMORY DEVICE WITH AN SPI INTERFACE	12/30/2003	10/748,447	4/26/2005	6,885,584	US
NMMS-0883.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY COMPRISING AN INTERNAL SUPPLY VOLTAGE MANAGEMENT DEVICE	4/22/2003	10/420,533	12/7/2004	6,829,169	US
NMMS-0884.00/US	Utility - ORG	Issued	PROGRAMMABLE FOR CIRCUIT WITH TWO SWITCHING THRESHOLDS	8/14/2003	10/641,337	5/24/2005	6,897,689	US
NMMS-0885.00/US	Utility - NSPCT	Issued	EEPROM MEMORY COMPRISING A NON-VOLATILE REGISTER INTEGRATED INTO THE MEMORY ARRAY THEREOF	8/21/2003	11/067,345	11/21/2006	7,139,195	US
NMMS-0886.00/US	Utility - ORG	Issued	DOUBLE READ STAGE SENSE AMPLIFIER	4/1/2004	10/816,204	4/18/2006	7,031,212	US
NMMS-0887.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR A FLASH MEMORY AND FLASH MEMORY THUS PRODUCED	1/22/2004	10/763,044	2/27/2007	7,183,160	US
NMMS-0888.00/US	Utility - ORG	Issued	FLASH MEMORY COMPRISING AN ERASE VERIFY ALGORITHM INTEGRATED INTO A PROGRAMMING ALGORITHM	2/26/2004	10/789,449	5/10/2005	6,891,756	US
NMMS-0889.00/US	Utility - ORG	Issued	NEGATIVE VOLTAGE WORD LINE DECODER HAVING COMPACT TERMINATING ELEMENTS	1/20/2004	10/760,631	5/16/2006	7,046,577	US
NMMS-0890.00/US	Utility - ORG	Issued	SECTORED FLASH MEMORY COMPRISING MEANS FOR CONTROLLING AND FOR REFRESHING MEMORY CELLS	2/9/2004	10/775,032	11/15/2005	6,965,526	US
NMMS-0891.00/US	Utility - ORG	Issued	METHOD FOR ERASING/PROGRAMMING A NON-VOLATILE ELECTRICALLY ERASABLE MEMORY	7/31/2004	10/903,927	3/14/2006	7,012,837	US
NMMS-0892.00/US	Utility - ORG	Issued	WORD-PROGRAMMABLE FLASH MEMORY	6/14/2004	10/867,381	7/18/2006	7,079,448	US
NMMS-0893.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING DIFFERENTIAL ISOLATION STRUCTURES IN A SEMICONDUCTOR ELECTRONIC DEVICE AND CORRESPONDING STRUCTURE	7/12/2004	10/890,529	10/26/2010	7,820,504	US
NMMS-0894.00/US	Utility - ORG	Issued	CHARGE PUMP CIRCUIT WITH A BRIEF SETTLING TIME AND HIGH OUTPUT VOLTAGE REGULATION PRECISION	11/5/2004	10/982,528	2/13/2007	7,176,748	US

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REEL: 029186 FRAME: 0879

Exhibit A - US Issued Patents

MTF-DeckNo	MaterType	MaterStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMMS-0895.00/US	Utility - ORG	Issued	METHOD FOR REDUCING NON-UNIFORMITY OR TOPOGRAPHY VARIATION BETWEEN AN ARRAY AND CIRCUITRY IN A PROCESS FOR MANUFACTURING SEMICONDUCTOR INTEGRATED NON-VOLATILE MEMORY DEVICES	2/16/2005	11/059,294	3/20/2007	7,192,820	US
NMMS-0896.00/US	Utility - ORG	Issued	EXPLOITING A STATISTICAL DISTRIBUTION OF THE VALUES OF AN ELECTRICAL CHARACTERISTIC IN A POPULATION OF AUXILIARY MEMORY CELLS FOR OBTAINING REFERENCE CELLS	12/23/2005	11/318,053	12/8/2009	7,630,263	US
NMMS-0897.00/US	Utility - ORG	Issued	SENSE AMPLIFIER WITH EQUALIZER	8/6/2004	10/913,788	11/14/2006	7,136,305	US
NMMS-0898.00/US	Utility - ORG	Issued	METHOD FOR REDUCING DEFECTS AFTER A METAL ETCHING IN SEMICONDUCTOR DEVICES	12/10/2004	11/009,687	10/30/2007	7,288,427	US
NMMS-0899.00/US	Utility - ORG	Issued	METHOD FOR PERFORMING ERROR CORRECTIONS OF DIGITAL INFORMATION CODED AS A SYMBOL SEQUENCE	3/19/2004	10/805,168	2/5/2008	7,328,397	US
NMMS-0900.00/US	Utility - ORG	Issued	INTEGRATED MEMORY SYSTEM	3/19/2004	10/805,182	6/1/2010	7,730,357	US
NMMS-0901.00/US	Utility - ORG	Issued	PHASE CHANGE MEMORY DEVICE WITH BIASING OF Deselected BIT LINES	8/25/2004	10/926,784	8/15/2006	7,092,277	US
NMMS-0902.00/US	Utility - ORG	Issued	MANUFACTURING METHOD FOR NON-ACTIVE ELECTRICALLY STRUCTURES IN ORDER TO OPTIMIZE THE DEFINITION OF ACTIVE ELECTRICALLY STRUCTURES IN AN ELECTRONIC CIRCUIT INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT	8/3/2004	10/911,220	2/23/2006	7,001,800	US
NMMS-0902.01/US	Utility - DIV	Issued	MANUFACTURING METHOD FOR NON-ACTIVE ELECTRICALLY STRUCTURES IN ORDER TO OPTIMIZE THE DEFINITION OF ACTIVE ELECTRICALLY STRUCTURES IN AN ELECTRONIC CIRCUIT INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT	1/19/2006	11/334,988	1/22/2008	7,320,904	US
NMMS-0903.00/US	Utility - ORG	Issued	SEALING METHOD FOR ELECTRONIC DEVICES FORMED ON A COMMON SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT STRUCTURE	10/22/2004	10/971,774	7/18/2006	7,078,294	US
NMMS-0904.00/US	Utility - ORG	Issued	SENSING CIRCUIT FOR A SEMICONDUCTOR MEMORY	8/6/2004	10/913,128	9/18/2007	7,272,059	US
NMMS-0905.00/US	Utility - ORG	Issued	BIT LINE DISCHARGE CONTROL METHOD AND CIRCUIT FOR A SEMICONDUCTOR MEMORY	10/22/2004	10/971,776	8/14/2007	7,257,039	US
NMMS-0906.00/US	Utility - ORG	Issued	SENSING CIRCUIT FOR A SEMICONDUCTOR MEMORY	8/1/2005	11/194,739	2/27/2007	7,184,348	US
NMMS-0908.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING NON-VOLATILE MEMORY DEVICES INTEGRATED IN A SEMICONDUCTOR SUBSTRATE	12/27/2005	11/319,798	9/2/2008	7,419,876	US
NMMS-0910.00/US	Utility - ORG	Issued	DISTRIBUTION OF AN ELECTRIC QUANTITY THROUGH A CIRCUIT	7/20/2005	11/185,905	4/21/2009	7,521,989	US
NMMS-0912.00/US	Utility - ORG	Issued	PAGE BUFFER CIRCUIT AND METHOD FOR A PROGRAMMABLE MEMORY DEVICE	6/24/2005	11/166,354	7/28/2009	7,567,456	US
NMMS-0913.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH INFORMATION LOSS SELF-DETECT CAPABILITY	5/1/2006	11/415,879	8/19/2008	7,414,902	US
NMMS-0914.00/US	Utility - ORG	Issued	NONVOLATILE PHASE CHANGE MEMORY DEVICE AND BIASING METHOD THEREFOR	8/2/2005	11/195,359	9/11/2007	7,269,080	US
NMMS-0916.00/US	Utility - ORG	Issued	PHASE CHANGE MEMORY CELL WITH JUNCTION SELECTOR AND MANUFACTURING METHOD THEREOF	12/19/2005	11/312,253	10/14/2008	7,436,692	US
NMMS-0917.00/US	Utility - ORG	Issued	READ/VERIFY CIRCUIT FOR MULTILEVEL MEMORY CELLS WITH RAMP READ VOLTAGE, AND READ/VERIFY METHOD THEREOF	7/8/2005	11/178,240	7/8/2008	7,397,702	US
NMMS-0919.00/US	Utility - ORG	Issued	METHOD OF MAKING A FLOATING GATE NON-VOLATILE MOS SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CAPACITIVE COUPLING AND DEVICE THUS OBTAINED	12/22/2005	11/317,641	8/30/2011	8,008,701	US
NMMS-0920.00/US	Utility - ORG	Issued	PROGRAMMABLE NAND MEMORY	7/14/2005	11/183,229	6/8/2010	7,733,697	US
NMMS-0921.00/US	Utility - ORG	Issued	DATA CONTROL UNIT CAPABLE OF CORRECTING BOOT ERRORS, AND CORRESPONDING SELF-CORRECTION METHOD	6/9/2005	11/149,948	10/28/2008	7,444,543	US
NMMS-0923.00/US	Utility - ORG	Issued	TRIMMING FUNCTIONAL PARAMETERS IN INTEGRATED CIRCUITS	4/25/2005	11/113,818	5/22/2007	7,221,212	US
NMMS-0924.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC NON-VOLATILE MEMORY DEVICES INTEGRATED IN A SEMICONDUCTOR SUBSTRATE	12/27/2005	11/319,750	2/5/2008	7,326,615	US

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Exhibit A - US Issued Patents

NMKS-0926.00/US	Utility - ORG	Issued	FLASH MEMORY DEVICE WITH NAND ARCHITECTURE WITH REDUCED CAPACITIVE COUPLING EFFECT	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMKS-0928.00/US	Utility - ORG	Issued	INTEGRATED ELECTRONIC DEVICE HAVING A LOW VOLTAGE ELECTRIC SUPPLY	4/11/2006	11/445,491	7/1/2008	7,394,694	US
NMKS-0929.00/US	Utility - ORG	Issued	REGULATOR CIRCUITRY AND METHOD	10/22/2003	10/695,294	6/20/2006	7,064,534	US
NMKS-0930.00/US	Utility - ORG	Issued	MAGNITUDE CONTENT ADDRESSABLE MEMORY	10/21/2003	10/690,368	6/12/2007	7,230,839	US
NMKS-0931.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR WITH STRESS MODE	10/21/2003	10/690,332	3/14/2006	7,012,417	US
NMKS-0932.00/US	Utility - ORG	Issued	USER RAM FLASH CLEAR	2/27/2004	10/786,581	2/6/2007	7,173,845	US
NMKS-0933.00/US	Utility - ORG	Issued	RESET INITIALIZATION	11/30/2004	10/999,751	4/24/2007	7,208,987	US
NMKS-0934.00/US	Utility - ORG	Issued	TAMPER MEMORY CELL	2/20/2004	10/783,935	5/29/2007	7,224,600	US
NMKS-0935.00/US	Utility - ORG	Issued	RESET RAMP CONTROL	12/15/2004	11/012,693	8/12/2008	7,411,433	US
NMKS-0936.00/US	Utility - ORG	Issued	ESD BONDING PAD	12/15/2004	11/013,123	2/19/2008	7,333,310	US
NMKS-0937.00/US	Utility - ORG	Issued	VOLTAGE TRANSLATING CONTROL STRUCTURE	12/15/2004	11/012,533	5/6/2008	7,368,947	US
NMKS-0938.00/US	Utility - ORG	Issued	RESET CIRCUIT	12/15/2004	11/012,712	10/2/2007	7,276,948	US
NMKS-0939.00/US	Utility - ORG	Abandoned	SENSE AMPLIFIER FOR READING A CELL OF A NON-VOLATILE MEMORY DEVICE	5/4/2005	11/121,616	1/23/2007	7,167,394	US
NMKS-0940.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR VERIFYING AND EVENTUALLY SUBSTITUTING DEFECTIVE REFERENCE CELLS OF A MEMORY	5/4/2005	11/121,615	2/13/2007	7,177,217	US
NMKS-0941.00/US	Utility - ORG	Issued	CIRCUIT FOR SELECTING/DESELECTING A BITLINE OF A NON-VOLATILE MEMORY	5/3/2005	11/120,766	8/7/2007	7,254,062	US
NMKS-0943.01/US	Utility - CON	Issued	METHOD FOR MANAGING BAD MEMORY BLOCKS IN A NONVOLATILE-MEMORY DEVICE AND NONVOLATILE-MEMORY DEVICE IMPLEMENTING THE MANAGEMENT METHOD	2/19/2008	12/033,855	10/13/2009	7,603,593	US
NMKS-0944.00/US	Utility - ORG	Issued	ROW DECODER FOR NAND MEMORIES	8/11/2005	11/702,632	2/20/2007	7,180,786	US
NMKS-0945.00/US	Utility - ORG	Issued	METHOD OF GENERATING AN ENABLE SIGNAL OF A STANDARD MEMORY CORE AND RELATIVE MEMORY DEVICE	7/7/2004	10/886,003	4/14/2009	7,519,751	US
NMKS-0946.00/US	Utility - ORG	Issued	INTEGRATED MEMORY DEVICE WITH MULTI-SECTOR SELECTION COMMANDS	11/8/2004	10/984,372	11/25/2008	7,457,908	US
NMKS-0947.00/US	Utility - ORG	Issued	SYNCHRONOUS MEMORY DEVICE WITH REDUCED POWER CONSUMPTION	5/25/2005	11/138,884	4/29/2008	7,366,012	US
NMKS-0948.00/US	Utility - ORG	Issued	FLASH MEMORY COMPRISING MEANS FOR CHECKING AND REFRESHING MEMORY CELLS IN THE ERASED STATE	6/14/2004	10/867,378	5/13/2006	7,050,335	US
NMKS-0949.00/US	Utility - ORG	Issued	MEMORY CIRCUIT WITH NON-VOLATILE IDENTIFICATION MEMORY AND ASSOCIATED METHOD	8/18/2004	10/921,365	6/27/2006	7,068,538	US
NMKS-0950.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR A CONTINUOUS READ COMMAND IN AN EXTENDED MEMORY ARRAY	12/9/2004	11/008,586	2/12/2008	7,330,381	US
NMKS-0952.00/US	Utility - ORG	Issued	SERIAL MEMORY COMPRISING MEANS FOR PROTECTING AN EXTENDED MEMORY ARRAY DURING A WRITE OPERATION	12/9/2004	11/008,588	10/30/2007	7,290,078	US
NMKS-0952.01/US	Utility - DIV	Issued	SERIAL MEMORY COMPRISING MEANS FOR PROTECTING AN EXTENDED MEMORY ARRAY DURING A WRITE OPERATION	9/10/2007	11/852,937	9/7/2010	7,793,033	US
NMKS-0953.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH UNIPOLAR AND BIPOLAR SELECTORS	9/22/2005	11/733,464	1/27/2009	7,483,296	US
NMKS-0954.00/US	Utility - ORG	Issued	ELECTRONIC NON-VOLATILE MEMORY DEVICE HAVING A NAND STRUCTURE AND BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/401,521	3/9/2010	7,675,788	US
NMKS-0957.00/US	Utility - ORG	Issued	MEMORY DEVICE	10/13/2005	11/249,763	3/27/2007	7,196,943	US
NMKS-0958.00/US	Utility - ORG	Issued	CHARGE-PUMP DEVICE WITH INCREASED CURRENT OUTPUT	11/9/2005	11/770,308	5/12/2009	7,532,060	US
NMKS-0959.00/US	Utility - ORG	Issued	INTEGRATED ELECTRONIC NON-VOLATILE MEMORY DEVICE HAVING NAND STRUCTURE	4/11/2006	11/279,384	11/13/2007	7,295,472	US
NMKS-0961.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/279,378	9/25/2007	7,274,594	US
NMKS-0963.00/US	Utility - ORG	Issued	PROGRAMMING METHOD OF MULTILEVEL MEMORIES AND CORRESPONDING CIRCUIT	10/27/2005	11/261,903	1/8/2008	7,317,637	US
NMKS-0964.00/US	Utility - ORG	Issued	MEMORY DEVICE	10/13/2005	11/250,176	4/1/2008	7,352,645	US
NMKS-0965.00/US	Utility - ORG	Issued	MEMORY DEVICE AND METHOD FOR OPERATING THE SAME WITH HIGH REJECTION OF THE NOISE ON THE HIGH-VOLTAGE SUPPLY LINE	9/30/2005	11/241,729	1/29/2008	7,324,379	US

PATENT

REEL: 029186 FRAME: 0881

Exhibit A - US Issued Patents

INT-Defacto	MadeType	MadeStatus	Title	FiledDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0966.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY DEVICE HAVING HIGH DENSITY NON-VOLATILE MEMORY CELLS AND A REDUCED CAPACITIVE INTERFERENCE CELL-TO-CELL	12/14/2005	11/300,053	1/15/2008	7,319,604	US
NMXS-0968.00/US	Utility - ORG	Issued	ROW DECODER CIRCUIT AND RELATED SYSTEM AND METHOD	5/15/2006	11/434,564	4/28/2009	7,525,851	US
NMXS-0969.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/279,381	7/8/2008	7,397,700	US
NMXS-0971.00/US	Utility - ORG	Issued	FLASH MEMORY DEVICE WITH REDUCED DRAIN STRESSES	2/6/2007	11/703,535	5/19/2009	7,535,770	US
NMXS-0972.00/US	Utility - ORG	Issued	REMOVABLE DATA STORAGE DEVICE AND RELATED ASSEMBLING METHOD	1/31/2006	11/344,519	2/2/2010	7,656,676	US
NMXS-0974.00/US	Utility - ORG	Issued	METHOD FOR CONFIGURING A VOLTAGE REGULATOR	11/16/2005	11/280,803	6/17/2008	7,388,793	US
NMXS-0974.01/US	Utility - DIV	Issued	VOLTAGE REGULATOR FOR THE PROGRAMMING CIRCUIT OF A MEMORY CELL	12/26/2007	11/964,250	4/27/2010	7,706,193	US
NMXS-0975.00/US	Utility - ORG	Issued	PROCESS FOR DIGGING A DEEP TRENCH IN A SEMICONDUCTOR BODY AND SEMICONDUCTOR BODY SO OBTAINED	12/28/2006	11/648,838	6/9/2009	7,544,620	US
NMXS-0976.00/US	Utility - ORG	Issued	METHOD AND SYSTEM FOR CONTROLLING MRAM WRITE CURRENT TO REDUCE POWER CONSUMPTION	3/31/2004	10/816,271	9/19/2006	7,110,289	US
NMXS-0977.00/US	Utility - ORG	Issued	MAGNETIC MEMORY CELL WITH PLURAL READ TRANSISTORS	10/31/2003	10/698,747	3/14/2006	7,012,832	US
NMXS-0978.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING A MEMORY DEVICE SUITABLE TO MINIMIZE THE LATERAL COUPLING EFFECTS BETWEEN MEMORY CELLS	2/6/2006	11/348,513	12/30/2008	7,473,571	US
NMXS-0979.00/US	Utility - ORG	Issued	DUAL RESISTANCE HEATER FOR PHASE CHANGE DEVICES AND MANUFACTURING METHOD THEREOF	12/19/2005	11/312,231	2/1/2011	7,880,123	US
NMXS-0983.00/US	Utility - ORG	Issued	HIGH-VOLTAGE SWITCH WITH LOW OUTPUT RIPPLE FOR NON-VOLATILE FLOATING-GATE MEMORIES	5/19/2006	11/437,405	4/21/2009	7,521,983	US
NMXS-0984.00/US	Utility - ORG	Issued	CHARGE-PUMP TYPE VOLTAGE-BOOSTING DEVICE WITH REDUCED RIPPLE, IN PARTICULAR FOR NON-VOLATILE FLASH MEMORIES	5/19/2006	11/437,268	5/12/2009	7,532,061	US
NMXS-0985.00/US	Utility - ORG	Issued	PHASE CHANGE MEMORY CELL WITH TUBULAR HEATER AND MANUFACTURING METHOD THEREOF	4/6/2006	11/398,858	10/21/2008	7,439,536	US
NMXS-0986.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY DEVICE HAVING HIGH INTEGRATION DENSITY NON-VOLATILE MEMORY CELLS AND A REDUCED CAPACITIVE COUPLING	12/14/2005	11/300,145	9/22/2009	7,593,247	US
NMXS-0987.00/US	Utility - ORG	Issued	METHOD FOR ACCESSING IN READING, WRITING AND PROGRAMMING TO A NAND NON-VOLATILE MEMORY ELECTRONIC DEVICE MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	11/20/2006	11/561,799	5/5/2009	7,529,881	US
NMXS-0987.01/US	Utility - DIV	Issued	METHOD FOR ACCESSING IN READING, WRITING AND PROGRAMMING TO A NAND NON-VOLATILE MEMORY ELECTRONIC DEVICE MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	3/24/2009	12/409,740	1/19/2010	7,649,778	US
NMXS-0988.00/US	Utility - ORG	Issued	VERTICAL MOSFET TRANSISTOR, IN PARTICULAR OPERATING AS A SELECTOR IN NONVOLATILE MEMORY DEVICES	4/26/2006	11/411,982	12/7/2010	7,847,329	US
NMXS-0988.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY IMPLEMENTED WITH LOW-VOLTAGES TRANSISTORS AND RELATED SYSTEM AND METHOD	11/27/2006	11/505,209	3/3/2009	7,499,345	US
NMXS-0990.00/US	Utility - ORG	Issued	CONTROL OF VOLTAGES DURING ERASE AND RE-PROGRAM OPERATIONS OF MEMORY CELLS	1/18/2006	11/334,205	10/30/2007	7,289,368	US
NMXS-0991.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH A RAMP-LIKE VOLTAGE BIASING STRUCTURE BASED ON A CURRENT GENERATOR	1/26/2006	11/340,414	4/15/2008	7,359,246	US
NMXS-0992.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR RETRIEVING DATA STORED IN SEMICONDUCTOR MEMORY CELLS	5/31/2006	11/444,892	1/6/2009	7,474,577	US
NMXS-0992.01/US	Utility - DIV	Issued	CIRCUIT AND METHOD FOR RETRIEVING DATA STORED IN SEMICONDUCTOR MEMORY CELLS	10/9/2008	12/748,843	2/1/2011	7,889,586	US
NMXS-0993.00/US	Utility - ORG	Issued	DATA STORING METHOD FOR A NON-VOLATILE MEMORY CELL ARRAY HAVING AN ERROR CORRECTION CODE	4/25/2006	11/411,010	1/26/2010	7,653,863	US
NMXS-0995.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH A RAMP-LIKE VOLTAGE BIASING STRUCTURE AND REDUCED NUMBER OF REFERENCE CELLS	3/3/2006	11/368,363	6/30/2009	7,554,861	US
NMXS-0996.00/US	Utility - ORG	Issued	METHOD OF MANAGING FAILS IN A NON-VOLATILE MEMORY DEVICE AND RELATIVE MEMORY DEVICE	11/8/2006	11/557,786	8/4/2009	7,571,362	US

PATENT

REEL: 029186 FRAME: 0882

Exhibit A - US Issued Patents

Pat. No.	Met. Type	Met. Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMMS-0997.00/US	Utility - NSPT	Issued	METHOD FOR MULTILEVEL PROGRAMMING OF PHASE CHANGE MEMORY CELLS USING A PERCOLATION ALGORITHM	6/1/2006	11/949,598	12/29/2009	7,639,526	US
NMMS-0998.00/US	Utility - ORG	Issued	FLASH MEMORY DEVICE WITH IMPROVED MANAGEMENT OF PROTECTION INFORMATION	5/10/2006	11/432,301	7/20/2010	7,761,675	US
NMMS-0999.00/US	Utility - ORG	Issued	NAND FLASH MEMORY WITH ERASE VERIFY BASED ON SHORTER EVALUATION TIME	7/28/2006	11/495,886	4/22/2008	7,362,616	US
NMMS-1000.00/US	Utility - ORG	Issued	TRANSISTOR STRUCTURE WITH HIGH INPUT IMPEDANCE AND HIGH CURRENT CAPABILITY	11/27/2006	11/605,190	7/14/2009	7,560,782	US
NMMS-1001.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH TIME-SHIFTING BASED EMULATION OF REFERENCE CELLS	3/2/2006	11/367,707	3/18/2008	7,345,905	US
NMMS-1002.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING A MEMORY DEVICE SUITABLE TO MINIMIZE FLOATING GATE COUPLING AND MEMORY DEVICE	4/2/2007	11/732,486	3/30/2010	7,688,633	US
NMMS-1003.00/US	Utility - ORG	Issued	NON VOLATILE MEMORY DEVICE ARCHITECTURE AND CORRESPONDING PROGRAMMING METHOD	2/28/2007	11/713,081	1/19/2010	7,649,791	US
NMMS-1004.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH MULTIPLE REFERENCES AND CORRESPONDING CONTROL METHOD	7/27/2006	11/460,531	1/12/2010	7,646,644	US
NMMS-1006.00/US	Utility - ORG	Issued	RAMP GENERATOR AND RELATIVE ROW DECODER FOR FLASH MEMORY DEVICE	5/3/2006	11/381,426	1/22/2008	7,321,512	US
NMMS-1007.00/US	Utility - ORG	Issued	ROW SELECTOR WITH REDUCED AREA OCCUPATION FOR SEMICONDUCTOR MEMORY DEVICES	2/8/2007	11/672,857	6/1/2011	7,965,561	US
NMMS-1008.00/US	Utility - ORG	Issued	BUILT-IN SELF TEST FOR A COUNTER SYSTEM	3/10/2005	11/076,785	12/13/2005	6,975,696	US
NMMS-1009.00/US	Utility - ORG	Issued	READING CIRCUIT AND METHOD FOR A NONVOLATILE MEMORY DEVICE	9/28/2005	11/238,137	7/10/2007	7,242,619	US
NMMS-1009.01/US	Utility - CON	Issued	READING CIRCUIT AND METHOD FOR A NONVOLATILE MEMORY DEVICE	6/8/2007	11/811,394	11/11/2008	7,450,428	US
NMMS-1010.00/US	Utility - ORG	Issued	SELF-ADAPTIVE OUTPUT BUFFER BASED ON CHARGE SHARING	7/7/2006	11/482,524	9/8/2009	7,586,331	US
NMMS-1011.00/US	Utility - ORG	Issued	CIRCUIT FOR GENERATING AN INTERNAL ENABLING SIGNAL FOR AN OUTPUT BUFFER OF A MEMORY	1/20/2006	11/337,030	5/19/2009	7,535,774	US
NMMS-1014.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR SIMULTANEOUSLY PROGRAMMING MEMORY CELLS	4/13/2006	11/279,663	12/4/2007	7,304,896	US
NMMS-1016.00/US	Utility - ORG	Issued	SEQUENTIAL PROGRAM-VERIFY METHOD WITH RESULT BUFFERING	3/29/2005	11/093,012	2/27/2007	7,184,310	US
NMMS-1017.00/US	Utility - ORG	Issued	OPTIMIZING WRITE/ERASE OPERATIONS IN MEMORY DEVICES	1/13/2005	11/035,013	4/22/2008	7,363,421	US
NMMS-1018.00/US	Utility - ORG	Issued	CONTROLLING OPERATION OF FLASH MEMORIES	1/14/2005	11/036,179	11/25/2008	7,457,909	US
NMMS-1026.00/US	Utility - ORG	Issued	MEMORY ARCHITECTURE	9/14/2006	11/469,754	7/28/2009	7,567,475	US
NMMS-1033.00/US	Utility - ORG	Issued	MEMORY ARCHITECTURE WITH SERIAL PERIPHERAL INTERFACE	9/8/2006	11/530,199	9/7/2010	7,793,031	US
NMMS-1038.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR OR NON-VOLATILE MEMORIES IMPLEMENTED WITH LOW-VOLTAGE TRANSISTORS	8/24/2007	11/844,470	8/17/2010	7,777,466	US
NMMS-1040.00/US	Utility - ORG	Issued	READING CIRCUIT FOR SEMICONDUCTOR MEMORY	4/27/2007	11/740,941	3/24/2009	7,508,717	US
NMMS-1043.00/US	Utility - ORG	Issued	SENSING CIRCUIT FOR SEMICONDUCTOR MEMORIES	4/24/2007	11/739,167	4/7/2009	7,515,493	US
NMMS-1044.00/US	Utility - ORG	Issued	ELECTRONIC DEVICE CONTAINING SEMICONDUCTOR POLYMERS AND CORRESPONDING MANUFACTURING PROCESS	1/30/2007	11/700,541	10/18/2011	8,039,831	US
NMMS-1046.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH A PAGE BUFFER HAVING AN IMPROVED LAYOUT ARRANGEMENT	7/25/2006	11/459,851	8/5/2008	7,408,819	US
NMMS-1047.00/US	Utility - ORG	Issued	REDUCTION OF THE TIME FOR EXECUTING AN EXTERNALLY COMMANDED TRANSFER OF DATA IN AN INTEGRATED DEVICE	3/16/2007	11/687,353	7/28/2009	7,567,107	US
NMMS-1048.00/US	Utility - ORG	Issued	SYNCHRONIZATION OF OPERATIONS IN DISTINCT MEMORY PARTITIONS	3/14/2007	11/686,133	4/7/2009	7,515,464	US
NMMS-1050.00/US	Utility - ORG	Issued	OPTIMIZED FLASH MEMORY ACCESS METHOD AND DEVICE	4/13/2007	11/787,101	9/6/2011	8,015,945	US
NMMS-1052.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR PROGRAMMING A MEMORY CELL, IN PARTICULAR OF THE NOR FLASH TYPE	4/16/2008	12/104,118	2/2/2010	7,656,712	US
NMMS-1053.00/US	Utility - ORG	Issued	HIGH VOLTAGE GENERATOR OF THE DAC-CONTROLLED TYPE	8/8/2006	11/463,260	4/27/2010	7,706,160	US

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Exhibit A - US Issued Patents

MTI-Device	Matter-Type	Matter-Status	Title	Filedate	SerialNo	IssueDate	PatentNo	Country
NMMS-1054.00/US	Utility - ORG	Issued	READING METHOD OF A MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE AND MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE	3/1/2007	11/713,376	3/15/2011	7,908,543	US
NMMS-1055.00/US	Utility - ORG	Issued	FAST PROGRAMMING MEMORY DEVICE	5/19/2008	12/123,359	9/13/2011	8,018,771	US
NMMS-1056.00/US	Utility - ORG	Issued	OUTPUT BUFFER	12/13/2006	11/538,321	12/9/2008	7,463,051	US
NMMS-1056.01/US	Utility - DIV	Issued	OUTPUT BUFFER	10/30/2008	12/261,101	9/28/2010	7,804,322	US
NMMS-1062.00/US	Utility - ORG	Issued	REFERENCE CELL LAYOUT WITH ENHANCED RTN IMMUNITY	4/27/2007	11/741,462	6/23/2009	7,551,465	US
NMMS-1066.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE	10/12/2006	11/549,067	1/27/2009	7,483,300	US
NMMS-1067.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING OF MEMORY CELLS, IN PARTICULAR OF THE FLASH TYPE, AND CORRESPONDING PROGRAMMING ARCHITECTURE	12/8/2006	11/536,382	10/20/2009	7,606,078	US
NMMS-1067.01/US	Utility - DIV	Issued	METHOD FOR PROGRAMMING OF MEMORY CELLS, IN PARTICULAR OF THE FLASH TYPE, AND CORRESPONDING PROGRAMMING ARCHITECTURE	9/20/2009	12/556,970	5/17/2011	7,944,751	US
NMMS-1068.00/US	Utility - ORG	Issued	AUTOMATIC REGULATION METHOD FOR THE REFERENCE SOURCES IN A NON-VOLATILE MEMORY DEVICE AND CORRESPONDING MEMORY DEVICE	6/29/2007	11/771,677	7/28/2009	7,567,470	US
NMMS-1070.00/US	Utility - ORG	Issued	METHOD FOR ACCESSING A MULTILEVEL NONVOLATILE MEMORY DEVICE OF THE FLASH NAND TYPE	7/20/2006	11/458,904	6/3/2008	7,382,660	US
NMMS-1071.00/US	Utility - ORG	Issued	CONFIGURATION OF A MULTILEVEL FLASH MEMORY DEVICE	7/28/2006	11/460,777	5/3/2011	7,937,576	US
NMMS-1075.00/US	Utility - ORG	Issued	ELECTRONIC DEVICE COMPRISING NON VOLATILE MEMORY CELLS WITH OPTIMIZED PROGRAMMING AND CORRESPONDING PROGRAMMING METHOD	2/28/2007	11/713,074	3/31/2009	7,512,032	US
NMMS-1079.00/US	Utility - CIP	Issued	SEALING METHOD FOR ELECTRONIC DEVICES FORMED ON A COMMON SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT STRUCTURE	7/17/2006	11/457,966	8/12/2008	7,410,872	US
NMMS-1084.00/US	Utility - ORG	Issued	METHOD FOR COMPACTING THE ERASED THRESHOLD VOLTAGE DISTRIBUTION OF FLASH MEMORY DEVICES DURING WRITING OPERATIONS	8/24/2007	11/844,480	5/5/2009	7,529,136	US
NMMS-1085.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR ELECTRICALLY PROGRAMMING A NON-VOLATILE	6/21/2007	11/766,493	3/3/2009	7,499,332	US
NMMS-1089.00/US	Utility - ORG	Issued	METHOD OF MANAGING A MULTILEVEL MEMORY DEVICE AND RELATED DEVICE	4/25/2008	12/109,525	5/4/2010	7,710,772	US
NMMS-1090.00/US	Utility - ORG	Issued	METHOD OF MANAGING A MEMORY DEVICE EMPLOYING THREE-LEVEL CELLS	2/28/2008	12/039,268	8/24/2010	7,782,665	US
NMMS-1091.00/US	Utility - ORG	Issued	MEMORY DEVICE EMPLOYING THREE-LEVEL CELLS AND RELATED METHODS OF MANAGING	11/2/2007	11/934,144	9/29/2009	7,596,023	US
NMMS-1094.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH FAIL SEARCH AND REDUNDANCY	7/20/2007	11/780,581	1/12/2010	7,546,655	US
NMMS-1094.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE AND METHOD OF HANDLING A DATUM READ FROM A MEMORY CELL	8/3/2007	11/833,400	5/12/2009	7,532,512	US
NMMS-1095.00/US	Utility - ORG	Issued	ADDRESS COUNTER FOR NONVOLATILE MEMORY DEVICE	7/27/2007	11/829,527	7/7/2009	7,558,152	US
NMMS-1104.01/US	Utility - CON	Expired	REDUCED-BREAK PLANOX PROCESS FOR THE FORMATION OF INTEGRATED ELECTRONIC COMPONENTS	11/23/1987	07/124,440	1/30/1990	4,897,365	US
NMMS-1105.01/US	Utility - DIV	Expired	A METHOD FOR MANUFACTURING MOS/CMOS MONOLITHIC INTEGRATED CIRCUITS INCLUDING SILICIDE AND POLYSILICON PATTERNING	12/15/1988	07/289,391	11/6/1990	4,968,645	US
NMMS-1106.00/US	Utility - ORG	Expired	ELECTRICALLY ALTERABLE, NONVOLATILE FLOATING GATE MEMORY DEVICE	5/27/1987	07/054,712	4/18/1989	4,823,175	US
NMMS-1107.00/US	Utility - ORG	Expired	NONVOLATILE, SEMICONDUCTOR MEMORY DEVICE	6/22/1987	07/064,480	3/28/1989	4,816,883	US
NMMS-1108.00/US	Utility - ORG	Expired	GENERATOR OF RESET PULSES UPON THE RISE OF THE POWER SUPPLY FOR CMOS-TYPE INTEGRATED CIRCUITS	4/28/1988	07/187,581	12/19/1989	4,888,497	US
NMMS-1109.00/US	Utility - ORG	Expired	PROCESS FOR MANUFACTURING CMOS INTEGRATED DEVICES WITH REDUCED GATE LENGTHS	12/14/1988	07/284,272	11/6/1990	4,968,639	US
NMMS-1110.00/US	Utility - ORG	Expired	CIRCUIT FOR SENSING THE STATE OF MATRIX CELLS IN MOS EPROM MEMORIES INCLUDING AN OFFSET CURRENT GENERATOR	1/18/1989	07/298,487	8/14/1990	4,949,307	US
NMMS-1111.00/US	Utility - ORG	Expired	CMOS VOLTAGE MULTIPLIER	6/28/1989	07/372,493	5/1/1990	4,922,402	US
NMMS-1112.00/US	Utility - ORG	Expired	CMOS LOGIC CIRCUIT FOR HIGH VOLTAGE OPERATION	6/30/1989	07/373,203	9/11/1990	4,956,569	US
NMMS-1113.00/US	Utility - ORG	Expired	FABRICATION OF CMOS DEVICES WITH REDUCED GATE LENGTH	7/18/1989	07/381,283	1/22/1991	4,987,088	US
NMMS-1114.00/US	Utility - ORG	Expired	FABRICATION OF CMOS INTEGRATED DEVICES WITH REDUCED GATE LENGTH AND LIGHTLY DOPED DRAIN	7/28/1989	07/386,189	3/5/1991	4,997,782	US

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Patent No.	Material Type	Material Status	Title	Filed Date	Serial No.	Issue Date	Payment No.	Country
NMMS-1115.00/US	Utility - ORG	Expired	GRIPPER FOR THE ADVANTAGEOUSLY ROBOTIZED HANDLING OF ONE OR MORE SILICON WAFERS AND/OR OF A SUPPORT FOR SUCH WAFERS	12/5/1989	07/446,196	10/8/1991	5,054,834	US
NMMS-1116.00/US	Utility - ORG	Expired	PROGRAMMABLE LOGIC DEVICE HAVING A PLURALITY OF PROGRAMMABLE LOGIC ARRAYS ARRANGED IN A MOSAIC LAYOUT TOGETHER WITH A PLURALITY OF INTERMINGLINGLY ARRANGED INTERFACING BLOCKS	12/27/1989	07/456,782	2/12/1991	4,992,680	US
NMMS-1117.00/US	Utility - ORG	Expired	METHOD OF MANUFACTURING INTEGRATED CIRCUITS HAVING ELECTRONIC COMPONENTS OF TWO DIFFERENT TYPES EACH HAVING PAIRS OF ELECTRODES OBTAINED FROM THE SAME POLYCRYSTALLINE SILICON LAYERS AND SEPARATED BY DIFFERENT DIELECTRIC MATERIALS	12/13/1990	07/625,764	12/24/1991	5,075,246	US
NMMS-1118.00/US	Utility - ORG	Expired	VOLTAGE BOOSTED PHASE OSCILLATOR FOR DRIVING A VOLTAGE MULTIPLIER	2/4/1991	07/655,049	3/17/1992	5,097,226	US
NMMS-1119.00/RE	Utility - REIS	Expired	SENSE CIRCUIT FOR READING DATA STORED IN NONVOLATILE MEMORY CELLS	6/8/1995	08/488,718	6/8/1993	RE36,579	US
NMMS-1119.00/US	Utility - ORG	Abandoned	READING CIRCUIT FOR NONVOLATILE MEMORY CELL	2/19/1991	07/657,083	6/8/1993	5,218,570	US
NMMS-1120.01/US	Utility - CIP	Issued	METHOD FOR ADJUSTING THE THRESHOLD OF A READ-ONLY MEMORY TO ACHIEVE LOW CAPACITANCE AND HIGH BREAKDOWN VOLTAGE	10/19/1993	08/139,800	1/23/1996	5,486,487	US
NMMS-1121.02/US	Utility - CON	Issued	METHOD FOR CARRYING OUT A BOOLEAN OPERATION BETWEEN ANY TWO BITS OF ANY TWO REGISTERS	12/27/1994	08/364,505	8/12/1997	5,657,484	US
NMMS-1122.00/US	Utility - ORG	Expired	DATA OUTPUT STAGE HAVING FEEDBACK LOOPS TO PRECHARGE THE OUTPUT NODE	4/26/1991	07/691,768	1/12/1993	5,179,300	US
NMMS-1123.00/US	Utility - ORG	Expired	READING DEVICE FOR EPROM MEMORY CELLS WITH THE OPERATIONAL FIELD INDEPENDENT OF THE THRESHOLD JUMP OF THE WRITTEN CELLS WITH RESPECT TO THE VIRGIN CELLS	7/5/1991	07/725,987	11/30/1993	5,267,202	US
NMMS-1124.00/US	Utility - ORG	Expired	METHOD FOR FORMATION OF CONTACT PLUGS UTILIZING ETCHBACK	10/1/1991	07/769,600	7/27/1993	5,231,051	US
NMMS-1125.01/US	Utility - CON	Expired	MONOSTABILIZED DYNAMIC PROGRAMMABLE LOGIC ARRAY IN CMOS TECHNOLOGY	10/30/1992	07/970,609	12/28/1993	5,274,282	US
NMMS-1126.00/US	Utility - ORG	Abandoned	TWO PHASES SCAN PATH WITH SINGLECLOCK	10/22/1991	07/781,360	7/6/1993	5,225,724	US
NMMS-1126.01/RE	Utility - REIS	Expired	OPERATIONAL ANALYSIS DEVICE OF THE SCAN PATH TYPE HAVING A SINGLE SCANNING CLOCK AND A SINGLE OUTPUT PHASE FOR AN INTEGRATED CIRCUIT	6/23/1997	08/876,130	7/6/1993	RE36,292	US
NMMS-1127.00/US	Utility - ORG	Expired	COMPENSATED SENSE CIRCUIT FOR STORAGE DEVICES	11/13/1991	07/791,973	1/25/1994	5,287,169	US
NMMS-1128.00/US	Utility - ORG	Expired	SENSE CIRCUIT FOR STORAGE DEVICES SUCH AS NON-VOLATILE MEMORIES, WITH COMPENSATED OFFSET CURRENT	11/13/1991	07/791,453	1/4/1994	5,276,644	US
NMMS-1129.00/US	Utility - ORG	Expired	SENSE CIRCUIT FOR STORAGE DEVICES SUCH A NON-VOLATILE MEMORIES, WITH ENHANCED SENSING DISCRIMINATION	12/12/1991	07/806,118	12/14/1993	5,270,590	US
NMMS-1130.00/US	Utility - ORG	Expired	FAST CAPACITIVE-LOAD DRIVING CIRCUIT PARTICULARLY MEMORIES	12/20/1991	07/811,323	2/1/1994	5,283,478	US
NMMS-1131.00/US	Utility - ORG	Expired	MEMORY CELL READING CIRCUIT	12/19/1991	07/810,480	11/2/1993	5,258,959	US
NMMS-1132.00/US	Utility - ORG	Issued	CURRENT OFFSET SENSE AMPLIFIER OF A MODULATED CURRENT OR CURRENT UNBALANCE TYPE FOR PROGRAMMABLE MEMORIES	5/4/1992	07/878,823	7/5/1994	5,327,379	US
NMMS-1132.01/US	Utility - CIP	Issued	CURRENT OFFSET SENSE AMPLIFIER OF A MODULATED CURRENT OR CURRENT UNBALANCE TYPE FOR PROGRAMMABLE MEMORIES	7/5/1994	08/270,498	10/24/1995	5,461,713	US
NMMS-1133.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR PROGRAMMABLE MEMORIES WITH A VIRTUALLY ENHANCED SOURCE OF SIGNAL	7/24/1992	07/919,606	4/18/1995	5,408,148	US
NMMS-1134.00/US	Utility - ORG	Issued	ZERO-CONSUMPTION POWER-ON RESET CIRCUIT	8/27/1992	07/936,857	6/14/1994	5,321,317	US
NMMS-1135.00/US	Utility - ORG	Issued	ROW DECODER FOR NAND-TYPE ROM	8/31/1992	07/938,731	9/13/1994	5,347,493	US
NMMS-1136.01/US	Utility - CON	Issued	METHOD OF FORMING HIGH-STABILITY METALLIC CONTACTS IN AN INTEGRATED CIRCUIT WITH ONE OR MORE METALLIZED LAYERS	3/28/1995	08/411,385	2/26/2002	6,350,676	US
NMMS-1137.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING MOS-TYPE INTEGRATED CIRCUITS	6/7/1995	08/475,555	12/9/1997	5,696,399	US
NMMS-1137.02/US	Utility - CON	Issued	PROCESS FOR MANUFACTURING MOS-TYPE INTEGRATED CIRCUITS	9/6/1995	08/524,080	9/2/1997	5,663,080	US
NMMS-1138.01/US	Utility - CON	Issued	DYNAMIC REFERENCE SYSTEM FOR SENSE AMPLIFIER	10/12/1993	08/135,442	10/11/1994	5,355,333	US
NMMS-1139.00/US	Utility - ORG	Issued	METALLIZATION OVER TUNGSTEN PLUGS	5/26/1993	08/068,139	4/18/1995	5,407,861	US
NMMS-1139.01/US	Utility - CON	Issued	METALLIZATION OVER TUNGSTEN PLUGS	4/18/1995	08/423,397	7/28/1998	5,786,272	US

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MTI-DocNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-1140.00/US	Utility - ORG	Issued	LOW SWITCHING NOISE OUTPUT BUFFER	6/28/1993	08/084,727	5/30/1995	5,420,525	US
NMXS-1141.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT ENTIRELY PROTECTED AGAINST ULTRAVIOLET RAYS	6/30/1993	08/086,342	8/8/1995	5,440,510	US
NMXS-1142.02/US	Utility - CON	Issued	VOLTAGE REGULATOR FOR MEMORY DEVICE	3/13/1993	08/615,727	1/6/1998	5,706,240	US
NMXS-1143.01/US	Utility - DIV	Issued	A NON-VOLATILE MEMORY IN AN INTEGRATED CIRCUIT	5/23/1995	08/447,772	10/22/1996	5,568,418	US
NMXS-1143.02/US	Utility - CON	Issued	METHOD OF FABRICATING NON-VOLATILE MEMORIES WITH OVERLAPPING LAYERS	6/6/1995	08/469,431	8/25/1998	5,798,279	US
NMXS-1144.00/US	Utility - ORG	Issued	METHOD OF MAKING NOR-TYPE ROM WITH LDD CELLS	6/28/1993	08/084,971	4/18/1995	5,407,852	US
NMXS-1144.02/US	Utility - CON	Issued	NOR-TYPE ROM WITH LDD CELLS AND PROCESS OF FABRICATION	12/23/1996	08/772,301	8/11/1998	5,793,086	US
NMXS-1145.01/US	Utility - DIV	Issued	METHOD OF EVALUATING THE GATE OXIDE OF NON-VOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	6/2/1995	08/460,540	5/7/1996	5,515,318	US
NMXS-1145.02/US	Utility - CON	Issued	METHOD OF EVALUATING THE GATE OXIDE OF NON-VOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	4/1/1997	08/829,935	8/11/1998	5,793,675	US
NMXS-1146.00/US	Utility - ORG	Issued	METHOD FOR MAKING DIRECT CONTACTS IN HIGH DENSITY MOS/CMOS PROCESSES	11/17/1993	08/153,620	12/13/1994	5,372,956	US
NMXS-1147.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR FILTERING HIGH RESOLUTION DIGITAL SIGNALS	1/28/1994	08/189,271	1/14/1997	5,594,677	US
NMXS-1148.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR FILTERING DIGITAL SIGNALS	1/28/1994	08/188,569	3/3/1998	5,724,395	US
NMXS-1149.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING INTEGRATED DEVICES INCLUDING FLASH-EEPROM MEMORIES AND TRANSISTORS	2/10/1994	08/195,369	6/10/1997	5,637,520	US
NMXS-1150.00/US	Utility - ORG	Abandoned	PROCESS FOR FABRICATING INTEGRATED DEVICES INCLUDING NONVOLATILE MEMORIES AND TRANSISTORS WITH TUNNEL OXIDE PROTECTION	2/15/1994	08/196,634	11/14/1995	5,466,622	US
NMXS-1150.01/US	Utility - CON	Issued	PROCESS FOR FABRICATING INTEGRATED DEVICES INCLUDING NONVOLATILE MEMORIES AND TRANSISTORS WITH TUNNEL OXIDE PROTECTION	7/13/1995	08/502,329	6/15/1999	5,913,120	US
NMXS-1151.00/US	Utility - ORG	Issued	NONVOLATILE EPROM, EEPROM OF FLASH-EEPROM MEMORY WITH TUNNEL OXIDE PROTECTION	2/15/1994	08/196,572	3/5/1996	5,497,345	US
NMXS-1152.00/US	Utility - ORG	Issued	NONVOLATILE FLASH-EEPROM MEMORY ARRAY WITH SOURCE CONTROL TRANSISTORS	3/15/1994	08/214,049	4/16/1996	5,508,956	US
NMXS-1153.00/US	Utility - ORG	Issued	METHOD OF READING, ERASING AND PROGRAMMING A NONVOLATILE FLASH-EEPROM MEMORY ARRAY USING SOURCE LINE SWITCHING TRANSISTORS	3/15/1994	08/212,907	12/24/1996	5,587,946	US
NMXS-1153.01/US	Utility - DIV	Issued	METHOD OF PROGRAMMING A NONVOLATILE FLASH-EEPROM MEMORY ARRAY USING SOURCE LINE SWITCHING TRANSISTORS	6/2/1995	08/458,346	5/27/1997	5,633,822	US
NMXS-1154.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH MEMORY MATRIX COMPRISING REDUNDANCY CELL COLUMNS ASSOCIATED WITH SINGLE MATRIX SECTORS	3/29/1994	08/219,204	11/21/1995	5,469,389	US
NMXS-1155.01/US	Utility - DIV	Issued	METHOD OF EVALUATING THE DIELECTRIC LAYER OF NONVOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	5/31/1995	08/454,854	2/18/1997	5,604,699	US
NMXS-1155.02/US	Utility - CON	Issued	METHOD FOR EVALUATING THE DIELECTRIC LAYER OF NONVOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	7/24/1996	08/685,782	1/27/1998	5,712,816	US
NMXS-1157.00/US	Utility - NSPCT	Issued	METHOD AND APPARATUS FOR CHECKING POST-ERASURE CONTENTS OF AN ERASABLE PERMANENT MEMORY	10/9/1992	08/075,543	1/9/1996	5,483,492	US
NMXS-1158.00/US	Utility - ORG	Issued	SUPPLY VOLTAGE DISTRIBUTION SYSTEM WITH REDUCED RESISTANCE FOR SEMICONDUCTOR DEVICES	7/27/2007	11/881,505	9/27/2011	8,027,217	US

PATENT

REEL: 029186 FRAME: 0886

Exhibit B - US Applications

MTI DckNo	MatterType	MatterStatus	Title	FiledDate	SerialNo	Country
NMXXS-0002.00/RE	Utility - REIS	Abandoned	METHOD FOR FIXING AN ELECTRONIC COMPONENT AND ITS CONTACTS TO A SUPPORT	7/13/1992	07/912,867	US
NMXXS-0007.00/US	Utility - ORG	Abandoned	POWER-ON-RESET CIRCUIT PROVIDING PROTECTION AGAINST POWER SUPPLY CUTS FOR INTEGRATED CIRCUIT	12/14/1993	08/166,970	US
NMXXS-0011.00/US	Utility - ORG	Abandoned	CONTENT ADDRESSABLE MEMORY	5/31/1990	07/531,011	US
NMXXS-0011.01/US	Utility - CON	Abandoned	CONTENT ADDRESSABLE MEMORY	3/15/1994	08/713,474	US
NMXXS-0011.02/US	Utility - CON	Abandoned	CONTENT ADDRESSABLE MEMORY	12/6/1994	08/350,516	US
NMXXS-0014.01/US	Utility - CON	Abandoned	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	4/20/1992	07/870,878	US
NMXXS-0014.02/US	Utility - CON	Abandoned	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	8/2/1993	08/101,281	US
NMXXS-0020.01/US	Utility - CON	Abandoned	SEMICONDUCTOR MEMORY WITH MULTIPLE CLOCKING FOR TEST MODE ENTRY	10/20/1992	07/964,103	US
NMXXS-0021.00/US	Utility - ORG	Abandoned	SEMICONDUCTOR MEMORY WITH INHIBITED TEST MODE ENTRY DURING POWER-UP	8/17/1990	07/570,148	US
NMXXS-0026.01/US	Utility - DIV	Abandoned	SEMICONDUCTOR MEMORY WITH POWER-ON RESET CONTROLLED LATCHED ROW LINE REPEATERS	2/12/1992	07/834,632	US
NMXXS-0032.00/US	Utility - ORG	Abandoned	SEMICONDUCTOR MEMORY WITH MULTIPLEXED REDUNDANCY	12/14/1990	07/627,823	US
NMXXS-0039.00/US	Utility - ORG	Abandoned	SIGNAL LINE PULSE ENHANCING CIRCUIT FOR INTEGRATED CIRCUITS	1/21/1992	07/823,680	US
NMXXS-0040.01/US	Utility - DIV	Abandoned	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	9/16/1994	08/307,519	US
NMXXS-0042.00/US	Utility - ORG	Abandoned	TRISTABLE DRIVER FOR INTERNAL DATA BUS LINES	12/17/1991	07/809,392	US
NMXXS-0051.00/US	Utility - ORG	Abandoned	PARALLELIZED BORROW LOOK AHEAD SUBTRACTOR	3/31/1992	07/860,981	US
NMXXS-0057.00/US	Utility - ORG	Abandoned	INPUT BUFFER CIRCUIT IMMUNE TO COMMON MODE POWER SUPPLY FLUCTUATIONS	11/2/1992	07/970,266	US
NMXXS-0057.01/US	Utility - CON	Abandoned	INPUT BUFFER CIRCUIT IMMUNE TO COMMON MODE POWER SUPPLY FLUCTUATIONS	5/9/1994	08/240,091	US
NMXXS-0059.00/US	Utility - ORG	Abandoned	INTEGRATED CIRCUIT OUTPUT DRIVER	8/31/1992	07/938,401	US
NMXXS-0061.00/US	Utility - ORG	Abandoned	MULTIPLE CLOCKED DYNAMIC SENSE AMPLIFIER	12/22/1992	07/995,580	US
NMXXS-0063.00/US	Utility - ORG	Abandoned	LEADFRAME FOR INTEGRATED CIRCUITS	8/31/1992	07/938,396	US
NMXXS-0063.02/US	Utility - DIV	Abandoned	LEADFRAME FOR INTEGRATED CIRCUITS	10/7/1994	08/319,799	US
NMXXS-0074.00/US	Utility - ORG	Abandoned	PROCESS FOR IMPROVING THE INTERFACE UNION AMONG DIELECTRIC MATERIALS IN AN INTEGRATED CIRCUIT MANUFACTURE	4/28/1994	08/235,173	US
NMXXS-0081.00/US	Utility - ORG	Abandoned	INTEGRATED CIRCUIT FOR THE PROGRAMMING OF A MEMORY CELL IN A NON-VOLATILE MEMORY REGISTER	11/30/1994	08/347,267	US
NMXXS-0088.00/US	Utility - ORG	Abandoned	LOAD SIGNAL GENERATING METHOD AND CIRCUIT FOR NONVOLATILE MEMORIES	2/21/1995	08/391,146	US
NMXXS-0089.00/US	Utility - ORG	Abandoned	COUNT UNIT FOR NONVOLATILE MEMORIES	12/28/1994	08/365,155	US
NMXXS-0091.00/US	Utility - ORG	Abandoned	VOLTAGE BOOSTER WITH AN ACCELERATION CIRCUIT	12/28/1994	08/365,284	US
NMXXS-0112.00/US	Utility - ORG	Abandoned	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	4/30/1993	08/056,301	US
NMXXS-0112.01/US	Utility - CON	Abandoned	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	2/23/1996	08/606,233	US
NMXXS-0112.03/US	Utility - CON	Abandoned	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	2/9/1998	09/020,980	US

PATENT

REEL: 029186 FRAME: 0887

Exhibit B - US Applications

MTL DktNo	Matter Type	Matter Status	Title	Filed Date	Serial No	Country
NMXS-0117.00/US	Utility - ORG	Abandoned	METHOD AND APPARATUS FOR PARALLEL TESTING OF MEMORY	6/30/1993	08/085,980	US
NMXS-0118.00/US	Utility - ORG	Abandoned	CLOCK GENERATION CIRCUIT HAVING COMPENSATION FOR SEMICONDUCTOR MANUFACTURING PROCESS VARIATIONS	6/30/1993	08/085,751	US
NMXS-0124.00/US	Utility - ORG	Abandoned	VARIABLE IMPEDANCE DELAY ELEMENTS	7/30/1993	08/100,624	US
NMXS-0124.01/US	Utility - CON	Abandoned	VARIABLE IMPEDANCE DELAY ELEMENTS	7/25/1995	08/507,023	US
NMXS-0128.00/US	Utility - ORG	Abandoned	REDUNDANT LINE DECODER MASTER ENABLE	9/30/1993	08/129,766	US
NMXS-0129.00/US	Utility - ORG	Abandoned	STRUCTURE TO UTILIZE A PARTIALLY FUNCTIONAL CACHE MEMORY BY INVALIDATION OF FAULTY CACHE MEMORY LOCATIONS	11/1/1993	08/146,228	US
NMXS-0144.00/US	Utility - ORG	Abandoned	BIAS CIRCUIT FOR TRANSISTOR OF A STORAGE CELL	2/27/1995	08/393,463	US
NMXS-0144.01/US	Utility - CON	Abandoned	BIAS CIRCUIT FOR TRANSISTOR OF A STORAGE CELL	7/1/1996	08/674,283	US
NMXS-0158.00/US	Utility - ORG	Abandoned	PROGRAMMABLE MEMORY ELEMENT	3/31/1995	08/413,969	US
NMXS-0160.00/US	Utility - ORG	Abandoned	HIGHLY-PLANAR INTERLAYER DIELECTRIC THIN FILMS IN INTEGRATED CIRCUITS	4/11/1995	08/419,826	US
NMXS-0161.02/US	Utility - DIV	Abandoned	PROCESS FOR THE PRODUCTION OF A SEMICONDUCTOR DEVICE HAVING BETTER INTERFACE ADHESION BETWEEN DIELECTRIC LAYERS	9/27/2000	09/670,723	US
NMXS-0166.00/US	Utility - ORG	Abandoned	MONOLITHICALLY INTEGRATED GENERATOR OF A PLURALITY OF VOLTAGE VALUES	11/29/1995	08/563,758	US
NMXS-0175.00/US	Utility - ORG	Abandoned	TWIN-WELL MANUFACTURING PROCESS FOR AN EEPROM INTEGRATED DEVICE WITH REDUCED BODY EFFECT	11/22/1995	08/561,840	US
NMXS-0180.02/US	Utility - DIV	Abandoned	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON VOLATILE MEMORY CELLS AND SIDE TRANSISTORS AND CORRESPONDING IC	7/6/1999	09/347,905	US
NMXS-0181.01/US	Utility - DIV	Abandoned	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS OF AT LEAST TWO DIFFERENT TYPES, AND CORRESPONDING IC	7/28/1998	09/123,295	US
NMXS-0188.00/US	Utility - ORG	Abandoned	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	2/28/1994	08/202,827	US
NMXS-0188.01/US	Utility - DIV	Abandoned	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	5/25/1995	08/450,087	US
NMXS-0188.02/US	Utility - CON	Abandoned	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	8/24/1995	08/518,959	US
NMXS-0188.03/US	Utility - CON	Abandoned	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	3/26/1997	08/826,269	US
NMXS-0212.00/US	Utility - ORG	Abandoned	CLOCK PULSE EXTENDER MODE FOR Clocked MEMORY DEVICES HAVING PRECHARGED DATA PATHS	2/15/1995	08/388,755	US
NMXS-0224.00/US	Utility - ORG	Abandoned	READING CIRCUIT FOR MULTILEVEL NON VOLATILE MEMORY CELL DEVICES	4/1/1996	08/625,895	US
NMXS-0244.00/US	Utility - ORG	Abandoned	SUPPLY VOLTAGES SWITCHES CIRCUIT	12/30/1996	08/775,109	US
NMXS-0249.01/US	Utility - CON	Abandoned	ANALOG VOLTAGE-SIGNAL SELECTOR DEVICE	12/11/1998	09/210,179	US
NMXS-0256.00/US	Utility - ORG	Abandoned	NONVOLATILE FLOATING-GATE MEMORY DEVICES, AND PROCESS OF FABRICATION	1/31/1997	08/792,621	US
NMXS-0256.02/US	Utility - DIV	Abandoned	NONVOLATILE FLOATING-GATE MEMORY DEVICES, AND PROCESS OF FABRICATION	6/6/2002	10/165,010	US

PATENT

REEL: 029186 FRAME: 0888

Exhibit B - US Applications

MTL-DocNo	MatterType	MatterStatus	Title	FileDate	SerialNo	Country
NMXX-0257.01/US	Utility - DIV	Abandoned	HIGH CAPACITY CAPACITOR AND CORRESPONDING MANUFACTURINGPROCESS	7/12/2000	09/614,955	US
NMXX-0270.00/US	Utility - ORG	Abandoned	METHOD TO PREVENT DISTURBS DURING THE PROGRAMMING AND ERASING PHASES IN A NON-VOLATILE MEMORY DEVICE	12/30/1996	08/775,110	US
NMXX-0287.00/US	Utility - CIP	Abandoned	CONSTANT CURRENT SOURCE WITH REDUCED SENSITIVITY TO SUPPLY VOLTAGE AND PROCESS VARIATION	3/8/1995	08/399,079	US
NMXX-0294.00/US	Utility - ORG	Abandoned	INTEGRATED MEMORY CIRCUIT WITH SEQUENCED BITLINES FOR STRESS TEST	7/31/1995	08/509,196	US
NMXX-0295.00/US	Utility - ORG	Abandoned	METHOD AND APPARATUS FOR TEST MODE ENTRY DURING POWER UP	6/6/1995	08/466,107	US
NMXX-0300.01/US	Utility - DIV	Abandoned	TEST MODE ACTIVATION AND DATA OVERRIDE	12/3/1999	09/454,800	US
NMXX-0307.00/US	Utility - ORG	Abandoned	CIRCUIT AND METHOD FOR TRACKING THE START OF A WRITE TO A MEMORY CELL	1/19/1996	08/589,139	US
NMXX-0308.00/US	Utility - ORG	Abandoned	CIRCUIT AND METHOD FOR TERMINATING A WRITE TO A MEMORY CELL	1/19/1996	08/588,737	US
NMXX-0310.00/US	Utility - ORG	Abandoned	MEMORY-ROW SELECTOR HAVING A TEST FUNCTION	1/19/1996	08/589,140	US
NMXX-0313.00/US	Utility - ORG	Abandoned	DEVICE AND METHOD FOR DRIVING A CONDUCTIVE PATH WITH A SIGNAL	1/19/1996	08/587,708	US
NMXX-0321.00/RE	Utility - REIS	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	9/7/2001	09/949,290	US
NMXX-0321.01/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	2/19/2002	10/078,169	US
NMXX-0321.02/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	8/16/2002	10/222,940	US
NMXX-0321.03/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	3/10/2003	10/385,189	US
NMXX-0321.04/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	8/21/2003	10/645,344	US
NMXX-0321.05/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	3/8/2004	10/796,635	US
NMXX-0321.06/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	9/13/2004	10/939,960	US
NMXX-0321.07/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	3/17/2005	11/083,367	US
NMXX-0333.00/US	Utility - ORG	Abandoned	MOS CAPACITOR WITH WIDE VOLTAGE AND FREQUENCY OPERATING RANGES	7/25/1997	08/900,328	US
NMXX-0393.00/US	Utility - ORG	Abandoned	DEPOSITION METHOD OF A DIELECTRIC STRUCTURE FOR PLANARIZING MEMORY DEVICES	6/30/1998	09/107,535	US

PATENT

REEL: 029186 FRAME: 0889

Exhibit B - US Applications

MTI-Dck#No	Matter Type	Matter Status	Title	Filed Date	Serial No.	Country
NMXX-0430.01/US	Utility - CON	Abandoned	METHOD FOR PROGRAMMING A FLASH EPROM- TYPE FOR MEMORY	10/22/1998	09/1177,324	US
NMXX-0432.00/US	Utility - ORG	Abandoned	PROCESS OF FINAL PASSIVATION OF AN INTEGRATED CIRCUIT DEVICE	4/14/1998	09/060,192	US
NMXX-0432.01/US	Utility - DIV	Abandoned	PROCESS OF FINAL PASSIVATION OF AN INTEGRATED CIRCUIT DEVICE	7/30/1999	09/365,355	US
NMXX-0466.00/US	Utility - ORG	Abandoned	HIGH VOLTAGE REGULATOR AND CORRESPONDING VOLTAGE REGULATION METHOD	11/4/1998	09/186,504	US
NMXX-0479.01/US	Utility - DIV	Abandoned	METHOD FOR EVALUATING AN INTEGRATED ELECTRONIC DEVICE	4/19/2001	09/838,946	US
NMXX-0479.02/US	Utility - CON	Abandoned	METHOD FOR EVALUATING AN INTEGRATED ELECTRONIC DEVICE	5/21/2001	09/862,665	US
NMXX-0492.00/US	Utility - ORG	Abandoned	VSS SWITCHING SCHEME FOR BATTERY BACKED-UP SEMICONDUCTOR DEVICES	10/31/1997	08/962,977	US
NMXX-0523.00/US	Utility - ORG	Abandoned	CIRCUIT STRUCTURE FOR PROVIDING A HIERARCHICAL DECODING IN SEMICONDUCTOR MEMORY DEVICES	9/29/1999	09/408,157	US
NMXX-0527.00/US	Utility - ORG	Abandoned	METHOD FOR ENHANCING SELECTIVITY BETWEEN A FILM OF A LIGHT-SENSITIVE MATERIAL AND A LAYER TO BE ETCHED IN ELECTRONIC SEMICONDUCTOR DEVICE FABRICATION PROCESSES	6/29/1999	09/342,318	US
NMXX-0568.00/US	Utility - ORG	Abandoned	ESD PROTECTION DEVICE FOR SEMICONDUCTOR INTEGRATED CIRCUIT STRUCTURE	7/28/2000	09/627,876	US
NMXX-0608.00/US	Utility - ORG	Abandoned	BIPOLAR TRANSISTOR PRODUCED USING PROCESSES COMPATIBLE WITH THOSE EMPLOYED IN THE MANUFACTURE OF MOS DEVICES	7/20/2000	09/619,720	US
NMXX-0609.00/US	Utility - ORG	Abandoned	NON-VOLATILE MEMORY DEVICE AND MANUFACTURING PROCESS THEREOF	12/6/2000	09/731,065	US
NMXX-0616.00/US	Utility - ORG	Abandoned	LOW RATE REMOVAL ETCH PROCESS IN THE MANUFACTURE OF SEMICONDUCTOR INTEGRATED DEVICES USING A DIELECTRIC FILM DEPOSITION CHAMBER	1/11/2001	09/759,125	US
NMXX-0634.00/US	Utility - ORG	Abandoned	INTEGRATED MOS TRANSISTOR WITH A HIGH THRESHOLD VOLTAGE AND LOW MULTIPLICATION COEFFICIENT	12/14/2000	09/737,842	US
NMXX-0704.00/US	Utility - ORG	Abandoned	PROCESS FOR PRODUCING OPTICALLY ACTIVE TITANIUM ALKOXIDE COMPLEXES	10/31/2001	09/001,918	US
NMXX-0719.00/US	Utility - ORG	Abandoned	METHOD AND DEVICE FOR REDUCING AVERAGE ACCESS TIME OF A NON-VOLATILE MEMORY DURING READING	12/28/2001	10/033,358	US
NMXX-0732.01/US	Utility - DIV	Abandoned	MANUFACTURING PROCESS OF AN INTERPOLY DIELECTRIC STRUCTURE FOR NON-VOLATILE SEMICONDUCTOR INTEGRATED MEMORIES	6/27/2006	11/476,361	US
NMXX-0753.01/US	Utility - CON	Abandoned	CONTACT LESS IC CARD WITH OPERATING SYSTEM USED IN CONTACT TYPE CARDS AND READER FOR SUCH CONTACTLESS CARDS	11/30/2006	11/564,864	US
NMXX-0758.01/US	Utility - DIV	Abandoned	Design failure mode effect analysis (DFMEA)	12/5/2005	11/294,044	US
NMXX-0775.01/US	Utility - DIV	Abandoned	PROCESS FOR MANUFACTURING INTEGRATED RESISTOR AND PHASE-CHANGE MEMORY ELEMENT INCLUDING THIS RESISTOR	8/11/2005	11/201,790	US
NMXX-0780.02/US	Utility - CON	Pending	PHASE CHANGE MEMORY CELL AND MANUFACTURING METHOD THEREOF USING MINITRENCHES	6/10/2011	13/158,291	US
NMXX-0789.01/US	Utility - CON	Abandoned	METHOD FOR MANUFACTURING ELECTRONIC CIRCUITS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE	12/6/2005	11/294,763	US
NMXX-0795.00/US	Utility - ORG	Abandoned	MEMORY DEVICE AND METHOD FOR READING SEQUENTIALLY GROUPS OF BITS FROM A MEMORY DEVICE	2/7/2003	10/360,515	US

PATENT

REEL: 029186 FRAME: 0890

Exhibit B - US Applications

Mtl Deking	Matter Type	Matter Status	Title	Filed Date	Serial No	Country
NMMS-0814.00/US	Utility - ORG	Abandoned	PROCESS FOR FORMING TRENCHES WITH OBLIQUE PROFILE AND ROUNDED TOP CORNERS	6/27/2003	10/608,855	US
NMMS-0824.01/US	Utility - DIV	Abandoned	ION-IMPLANTATION MACHINE, CONTROL METHOD THEREOF, AND PROCESS FOR MANUFACTURING INTEGRATED DEVICES	6/7/2006	11/449,129	US
NMMS-0841.00/US	Utility - ORG	Abandoned	METHOD AND APPARATUS FOR DETECTING A LEAK OF EXTERNAL AIR INTO A PLASMA REACTOR	8/29/2003	10/652,862	US
NMMS-0846.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING A MEMORY DEVICE HAVING SELECTOR TRANSISTORS FOR STORAGE ELEMENTS AND MEMORY DEVICE FABRICATED THEREBY	4/30/2004	10/836,651	US
NMMS-0848.00/US	Utility - ORG	Abandoned	PROCESS FOR THE FORMATION OF DIELECTRIC ISOLATION STRUCTURES IN SEMICONDUCTOR DEVICES	5/25/2004	10/853,565	US
NMMS-0848.01/US	Utility - DIV	Abandoned	PROCESS FOR THE FORMATION OF DIELECTRIC ISOLATION STRUCTURES IN SEMICONDUCTOR DEVICES	1/16/2008	12/014,883	US
NMMS-0852.00/US	Utility - ORG	Abandoned	NON-VOLATILE MEMORY CELL COMPRISING DIELECTRIC LAYERS HAVING A LOW DIELECTRIC CONSTANT AND CORRESPONDING MANUFACTURING PROCESS	12/30/2003	10/749,130	US
NMMS-0867.00/US	Utility - NSPCT	Abandoned	ELEKTRONSPEKTROSKOP MIT EMISSION, DIE DURCH EINEN MONOCHROMATISCHEN ELEKTROGENSTRAHL INDUZIERT WIRD	10/6/2004	10/574,868	US
NMMS-0870.01/US	Utility - DIV	Abandoned	MOS DEVICE AND A PROCESS FOR MANUFACTURING MOS DEVICES USING A DUAL-POLYSILICON LAYER TECHNOLOGY WITH SIDE CONTACT	6/29/2006	11/479,369	US
NMMS-0898.01/US	Utility - DIV	Pending	METHOD FOR REDUCING DEFECTS AFTER A METAL ETCHING IN SEMICONDUCTOR DEVICES	9/14/2007	11/855,229	US
NMMS-0899.01/US	Utility - DIV	Pending	METHOD FOR PERFORMING ERROR CORRECTIONS OF DIGITAL INFORMATION CODIFIED AS A SYMBOL SEQUENCE	12/10/2007	12/001,294	US
NMMS-0903.01/US	Utility - CIP	Abandoned	SEALING METHOD FOR ELECTRONIC DEVICES FORMED ON A COMMON SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT STRUCTURE	7/17/2006	11/457,948	US
NMMS-0907.00/US	Utility - ORG	Pending	METHOD OF MAKING A FLOATING-GATE NON-VOLATILE MOS SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CAPACITIVE COUPLING	12/22/2005	11/317,679	US
NMMS-0909.00/US	Utility - ORG	Abandoned	PROCESS FOR DEFINING INTEGRATED CIRCUITS IN SEMICONDUCTOR ELECTRONIC DEVICES	11/16/2005	11/280,186	US
NMMS-0915.00/US	Utility - ORG	Abandoned	METHOD AND SYSTEM FOR CORRECTING LOW LATENCY ERRORS IN READ AND WRITE NON VOLATILE MEMORIES, PARTICULARLY OF THE FLASH TYPE	6/30/2005	11/173,896	US
NMMS-0918.00/US	Utility - ORG	Abandoned	NONVOLATILE MEMORY HAVING CONDUCTIVE FILM BETWEEN ADJACENT MEMORY CELLS	11/2/2006	11/592,020	US
NMMS-0918.01/US	Utility - DIV	Abandoned	PROCESS FOR MANUFACTURING A NONVOLATILE MEMORY HAVING CONDUCTIVE FILM BETWEEN ADJACENT MEMORY CELLS	5/21/2010	12/785,346	US
NMMS-0919.01/US	Utility - CON	Pending	METHOD OF MAKING A FLOATING GATE NON-VOLATILE MOS SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CAPACITIVE COUPLING AND DEVICE THUS OBTAINED	8/26/2011	13/219,491	US
NMMS-0922.00/US	Utility - ORG	Abandoned	METHOD AND SYSTEM FOR CORRECTING ERRORS IN ELECTRONIC MEMORY DEVICES	6/29/2005	11/169,497	US

PATENT

REEL: 029186 FRAME: 0891

Exhibit B - US Applications

MTI DocNo	MatterType	MatterStatus	Title	FiledDate	SerialNo	Country
NMXX-0927.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING A NON-VOLATILE MEMORY ELECTRONIC DEVICE INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING DEVICE	12/29/2006	11/618,370	US
NMXX-0927.01/US	Utility - DIV	Pending	PROCESS FOR MANUFACTURING A NON-VOLATILE MEMORY ELECTRONIC DEVICE INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING DEVICE	5/13/2010	12/779,150	US
NMXX-0933.00/PR	Prov - ORG	Abandoned	RESET INITIALIZATION	12/18/2003	60/530,726	US
NMXX-0934.00/PR	Prov - ORG	Abandoned	TAMPER MEMORY CELL	1/8/2004	60/535,064	US
NMXX-0935.00/PR	Prov - ORG	Completed	RESET RAMP CONTROL	12/18/2003	60/530,727	US
NMXX-0936.00/PR	Prov - ORG	Abandoned	ESD BONDING PAD	12/18/2003	60/530,736	US
NMXX-0937.00/PR	Prov - ORG	Abandoned	VOLTAGE TRANSLATING CONTROL STRUCTURE	12/18/2003	60/530,576	US
NMXX-0938.00/PR	Prov - ORG	Abandoned	RESET CIRCUIT	12/18/2003	60/530,848	US
NMXX-0939.00/RE	Utility - REIS	Abandoned	SENSE AMPLIFIER FOR READING A CELL OF A NON-VOLATILE MEMORY DEVICE	1/23/2009	12/359,221	US
NMXX-0942.00/US	Utility - ORG	Abandoned	MASS MEMORY DEVICE BASED ON A FLASH MEMORY WITH MULTIPLE BUFFERS	6/4/2004	10/861,341	US
NMXX-0943.00/US	Utility - ORG	Abandoned	METHOD FOR MANAGING BAD MEMORY BLOCKS IN A NONVOLATILE-MEMORY DEVICE, AND NONVOLATILE-MEMORY DEVICE IMPLEMENTING THE MANAGEMENT METHOD	6/14/2005	11/152,675	US
NMXX-0955.00/US	Utility - ORG	Abandoned	PHASE CHANGE MEMORY DEVICE AND MANUFACTURING PROCESS THEREOF	1/23/2006	11/337,787	US
NMXX-0960.00/US	Utility - ORG	Abandoned	EMBEDDED STORAGE DEVICE WITH INTEGRATED DATA-MANAGEMENT FUNCTIONS AND STORAGE SYSTEM INCORPORATING IT	8/16/2005	11/205,766	US
NMXX-0962.00/US	Utility - ORG	Pending	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/279,385	US
NMXX-0962.01/US	Utility - CON	Pending	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	8/5/2011	13/198,978	US
NMXX-0970.00/US	Utility - ORG	Abandoned	NON-VOLATILE MEMORY ELECTRONIC DEVICE	12/28/2006	11/617,472	US
NMXX-0973.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING A MEMORY WITH LOCAL ELECTRICAL CONTACT BETWEEN THE SOURCE LINE AND THE WELL	1/12/2006	11/331,826	US
NMXX-0979.01/US	Utility - DIV	Pending	DUAL RESISTANCE HEATER FOR PHASE CHANGE DEVICES AND MANUFACTURING METHOD THEREOF	12/28/2010	12/980,141	US
NMXX-0981.00/US	Utility - ORG	Abandoned	PROCESS FOR PHYSICAL VAPOR DEPOSITION OF A CHALCOGENIDE MATERIAL LAYER AND CHAMBER FOR PHYSICAL VAPOR DEPOSITION OF A CHALCOGENIDE MATERIAL LAYER OF A PHASE CHANGE MEMORY DEVICE	4/5/2006	11/398,849	US
NMXX-0982.00/US	Utility - ORG	Abandoned	LOW-RIPPLE BOOSTED VOLTAGE GENERATOR	12/30/2005	11/323,937	US
NMXX-0988.01/US	Utility - CON	Pending	VERTICAL MOSFET TRANSISTOR, IN PARTICULAR OPERATING AS A SELECTOR IN NONVOLATILE MEMORY DEVICES	8/24/2010	12/862,624	US
NMXX-0994.00/US	Utility - ORG	Abandoned	NON VOLATILE MEMORY DEVICE SUPPORTING HIGH-PARALLELISM TEST AT WAFER LEVEL	12/15/2005	11/304,488	US
NMXX-0996.00/RE	Utility - REIS	Pending	METHOD OF MANAGING FAILS IN A NON-VOLATILE MEMORY DEVICE AND RELATIVE MEMORY DEVICE	8/4/2011	13/198,665	US
NMXX-1005.00/US	Utility - ORG	Abandoned	MEMORY DEVICE AND RELATIVE CONTROL METHOD	2/21/2007	11/677,411	US

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REEL: 029186 FRAME: 0892

Exhibit B - US Applications

MTI-IdNo	Matter Type	Matter Status	Title	File Date	Serial No.	Country
NMXXS-1012.00/US	Utility - ORG	Abandoned	METHOD AND ARCHITECTURE FOR RESTRICTING ACCESS TO A MEMORY DEVICE	1/19/2006	11/336,411	US
NMXXS-1013.00/US	Utility - ORG	Pending	ENHANCED SECURITY MEMORY ACCESS METHOD AND ARCHITECTURE	1/19/2006	11/337,085	US
NMXXS-1015.00/US	Utility - ORG	Abandoned	BASIC SEMICONDUCTOR ELECTRONIC CIRCUIT WITH REDUCED SENSITIVITY TO PROCESS VARIATIONS	6/19/2006	11/455,896	US
NMXXS-1020.00/US	Utility - ORG	Pending	FLASH MEMORY DEVICE WITH A LOW PIN COUNT (LPC) COMMUNICATION INTERFACE	10/28/2005	11/261,131	US
NMXXS-1025.00/US	Utility - ORG	Abandoned	READING METHOD OF A NON-VOLATILE ELECTRONIC DEVICE AND CORRESPONDING DEVICE	5/24/2007	11/753,368	US
NMXXS-1031.00/US	Utility - ORG	Abandoned	METHOD FOR PROGRAMMING/ERASING A NON VOLATILE MEMORY CELL DEVICE, IN PARTICULAR FOR FLASH TYPE MEMORIES	3/9/2007	11/684,052	US
NMXXS-1032.00/US	Utility - ORG	Abandoned	NON-ACTIVE ELECTRICALLY STRUCTURES OF INTEGRATED ELECTRONIC CIRCUIT	5/29/2007	11/754,494	US
NMXXS-1034.00/US	Utility - ORG	Abandoned	CHARGE PUMP SYSTEMS AND METHODS	11/27/2006	11/605,193	US
NMXXS-1035.00/US	Utility - ORG	Abandoned	VOLTAGE SWITCHING CIRCUITS AND METHODS	3/21/2007	11/726,707	US
NMXXS-1036.00/US	Utility - ORG	Abandoned	ROW SELECTOR FOR A SEMICONDUCTOR MEMORY DEVICE	3/21/2007	11/726,726	US
NMXXS-1037.00/US	Utility - ORG	Abandoned	COLUMN DECODING SYSTEM FOR SEMICONDUCTOR MEMORY DEVICES IMPLEMENTED WITH LOW VOLTAGE TRANSISTORS	4/12/2007	11/787,220	US
NMXXS-1039.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING A NON-VOLATILE MEMORY DEVICE	9/27/2006	11/528,500	US
NMXXS-1041.00/US	Utility - NSPCT	Pending	SEMICONDUCTOR FIELD-EFFECT TRANSISTOR, MEMORY CELL AND MEMORY DEVICE	12/23/2009	12/293,534	US
NMXXS-1042.00/US	Utility - ORG	Abandoned	TEST CARTRIDGE WITH INTERNAL GENERATION OF THE TEST SIGNALS	12/20/2007	11/961,266	US
NMXXS-1045.00/US	Utility - ORG	Abandoned	METHOD FOR MANUFACTURING NON VOLATILE MEMORY CELLS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE	12/27/2006	11/647,504	US
NMXXS-1049.00/US	Utility - ORG	Abandoned	METHOD FOR LOCALLY SUPPRESSING A DISTURBANCE OF A REFERENCE LINE	2/27/2007	11/712,239	US
NMXXS-1050.01/US	Utility - CON	Pending	OPTIMIZED FLASH MEMORY ACCESS METHOD AND DEVICE	8/3/2011	13/197,056	US
NMXXS-1054.01/US	Utility - CON	Pending	READING METHOD OF A MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE AND MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE	3/14/2011	13/047,678	US
NMXXS-1055.01/US	Utility - DIV	Pending	FAST PROGRAMMING MEMORY DEVICE	6/7/2011	13/155,347	US
NMXXS-1058.00/US	Utility - ORG	Abandoned	Process for manufacturing an electronic device integrated on semiconductor substrate comprising non volatile floating gate memories and an associated circuitry and corresponding electronic device	2/14/2008	12/070,175	US
NMXXS-1060.00/US	Utility - ORG	Abandoned	SEMICONDUCTOR-INTEGRATED ELECTRONIC DEVICE HAVING A PLURALITY OF LEADS	7/6/2007	11/774,416	US
NMXXS-1061.00/US	Utility - ORG	Abandoned	MANUFACTURING METHOD OF AN INTEGRATED CIRCUIT FORMED ON A SEMICONDUCTOR SUBSTRATE	9/4/2007	11/899,275	US
NMXXS-1065.00/US	Utility - ORG	Abandoned	Process for manufacturing integrated circuits formed on a semiconductor substrate and comprising tungsten layers	3/5/2008	12/074,776	US

PATENT

REEL: 029186 FRAME: 0893

Exhibit B - US Applications

MTI-DefNo	MatterType	MatterStatus	Title	FileDate	SerialNo	Country
NMXX-1069.00/US	Utility - ORG	Allowed	METHOD AND DEVICE FOR DETECTING POSSIBLE CORRUPTION OF SECTOR PROTECTION INFORMATION OF A NON-VOLATILE MEMORY STORED IN AN ON BOARD VOLATILE MEMORY ARRAY AT POWER-ON	3/29/2007	11/693,360	US
NMXX-1071.01/US	Utility - CON	Pending	CONFIGURATION OF A MULTILEVEL FLASH MEMORY DEVICE	3/15/2011	13/048,760	US
NMXX-1072.00/US	Utility - ORG	Abandoned	VOLTAGE REGULATOR MADE OF HIGH VOLTAGE TRANSISTORS	12/3/2007	11/949,517	US
NMXX-1074.00/US	Utility - ORG	Abandoned	METHOD OF PROGRAMMING A FOUR-LEVEL FLASH MEMORY DEVICE AND A RELATED PAGE BUFFER	7/28/2006	11/460,835	US
NMXX-1074.01/US	Utility - DIV	Pending	METHOD OF PROGRAMMING A MULTI-LEVEL MEMORY DEVICE	8/31/2009	12/551,383	US
NMXX-1075.01/US	Utility - CON	Pending	ELECTRONIC DEVICE COMPRISING NON VOLATILE MEMORY CELLS WITH OPTIMIZED PROGRAMMING AND CORRESPONDING PROGRAMMING METHOD	2/19/2009	12/388,656	US
NMXX-1075.02/US	Utility - CON	Pending	ELECTRONIC DEVICE COMPRISING NON VOLATILE MEMORY CELLS WITH OPTIMIZED PROGRAMMING AND CORRESPONDING PROGRAMMING METHOD	3/22/2011	13/053,723	US
NMXX-1076.00/US	Utility - ORG	Abandoned	METHOD OF FIXING READ EVALUATION TIME IN A NON VOLATILE NAND TYPE MEMORY DEVICE	9/13/2007	11/854,713	US
NMXX-1077.00/US	Utility - ORG	Abandoned	PLASMA DEPOSITION PROCESS WITH VARIABLE PROCESS PARAMETERS	11/26/2007	11/944,791	US
NMXX-1078.00/US	Utility - NSPCT	Pending	PHASE CHANGE MEMORY DEVICE	2/22/2010	12/442,392	US
NMXX-1080.00/US	Utility - ORG	Abandoned	MANUFACTURING PROCESS OF AN ORGANIC MASK FOR MICROELECTRONIC INDUSTRY	4/13/2007	11/734,818	US
NMXX-1086.00/US	Utility - ORG	Abandoned	MEMORY DEVICE	8/24/2007	11/895,609	US
NMXX-1097.00/US	Utility - ORG	Pending	NON-VOLATILE MEMORY CIRCUIT, SYSTEM AND METHOD	4/17/2008	12/148,521	US
NMXX-1104.00/US	Utility - ORG	Abandoned	REDUCED-BEAK PLANOX PROCESS FOR THE FORMATION OF INTEGRATED ELECTRONIC COMPONENTS	10/4/1985	06/784,324	US
NMXX-1105.00/US	Utility - ORG	Abandoned	METHOD FOR MANUFACTURING MOS/CMOS MONOLITHIC INTEGRATED CIRCUITS INCLUDING SILICIDE AND POLYSILICON PATTERNING	12/11/1986	06/940,501	US
NMXX-1120.00/US	Utility - ORG	Abandoned	METHOD FOR ADJUSTING THE THRESHOLD OF A READ-ONLY MEMORY TO ACHIEVE LOW CAPACITANCE AND HIGH BREAKDOWN VOLTAGE	3/29/1991	07/678,557	US
NMXX-1121.00/US	Utility - ORG	Abandoned	METHOD FOR CARRYING OUT A BOOLEAN OPERATION BETWEEN ANY TWO BITS OF ANY TWO REGISTERS	4/24/1991	07/690,495	US
NMXX-1121.01/US	Utility - CON	Abandoned	METHOD FOR CARRYING OUT A BOOLEAN OPERATION BETWEEN ANY TWO BITS OF ANY TWO REGISTERS	2/9/1993	08/015,307	US
NMXX-1124.00/RE	Utility - REIS	Abandoned	METHOD FOR FORMATION OF CONTACT PLUGS UTILIZING ETCHBACK	7/26/1995	08/507,545	US
NMXX-1124.01/US	Utility - CON	Abandoned	METHOD FOR FORMATION OF CONTACT PLUGS UTILIZING ETCHBACK	1/16/1997	08/784,040	US
NMXX-1125.00/US	Utility - ORG	Abandoned	MONOSTABILIZED DYNAMIC PROGRAMMABLE LOGIC ARRAY IN CMOS TECHNOLOGY	9/27/1991	07/767,141	US
NMXX-1126.00/RE	Utility - REIS	Abandoned	OPERATIONAL ANALYSIS DEVICE OF THE SCAN PATH TYPE HAVING A SINGLE SCANNING CLOCK AND A SINGLE OUTPUT PHASE FOR AN INTEGRATED CIRCUIT	7/6/1995	08/498,856	US
NMXX-1135.01/US	Utility - DIV	Abandoned	ROW DECODER FOR NAND-TYPE ROM	10/26/1995		US
NMXX-1136.00/US	Utility - ORG	Abandoned	PLANARIZED CONTACT WITH A SANDWICH OF TUNGSTEN AND ALUMINIUM.	11/23/1992	07/980,550	US
NMXX-1137.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING MOS-TYPE INTEGRATED CIRCUITS	11/23/1992	07/980,453	US
NMXX-1138.00/US	Utility - ORG	Abandoned	DYNAMIC REFERENCE SYSTEM FOR SENSE AMPLIFIER	9/22/1992	07/949,678	US

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