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1. Name of conveying party(ies)

QUALCOMM Incorporated

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance/Execution Date(s):

Execution Date(s) February 22, 2008

- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Joint Research Agreement
☐ Government Interest Assignment
☐ Executive Order 9424, Confirmatory License
☐ Other _____

2. Name and address of receiving party(ies)

Name: QUALCOMM MEMS Technologies, Inc.

Internal Address: _____

Street Address: 5775 Morehouse Drive

City: San Diego

State: CA

Country: USA Zip: 92121

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application or patent number(s):

☐ This document serves as an Oath/Declaration (37 CFR 1.63).

A. Patent Application No.(s)

B. Patent No.(s)

13/150482

Additional numbers attached? ☐ Yes ☒ No

5. Name and address to whom correspondence concerning document should be mailed:

Name: Knobbe Martens Olson & Bear, LLP

Internal Address: 14th Floor

Street Address: 2040 Main Street

City: Irvine

State: CA Zip: 92614

Phone Number: (949) 760-0404

Docket Number: QCO.055C1

Email Address: efilig@knobbe.com

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 1.21(h) & 3.41) \$40.00

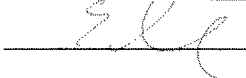
- ☒ Authorized to be charged to deposit account
☐ Enclosed
☐ None required (government interest not affecting title)

8. Payment Information

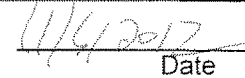
Deposit Account Number 11-1410

Authorized User Name Knobbe Martens Olson & Bear, LLP

9. Signature:



Signature



Date

Eric M. Nelson

Name of Person Signing

Total number of pages including cover sheet, attachments, and documents: 13

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:
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ASSIGNMENT

WHEREAS, QUALCOMM Incorporated, a Delaware corporation having offices at 5775 Morehouse Drive, San Diego, California 92121 U.S.A. (hereinafter "ASSIGNOR"), represents that it is the sole owner of the entire right, title, and interest to certain new and useful improvements for which ASSIGNOR has obtained issued Letters Patents and/or has filed applications for Letters Patents in the United States (hereinafter "the Patents and Patent Applications") identified in the list attached hereto as Exhibit A (in 10 pages);

WHEREAS, QUALCOMM MEMS Technologies, Inc., a Delaware corporation having offices at 5775 Morehouse Drive, San Diego, California 92121 U.S.A. (hereinafter "ASSIGNEE") desires to acquire the entire right, title, and interest in and to said improvements and said Patents and Patent Applications;

NOW, THEREFORE, in consideration of One Dollar (\$1.00) to ASSIGNOR, and other good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR hereby further acknowledges that, with an effective date of February 22, 2008, it has sold, assigned, and transferred, and by these presents does hereby sell, assign, and transfer, unto ASSIGNEE, its successors, legal representatives, and assigns, the entire right, title, and interest throughout the world in, to, and under the said improvements, the said Patents and Patent Applications, all Patents that may be granted thereon, all provisional applications relating thereto, and all divisions, continuations, reissues, reexaminations, renewals, and extensions thereof, and all rights of priority under International Conventions and applications for Letters Patent that may have been or hereafter be filed for said improvements or for the said Patents and Patent Applications in any country or countries foreign to the United States; and ASSIGNOR hereby authorizes and requests the Commissioner of Patents of the United States, and any Official of any country foreign to the United States, whose duty it is to issue patents on applications as aforesaid, to issue all Letters Patents for said improvements and all Letters Patents resulting from the Patents and Patent Applications to ASSIGNEE, its successors, legal representatives, and assigns, in accordance with the terms of this Agreement.

ASSIGNOR does hereby sell, assign, transfer, and convey to ASSIGNEE, its successors, legal representatives, and assigns all claims for damages and all remedies arising out of any violation of the rights assigned hereby that may have accrued prior to the date of assignment to ASSIGNEE, or may accrue hereafter, including, but not limited to, the right to sue for, collect, and retain damages for past infringements of the Letters Patents before or after issuance;

ASSIGNOR hereby covenants and agrees that it will communicate to ASSIGNEE, its successors, legal representatives, and assigns any facts known to ASSIGNOR respecting the Patents and Patent Applications immediately upon becoming aware of those facts, and that it will testify in any legal proceeding involving any of the Patents and Patent Applications, will sign all lawful papers, execute all divisional, continuing, and reissue applications, make all rightful oaths, and will generally do everything

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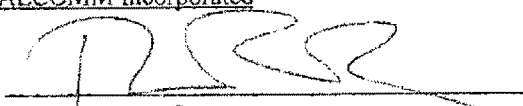
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possible to aid ASSIGNEE, its successors, legal representatives, and assigns to obtain and enforce the Patents and Patent Applications in all countries.

IN TESTIMONY WHEREOF, I hereunto set my hand and seal this 22nd day of February, 2008

QUALCOMM IncorporatedBy: Name Printed: Thomas R. FouseTitle: Vice President, Chief Patent CounselDate: 2/22/08

IN TESTIMONY WHEREOF, acknowledged hereunto this 25th day of February, 2008

QUALCOMM MEMS Technologies, Inc.By: Name Printed: JOHN BATEYTitle: PRESIDENT, QATDate: 2/25/083738017
050807

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EXHIBIT A
QUALCOMM Incorporated – QUALCOMM MEMS Technologies, Inc.
Assignment With Effective date of February 22, 2008

Case No.	Client Ref	Title of Invention:	Application No.	Filing Date:	Patent No:	Date Issued:
QCO.017A	050279	METHODS FOR PRODUCING MEMS WITH PROTECTIVE COATINGS USING MULTI-COMPONENT SACRIFICIAL LAYERS	11/367098	3/2/06		
QCO.021A	050351	INTERFEROMETRIC MODULATOR WITH INTERNAL POLARIZATION AND DRIVE METHOD	11/145416	6/3/05		
QCO.029A	050535	SYSTEM AND METHOD OF DRIVING A MEMS DISPLAY DEVICE	11/413239	4/28/06		
QCO.029PR	050535P1	SYSTEM AND METHOD FOR DRIVING A MEMS DISPLAY DEVICE	60/678361	5/5/05		
QCO.030PR	050653P1	DYNAMIC DRIVER IC AND DISPLAY PANEL CONFIGURATION	60/678482	5/5/05		
QCO.030A	050653	DYNAMIC DRIVER IC AND DISPLAY PANEL CONFIGURATION	11/429571	5/5/06		
QCO.031A	050386	SYSTEMS AND METHODS OF ACTUATING MEMS DISPLAY ELEMENTS	11/404449	4/14/06		
QCO.031PR	050386P1	SYSTEM AND METHOD FOR DRIVING A MEMS DISPLAY DEVICE	60/678473	5/5/05		
QCO.032A1	050851U1	MEMS DEVICES HAVING OVERLYING SUPPORT STRUCTURES AND METHODS OF FABRICATING THE SAME	11/490880	7/21/06		
QCO.032A2	050851U2	MEMS DEVICES HAVING SUPPORT STRUCTURES AND METHODS OF FABRICATING THE SAME	11/491490	7/21/06		
QCO.032PR	050851P1	METHOD OF FABRICATING AN INTERFEROMETRIC MODULATOR COMPRISING RIGID SUPPORT STRUCTURES	60/701655	7/22/05		
QCO.033A	051008	SUPPORT STRUCTURE FOR MEMS DEVICE AND METHODS THEREFOR	11/491389	7/21/06		
QCO.033PR	051008P1	SUPPORT STRUCTURE FOR MEMS DEVICE	60/702080	7/22/05		
QCO.034A1	050914U1	METHODS FOR ETCHING LAYERS WITHIN A MEMS DEVICE TO ACHIEVE A TAPERED EDGE	11/506770	8/18/06		
QCO.034A2	050914U2	METHODS FOR FORMING LAYERS WITHIN A MEMS DEVICE USING LIFTOFF PROCESSES TO ACHIEVE A TAPERED EDGE	11/506622	8/18/06		

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EXHIBIT A
QUALCOMM Incorporated – QUALCOMM MEMS Technologies, Inc.
Assignment With Effective date of February 22, 2008

Case No.	Client Ref	Title of Invention:	Application No.	Filing Date:	Patent No:	Date Issued:
QCO.034A3	050914U3	MEMS DEVICES HAVING SUPPORT STRUCTURES WITH SUBSTANTIALLY VERTICAL SIDEWALLS AND METHODS FOR FABRICATING THE SAME	11/506600	8/18/06		
QCO.034A4	050914U4	MEMS DEVICE HAVING SUPPORT STRUCTURES CONFIGURED TO MINIMIZE STRESS-RELATED DEFORMATION AND METHODS FOR FABRICATING SAME	11/506594	8/18/06		
QCO.034PR	050914P1	SUPPORT STRUCTURES FOR MEMS DEVICES AND METHODS FOR FORMING THE SAME	607/10019	8/19/05		
QCO.035A	051355	MEMS DEVICE AND INTERCONNECTS FOR SAME	11/540485	9/29/06		
QCO.035PR	051355P1	MEMS DEVICE HAVING INTERCONNECTS FORMED OF SACRIFICIAL MATERIAL	607/23540	9/30/05		
QCO.037PR	050377P1	ETCHING SYSTEM AND METHOD	607/18920	9/20/05		
QCO.039A	050838	METHOD OF CREATING MEMS DEVICE CAVITIES BY A NON-ETCHING PROCESS	11/321134	12/29/05		
QCO.040A	050987	SILICON-RICH SILICON NITRIDES AS ETCH STOPS IN MEMS MANUFACTURE	11/334990	1/18/06		
QCO.041A	051184	ELECTRICAL CONDITIONING OF MEMS DEVICE AND INSULATING LAYER THEREOF	11/360131	2/22/06		
QCO.043A	050188	MODE INDICATOR FOR INTERFEROMETRIC MODULATOR DISPLAYS	11/405116	4/17/06		
QCO.044A	050370	METHOD AND SYSTEM FOR UPDATING OF DISPLAYS SHOWING DETERMINISTIC CONTENT	11/673330	2/9/07		
QCO.044PR	050370P1	METHOD AND SYSTEM FOR UPDATING OF DISPLAYS SHOWING DETERMINISTIC CONTENT	607/72613	2/10/06		
QCO.045A	050702	INTERCONNECT STRUCTURE FOR MEMS DEVICE	11/331705	1/13/06		
QCO.046A	050576	SYSTEM AND METHOD FOR POWER REDUCTION WHEN DECOMPRESSING VIDEO STREAMS FOR INTERFEROMETRIC MODULATOR DISPLAYS	11/317421	12/22/05		
QCO.048A	060012	METHOD AND SYSTEM FOR WRITING DATA TO MEMS DISPLAY ELEMENTS	11/296656	12/7/05		
QCO.049A	051229	INTERFEROMETRIC OPTICAL DISPLAY SYSTEM WITH BROADBAND CHARACTERISTICS	11/401023	4/10/06		
QCO.050A	051264	NON-PLANAR SURFACE STRUCTURES AND PROCESS FOR MICROELECTROMECHANICAL SYSTEMS	11/406776	4/19/06		

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EXHIBIT A
QUALCOMM Incorporated - QUALCOMM MEMS Technologies, Inc.
Assignment With Effective date of February 22, 2008

Case No.	Client Ref	Title of Invention:	Application No.	Filing Date:	Patent No.	Date Issued:
QCO.051A	051308	NON-PLANAR SURFACE STRUCTURES AND PROCESS FOR MICROELECTROMECHANICAL SYSTEMS	11/406981	4/19/06		
QCO.052A	051324	NON-PLANAR SURFACE STRUCTURES AND PROCESS FOR MICROELECTROMECHANICAL SYSTEMS	11/406866	4/19/06		
QCO.053A	050618	MEMS DEVICE HAVING A LAYER MOVABLE AT ASYMMETRIC RATES	11/360162	2/23/06		
QCO.054A	050427	BACK-TO-BACK DISPLAYS	11/439012	5/22/06		
QCO.055A	050730	METHOD FOR PROVIDING AND REMOVING DISCHARGING INTERCONNECT FOR CHIP-ON GLASS OUTPUT LEADS AND STRUCTURES THEREOF	11/358997	2/21/06		
QCO.056A	051326	PASSIVE CIRCUITS FOR DE-MULTIPLEXING DISPLAY INPUTS	11/479865	6/29/06		
QCO.057A	050857U1	SWITCHES FOR SHORTING DURING MEMS ETCH RELEASE	11/647822	12/29/06		
QCO.057A2	050857U2	PERIPHERAL SWITCHES FOR MEMS DISPLAY TEST	11/648244	12/29/06		
QCO.058A	050187	MEMS DEVICE WITH INTEGRATED OPTICAL ELEMENT	11/656681	1/23/07		
QCO.058PR	060187P1	MEMS DEVICE WITH INTEGRATED OPTICAL ELEMENT	60/762723	1/27/06		
QCO.059A	060216	SYSTEM AND METHOD FOR PROVIDING RESIDUAL STRESS TEST STRUCTURES	11/453633	6/15/06		
QCO.059PR	060216P1	SYSTEM AND METHOD FOR PROVIDING RESIDUAL STRESS TEST STRUCTURES	60/757048	1/6/06		
QCO.060A	060004	MICROELECTROMECHANICAL DEVICE AND METHOD UTILIZING NANOPARTICLES	11/407730	4/19/06		
QCO.061A	060032	MICROELECTROMECHANICAL DEVICE AND METHOD UTILIZING A POROUS SURFACE	11/407470	4/19/06		
QCO.061C1	060032C1	MICROELECTROMECHANICAL DEVICE AND METHOD UTILIZING A POROUS SURFACE	11/8669467	10/9/07		
QCO.062A	050985	PATTERNING OF MECHANICAL LAYER IN MEMS TO REDUCE STRESSES AT SUPPORTS	11/445529	6/1/06		
QCO.063A	060024	ELECTRODE AND INTERCONNECT MATERIALS FOR MEMS DEVICES	11/416920	5/3/06		
QCO.064A	060141	PROCESS AND STRUCTURE FOR FABRICATION OF MEMS DEVICE HAVING ISOLATED EDGE POSTS	11/445607	6/1/06	7,321,457	1/22/08

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EXHIBIT A
QUALCOMM Incorporated - QUALCOMM MEMS Technologies, Inc.
Assignment With Effective date of February 22, 2008

Case No.	Client Ref	Title of Invention:	Application No.	Filing Date:	Patent No:	Date Issued:
QCO.064DV1	060141D1	PROCESS AND STRUCTURE FOR FABRICATION OF MEMS DEVICE HAVING ISOLATED EDGE POSTS	11/960602	12/19/07		
QCO.065A	050424	COMPATIBLE MEMS SWITCH ARCHITECTURE	11/591809	11/2/06		
QCO.066A	050946	METHOD AND APPARATUS FOR LOW RANGE BIT DEPTH ENHANCEMENT FOR MEMS DISPLAY ARCHITECTURES	11/454162	6/15/06		
QCO.067A	050648	TWO PRIMARY COLOR DISPLAY	11/595621	11/9/06		
QCO.068A	050340	SYSTEMS AND METHODS FOR DRIVING MEMS DISPLAY	11/472880	6/21/06		
QCO.069A	051222	METHOD OF MANUFACTURING MEMS DEVICES PROVIDING AIR GAP CONTROL	11/478702	6/30/06		
QCO.070A	060209	MEMS SWITCHES WITH DEFORMING MEMBRANES	11/641649	12/19/06		
QCO.071A	050855	HIGH PROFILE CONTACTS FOR MICROELECTROMECHANICAL SYSTEMS	11/504319	8/15/06		
QCO.072A	060310U1	SUPPORT STRUCTURE FOR FREE-STANDING MEMS DEVICE AND METHODS FOR FORMING THE SAME	11/476317	6/28/06		
QCO.072A2	060310U2	SUPPORT STRUCTURE FOR FREE-STANDING MEMS DEVICE AND METHODS FOR FORMING THE SAME	11/476998	6/28/06		
QCO.073A	061102	OPTICAL INTERFERENCE TYPE PANEL AND THE MANUFACTURING METHOD THEREOF	10/249061	3/13/03	6747800	6/8/04
QCO.074A	061103	OPTICAL INTERFERENCE TYPE OF COLOR DISPLAY HAVING OPTICAL DIFFUSION LAYER BETWEEN SUBSTRATE AND ELECTRODE	10/249243	3/26/03		
QCO.074DV1	61103D1	OPTICAL INTERFERENCE TYPE OF COLOR DISPLAY	10/711685	9/30/04	7038752	5/2/06
QCO.075A	061104	OPTICAL INTERFERENCE COLOR DISPLAY AND OPTICAL INTERFERENCE MODULATOR	10/249244	3/26/03	5912022	6/29/05
QCO.076A	061109	COLOR CHANGEABLE PIXEL	10/670734	9/26/03	6982820	1/3/06
QCO.076DV1	061109D1	COLOR CHANGEABLE PIXEL	10/901163	7/29/04	7006272	2/28/06
QCO.077A	061110	STRUCTURE OF A LIGHT-INCIDENCE ELECTRODE OF AN OPTICAL INTERFERENCE DISPLAY PLATE	10/670737	9/26/03		
QCO.078A	061106	METHOD FOR FABRICATING AN INTERFERENCE DISPLAY UNIT	10/705824	11/13/03	7198973	4/3/07
QCO.079A	061108	METHOD FOR FABRICATING AN INTERFERENCE DISPLAY UNIT	10/713508	11/14/03		
QCO.080A	061107	INTERFERENCE DISPLAY UNIT	10/706923	11/14/03	6995890	2/7/06

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EXHIBIT A
QUALCOMM Incorporated - QUALCOMM MEMS Technologies, Inc.
Assignment With Effective date of February 22, 2008

Case No.	Client Ref	Title of Invention:	Application No.	Filing Date:	Patent No:	Date Issued:
QCO.080DV1	061107D1	METHOD FOR FABRICATING AN INTERFERENCE DISPLAY UNIT	11/069938	3/3/05	7015095	3/21/06
QCO.081A	061111	STRUCTURE OF A STRUCTURE RELEASE AND A METHOD FOR MANUFACTURING THE SAME	10/725585	12/3/03	6870654	3/22/05
QCO.082A	061105	STRUCTURE OF AN OPTICAL INTERFERENCE DISPLAY CELL	10/742062	12/20/03	6882458	4/19/05
QCO.083A	061100	OPTICAL-INTERFERENCE TYPE DISPLAY PANEL AND METHOD FOR MAKING THE SAME	10/752666	1/8/04	7172915	2/6/07
QCO.084A	061101	OPTICAL-INTERFERENCE TYPE REFLECTIVE PANEL AND METHOD FOR MAKING THE SAME	10/752811	1/8/04	6999236	2/14/06
QCO.084DV1	061101D1	METHOD FOR MAKING AN OPTICAL INTERFERENCE TYPE REFLECTIVE PANEL	11/261466	10/31/05	7323217	1/29/08
QCO.085A	061112	METHOD FOR FABRICATING OPTICAL INTERFERENCE DISPLAY CELL	10/796997	3/11/04	7078293	7/18/06
QCO.085C1	061112C1	OPTICAL INTERFERENCE DISPLAY CELL AND METHOD OF MAKING THE SAME	11/413603	4/28/06		
QCO.086A	061118	OPTICAL INTERFERENCE DISPLAY PANEL AND MANUFACTURING METHOD THEREOF	10/807128	3/24/04		
QCO.087A	061119	AN INTERFERENCE DISPLAY CELL	10/807129	3/24/04	7193768	3/20/07
QCO.087DV1	061119D1	INTERFERENCE DISPLAY CELL AND FABRICATION METHOD THEREOF	11/221806	9/9/05		
QCO.088A	061117	OPTICAL INTERFERENCE DISPLAY PANEL	10/807142	3/24/04	6999225	2/14/06
QCO.089A	061122	STRUCTURE OF AN OPTICAL INTERFERENCE DISPLAY UNIT	10/807143	3/24/04	6958847	10/25/05
QCO.090A	061116	OPTICAL INTERFERENCE DISPLAY PANEL	10/807147	3/24/04	7307776	12/11/07
QCO.090DV1	061116D1	OPTICAL INTERFERENCE DISPLAY PANEL	11/368683	3/7/06		
QCO.091A	061121	STRUCTURE OF A MICRO ELECTRO MECHANICAL SYSTEM AND THE MANUFACTURING METHOD THEREOF	10/810560	3/29/04	7291921	11/6/07
QCO.091C1	061121C1	STRUCTURE OF A MICRO ELECTRO MECHANICAL SYSTEM AND THE MANUFACTURING METHOD THEREOF	11/925551	10/26/07		
QCO.091DV1	061121D1	STRUCTURE OF A MICRO ELECTRO MECHANICAL SYSTEM AND THE MANUFACTURING METHOD THEREOF	11/925477	10/26/07		

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EXHIBIT A
QUALCOMM Incorporated – QUALCOMM MEMS Technologies, Inc.
Assignment With Effective date of February 22, 2008

Case No.	Client Ref	Title of Invention:	Application No.	Filing Date:	Patent No:	Date Issued:
QCO.092A	061124	MICRO ELECTRO MECHANICAL SYSTEM DISPLAY CELL AND METHOD FOR FABRICATING THEREOF	10/812257	3/29/04	6882461	4/19/05
QCO.093A	061115	COLOR-CHANGEABLE PIXELS OF AN OPTICAL INTERFERENCE DISPLAY PANEL	10/815884	3/31/04		
QCO.094A	061113	INTERFEROMETRIC MODULATION PIXELS AND MANUFACTURING METHOD THEREOF	10/815905	3/31/04		
QCO.095A	061114	INTERFEROMETRIC MODULATION PIXELS AND MANUFACTURING METHOD THEREOF	10/815947	4/2/04	6952303	10/4/05
QCO.096A	061120	OPTICAL INTERFERENCE REFLECTIVE ELEMENT AND REPAIRING AND MANUFACTURING METHODS THEREOF	10/873014	6/21/04	6980350	12/27/05
QCO.097A	061123	INTERFERENCE DISPLAY PLATE AND MANUFACTURING METHOD THEREOF	10/884555	7/2/04		
QCO.098A	061126	STRUCTURE OF A MICRO ELECTRO MECHANICAL SYSTEM	10/950927	10/12/04		
QCO.099A	061125	METHOD OF MANUFACTURING OPTICAL INTERFERENCE COLOR DISPLAY	11/133641	5/20/05		
QCO.105A	050671	SUPPORT STRUCTURE FOR MEMS DEVICE AND METHODS THEREFOR	11/491047	7/21/06		
QCO.106A	060295U1	MEMS STRUCTURES, METHODS OF FABRICATING MEMS COMPONENTS ON SEPARATE SUBSTRATES AND ASSEMBLY OF SAME	11/863079	9/27/07		
QCO.107A	060418	SELECTIVE ETCHING OF MEMS USING GASEOUS HALIDES AND REACTIVE CO-ETCHANTS	11/497726	8/2/06		
QCO.108A	060836	SACRIFICIAL SPACER PROCESS AND RESULTANT STRUCTURE FOR MEMS SUPPORT STRUCTURE	11/583575	10/19/06		
QCO.109A	060717	HYBRID COLOR SYNTHESIS FOR MULTISTATE REFLECTIVE MODULATOR DISPLAYS	11/699542	1/29/07		
QCO.110A	060456	DUAL FILM LIGHT GUIDE FOR ILLUMINATING DISPLAYS	11/742299	4/30/07		
QCO.111A	060289	ANGLE SWEEPING HOLOGRAPHIC ILLUMINATOR	11/467879	8/28/06		
QCO.112A	061032U1	MEMS DEVICES AND PROCESSES FOR PACKAGING SUCH DEVICES	11/734730	4/12/07		
QCO.112A3	061032U3	PACKAGING A MEMS DEVICE USING A FRAME	11/735362	4/13/07		

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EXHIBIT A
QUALCOMM Incorporated – QUALCOMM MEMS Technologies, Inc.
Assignment With Effective date of February 22, 2008

Case No.	Client Ref	Title of Invention:	Application No.	Filing Date:	Patent No:	Date Issued:
QCO.112PR	061032P1	MEMS DEVICES AND PROCESSES FOR PACKAGING SUCH DEVICES	60/791730	4/13/06		
QCO.113A	060907	DETERMINATION OF INTERFEROMETRIC MODULATOR MIRROR CURVATURE AND AIRGAP VARIATION USING DIGITAL PHOTOGRAPHS	11/479392	6/30/06		
QCO.114PR	061314P1	PACKAGING OF MEMS DEVICE WITH OPEN SIDE	60/801542	5/17/06		
QCO.115A	061315	DESICCANT IN A MEMS DEVICE	11/750279	5/17/07		
QCO.115PR	061315P1	DESICCANT IN A MEMS DEVICE	60/801356	5/17/06		
QCO.116A	061329	PASSIVE CIRCUITS FOR DE-MULTIPLEXING DISPLAY INPUTS	11/704450	2/8/07		
QCO.117A	060757	ARBITRARY POWER FUNCTION USING LOGARITHM LOOKUP TABLE	11/698609	12/5/07		
QCO.118A	060819	CAPACITIVE MEMS DEVICE WITH PROGRAMMABLE OFFSET VOLTAGE CONTROL	11/938673	11/12/07		
QCO.119PR	061212P1	TREATING UNDERLYING LAYERS FOR CONTROL OF HILLOCK FORMATION IN REFLECTING LAYERS	60/910184	4/4/07		
QCO.120A	061384	MEMS DEVICE HAVING A RECESSED CAVITY AND METHODS THEREFOR	11/765981	6/20/07		
QCO.120PR	061384P1	MEMS DEVICE HAVING A RECESSED CAVITY AND METHODS THEREFOR	60/815905	6/21/06		
QCO.121A	060989	MEMS PROCESSING	11/566172	12/1/06		
QCO.122A	051075	METHOD AND APPARATUS FOR MEASURING THE FORCE OF STICTION OF A MEMBRANE IN A MEMS DEVICE	11/614795	12/21/06		
QCO.123A	051269	INTERNAL OPTICAL ISOLATION STRUCTURE FOR INTEGRATED FRONT OR BACK LIGHTING	11/544978	10/6/06		
QCO.124A	060847	MICROELECTROMECHANICAL DEVICE AND METHOD UTILIZING CONDUCTING LAYERS SEPARATED BY STOPS	11/692734	3/28/07		
QCO.125A	061184	MICROELECTROMECHANICAL DEVICE WITH OPTICAL FUNCTION SEPARATED FROM MECHANICAL AND ELECTRICAL FUNCTION	11/772751	7/2/07		
QCO.126A	061029	SYSTEM AND METHOD FOR REDUCING VISUAL	11/545104	10/6/06		

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EXHIBIT A
QUALCOMM Incorporated – QUALCOMM MEMS Technologies, Inc.
Assignment With Effective date of February 22, 2008

Case No.	Client Ref	Title of Invention:	Application No.	Filing Date:	Patent No:	Date issued:
		ARTIFACTS IN DISPLAYS				
QCO.129A	061011	SYSTEMS AND METHODS OF PROVIDING A LIGHT GUIDING LAYER	11/669074	1/30/07		
QCO.130A	061401	ALUMINUM FLUORIDE FILMS FOR MICROELECTROMECHANICAL SYSTEM APPLICATIONS	11/646059	12/27/06		
QCO.131A	061434	CRITICAL DIMENSION CONTROL FOR PHOTOLITHOGRAPHY FOR MICROELECTROMECHANICAL SYSTEMS DEVICES	11/657844	1/25/07		
QCO.132A	061503	INFRARED AND DUAL MODE DISPLAYS	11/766725	6/21/07		
QCO.142A1	061244U1	MICROELECTROMECHANICAL SYSTEM HAVING A DIELECTRIC MOVABLE MEMBRANE AND A MIRROR	11/746513	5/9/07		
QCO.142A2	061244U2	MICROELECTROMECHANICAL SYSTEM HAVING A DIELECTRIC MOVABLE MEMBRANE AND A MIRROR	11/746443	5/9/07		
QCO.144A	061559	MEMS DEVICE AND INTERCONNECTS FOR SAME	11/613922	12/20/06		
QCO.145PR	061772P1	METHOD FOR INTEGRATING A LIGHT DIFFUSER IN AN ILLUMINATION DEVICE OF A DISPLAY SYSTEM	60/850024	10/6/06		
QCO.146PR	061933P1	ULTRA THIN LIGHT GUIDE	60/850025	10/6/06		
QCO.147PR	061845P1	APPARATUS AND METHOD FOR REDUCING BACK REFLECTION FROM A DISPLAY	60/850141	10/6/06		
QCO.148PR	061968P1	ILLUMINATION DEVICE WITH BUILT-IN LIGHT COUPLER	60/850189	10/6/06		
QCO.149PR	070076P1	THIN LIGHT BAR AND METHOD OF MANUFACTURING	60/828511	10/6/06		
QCO.150PR	061976P1	INTERFEROMETRIC MODULATOR DISPLAY DEVICE WITH A HOLOGRAPHIC LAYER	60/850759	10/10/06		
QCO.151A	061205	SYSTEM AND METHOD FOR MEASURING ADHESION FORCES IN MEMS DEVICES	11/842914	8/21/07		
QCO.152A	061224	SYSTEM AND METHOD FOR MEASURING RESIDUAL STRESS	11/690708	3/23/07		
QCO.153A	061710	MECHANICAL RELAXATION TRACKING AND RESPONDING IN MEMS DRIVER	11/777123	7/12/07		
QCO.154A	061732	MEMS DEVICE AND INTERCONNECTS FOR SAME	11/835308	8/7/07		
QCO.155A	070350	MEMS CAVITY-COATING LAYERS AND METHODS	11/689430	3/21/07		

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QCO.156A	061906	MODULATING THE INTENSITY OF LIGHT FROM AN INTERFEROMETRIC REFLECTOR	11/776490	7/11/07		
QCO.156PR	061906P1	MODULATING THE INTENSITY OF LIGHT FROM AN INTERFEROMETRIC REFLECTOR	60/887791	2/1/07		
QCO.158A	061881	INTERFEROMETRIC MODULATOR DISPLAYS WITH REDUCED COLOR SENSITIVITY	11/750891	5/18/07		
QCO.159A	061891	INTEGRATED IMODS AND SOLAR CELLS ON A SUBSTRATE	11/773757	7/5/07		
QCO.160A	070094	ESD PROTECTION FOR MEMS DISPLAY PANELS	11/836045	8/8/07		
QCO.161A	070325	HIGH-APERTURE-RATIO TOP-REFLECTIVE AM-IMOD DISPLAYS	11/765276	6/19/07		
QCO.163A	070429	METHOD OF PATTERNING MECHANICAL LAYER FOR MEMS STRUCTURES	11/763234	6/14/07		
QCO.169PR	070993P1	EQUIPMENT AND METHODS FOR ETCHING OF MEMS	60/890824	2/20/07		
QCO.170PR	070774P1	SYSTEM AND METHOD FOR PACKAGING A MEMS DEVICE	60/890445	2/16/07		
QCO.171PR	071012P1	SYSTEM AND METHOD FOR PACKAGING A MEMS DEVICE	60/890153	2/15/07		
QCO.172A	051162	INTERFEROMETRIC OPTICAL MODULATOR WITH BROADBAND REFLECTION CHARACTERISTICS	11/847205	8/29/07		
QCO.178A	061784	INTERFEROMETRIC MODULATOR DISPLAY DEVICES	11/971830	1/9/08		
QCO.178PR	061784P1	INTERFEROMETRIC MODULATOR DISPLAY DEVICES	60/972717	9/14/07		
QCO.179A	070634	INDICATION OF THE END-POINT REACTION BETWEEN XEF2 AND MOLYBDENUM	11/767430	6/22/07		
QCO.180A	070425	MEMS DEVICES HAVING IMPROVED UNIFORMITY AND METHODS FOR MAKING THEM	11/773357	7/3/07		
QCO.183PR	071363P1	ELECTROMECHANICAL DEVICE TREATMENT WITH WATER VAPOR	60/947353	6/29/07		
QCO.184A	051137	METHOD AND APPARATUS FOR REDUCING BACK-GLASS DEFLECTION IN AN INTERFEROMETRIC MODULATOR DISPLAY DEVICE	11/313436	12/20/05		

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QCO.185A	050976	METHOD AND APPARATUS FOR PROVIDING BACK-LIGHTING IN AN INTERFEROMETRIC MODULATOR DISPLAY DEVICE	11/357702	2/17/06		
QCO.186A	050636	METHOD AND APPARATUS FOR PROVIDING BRIGHTNESS CONTROL IN AN INTERFEROMETRIC MODULATOR (IMOD) DISPLAY	11/408753	4/21/06		
QCO.187A	060040	LINEAR SOLID STATE ILLUMINATOR	11/472879	6/21/06		
QCO.188A	051339	LIGHT GUIDE INCLUDING OPTICAL SCATTERING ELEMENTS AND A METHOD OF MANUFACTURE	11/588947	10/27/06		
QCO.189A	061431	METHOD AND APPARATUS FOR PROVIDING A LIGHT ABSORBING MASK IN AN INTERFEROMETRIC MODULATOR DISPLAY	11/683787	3/8/07		
QCO.191A	071244	PERIODIC DIMPLE ARRAY	11/949612	12/3/07		
QCO.191PR	071244P1	PERIODIC DIMPLE ARRAY	60/972715	9/14/07		
QCO.192A	071583	THIN FILM SOLAR CONCENTRATOR/COLLECTOR	11/941851	11/16/07		
QCO.194A	071354	DECOUPLED HOLOGRAPHIC FILM AND DIFFUSER	11/952941	12/7/07		
QCO.207A	071773	INTERFEROMETRIC PHOTOVOLTAIC CELL	11/949699	12/3/07		
QCO.208PR	072000P1	MEMS DEVICES AND METHODS OF FABRICATING THE SAME	60/951930	7/25/07		
QCO.231PR	072388P1	ETCHING PROCESSES USED IN MEMS PRODUCTION	60/972748	9/14/07		
QCO.233A	040224	INTEGRATED AUDIO CODED WITH SILICON AUDIO TRANSDUCER	10/916929	8/11/04		
QCO.235A	080171	PHOTOVOLTAICS WITH INTERFEROMETRIC MASKS	11/950392	12/4/07		
QCO.240PR	080419P1	PHOTOVOLTAICS WITH INTERFEROMETRIC MASKS	61/014405	12/17/07		

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