

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT										
NATURE OF CONVEYANCE:	Corrective Assignment to correct the erroneous recordation and request removal of Pat. Appl. No. 09/484,549 (Pat No. 7,661,107) previously recorded on Reel 023574 Frame 0593. Assignor(s) hereby confirms the assignment of Assignor's interest except for Pat. Appl. No. 09/484,549 (Pat No. 7,661,107).										
CONVEYING PARTY DATA											
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>ATI International SRL</td> <td>11/18/2009</td> </tr> </tbody> </table>		Name	Execution Date	ATI International SRL	11/18/2009						
Name	Execution Date										
ATI International SRL	11/18/2009										
RECEIVING PARTY DATA											
<table border="1"> <tr> <td>Name:</td> <td>ATI Technologies ULC</td> </tr> <tr> <td>Street Address:</td> <td>One Commerce Valley Drive East</td> </tr> <tr> <td>City:</td> <td>Markham, Ontario</td> </tr> <tr> <td>State/Country:</td> <td>CANADA</td> </tr> <tr> <td>Postal Code:</td> <td>L3T 7N6</td> </tr> </table>		Name:	ATI Technologies ULC	Street Address:	One Commerce Valley Drive East	City:	Markham, Ontario	State/Country:	CANADA	Postal Code:	L3T 7N6
Name:	ATI Technologies ULC										
Street Address:	One Commerce Valley Drive East										
City:	Markham, Ontario										
State/Country:	CANADA										
Postal Code:	L3T 7N6										
PROPERTY NUMBERS Total: 1											
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Patent Number:</td> <td>7661107</td> </tr> </tbody> </table>		Property Type	Number	Patent Number:	7661107						
Property Type	Number										
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CORRESPONDENCE DATA											
<p>Fax Number: 2023712540 <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i></p> <p>Phone: 202.371.2600 Email: cshea@skgf.com Correspondent Name: Sterne, Kessler, Goldstein & Fox P.L.L.C Address Line 1: 1100 New York Avenue, N.W. Address Line 4: Washington, DISTRICT OF COLUMBIA 20005</p>											
ATTORNEY DOCKET NUMBER:	1972.0860000										
NAME OF SUBMITTER:	Jason D. Eisenberg, Reg. No. 43,447										
<p>Total Attachments: 33 source=19720860000assignment#page1.tif source=19720860000assignment#page2.tif</p>											

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PATENT ASSIGNMENT

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SUBMISSION TYPE:		NEW ASSIGNMENT
NATURE OF CONVEYANCE:		ASSIGNMENT
CONVEYING PARTY DATA		
Name		Execution Date
ATI International SRL		11/18/2009
RECEIVING PARTY DATA		
Name:	ATI Technologies ULC	
Street Address:	One Commerce Valley Drive E	
City:	Markham, Ontario	
State/Country:	CANADA	
PROPERTY NUMBERS Total: 362		
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Patent Number:	5329290	
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PATENT
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PATENT
 REEL: 029300 FRAME: 0315

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Application Number:	09981484
Application Number:	09484549
Application Number:	09471877
Application Number:	09473316
Application Number:	12428045

CORRESPONDENCE DATA

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Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

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REEL: 023574 FRAME: 0603

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REEL: 029300 FRAME: 0325

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Address Line 2: Suite 2500
Address Line 4: Chicago, ILLINOIS 60601

ATTORNEY DOCKET NUMBER:

00100.09.2000

NAME OF SUBMITTER:

Patricia O'Donoghue

Total Attachments: 21

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PATENT
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REEL: 029300 FRAME: 0326

PATENT ASSIGNMENT

TO WHOM IT MAY CONCERN:

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, be it known that ATI International SRL, a subsidiary of ATI Technologies ULC, located at Beaumont House, Hastings, Christ Church, Barbados, West Indies, ("Assignor"), has sold, conveyed, assigned, transferred and delivered, and by these presents does hereby sell, convey, assign, transfer and deliver, unto ATI Technologies ULC, an Alberta corporation, located at 1 Commerce Valley Drive East, Markham, Ontario, Canada ("Assignee"), its successors, legal representatives and/or assigns, the whole of Assignor's worldwide right, title and interest in and to: (i) the patents and patent applications listed in Schedule A attached hereto; (ii) all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissued patents, reexaminations, and extensions of such patents and patent applications that have been or shall be issued in the United States and all foreign countries; and (iii) all rights of priority resulting from the filing of said patents and patent applications (collectively, the "Patents").

Said sale, conveyance, assignment and transfer includes, without limitation, the rights to enforce, assert and sue for past, present and future infringement of the Patents, and the rights to recover and collect for past, present and future damages related to the Patents.

Assignor agrees to execute or procure any further necessary assurance of the title to said Patents and to, at any time, upon the reasonable request and at the expense of Assignee, execute all papers that may be necessary or reasonably desirable to perfect the title to said Patents which may be granted therefore in Assignee, its successors, assigns or other legal representatives, and will make all rightful oaths or declarations affirming such transfer of ownership, and do all lawful acts requisite for procuring the same therein, without further compensation, but at the reasonable request and expense of Assignee, its successors, assigns or other legal representatives.

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PATENT

REEL: 029300 FRAME: 0327

IN WITNESS WHEREOF, ATI International SRL has caused this Patent Assignment to be signed on its behalf on this 18th day of November, 2009.

FOR ASSIGNOR
ATI International SRL

Robert Feldstein
(Signature)

Robert Feldstein
(Print or type name)

VP, ~~GA~~ ^{NS} ~~Game Console~~ VP, AMD
(Print or type title)

WITNESS

Christine F. McDade
(Signature)

Christine F. McDade
(Print or type name)

90 Central St. Roxborough, MA 01719
(Print or type address)

SIGNATURE PAGE TO PATENT ASSIGNMENT FROM ATI BARBADOS TO ATI ULC

PATENT
REEL: 029300 FRAME: 0328

PATENT
REEL: 029300 FRAME: 0328

IN WITNESS WHEREOF, ATI Technologies ULC hereby accepts this Assignment on this 13th day of November 2009.

FOR ASSIGNEE
ATI Technologies ULC

[Signature]
(Signature)

Benjamin Bar-Haim
(Print or type name)

Director
(Print or type title)

WITNESS

[Signature]
(Signature)

Kelly Flacks
(Print or type name)

1 Conners Valley Drive East
(Print or type address) Markham, ON L3T 7N6

SIGNATURE PAGE TO PATENT ASSIGNMENT FROM ATI BARBADOS TO ATI ULC

**SCHEDULE A
USA**

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	07/773,920	03/21/90	07/12/94	5,329,290	Monitor Control Circuit	FIRE01 US
USA	08/285,379	08/03/94	12/19/95	5,477,543	Structure and Method For Shifting and Reordering A Plurality of Data Bytes	M2884 US
USA	08/285,376	08/03/94	12/17/96	5,586,070	Structure and Method For Embedding Two Small Multipliers In A Larger Multiplier	M2882 US
USA	08/281,377	07/27/94	04/22/97	5,623,434	Structure and Method of Using An Arithmetic and Logic Unit For Carry Propagation Stage of A Multiplier	M2883 US
USA	08/281,334	07/27/94	04/29/97	5,625,784	Variable Length Instructions Packaged In A Fixed Length Double Instruction	M2881 US
USA	08/537,495	10/02/95	09/02/97	5,664,154	M/A For Optimizing Retry Time Upon Cache-Miss By Selecting A Delay Time According To Whether The Addressed Locations Dirty Bit Indicates A Write-Back	M3251 US
USA	08/555,626	11/09/95	09/09/97	5,665,928	Method & Apparatus For Spline Parameter Transmissions In Sound Synthesis	M3634 US
USA	08/416,372	04/04/95	01/27/98	5,712,799	Method and Structure For Performing Motion Estimation Using Reduced Precision Pixel Intensity Values	M2880 US
USA	08/556,416	11/09/95	03/10/98	5,727,211	System and Method For Fast Context Switching Between Tasks	M 3566 US
USA	08/541,563	10/10/95	05/12/98	5,751,622	Structure and Method For Signed Multiplication Using Large Multiplier Having Two Embedded Signed Multipliers	M3468 US
USA	08/537,496	10/02/95	08/25/98	5,799,169	Emulated Registers	M3308 US
USA	08/541,562	10/10/95	09/22/98	5,812,437	Programmable Logic Unit For Arithmetic, Logic and Equality Functions	M3595 US
USA	08/555,538	11/09/95	01/12/99	5,859,787	Arbitrary-Ratio Sampling Rate Converter Using Approximation By Segmented Polynomial Functions	M3191 US
USA	08/541,565	10/10/95	01/26/99	5,864,704	Multimedia Processor Using Variable Length Instructions	M3594 US
USA	08/698,365	08/15/96	03/16/99	5,883,640	Computing Apparatus and Operating Method Using String Caching To Improve Graphics Performance	M4207 US
USA	08/698,366	08/15/96	09/07/99	5,949,439	Computing Apparatus and Operating Method Using Software Queues	M4208 US
USA	08/853,303	05/09/97	09/21/99	5,956,252	Method and Apparatus for an Integrated Circuit that Is Reconfigurable Based on Testing Results	970424-2
USA	08/876,731	06/16/97	11/02/99	5,977,836	Method and Apparatus for Controlling an Output Frequency of a Phase Locked Loop	970325-2

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PATENT

REF: 029374 FRAME: 0608

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REEL: 029300 FRAME: 0330

SCHEDULE A
USA

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	08/747,374	11/12/96	11/09/99	5,982,373	Dynamic Enhancement/Reduction of Graphical Image Data Resolution	M4621 US
USA	08/918,226	08/25/97	12/14/99	6,002,410	Reconfigurable Texture Cache	M5120 US
USA	09/100,446	06/19/98	06/06/00	6,073,156	Circuit and Method For Wrap-Around Sign Extension For Signed Numbers Using Replacement of Most Significant bit	M5827 US
USA	09/100,266	06/19/98	06/27/00	6,081,823	Circuit and Method For Wrap Around Sign Extension For Signed Numbers	M 5878 S
USA	09/351,101	07/08/99	09/26/00	6,124,542	Wavefunction Sound Sampling Synthesis Modified Adder Tree Structure and Method Using Logic And Gates To Generate Carry-In Values	M5885 US
USA	09/344,912	06/24/99	10/03/00	6,127,842	Differential Input Receiver and Method For Reducing Noise	M5610 US
USA	09/210,969	12/14/98	10/17/00	6,133,772	Method and Apparatus For Providing Compatibility With Synchronous Dynamic Random Access Memory (SDRAM) and Double Data Rate (DDR) Memory	990019 BT
USA	09/524,433	03/13/00	11/28/00	6,154,419	Method and Apparatus For Dynamically Blending Image Input Layers	000014 BS
USA	09/211,615	12/15/98	12/05/00	6,157,415	Powerup Sequence Artificial Voltage Supply Circuit	990007 BT
USA	09/274,456	03/22/99	12/12/00	6,160,430	Method and Apparatus For Sequencing Palette Updates In A Video Graphics System	990040 BT
USA	09/166,019	10/05/98	12/26/00	6,166,724	Booth Multiplication Structure Which Selectively Integrates The Function Of Either Of Incrementing Or Negating With The Function of Booth Multiplication	980087 S
USA	09/099,575	06/19/98	12/26/00	6,167,422	Floating Point Arithmetic Logic Unit Rounding Using At Least One Least Significant Bit	M5600 S
USA	09/156,817	09/18/98	03/06/01	6,199,089	Floating Point Arithmetic Logic Unit Leading Zero Count Using Fast Approximate Rounding	M5595 US
USA	09/157,090	09/18/98	03/20/01	6,205,461	Interpreter For Stack-Based Languages A Circuit and Method For Fast Squaring By Breaking The Square Into A Plurality Of Terms	M5596 US
USA	09/134,647	08/14/98	03/20/01	6,205,578	Method and Apparatus For Booting Up a Computing System With Enhanced Graphics	M 5889 S
USA	09/138,301	08/21/98	07/10/01	6,260,056	Voltage Supply Discriminator and Method	990002 BT
USA	09/183,882	10/30/98	08/07/01	6,272,627		990017 BT
USA	09/211,115	12/14/98	10/02/01	6,297,583		

SCHEDULE A
USA

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/224,692	03/04/99	10/02/01	6,297,832	Method and Apparatus For Memory Access Scheduling In A Video Graphics System	980031 B
USA	09/166,038	10/05/98	10/02/01	6,297,835	Method and Apparatus for Processing Data as Different Sizes	980089 BT
USA	09/288,433	04/08/99	02/12/02	6,347,154	Configurable Horizontal Scaler For Video Decoding And Method Therefore	990031 BT
USA	09/197,720	11/23/98	02/26/02	6,351,681	Method and Apparatus For A Multi-Chip Module That Is Testable And Reconfigurable Based on Testing Results	980004 B
USA	09/356,732	07/19/99	05/14/02	6,389,519	Method and Apparatus for Providing Probe Based Bus Locking And Address Locking	990080 BS
USA	09/407,131	09/27/99	05/21/02	6,393,534	Scheduler For Avoiding Bank Conflicts In Issuing Concurrent Requests to Main Memory	M 5530 S
USA	09/428,850	10/28/99	05/28/02	6,397,379	Recording In A Program Execution Profile References to a Memory-Mapped Active Device	4006 W S
USA	09/730,890	12/06/00	06/25/02	6,411,142	Common Bias and Differential Structure Based DLL with Fast Lockup Circuit and Current Range Calibration for Process Variation	000157 BN
USA	09/333,736	06/15/99	07/23/02	6,424,320	Method and Apparatus for Rendering Video	990069BT
USA	09/418,885	10/14/99	07/23/02	6,424,345	Binsorter Triangle Insertion Optimization	M 7036 S
USA	09/412,026	10/04/99	07/30/02	6,427,177	Method and Apparatus For Configuring Multiple Devices In A Computer System	990151 B T
USA	09/339,732	06/24/99	09/03/02	6,445,392	Method and Apparatus For Simplified Anti-Aliasing In A Video Graphics System	990075 B M
USA	09/328,971	06/09/99	09/10/02	6,449,671	Method and Apparatus For Busing Data Elements	990071 B S
USA	09/451,563	11/30/99	09/17/02	6,452,599	Method and Apparatus For Generating A Specific Computer Hardware Component Exception Handler	990146 B S
USA	09/343,064	06/29/99	09/24/02	6,456,334	Method and Apparatus For Displaying Video In A Data Processing System	990065 B T
USA	09/432,856	11/02/99	09/24/02	6,457,034	Method and Apparatus For Accumulation Buffering In The Video Graphics System	990137 B M
USA	09/212,141	12/15/98	10/08/02	6,462,786	Method and Apparatus For Blending Image Input Layers	990008 B T
USA	09/166,302	10/05/98	12/03/02	6,490,686	Method and Apparatus for Restricting Privileged Access to Distributed Content Information	980088 B

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**SCHEDULE A
USA**

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/388,975	09/02/99	12/24/02	6,499,072	Data Bus Bandwidth Allocation Apparatus and Method	990051 B N
USA	09/675,293	09/29/00	12/31/02	6,502,173	Method Therefore	000143 B T
USA	09/847,914	05/01/01	01/14/03	6,507,886	Scheduler For Avoiding Bank Conflicts In Issuing Concurrent Requests To Main Memory	M 5530 S-D1
USA	09/563,289	05/03/00	01/28/03	6,511,185	Method and Apparatus For Compensating A Projected Image	000020 B T
USA	09/438,573	11/10/99	02/25/03	6,526,459	Allocation of Input/Output Bus Address Space To Native Input/Output Devices	990127 B S
USA	09/416,198	10/11/99	02/25/03	6,526,514	Method and Apparatus For Power Management Interrupt Processing In A Computer System	990124 B S
USA	09/654,671	09/05/00	03/18/03	6,535,208	Method and Apparatus For Locking A Plurality Of Display Synchronization Signals	000048 B T
USA	09/233,815	01/20/99	03/18/03	6,535,217	Integrated Circuit For Graphics Processing Including Configurable Display Interface and Method Therefore	990013 B T
USA	09/376,874	08/18/99	03/25/03	6,539,439	Method and Apparatus For Interfacing A Bus with Input/Output Devices	990105 B S
USA	09/469,084	12/21/99	04/01/03	6,541,996	Dynamic Impedance Compensation Circuit and Method	990117 B T
USA	09/314,209	05/19/99	04/01/03	6,542,159	Apparatus To Control Memory Accesses In A Video System And Method Thereof	990046 B T
USA	09/281,631	03/30/99	04/01/03	6,542,198	Method for optimizing video playback at arbitrary refresh rates	M 7040 S
USA	09/347,201	07/02/99	04/08/03	6,546,449	Video Controller For Accessing Data In A System and Method Thereof	990061 B T
USA	09/506,782	02/18/00	04/08/03	6,546,483	Method and Apparatus For Configuring A Device Assigned To A Fixed Address Space	000024 B T
USA	09/434,198	11/04/99	04/15/03	6,549,959	Detecting Modification to Computer Memory By A DMA Device	4007 W S
USA	09/295,949	04/21/99	04/22/03	6,552,697	Method and Apparatus For Generating Stereoscopic Three-Dimensional Images	990058 B T
USA	09/552,931	04/20/00	04/22/03	6,552,733	Configurable Vertex Blending Circuit and Method Therefore	000076 B O
USA	09/305,401	05/05/99	05/06/03	6,559,844	Method and Apparatus For Generating Multiple Views Using A Graphics Engine	990063 B T
USA	09/211,220	12/14/98	05/13/03	6,563,506	Method and Apparatus For Memory Bandwidth Allocation And Control In A Video Graphics System	990014 B T
USA	09/102,028	06/22/98	05/13/03	6,563,863	Computer Modem	4010 W S
USA	09/416,819	10/08/99	05/20/03	6,567,127	Method and Apparatus For Enhanced Video Encoding	990112 B T

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**SCHEDULE A
USA**

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/557,094	04/21/00	05/27/03	6,570,577	System For Nonlinear Correction of Video Signals and Method Thereof	000067 B T
USA	09/989,266	11/20/01	05/27/03	6,571,315	Cache Memory Management Method and Apparatus For Gating	000012D1
USA	09/415,144	10/11/99	06/03/03	6,574,693	Interrupts In A Computing System	990123 B S
USA	09/323,983	06/01/99	06/10/03	6,577,639	Reducing Modem Transmit Latency	4012 W S
USA	09/310,184	05/12/99	06/17/03	6,581,085	Approximation Circuit and Method	M 7048 S
USA	09/227,432	01/08/99	06/24/03	6,583,793	Method and Apparatus for Mapping Live Video on to Three Dimensional Objects	980097 B
USA	09/652,956	08/31/00	07/15/03	6,594,312	Method and Apparatus For Motion Estimation Quantization Dithering	000074 B S
USA	09/456,624	12/08/99	07/15/03	6,594,750	Method and Apparatus For Handling An Accessed Bit In A Page Table Entry	990110 B S
USA	09/685,699	10/10/00	09/02/03	6,614,675	Pipelined Content Addressable Memory With Read Only Element Encoding Scheme	000155 BN
USA	09/183,610	10/30/98	09/09/03	6,618,026	Method and Apparatus for Controlling Multiple Displays from a Drawing Surface	980091 B
USA	09/518,444	03/03/00	09/16/03	6,621,490	Method and Apparatus For Motion Compensation Using Hardware-Assisted Abstraction Layer	000036 B S
USA	09/556,485	04/21/00	09/23/03	6,624,818	Method and Apparatus For Shared Microcode In A Multi-Thread Computation Engine	000063 B O
USA	09/556,474	04/21/00	10/07/03	6,630,935	Geometric Engine Including A Computational Module For Use In A Video Graphics Controller	000065 B O
USA	09/630,783	08/02/00	10/21/03	6,636,221	Graphics Processing System With Enhanced Bus Bandwidth Utilization and Method Therefore	000031 B M
USA	09/630,782	08/02/00	10/21/03	6,636,223	Graphics Processing System With Logic Enhanced Memory and Method Therefore	000015 BM
USA	09/374,359	08/13/99	10/21/03	6,636,607	Method and Apparatus For Controlling Display of Content Signals	990087 B T
USA	09/451,638	11/30/99	11/04/03	6,643,673	Method and Apparatus For Arithmetic Shifting	990128 B S
USA	09/245,531	02/05/99	11/18/03	6,650,334	Optimizing Texture Tag Checking For Three-Dimensional Computer Graphics	M 7015 S
USA	09/449,956	11/29/99	11/18/03	6,651,159	Floating Point Register Stack Management For CISC	M 5531 S
USA	09/257,291	02/25/99	12/02/03	6,657,634	Dynamic Graphics And/Or Video Memory Power Reducing Circuit and Method	990026 B T
USA	09/614,931	07/12/00	12/02/03	6,658,531	Method and Apparatus For Accessing Graphics Cache Memory	990047 C1
USA	09/579,202	05/26/00	12/09/03	6,662,257	Multiple Device Bridge Apparatus and Method Thereof	000103 B T

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**SCHEDULE A
USA**

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/388,976	09/02/99	12/16/03	6,665,358	Differential Input Receiver And Method For Reducing Noise	990130 BT
USA	09/669,276	09/26/00	12/23/03	6,667,746	Pre-blending Two Textures	000147 BT
USA	09/619,203	07/19/00	12/30/03	6,670,955	Method and System for Sort Independent Alpha Blending Of Graphic Fragments	000094 B M
USA	09/625,581	07/26/00	01/06/04	6,674,441	Method and Apparatus For Improving Performance of An Accelerated Graphics Port (AGP) Device	000056 B S
USA	09/411,661	10/04/99	01/13/04	6,678,780	Method and Apparatus for Supporting Multiple bus masters with the accelerated graphics protocol (AGP) bus	990113
USA	09/628,050	07/27/00	02/03/04	6,687,295	Method and Apparatus For Motion Estimation For Encoding Sequential Frames	000058 B S
USA	09/772,035	01/29/01	02/10/04	6,690,427	Method and Apparatus For De-Interlacing/Re-Interlacing Video By A Graphics Processor	000160 B T
USA	09/518,035	03/03/00	02/17/04	6,693,959	Method and Apparatus for Indexing and Locating Key Frames In Streaming and Variable-Frame- Length Data	000057B5
USA	09/360,568	07/26/99	02/17/04	6,694,461	System and Method For Testing Integrated Memories	990066 B T
USA	09/540,456	03/31/00	02/17/04	6,694,492	Method and Apparatus For Optimizing Production Yield and Operational Performance of Integrated Circuits	000013 B S
USA	09/716,735	11/20/00	03/09/04	6,704,021	Method and Apparatus for Efficiently Processing Vertex Information In a Video Graphics System	000162 B O
USA	09/564,963	05/04/00	04/06/04	6,717,967	Video Compression Method and Apparatus Employing Error Generation and Error Compression	000022 B T
USA	09/457,648	12/09/99	04/13/04	6,720,964	Method and Apparatus For Processing Portions Of Primitives That Are Being Rendered	990139 BM
USA	09/556,472	04/21/00	05/04/04	6,731,294	Vector Engine with Pre-Accumulation Buffer and Method Therefore	000060 B O
USA	09/527,754	03/17/00	06/01/04	6,744,432	Method and Apparatus For Determining A Representative Z Values In A Video Graphics System	000032 B M
USA	09/376,830	08/18/99	06/01/04	6,745,318	Method and Apparatus Of Configurable Processing	990093 B S
USA	09/553,977	04/20/00	06/08/04	6,747,654	Multiple Device Frame Synchronization Method and Apparatus	990150 B T

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PATENT

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**SCHEDULE A
USA**

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/537,993	03/29/00	06/08/04	6,748,490	Method and Apparatus For Maintaining Data Coherency In A Shared Memory System	000033 B M
USA	09/551,107	04/18/00	06/08/04	6,748,496	Method and Apparatus For Providing Cacheable Data To A Peripheral Device	000044 B T
USA	09/268,475	03/16/99	06/15/04	6,750,920	Method and Apparatus For Adjusting Bias and Amplitude of A Video Signal System For Responding To A Power Saving Mode and Method Thereof	990033 B T
USA	09/387,535	08/31/99	06/15/04	6,751,742	Adapter And Method To Connect A Component Video Input Television To A Video Providing Unit	990099 B T
USA	09/703,784	11/01/00	06/22/04	6,753,881	Method and Apparatus For Asynchronous Frame Synchronization Display FIFO Memory Management System	000049 B T
USA	09/316,458	05/21/99	05/22/04	6,754,234	Method and Apparatus For Cache Management For A Digital VCR Archive	990054 B T
USA	09/369,784	08/06/99	06/29/04	6,756,988	Modifying Program Execution Based on Profiling	990121 B T
USA	09/536,431	03/27/00	07/06/04	6,760,535	Method and Apparatus For Multi-TV Tuner Display of Video Information	000045 B T
USA	09/339,797	06/24/99	07/13/04	6,763,452	Method and Apparatus For Improving Processing Throughput in A Video Graphics System	4019 W S
USA	09/691,876	10/19/00	07/20/04	6,766,100	Method and Apparatus For Improving Palette Anti-Sparkle Enhancement	000080 B T
USA	09/759,537	01/12/01	08/03/04	6,771,269	Variable Length Code Decoder	000163 B O
USA	09/707,037	11/06/00	08/10/04	6,774,903	Method and Apparatus For Out of Order Memory Processing Within An In Order Processor	000149 B T
USA	09/444,010	11/19/99	08/10/04	6,775,414	Memory Range Access Flags for Performance Optimization	M 5548 S
USA	09/416,196	10/11/99	08/10/04	6,775,756	Method of Filtering and Apparatus Therefore	990125 B S
USA	09/710,943	11/13/00	08/17/04	6,778,178	Computer Execution by Opportunistic Adaptation	000151 B T
USA	09/570,341	05/12/00	08/17/04	6,778,600	Software Test System and Method	000117 B T
USA	09/429,377	10/28/99	08/17/04	6,779,107	Video Graphic Interface Device And Method For Portrait and Landscape Image Display Modes	40048 W S
USA	09/603,946	06/27/00	08/17/04	6,779,134	Apparatus and Method For Transmitting Data	000046 B T
USA	09/834,661	04/13/01	08/24/04	6,781,587	Safety Net Paradigm For Managing Two Computer Execution Modes	000146 B T
USA	09/579,203	05/26/00	09/07/04	6,789,154	Method and Apparatus For Compressing and Storing Image Data	000102 B T
USA	09/432,753	11/03/99	09/07/04	6,789,181	Method and Apparatus For Transposing Bits	4020 W S
USA	09/349,744	07/08/99	10/19/04	6,807,311		990055 B T
USA	09/471,350	12/23/99	11/09/04	6,816,593		000016 B S

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PATENT
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SCHEDULE A

USA

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/408,991	09/29/99	11/30/04	6,825,842	Method and System For Queueing Draw Operations	990129 B T
USA	09/339,749	06/24/99	11/30/04	6,826,748	Profiling Program Execution Into Registers of a Computer	4018 W S
USA	09/655,552	09/06/00	12/14/04	6,831,492	Common-Bias and Differential Structure Based DLL	000120 BN
USA	09/534,656	03/24/00	12/14/04	6,831,652	Method and System For Storing Graphics Data	000070 B T
USA	10/123,983	04/16/02	12/21/04	6,833,746	Pre-Buffer Voltage Level Shifting Circuit and Method	990018 C1
USA	09/626,655	07/27/00	01/25/05	6,847,408	Method and Apparatus For Reducing Noise In An Image Sequence	000075 B S
USA	09/287,776	04/07/99	02/08/05	6,853,355	Switchable Video Overlay Apparatus and Method	990027 B T
USA	09/305,380	05/05/99	03/15/05	6,868,224	Method and Apparatus For Providing Multimedia Playback Controlling Two Monitors with Transmission of Display Data Using A FIFO Buffer	990045 B T
USA	09/308/707	05/21/99	03/22/05	6,870,518	Video Graphics System That Includes Custom Memory and Supports Anti Aliasing and Method Therefor	FIRE03 US
USA	09/630,816	08/02/00	03/29/05	6,873,323	Method and Apparatus For Virtual Address Translation	000098 B M
USA	09/352,959	07/14/99	04/26/05	6,886,090	Logic Enhanced Memory and Method Therefor	990094 B S
USA	09/630,784	08/02/00	05/31/05	6,900,812	Method and Apparatus For Improved Graphics Rendering Performance	000079 B T
USA	09/789,074	02/20/01	06/07/05	6,903,739	Graphic Display System Having A Frame Buffer With First and Second Memory Portions	000096 B M
USA	09/436,882	11/09/99	06/07/05	6,904,515	Multi-Instruction Set Flag Preservation Apparatus and Method	990096 B S
USA	09/687,322	10/13/00	06/14/05	6,907,597	Method and Apparatus For Constructing An Executable Program In Memory Apparatus For High Data Rate Synchronous Interface and Method Thereof	000081 B T
USA	09/657,255	01/02/01	07/12/05	6,918,047	Output Driver Apparatus And Method Thereof	000141 B T
USA	09/643,090	08/21/00	07/26/05	6,922,079	Method and Apparatus For Providing A Bus-Encrypted Copy Protection Key To An Unsecured Bus	000142 B T
USA	09/798,538	03/02/01	08/23/05	6,934,389	Exception Mechanism For A Computer	000129 B T
USA	09/667,226	09/21/00	08/23/05	6,934,832	Method and Apparatus for Processing Non-Planar Video Graphics Primitives	000052 B S
USA	09/852,808	05/10/01	09/06/05	6,940,503		000030 BM

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USA**

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/322,443	05/28/99	09/06/05	6,941,545	Profiling of Computer Programs Executing In Virtual Memory Systems	4003 W S
USA	09/741,456	12/19/00	09/27/05	6,950,772	Dynamic Component To Input Signal Mapping System	000050 B T
USA	09/628,867	07/27/00	11/08/05	6,964,054	Method and Apparatus For Posting A Computer Having Multiple Video Adapters	000132 B T
USA	09/552,932	04/20/00	11/22/05	6,967,664	Method and Apparatus For Primitive Processing In A Graphics System	000077 B O
USA	09/712,360	11/14/00	12/13/05	6,975,809	Method and Apparatus For Passing Clear DVD Data In A Computer	000159 BM
USA	09/168,023	10/08/98	12/13/05	6,976,265	Method and Apparatus for Controlling Display of Content Signals	980090 B
USA	09/332,263	06/11/99	12/12/05	6,978,462	Profiling Execution of a Sequence of Events Occuring During a Profiled Execution Interval that Matches Time-Independent Selection Criteria of Events to be Profiled	4015 W S
USA	09/669,274	09/26/00	12/27/05	6,980,787	Image Rejection Mixing in WideBand Applications	000150 BT
USA	09/574,327	05/19/00	02/28/06	7,006,117	Apparatus For Testing Digital Display Driver and Method Thereof	000071 B T
USA	09/563,483	05/02/00	03/14/06	7,012,613	Method and Apparatus For Fragment Scriptor For Use in Over-Sampling Anti-Aliasing	000054 B M
USA	09/334,530	06/16/99	03/14/06	7,013,456	Profiling Execution of Computer Programs	4017 W S
USA	09/633,463	08/07/00	03/21/06	7,015,976	Automatic Panning of Digital Content While Zoomed	000108 B T
USA	09/687,858	10/13/00	03/21/06	7,017,070	Apparatus For Synchronization of Double Data Rate Signaling	000078 B T
USA	09/666,110	09/20/00	05/16/06	7,047,394	Computer For Execution of Two Instruction Sets	000051 B S
USA	09/549,782	04/14/00	05/23/06	7,050,460	Method and Apparatus For Multiplexing Data Streams	000073 B S
USA	09/923,759	08/06/01	05/30/06	7,053,863	Wireless Device Method and Apparatus with Drawing Command	000135 BT
USA	09/850,356	05/07/01	05/30/06	7,055,038	Method and Apparatus For Maintaining Secure And NonSecure Data In A Shared Memory System	000128 BT
USA	09/272,069	03/18/99	06/13/06	7,061,504	Method and Apparatus For Configurable Gamma Correction In A Video Graphics Circuit	990034 B T
USA	09/626,325	07/26/00	06/20/06	7,065,633	System For Delivering Exception Raised In First Architecture To Operating System Coded In Second Architecture In Dual Architecture CPU	000053 B S
USA	09/387,534	08/31/99	06/27/06	7,068,329	Method and System For Providing A Video Signal	990102 B T

**SCHEDULE A
USA**

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/429,094	10/28/99	06/27/06	7,069,421	Side Tables Annotating an Instruction Stream	4004C W S
USA	09/669,352	09/26/00	08/08/06	7,089,426	Method and System For Encryption System For Digital Time Shifting and Method Thereof	000092 B T
USA	09/707,060	11/06/00	08/22/06	7,095,945	Method and Apparatus For Configuring An Input/Output Buffer For An Integrated Bus Bridge and Graphics Controller	000069 B T
USA	09/586,868	06/05/00	08/22/06	7,096,303	Method and Apparatus To Optimize Receiving Signals Reflection	000043 B T
USA	09/651,944	08/31/00	09/12/06	7,106,125	Method and Apparatus For Multi-Thread Accumulation Buffering In A Computation Engine	000152 B T
USA	09/556,473	04/21/00	09/19/06	7,111,156	Profiling Program Execution To Identify Frequently Executed Portions and to Assist Binary Translation	000061 B O
USA	09/425,401	10/22/99	09/19/06	7,111,290	Method and Apparatus For High Speed Block Mode Triangle Rendering	4016 W S
USA	09/629,415	08/01/00	10/24/06	7,126,600	Method and Apparatus For Providing A Bus-Encrypted Copy Protection Key To An Unsecured Bus	000115 B M
USA	11/175,882	07/06/05	10/31/06	7,130,425	Profiling Ranges of Execution of A Computer Program	000129 C1
USA	09/330,852	06/11/99	11/14/06	7,137,110	Method and Apparatus For Tessellation Lighting	4014 W S
USA	09/585,217	06/01/00	12/05/06	7,145,564	Write Once System and Method for Facilitating Digital Encrypted Transmissions	000035 B M
USA	09/704,329	11/02/00	02/13/07	7,178,035	Managing Instruction Side Effects	000153 B T
USA	09/672,440	09/28/00	06/05/07	7,228,404	Single Gate Oxide Differential Receiver and Method	000125 B S
USA	09/211,469	12/14/98	07/03/07	7,239,198	Encryption/Decryption Instruction Set Enhancement	990020 B T
USA	09/419,828	10/14/99	08/07/07	7,254,231	Detecting Reordered Side-Effects	M 7084 S
USA	09/434,394	11/04/99	08/07/07	7,254,806	Executing Programs For a First Computer Architecture on a Computer of a Second Architecture	4009 W S
USA	09/239,194	01/28/99	09/25/07	7,275,246	Method and Apparatus For Rate Control For Constant-Bit-Rate Finite-Buffer-Size Video Encoder	4013 W S
USA	09/552,761	04/18/00	10/02/07	7,277,483	Method and Apparatus For Independent Video and Graphics Scaling In A Video Graphics System	000055 B S
USA	09/213,748	12/17/98	04/29/08	7,365,757	Method and Apparatus For Protection of Computer Assets From Unauthorized Access	980041 B
USA	09/586,907	06/05/00	06/24/08	7,392,398	Method and Apparatus For Detecting a Flat Panel Display Monitor	000037 B S
USA	09/432,855	11/02/99	08/19/08	7,414,606	Optimized Primitive Filler	990136 B T
USA	09/629,337	08/01/00	08/19/08	7,414,635		000116 B M

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SCHEDULE A
USA

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/484,123	01/13/00	01/27/09	7,483,042	Video Graphics Module Capable Of Blending Multiple Image Layers	990021 B T
USA	11/282,413	11/18/05	05/05/09	7,530,007	Iterative Decoding of Packet Data	040044 N
USA	10/075,149	02/14/02		7,543,101	System of Accessing Data In A Video System and Method Thereof	990060 D-1
USA	10/865,469	06/10/04	08/11/09	7,574,065	Variable Length Code Decoder	M 5548 D1
USA	09/314,210	05/19/99	01/09/01	6,173,367	Method and Apparatus For Accessing Graphics Cache Memory	990047 BT
USA	09/186,037	11/04/98	01/30/01	6,182,196	Method and Apparatus For Arbitrating Access Requests To A Memory	980098 B
USA	09/219,469	12/23/98	02/13/01	6,188,437	Deinterlacing Technique	M6000 S
USA	09/165,181	10/01/98	02/20/01	6,191,622	Time Interleaved Common Mode Feedback Circuit With Process Compensated Bias	M4952 US
USA	09/213,195	12/17/98	02/20/01	6,191,788	Method and Apparatus For Approximating NonLinear Functions In A Graphics System	990006 BM
USA	09/100,499	06/19/98	03/06/01	6,199,090	Double Incrementing, Low Overhead, Adder	M5611 US
USA	09/166,346	10/05/98	03/13/01	6,201,547	Method and Apparatus For Sequencing Texture Updates In A Video Graphics System	980076 B
USA	09/185,879	11/03/98	03/27/01	6,208,354	Method and Apparatus For Displaying Multiple Graphics Images In A Mixed Video Graphics Display	980035 B
USA	09/351,096	07/09/99	04/10/01	6,215,341	Deceleration Circuit	990082 BT
USA	09/213,191	12/17/98	06/05/01	6,243,144	Adaptive False Color Video Filter and Method	980029 B
USA	09/211,692	12/14/98	06/19/01	6,249,288	Multi Thread Display Controller	M 5991 S
USA	09/099,850	06/19/98	06/19/01	6,249,799	A Selective Carry Boundary Concurrent Discrete Time Oscillators (DTO) For Video and Closed Caption Encoding	M 5826 S
USA	09/170,519	10/13/98	07/03/01	6,256,070	Interpreter With Reduced Memory Access and Improved Jump-Through-Register Handling	980086 BN
USA	09/134,042	08/14/98	07/03/01	6,256,784	Apparatus and Method For Configurable Use of Groups of Pads Of A System on a Chip	M 5526 S
USA	09/435,116	11/05/99	07/17/01	6,262,594	Two-Port Memory to Connect Multiprocessor Bus to Multiple Peripherals	M 5557 S
USA	09/136,209	08/18/98	07/17/01	6,263,390	Three Level Pre-Buffer Voltage Level Shifting Circuit and Method	M 5534 S
USA	09/609,022	06/30/00	07/31/01	6,268,744	Method and Apparatus For More Accurate Color Base Conversion Of YUV Video Data	990038-C1
USA	09/324,211	06/02/99	07/31/01	6,268,847	Method and Apparatus For Detecting Interrupt Requests In Video Graphics and Other Systems	990022 BT
USA	09/231,394	03/13/99	08/21/01	6,279,067		990035 BT

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SCHEDULE A
USA

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/328,976	06/09/99	08/21/01	6,279,080	Method and Apparatus for Association of Memory Locations with a Cache Location Having a Flush Buffer	980101
USA	09/226,931	01/08/99	09/04/01	6,285,371	Method and Apparatus for Providing a Three Dimensional Transition between Displayed Images	980095 BM
USA	09/100,385	06/19/98	09/04/01	6,286,023	A Partitioned Adder Tree Supported By A Multiplexer Configuration	M 5525 S
USA	09/310,564	05/12/99	09/04/01	6,286,092	Paged Based Memory Address Translation Table Update Method and Apparatus	990050 BN
USA	09/259,373	02/26/99	09/11/01	6,288,729	Method and Apparatus for A Graphics Controller To Extend Graphics Memory	990009 BT
USA	09/223,506	12/30/98	10/02/01	6,287,852	Video Display Method and Apparatus With Synchronized Video Playback and Weighted Frame Creation	980092 B
USA	09/283,693	04/01/99	10/09/01	6,301,596	Partial Sum Filter And Method Therefore	990032 BT
USA	09/376,824	08/18/99	10/09/01	6,301,648	Method and Apparatus For Processing Memory Accesses Utilizing a TLB	990103 BS
USA	09/432,874	11/02/99	11/06/01	6,314,490	Method and Apparatus For Memory Addressing	990138 B M
USA	09/328,333	06/09/99	11/20/01	6,321,314	Method and Apparatus For Restricting Memory Access	990072 B S
USA	09/328,332	06/09/99	11/27/01	6,324,635	Method and Apparatus For Address Paging Emulation	990073 BS
USA	09/186,034	11/03/98	12/04/01	6,326,984	Method and Apparatus For Storing And Displaying Video Image Data In A Video Graphics System	980042 B
USA	09/183,884	10/30/98	12/04/01	6,327,002	Method and Apparatus for Video Signal Processing in a Video System	990001 BT
USA	09/149,881	09/08/98	12/11/01	6,330,581	Apparatus and A Method For Address Generation	M 5533 S
USA	09/227,635	01/08/99	12/18/01	6,331,852	Method and Apparatus for Providing a Three Dimensional Object on Live Video	980096 B
USA	09/166,033	10/05/98	12/18/01	6,331,854	Method and Apparatus for Accelerating Animation in a Video Graphics System	980075 B
USA	09/376,831	08/18/99	12/18/01	6,332,184	Method and Apparatus For Modifying Memory Accesses Utilizing TLB Entries	990104 B S
USA	09/212,132	12/15/98	01/01/02	6,335,761	Method and Apparatus For Converting Color Base Of An Image Layer	990011 B T
USA	09/356,398	07/16/99	01/15/02	6,339,428	Method and Apparatus for Compressed Texture Caching in a Video Graphics System	990089 B M

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**SCHEDULE A
USA**

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/483,196	01/14/00	01/22/02	6,340,943	Analog to Digital Converter Method and Apparatus	990132 BT
USA	09/350,625	07/09/99	01/29/02	6,342,996	Single Gate Oxide High to Low Level Converter Circuit With Overvoltage Protection	990083 BT
USA	09/365,570	08/02/99	02/26/02	6,351,182	Circuit and Method For Providing A Reference Voltage	990116 B T
USA	09/183,880	10/30/98	02/26/02	6,351,222	Method and Apparatus for Receiving An Input By An Entertainment Device	990003 BT
USA	09/527,753	03/17/00	03/05/02	6,353,874	Method and Apparatus For Controlling And Caching Memory Read Operations In A Processing System	000034 BM
USA	09/614,084	07/11/00	03/19/02	6,359,485	Differential Input Receiver and Method for Reducing Noise	990019 C1
USA	09/272,857	03/19/99	03/26/02	6,362,942 B2	Input Stage Protection Circuit For A Receiver	990037 B T
USA	09/206,679	12/07/98	04/02/02	6,366,325	Single Port Video Capture Circuit and Method	980093 B
USA	09/272,463	03/19/99	04/09/02	6,370,630	Method and Apparatus For Controlling Data Flow In A Data Processor	990028 B T
USA	09/379,197	08/20/99	04/16/02	6,373,282	Single Gate Oxide Cascaded Output Buffer Stage and Method	990115 BT
USA	09/146,484	09/02/98	04/16/02	6,374,341	Apparatus and A Method For Variable Size Pages Using Fixed Size Translation Lookaside Buffer Entries	M 5528 S
USA	09/484,552	01/18/00	04/23/02	6,375,495	Mixed Signal Connector	990140 B T
USA	09/426,323	10/25/99	05/07/02	6,385,712	Method and Apparatus For Segregation of Virtual Address Space	990107 B S
USA	09/159,271	09/22/98	05/21/02	6,393,453	Circuit and Method For Fast Squaring	M 5890 S
USA	09/407,224	09/28/99	05/21/02	6,393,512	Circuit and Method For Detecting Bank Conflicts in Accessing Adjacent Banks	M 7126 S
USA	09/493,043	01/27/00	05/21/02	6,393,522	Method and Apparatus for Cache Memory Management	000012 BS
USA	09/215,413	12/18/98	05/21/02	6,393,527	Prefetch Buffer With Continue Detect	M 7013 S
USA	09/272,464	03/19/99	05/28/02	6,397,327	Method and Apparatus For Configuring A Computer System	990030 B T
USA	09/388,988	09/02/99	06/04/02	6,400,546	I/O Pad Voltage Protection Circuit and Method	990131 BT
USA	09/238,669	01/26/99	06/04/02	6,400,895	Method for Optimizing MPEG-2 Video Playback Consistency	M 7043 S
USA	09/717,547	11/21/00	06/11/02	6,404,428	Method & Apparatus For Selectively Providing Drawing Commands To A Graphics Processor To Improve Processing Efficiency Of A Video Graphics System	000164 B0
USA	09/288,730	04/09/99	06/18/02	6,407,742	Method and Apparatus for Combining Multiple Line Elements to Produce Resultant Line Data	990015 B T

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**SCHEDULE A
USA**

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/344,175	06/24/99	07/02/02	6,415,311	Sign Extension Circuit and Method For Unsigned Multiplication and Accumulation	M 5679 S
USA	09/473,154	12/28/99	07/09/02	6,418,524	Method and Apparatus For Dependent Segmentation and Paging Processing	000019 B S
USA	09/211,496	12/14/98	08/06/02	6,429,716	Pre-Buffer Voltage Level Shifting Circuit and Method	990018 B T
USA	09/158,623	09/22/98	08/06/02	6,430,357	Text Data Extraction System For Interleaved Video Data Streams	980071 B
USA	09/377,004	08/18/99	08/06/02	6,430,646	A Method and Apparatus For Interfacing A Processor With A Bus	990095 B S
USA	09/364,442	07/30/99	08/13/02	6,433,788	Dual Fragment-Cache Pixel Processing Circuit and Method Therefore	990091 B M
USA	09/335,351	06/17/99	08/27/02	6,442,582	Multiplier Carry Bit Compression Apparatus and Method	990079 B S
USA	09/376,190	08/18/99	08/27/02	6,442,656	Method and Apparatus For Interfacing Memory With A Bus	990106 B S
USA	09/164,948	10/01/98	09/03/02	6,445,322 B2	Digital-to-Analog Converter with Improved Output Impedance Switch	M 6057 S
USA	09/690,396	10/17/00	09/03/02	6,445,329	High Speed Analog to Digital Converter	000158 BN
USA	09/212,197	12/15/98	09/03/02	6,445,394	Serialized Mapped Memory Configuration For A Video Graphics Chip	980074 B
USA	09/259,372	02/26/99	09/10/02	6,448,974	M&A For Chroma Key Data Modifying Insertion Without Video Image Fragmentation	990010 B T
USA	09/370,800	08/09/99	09/17/02	6,452,591	Method & Apparatus For A Data Transmitter	990086 BT
USA	09/459,809	12/13/99	09/17/02	6,452,602	Method and Apparatus For Storing Compressed Data	000005 B M
USA	09/449,012	11/24/99	09/17/02	6,453,330	High Precision Bilinear Interpolation Method and Apparatus For Multi-Pass	M 7016 S
USA	09/457,819	12/09/99	09/24/02	6,456,291	Texture Mapping	000007 B M
USA	09/496,731	02/02/00	10/01/02	6,459,438	Method and Apparatus For Determining Clipping Distance	000010 B O
USA	09/272,858	03/19/99	10/01/02	6,459,553	Single Gate Oxide Electrostatic Discharge Protection Circuit	990036 B T
USA	09/158,748	09/22/98	10/01/02	6,460,065	A Circuit and Method For Partial Product Bit Shifting	M 5988 S
USA	09/347,202	07/02/99	10/22/02	6,469,703	System of Accessing Data in A Graphics System and Method Thereof	990060 B T
USA	09/457,649	12/09/99	10/29/02	6,473,086	Method and Apparatus For Graphics Processing Using Parallel Graphics Processors	000006 B M
USA	09/240,812	03/03/99	10/29/02	6,473,101	Single Surface Multi-View Panning System And Method For Multiple Displays	980094 B

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PATENT
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SCHEDULE A
USA

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/387,870	09/01/99	11/05/02	6,476,811	M&A For Compressing Parameter Values For Pixels In A Display Frame	990077 B M
USA	09/891,931	06/26/01	11/12/02	6,480,051 B2	Voltage Supply Discriminator and Method	990017 C1
USA	09/527,752	03/17/00	11/19/02	6,483,505	Method and Apparatus For Multipass Pixel Processing	000004 B M
USA	09/314,561	05/19/99	11/26/02	6,486,884	Apparatus For Accessing Memory In A Video System and Method Thereof	990057 B T
USA	09/337,293	06/21/99	12/31/02	6,501,479	Multi-Format Vertex Data Processing Apparatus and Method	990052 B N
USA	09/314,208	05/19/99	01/07/03	6,504,549	Apparatus To Arbitrate Among Clients Requesting Memory Access In a Video System and Method Thereof	990056 B T
USA	09/496,732	02/02/00	01/14/03	6,507,348	Method and Apparatus For Clipping An Object Element in Accordance With A Clip Volume	000008 B O
USA	09/496,735	02/02/00	01/28/03	6,512,524	Method and Apparatus For Object Element Attribute Determination Using Barycentric Coordinates	000009 B O
USA	09/630,914	08/02/00	03/11/03	6,532,515	Method and Apparatus For Performing Selective Data Reads From A Memory	000099 B M
USA	09/675,368	09/29/00	03/11/03	6,532,525	Method and Apparatus For Accessing Memory	000144 B T
USA	09/498,586	02/04/00	04/22/03	6,553,445	Method and Apparatus For Reducing Noise Associated With Switched Outputs	000072 B T
USA	09/339,886	06/25/99	05/06/03	6,559,859	Method and Apparatus For Providing Video Signals	990062 B T
USA	09/626,657	07/27/00	05/20/03	6,567,084	Lighting Effect Computation Circuit and Method Thereof	000059 B O
USA	09/451,300	11/29/99	06/10/03	6,578,134	Multi-Branch Resolution	M 5524 S
USA	09/272,461	03/19/99	08/12/03	6,606,098	Method and Apparatus Having An Extended Video Graphics Bus	990042 B T
USA	09/316,441	05/21/99	08/12/03	6,606,450	Method and Apparatus For Processing Video Signals Having Associated Access Restriction Data	990041 B T
USA	09/350,778	07/09/99	09/09/03	6,618,508	Motion Compensation Device	M 7014 S
USA	09/225,117	01/04/99	09/16/03	6,621,499	Video Processor With Multiple Overlay Generators and/or Flexible Bidirectional Video Data Port	980027 B
USA	09/226,982	01/08/99	09/16/03	6,621,509	Method and Apparatus For Providing A Three Dimensional Graphical User Interface	980099 B
USA	09/280,359	03/29/99	09/23/03	6,624,797	Method and Apparatus For Providing Video and Control To A Monitor	990048 B T
USA	09/579,432	05/28/00	10/14/03	6,633,296	Apparatus For Providing Data To A Plurality Of Graphics Processors and Method Thereof	000104 B T

SCHEDULE A
USA

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/416,197	10/11/99	10/14/03	6,633,840	Method and Apparatus For Processing Interrupts In A Computing System	990122 B S
USA	09/556,475	04/21/00	10/28/03	6,640,299	Method and Apparatus For Arbitrating Access To A Computational Engine For Use In A Video Graphics Controller	000066 B O
USA	09/376,820	08/18/99	11/04/03	6,643,726	Method of Manufacturer and Apparatus Of An Integrated Computing System	990092 B S
USA	09/438,308	11/11/99	11/04/03	6,643,756	System and Method for Accessing Data	990118 BT
USA	09/730,954	12/06/00	11/11/03	6,646,512 B2	Self-Bias and Differential Structure Based PLL With Fast Lockup Circuit and Current Range Calibration For Process Variation	000156 BN
USA	09/324,210	06/02/99	11/25/03	6,654,023	Method and Apparatus For Controlling Mip Map Transitions In A Video Graphics System	990088 B M
USA	09/490,888	01/27/00	11/25/03	6,654,872	Variable Length Instruction Alignment Device and Method	990098 B S
USA	09/579,224	05/26/00	12/30/03	6,670,958	Method and Apparatus For Routing Data To Multiple Graphics Devices	000100 B T
USA	09/471,878	12/23/99	01/06/04	6,675,181	Method and Apparatus For Determining A Byte Select Vector For A Crossbar Shifter	000017 B S
USA	09/556,470	04/21/00	01/06/04	6,675,285	Geometric Engine Including A Computational Module Without Memory Contention	000064 B O
USA	09/336,491	06/18/99	01/13/04	6,678,465	Method and Apparatus For Restricting A Video Output Of A Computing System Based On Copy Protection Information	980053 B
USA	09/496,730	02/02/00	02/03/04	6,686,924	Method and Apparatus For Parallel Processing of Geometric Aspects of Video Graphics Data	000011 B O
USA	09/316,442	05/21/99	02/10/04	6,690,880	Method and Apparatus For Copy Protection Detection In a Video Signal	990053 B T
USA	09/724,456	11/28/00	02/24/04	6,697,033	Method and System For Changing A Display Device On A Computer System During Operation Thereof	000131 B T
USA	09/425,037	10/19/99	03/02/04	6,701,426	Multiple Instruction Set Processor With Adaptive Branch Prediction	990081 B S
USA	09/518,889	03/06/00	03/30/04	6,714,720	Method and Apparatus For Storing Multi-Media Data	000040 B S
USA	09/699,858	10/30/00	03/30/04	6,715,053	Method and Apparatus For Controlling Memory Client Access to Address Ranges In a Memory Pool	000139 BT
USA	09/433,101	11/03/99	04/06/04	6,717,989	Video Decoding Apparatus and Method For A Shared Display Memory System	990012 B T

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SCHEDULE A
USA

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	09/579,006	05/26/00	04/27/04	6,728,820	Method of Configuring, Controlling, and Accessing A Bridge and Apparatus Therefor	000101 B T
USA	09/556,471	04/21/00	04/27/04	6,728,869	Method and Apparatus For Memory Latency Avoidance In A Processing System	000062 B O
USA	09/607,814	06/30/00	07/20/04	6,766,427	Method and Apparatus For Loading Data From Memory To A Cache	000029 B S
USA	09/390,261	09/03/99	10/26/04	6,809,737	Method and Apparatus For Supporting Multiple Monitor Orientations	990085 B T
USA	09/181,973	10/29/98	01/25/05	6,847,335	Serial Communication Circuit With Display Detector Interface Bypass Circuit	980073 B
USA	09/325,882	06/04/99	01/25/05	6,848,058	Power Reduction Circuit and Method With Multi Clock Branch Control	990039 B T
USA	09/471,676	12/23/99	02/01/05	6,850,692	Method and Apparatus For Successive Linear Approximation To Obtain A specific Point of A Non-Linear Monotonic Function	000018 B S
USA	09/397,188	09/16/99	02/08/05	6,853,381	Method and Apparatus For A Write Behind Raster	990067 B T
USA	09/348,317	07/07/99	10/11/05	6,954,923	Recording Classification of Instructions Executed by a Computer	4002 W S
USA	09/270,256	03/15/99	01/02/07	7,158,140	Method and Apparatus For Rendering An Image In A Video Graphics Adapter	990044 B T
USA	10/731,217	12/09/03	10/13/09	7,603,025	Method and Apparatus For Copy Protection Detection In a Video Signal	990053 D1
USA	09/491,121	01/24/00			Method and System For Handling Data	000001 B T
USA	11/681,492	03/02/07			Method and Apparatus For Rate Control For Constant-Bit-Rate Finite-Buffer-Size Video Encoder	000055 C1
USA	11/492,681	07/25/06			System For Digital Time Shifting and Method Thereof	000069C
USA	09/923,768	08/06/01			Wireless Display Apparatus and Method	000082 B T
USA	10/869,165	06/16/04			Method and System for Using General and Appliance Operating Systems in a Single Information Handling Device	000090 C-1
USA	09/645,670	08/24/00			Method and Apparatus For Automatic Time-Shifting For A Content Recorder	000107 B T
USA	09/672,841	09/28/00			Validation of Memory References	000126 B S
USA	11/300,542	12/14/05			Method and Apparatus For Maintaining Secure And NonSecure Data In A Shared Memory System	000128 C-1
USA	10/074,064	02/12/02			Method and Apparatus For A Data Bridge In A Computer System	000130 B T

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USA

Country Code	Application No	Application Date	Grant Date	Patent No	Title	Docket No
USA	11/649,612	01/04/07			Write Once System and Method for Facilitating Digital Encrypted Transmissions	000153 C1
USA	11/982,419	10/31/07			Computer with Two Execution Modes	4000 C1
USA	09/385,394	08/30/99			Computer For Executing Two Different Instruction Sets	4000 W S
USA	11/003,768	12/02/04			Transferring Execution From One Computer Instruction Stream To Another	4004
USA	11/004,729	12/02/04			Table Look-Up for Control of Instruction Execution	4004A
USA	11/904,007	09/26/07			Executing Programs For a First Computer Architecture on a Computer of a Second Architecture	4013 C1
USA	11/855,676	09/14/07			Method and Apparatus For Independent Video and Graphics Scaling In A Video Graphics Systems	980041 C1
USA	09/301,249	04/28/99			Method and Apparatus For Determining View Parameters of One Channel While Viewing Another Channel	980049 B
USA	11/238,230	09/29/05			Method and Apparatus for Controlling Display of Content Signals	980090 C-1
USA	09/227,706	01/08/99			Method and Apparatus for Providing User Interface Operation	980100 B
USA	12/429,833	04/24/09			Memory Device for Providing Data in a Graphics System and Method and Apparatus Thereof	990060D1C1
USA	09/398,913	09/14/99			Method and Apparatus For Receiving Digital Video Signals	990068 B T
USA	09/981,484	10/17/01			Method and Apparatus for Rendering Video	990069 C-1
USA	09/484,549	01/18/00			Method and Apparatus For Dynamic Allocation of Processing Resources	990108 B S
USA	09/471,877	12/23/99			Method of Integrating A Personal Computing System and Apparatus Thereof	990145 B S
USA	09/473,316	12/28/99			Method and Apparatus For Graphics Rasterization	ARTX007
USA	12/428,045	04/22/09			Variable Length Code Decoder	M 5548 C1

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RECORDED: 11/20/2009

PATENT

REEL: 022674 FRAME: 0626

PATENT

RECORDED: 11/14/2012

REEL: 029300 FRAME: 0347