

## PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
NVIDIA INTERNATIONAL INC.	12/03/2012
RECEIVING PARTY DATA	
Name:	NVIDIA CORPORATION
Street Address:	2701 SAN TOMAS EXPRESSWAY
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95050
PROPERTY NUMBERS Total: 106	
Property Type	Number
Patent Number:	5422996
Patent Number:	5293474
Patent Number:	5032981
Patent Number:	5185602
Patent Number:	5412800
Patent Number:	5402506
Patent Number:	5313224
Patent Number:	5748163
Patent Number:	5577203
Patent Number:	5581280
Patent Number:	5553220
Patent Number:	6208325
Patent Number:	5436641
Patent Number:	6075513
Patent Number:	5642139

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Patent Number:	6023266
Patent Number:	5625768
Patent Number:	5664162
Patent Number:	5684941
Patent Number:	5611041
Patent Number:	5543850
Patent Number:	5598525
Patent Number:	6121949
Patent Number:	5630174
Patent Number:	5712657
Patent Number:	5680591
Patent Number:	6078319
Patent Number:	5630043
Patent Number:	5699498
Patent Number:	5859635
Patent Number:	5657055
Patent Number:	5742298
Patent Number:	5748920
Patent Number:	5929837
Patent Number:	6097401
Patent Number:	5850207
Patent Number:	5815168
Patent Number:	5812138
Patent Number:	5917505
Patent Number:	5835153
Patent Number:	5850266
Patent Number:	5740383
Patent Number:	5754191
Patent Number:	6121974
Patent Number:	6326975
Patent Number:	5798763
Patent Number:	5959637
Patent Number:	5909219
Patent Number:	5936635
Patent Number:	5748968

	5828382
Patent Number:	6204863
Patent Number:	6310599
Patent Number:	5987582
Patent Number:	5929869
Patent Number:	5973701
Patent Number:	5831640
Patent Number:	5881016
Patent Number:	6266753
Patent Number:	5841442
Patent Number:	6052127
Patent Number:	5835097
Patent Number:	6130674
Patent Number:	5940090
Patent Number:	6184893
Patent Number:	6002409
Patent Number:	5999200
Patent Number:	6088046
Patent Number:	5949421
Patent Number:	6259455
Patent Number:	6032201
Patent Number:	6157386
Patent Number:	5986663
Patent Number:	6061073
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Patent Number:	5999199
Patent Number:	6115032
Patent Number:	6219040
Patent Number:	6292191
Patent Number:	6054993
Patent Number:	6181347
Patent Number:	6252606
Patent Number:	5751265
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Patent Number:	5543842

	5539464
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Patent Number:	5682170
Patent Number:	5778250
Patent Number:	5862320
Patent Number:	5844576
Patent Number:	5969728
Patent Number:	6215459
Patent Number:	6542150
Patent Number:	5668571
Patent Number:	7209133
Patent Number:	6750862
Patent Number:	4954819
Patent Number:	5061919
Patent Number:	5361386
Patent Number:	5381519
Patent Number:	5598517
Patent Number:	5651104
Patent Number:	5657047
Patent Number:	5821944
Patent Number:	6236408

#### CORRESPONDENCE DATA

Fax Number: 2028427899  
*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*  
 Phone: 6508435622  
 Email: bjutras@cooley.com, zPatDCDocketing@cooley.com  
 Correspondent Name: COOLEY LLP  
 Address Line 1: 777 - 6TH STREET NW, SUITE 1100  
 Address Line 2: WILLIAM S. GALLIANI | HN  
 Address Line 4: WASHINGTON, DISTRICT OF COLUMBIA 20001

ATTORNEY DOCKET NUMBER:	NVID-CORR 140060-2000
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NAME OF SUBMITTER:	William S. Galliani-bj
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Total Attachments: 13

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**PATENT**  
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PATENT

ASSIGNMENT OF PATENT RIGHTS  
(Company to Company)

**Nvidia International Inc.**, a corporation duly organized under and pursuant to the laws of the Cayman Islands, and having its principal place of business at Hastings Financial Centre, 2<sup>nd</sup> Floor, Hastings, Christ Church WI Barbados (herein referred to as "Assignor") owns the entire right, title and interest in any Letters Patent(s) ("said patent(s)") and any Patent application(s) ("said application(s)") set forth on the attached Schedule A, as well as any invention(s) ("said invention(s)") disclosed in said application(s) and said patent(s).

WHEREAS, **Nvidia Corporation**, a corporation duly organized under and pursuant to the laws of Delaware, and having its principal place of business at 2701 San Tomas Expressway, Santa Clara, CA 95050 (herein referred to as "Assignee"), is desirous of acquiring the entire right, title, and interest in and to said invention(s), said application(s), and said patent(s), the right to file applications on said invention(s), the entire right, title and interest in and to any applications for Letters Patent of the United States or other countries claiming priority to said application(s), the right to recover damages, including provisional or other royalties, for prior infringements of said application(s) and said patent(s), and the entire right, title, and interest in and to any Letters Patent or Patents, United States or foreign, to be obtained for said invention(s) and said application(s);

NOW, THEREFORE, in consideration of One Dollar (\$1.00) and other good and sufficient consideration, the receipt of which is hereby acknowledged, the Assignor has sold, assigned, transferred, and set over, and by these presents does sell, assign, transfer, and set over, unto the Assignee, its successors, legal representatives, and assigns the entire right, title, and interest in and to said invention(s), said application(s), and said patent(s), the right to file applications on said invention(s), the entire right, title and interest in and to any applications for Letters Patent of the United States or other countries claiming priority to said application(s), including divisions, continuations, and continuations-in-part of said application(s), the right to recover damages, including provisional or other royalties, for prior infringements of said application(s) and said patent(s), the entire right, title and interest in and to any and all Letters Patent or Patents, United States or foreign, to be obtained for said invention(s) and said application(s), the entire right, title and interest in and to any and all reissues and extensions of said patent(s), and all rights under the Hague Convention, the Paris Convention for the Protection of Industrial Property, and under the Patent Cooperation Treaty, the same to be held and enjoyed by the Assignee, for its own use and behalf and the use and behalf of its successors, legal representatives, and assigns, to the full end of the term or terms for which Letters Patent or Patents may be granted as fully and entirely as the same would have been held and enjoyed by the Assignor had this sale and assignment not been made;


AND for the same consideration, the Assignor hereby covenants and agrees to and with the Assignee, its successors, legal representatives, and assigns, that, at the time of execution and delivery of these presents, the Assignor is the sole and lawful owner of the entire right, title, and interest in and to said invention(s), said application(s), and said patent(s), and that the same are

unencumbered, and that the Assignor has good and full right and lawful authority to sell and convey the same in the manner herein set forth;

AND for the same consideration, the Assignor hereby covenants and agrees to and with the Assignee, its successors, legal representatives, and assigns that the Assignor will, whenever counsel of the Assignee, or the counsel of its successors, legal representatives, and assigns, shall advise that any proceeding in connection with said invention(s), said application(s), said patent(s), any application claiming priority to said application(s), any reissue or extension of said patent(s), and any United States or foreign Letters Patent or Patents for said invention(s) or said application(s), including interference proceedings, is lawful and desirable, sign all papers and documents, take all lawful oaths, and do all acts necessary or required to be done for the procurement, maintenance, enforcement and defense of Letters Patent or Patents for said invention(s), without charge to the Assignee, its successors, legal representatives, and assigns, but at the cost and expense of the Assignee, its successors, legal representatives, and assigns;

AND the Assignor hereby requests the Commissioner of Patents to issue any and all aforementioned patent(s) of the United States to the Assignee, as the Assignee of said invention(s) and the Letters Patent to be issued thereon for the sole use and behalf of the Assignee, its successors, legal representatives, and assigns.

Date: 12-3-12

By:   
Name: Karen Burns  
Title: Director  
Company: Nvidia International Inc.

**SCHEDULE A**

Letters Patent(s)

SYSTEM FOR RASTER IMAGING WITH AUTOMATIC CENTERING AND IMAGE COMPRESSION

US Patent 5,422,996

Issue Date 06/06/1995

SYSTEM FOR RASTER IMAGING WITH AUTOMATIC CENTERING AND IMAGE COMPRESSION

US Patent 5,293,474

Issue Date 03/08/1994

ADDRESSING TECHNIQUE FOR TRANSPARENTLY EXTENDING DATA PROCESSING SYSTEM ADDRESS SPACE

US Patent 5,032,981

Issue Date 07/16/1991

METHOD AND APPARATUS FOR PRODUCING PERCEPTION OF HIGH QUALITY GRAYSCALE SHADING ON DIGITALLY COMMANDED DISPLAYS

US Patent 5,185,602

Issue Date 02/09/1993

SYSTEM FOR RUNNING INCOMPATIBLE GRAPHICS PROGRAMS

US Patent 5,412,800

Issue Date 05/02/1995

APPARATUS FOR QUANTIZING PIXEL INFORMATION TO AN OUTPUT VIDEO DISPLAY SPACE

US Patent 5,402,506

Issue Date 03/28/1995

SHADE GRADATION ENHANCEMENT AND FLICKER REDUCTION ON DISPLAYS PROVIDING MORE THAN TWO NATIVE SHADES

US Patent 5,313,224

Issue Date 05/17/1994

DITHERING PROCESS FOR PRODUCING SHADED IMAGES ON DISPLAY SCREENS

US Patent 5,748,163

Issue Date 05/05/1998

VIDEO PROCESSING APPARATUS, SYSTEMS AND METHODS

US Patent 5,577,203

Issue Date 11/19/1996



VIDEO PROCESSING APPARATUS, SYSTEMS AND METHODS

US Patent 5,581,280

Issue Date 12/03/1996

MANAGING AUDIO DATA USING A GRAPHICS DISPLAY CONTROLLER

US Patent 5,553,220

Issue Date 09/03/1996

IMAGE ROTATION FOR VIDEO DISPLAYS

US Patent 6,208,325

Issue Date 03/27/2001

FLEXIBLE GRAPHICS INTERFACE FOR MULTIPLE DISPLAY MODES

US Patent 5,436,641

Issue Date 07/25/1995

METHOD AND APPARATUS FOR AUTOMATICALLY MAINTAINING A  
PREDETERMINED IMAGE QUALITY IN A DISPLAY SYSTEM

US Patent 6,075,513

Issue Date 06/13/2000

PCMCIA VIDEO CARD

US Patent 5,642,139

Issue Date 06/24/1997

PCMCIA VIDEO CARD

US Patent 6,023,266

Issue Date 02/08/2000

METHOD AND APPARTUS FOR CORRECTING ERRORS IN PIXEL CHARACTERISTICS  
WHEN INTERPOLATING POLYGONS INTO A PIXEL GRID

US Patent 5,625,768

Issue Date 04/29/1997

GRAPHICS ACCELERATOR WITH DUAL MEMORY CONTROLLERS

US Patent 5,664,162

Issue Date 09/02/1997

INTERPOLATION RENDERING OF POLYGONS INTO A PIXEL GRID

US Patent 5,684,941

Issue Date 11/04/1997

MEMORY BANDWIDTH OPTIMIZATION

US Patent 5,611,041

Issue Date 03/11/1997

SYSTEM AND METHOD FOR DISPLAYING CLOSED CAPTION DATA ON A PC MONITOR

US Patent 5,543,850

Issue Date 08/06/1996

APPARATUS, SYSTEMS AND METHODS FOR CONTROLLING GRAPHICS AND VIDEO DATA IN MULTIMEDIA DATA PROCESSING AND DISPLAY SYSTEMS

US Patent 5,598,525

Issue Date 01/28/1997

METHOD AND APPARATUS FOR AUTOMATICALLY MAINTAINING A PREDETERMINED IMAGE QUALITY IN A DISPLAY SYSTEM

US Patent 6,121,949

Issue Date 09/19/2000

PCMCIA VIDEO CONTROLLER WITH DIRECT VIDEO PORT AND SEPARATE AUDIO CONNECTION

US Patent 5,630,174

Issue Date 05/13/1997

METHOD AND APPARATUS FOR ADAPTIVE DITHERING

US Patent 5,712,657

Issue Date 01/27/1998

METHOD AND APPARATUS FOR MONITORING A ROW ADDRESS STROBE SIGNAL IN A GRAPHICS CONTROLLER

US Patent 5,680,591

Issue Date 10/21/1997

PROGRAMMABLE CORE-VOLTAGE SOLUTION FOR A VIDEO CONTROLLER

US Patent 6,078,319

Issue Date 06/20/2000

ANIMATED TEXTURE MAP

US Patent 5,630,043

Issue Date 05/13/1997

TECHNIQUE AND APPARATUS FOR COLOR EXPANSION INTO A NON-ALIGNED 24 BIT RGB COLOR-SPACE FORMAT

US Patent 5,699,498

Issue Date 12/16/1997

POLARITY SYNCHRONIZATION METHOD AND APPARATUS FOR VIDEO SIGNALS IN A COMPUTER SYSTEM

US Patent 5,859,635

Issue Date 01/12/1999

METHOD AND APPARATUS FOR READING AHEAD DISPLAY DATA INTO A CRT  
FIFO OF A GRAPHICS CONTROLLER

US Patent 5,657,055

Issue Date 08/12/1997

64 BIT WIDE VIDEO FONT CACHE

US Patent 5,742,298

Issue Date 04/21/1998

TRANSACTION QUEUE IN A GRAPHICS CONTROLLER CHIP

US Patent 5,748,920

Issue Date 05/05/1998

METHOD AND APPARATUS FOR DETERMINING REPRESENTATIVE CHROMINANCE  
COMPONENTS

US Patent 5,929,837

Issue Date 07/27/1999

INTEGRATED GRAPHICS PROCESSOR HAVING A BLOCK TRANSFER ENGINE FOR  
AUTOMATIC GRAPHIC OPERATIONS IN A GRAPHICS SYSTEM

US Patent 6,097,401

Issue Date 08/01/2000

A METHOD AND APPARATUS FOR MINIMIZING EFFECTS OF SLOPE OVERLOAD  
CONDITION WHEN USING DIFFERENTIAL PULSE CODE MODULATION SCHEME

US Patent 5,850,207

Issue Date 12/15/1998

TILE MEMORY ADDRESSING WITH PROGRAMMABLE TILE DIMENSIONS

US Patent 5,815,168

Issue Date 09/29/1998

METHOD AND APPARATUS FOR OBJECT IDENTIFICATION AND COLLISION  
DETECTION IN THREE DIMENSIONAL GRAPHICS SPACE

US Patent 5,812,138

Issue Date 09/22/1998

METHOD AND APPARATUS FOR PREFETCHING A NEXT INSTRUCTION USING  
DISPLAY LIST PROCESSING IN A GRAPHICS PROCESSOR

US Patent 5,917,505

Issue Date 06/29/1999

SOFTWARE TELETEXT DECODER ARCHITECTURE

US Patent 5,835,153

Issue Date 11/10/1998

VIDEO PORT INTERFACE SUPPORTING MULTIPLE DATA FORMATS

US Patent 5,850,266

Issue Date 12/15/1998

DYNAMIC ARBITRATION PRIORITY

US Patent 5,740,383

Issue Date 04/14/1998

METHOD AND APPARATUS FOR OPTIMIZING PIXEL DATA WRITE OPERATIONS TO A TILE-BASED FRAME BUFFER

US Patent 5,754,191

Issue Date 05/19/1998

PRIORITY METHODS FOR TEXTURE MAP STORAGE

US Patent 6,121,974

Issue Date 09/19/2000

PRIORITY METHODS FOR TEXTURE MAP STORAGE

US Patent 6,326,975

Issue Date 12/04/2001

OBJECT REFERENCED MEMORY SEGMENTATION

US Patent 5,798,763

Issue Date 08/25/1998

A METHOD AND APPARATUS FOR EXECUTING A RASTER OPERATION IN A GRAPHICS CONTROLLER CIRCUIT

US Patent 5,959,637

Issue Date 09/28/1999

EMBEDDING A TRANSPARENCY ENABLE BIT AS PART OF A RESIZING BIT BLOCK TRANSFER OPERATION

US Patent 5,909,219

Issue Date 06/01/1999

SYSTEM AND METHOD OF RENDERING POLYGONS INTO A PIXEL GRID

US Patent 5,936,635

Issue Date 08/10/1999

REQUESTING DEVICE CAPABLE OF CANCELING ITS MEMORY ACCESS REQUESTS UPON DETECTING OTHER SPECIFIC REQUESTING DEVICES SIMULTANEOUSLY ASSERTING ACCESS REQUESTS

US Patent 5,748,968

Issue Date 05/05/1998

METHOD AND APPARATUS FOR DYNAMIC XY TILED TEXTURE CACHING  
US Patent 5,828,382  
Issue Date 10/27/1998

METHOD AND APPARATUS FOR DYNAMIC XY TILED TEXTURE CACHING  
US Patent 6,204,863  
Issue Date 03/20/2001

METHOD AND APPARATUS FOR PROVIDING LCD PANEL PROTECTION IN AN LCD  
DISPLAY CONTROLLER  
US Patent 6,310,599  
Issue Date 10/30/2001

A METHOD OF OBTAINING A BUFFER OF CONTIGUOUS MEMORY AND BUILDING  
A PAGE TABLE  
US Patent 5,987,582  
Issue Date 11/16/1999

TEXTURE MAP STORAGE WITH UV REMAPPING  
US Patent 5,929,869  
Issue Date 07/27/1999

DYNAMIC SWITCHING OF TEXTURE MIP-MAPS BASED ON PIXEL DEPTH  
US Patent 5,973,701  
Issue Date 10/26/1999

ENHANCED TEXTURE MAP DATA FETCHING CIRCUIT AND METHOD  
US Patent 5,831,640  
Issue Date 11/03/1998

METHOD AND APPARATUS FOR OPTIMING POWER CONSUMPTION AND MEMORY  
BANDWIDTH IN A VIDEO CONTROLLER USING SGRAM AND SDRAM POWER  
REDUCTION MODES  
US Patent 5,881,016  
Issue Date 03/09/1999

MEMORY MANAGER FOR MULTI-MEDIA APPARATUS AND METHOD THEREFOR  
US Patent 6,266,753  
Issue Date 07/24/2001

METHOD FOR COMPUTING PARAMETERS USED IN A NON-HOMOGENOUS SECOND  
ORDER PERSPECTIVE TEXTURE MAPPING PROCESS USING INTERPOLATION  
US Patent 5,841,442  
Issue Date 11/24/1998

CIRCUIT FOR DETERMINING NON-HOMOGENOUS SECOND ORDER PERSPECTIVE  
TEXTURE MAPPING COORDINATES USING LINEAR INTERPOLATION

US Patent 6,052,127

Issue Date 04/18/2000

NON-HOMOGENOUS 2ND ORDER PERSPECTIVE TEXTURE MAPPING USING LINEAR  
INTERPOLATION PARAMETERS

US Patent 5,835,097

Issue Date 11/10/1998

DYNAMICALLY SELECTABLE TEXTURE FILTER FOR COMPUTER GRAPHICS

US Patent 6,130,674

Issue Date 10/10/2000

METHOD AND APPARATUS FOR INTERNALLY CACHING THE MINIMUM AND  
MAXIMUM XY PIXEL ADDRESS VALUES IN A GRAPHICS SUBSYSTEM

US Patent 5,940,090

Issue Date 08/17/1999

METHOD AND SYSTEM FOR FILTERING TEXTURE MAP DATA FOR IMPROVED  
IMAGE QUALITY IN A GRAPHICS COMPUTER SYSTEM

US Patent 6,184,893

Issue Date 02/06/2001

ARBITRATION FOR SHARED GRAPHICS PROCESSING RESOURCES

US Patent 6,002,409

Issue Date 12/14/1999

METHOD AND APPARATUS FOR AUTOMATICALLY CONTROLLING THE  
DESTINATION OF A GRAPHICS COMMAND IN A REGISTER FILE

US Patent 5,999,200

Issue Date 12/07/1999

HOST DMA THROUGH SUBSYSTEM XY PROCESSING

US Patent 6,088,046

Issue Date 07/11/2000

A METHOD AND SYSTEM FOR EFFICIENT REGISTER SORTING FOR THREE  
DIMENSIONAL GRAPHICS

US Patent 5,949,421

Issue Date 09/07/1999

SPECULAR TEXTURE MAPS IN A GRAPHICS PROCESSOR

US Patent 6,259,455

Issue Date 07/10/2001

IDENTIFYING AND LOADING CHIP SPECIFIC PORT MANAGER DRIVERS ON PLUG  
AND PLAY OPERATING SYSTEMS WHILE MAINTAINING BACKWARD  
COMPATIBILITY

US Patent 6,032,201

Issue Date 02/29/2000

MIP MAP BLENDING IN A GRAPHICS PROCESSOR

US Patent 6,157,386

Issue Date 12/05/2000

AUTO LEVEL OF DETAIL-BASED MIP MAPPING IN A GRAPHICS PROCESSOR

US Patent 5,986,663

Issue Date 11/16/1999

TRACKING OF GRAPHICS POLYGON DATA FROM DIFFERENT CLOCK DOMAINS IN  
A GRAPHICS PROCESSOR

US Patent 6,061,073

Issue Date 05/09/2000

PIXEL DATA X STRIPING IN A GRAPHICS PROCESSOR

US Patent 6,031,550

Issue Date 02/29/2000

NON-SEQUENTIAL FETCH AND STORE OF XY PIXEL DATA IN A GRAPHICS  
PROCESSOR

US Patent 5,999,199

Issue Date 12/07/1999

CRT TO FPD CONVERSION/PROTECTION APPARATUS AND METHOD

US Patent 6,115,032

Issue Date 09/05/2000

CRT TO FPD CONVERSION/PROTECTION APPARATUS AND METHOD

US Patent 6,219,040

Issue Date 04/17/2001

DYNAMICALLY SELECTABLE MIP MAP BLENDING FOR A SOFTWARE GRAPHICS  
ENGINE

US Patent 6,292,191

Issue Date 09/18/2001

CHROMA KEYED SPECULAR TEXTURE MAPPING IN A GRAPHICS PROCESSOR

US Patent 6,054,993

Issue Date 04/25/2000

SELECTABLE MODE SMOOTHING TEXTURE FILTER FOR COMPUTER GRAPHICS  
US Patent 6,181,347  
Issue Date 01/30/2001

ERROR CORRECTION IN A GRAPHICS PROCESSOR  
US Patent 6,252,606  
Issue Date 06/26/2001

APPARATUS AND METHOD FOR PRODUCING SHADED IMAGES ON DISPLAY  
SCREENS  
US Patent 5,751,265  
Issue Date 05/12/1998

APPARATUS, SYSTEMS AND METHODS FOR PROVIDING MULTIPLE VIDEO DATA  
STREAMS FROM A SINGLE SOURCE  
US Patent 5,455,626  
Issue Date 10/03/1995

APPARATUS, SYSTEMS AND METHODS FOR PROVIDING MULTIPLE VIDEO DATA  
STREAMS FROM A SINGLE SOURCE  
US Patent 5,543,842  
Issue Date 08/06/1996

APPARATUS, SYSTEMS AND METHODS FOR PROVIDING MULTIPLE VIDEO DATA  
STREAMS FROM A SINGLE SOURCE  
US Patent 5,539,464  
Issue Date 07/23/1996

APPARATUS, SYSTEMS AND METHODS FOR PROVIDING MULTIPLE VIDEO DATA  
STREAMS FROM A SINGLE SOURCE  
US Patent 5,539,465  
Issue Date 07/23/1996

METHOD AND APPARATUS FOR EXPANDING AND CENTERING VGA TEXT AND  
GRAPHICS  
US Patent 5,682,170  
Issue Date 10/28/1997

METHOD AND APPARATUS FOR DYNAMICALLY ADJUSTING THE NUMBER OF  
STAGES OF A MULTIPLE STAGE PIPELINE  
US Patent 5,778,250  
Issue Date 07/07/1998



SDRAM DIMM PRESENCE DETECT INTERFACE

US Patent 5,862,320

Issue Date 01/19/1999

TILED LINEAR HOST TEXTURE STORAGE

US Patent 5,844,576

Issue Date 12/01/1998

SYSTEM AND METHOD OF SYNCHRONIZING MULTIPLE BUFFERS FOR DISPLAY

US Patent 5,969,728

Issue Date 10/19/1999

DUAL DISPLAY VIDEO CONTROLLER

US Patent 6,215,459

Issue Date 04/10/2001

METHOD AND APPARATUS FOR ASYNCHRONOUS DISPLAY OF GRAPHIC IMAGES

US Patent 6,542,150

Issue Date 04/01/2003

METHOD AND APPARATUS FOR GENERATING HARDWARE ICONS AND CURSORS

US Patent 5,668,571

Issue Date 09/16/1997

METHOD AND APPARATUS FOR ASYNCHRONOUS DISPLAY OF GRAPHIC IMAGES

US Patent 7,209,133

Issue Date 04/24/2007

METHOD AND SYSTEM FOR PERFORMING ENHANCED LIGHTING FUNCTIONS FOR TEXTURE MAP DATA

US Patent 6,750,862

Issue Date 06/15/2004

COMPUTER GRAPHICS WINDOWING SYSTEM FOR THE DISPLAY OF MULTIPLE DYNAMIC IMAGES

US Patent 4,954,819

Issue Date 09/04/1990

COMPUTER GRAPHICS DYNAMIC CONTROL SYSTEM

US Patent 5,061,919

Issue Date 10/29/1991

SYSTEM FOR POLYGON INTERPOLATION USING INSTANTANEOUS VALUES IN A VARIABLE

US Patent 5,361,386

Issue Date 11/01/1994

SYSTEM FOR LINE INTERPOLATION FOR COMPUTER GRAPHICS DISPLAYS  
US Patent 5,381,519  
Issue Date 01/10/1995

COMPUTER GRAPHICS PIXEL RENDERING SYSTEM WITH MULTI-LEVEL  
SCANNING  
US Patent 5,598,517  
Issue Date 01/28/1997

COMPUTER GRAPHICS SYSTEM AND PROCESS FOR ADAPTIVE SUPERSAMPLING  
US Patent 5,651,104  
Issue Date: 07/22/1997

METHOD AND APPARATUS FOR ZOOMING IMAGES ON A VIDEO DISPLAY  
US Patent 5,657,047  
Issue Date 08/12/1997

COMPUTER GRAPHICS PIXEL RENDERING SYSTEM WITH MULTI-LEVEL  
SCANNING  
US Patent 5,821,944  
Issue Date 10/13/1998

COMPUTER GRAPHICS PIXEL RENDERING SYSTEM WITH MULTI-LEVEL  
SCANNING  
US Patent 6,236,408  
Issue Date 05/22/2001