PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the Remove Patent Number 6440516. previously recorded on Reel 029485 Frame 0816. Assignor(s) hereby confirms the Assignment.

CONVEYING PARTY DATA

Name	Execution Date
STMicroelectronics, Inc. (formerly known as SGS-Thomson Microelectronics, Inc.)	01/27/2008

RECEIVING PARTY DATA

Name:	Micron Technology, Inc.
Street Address:	8000 S. Federal Way
Internal Address:	MS 1-525
City:	Boise
State/Country:	IDAHO
Postal Code:	83716-9632

PROPERTY NUMBERS Total: 1

Property Type	Number
Patent Number:	5440516

CH $40.00 5440516

CORRESPONDENCE DATA

Fax Number:
Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone:	(208) 3684591
Email:	tpaulin@micron.com
Correspondent Name:	Micron Technology, Inc.
Address Line 1:	8000 S. Federal Way
Address Line 2:	MS 1-525
Address Line 4:	Boise, IDAHO 83716-9632

NAME OF SUBMITTER:	Michael E. Romani

Total Attachments: 67
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PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
STMicroelectronics, Inc. (formerly known as SGS-Thomson Microelectronics, Inc.)	01/27/2008

RECEIVING PARTY DATA

Name:	Micron Technology, Inc.
Street Address:	8000 S. Federal Way
Internal Address:	MS 1-525
City:	Boise
State/Country:	IDAHO
Postal Code:	83716-9632

PROPERTY NUMBERS Total: 19

Property Type	Number
Patent Number:	5526318
Patent Number:	5365129
Patent Number:	5349246
Patent Number:	5469116
Patent Number:	6440516
Patent Number:	5491444
Patent Number:	5424985
Patent Number:	5493537
Patent Number:	5471157
Patent Number:	5521880

PATENT
REEL: 029505 FRAME: 0013

Patent Number:	5493532
Patent Number:	5625603
Patent Number:	5592422
Patent Number:	5619462
Patent Number:	5703512
Patent Number:	5612918
Patent Number:	5790462
Patent Number:	5557573
Patent Number:	5719445

CORRESPONDENCE DATA

Fax Number:	
Phone:	(208) 368-4591
Email:	tpaulin@micron.com

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.

Correspondent Name:	Micron Technology, Inc.
Address Line 1:	8000 S. Federal Way
Address Line 2:	MS 1-525
Address Line 4:	Boise, IDAHO 83716-9632

NAME OF SUBMITTER:	Michael E. Romani
Signature:	/Michael E. Romani/
Date:	12/17/2012

Total Attachments: 60
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RECEIPT INFORMATION

EPAS ID:	PAT2205058
Receipt Date:	12/17/2012
Fee Amount:	$760

ASSIGNMENT

WHEREAS, STMicroelectronics, Inc., (formerly known as SGS-Thomson Microelectronics, Inc.), a corporation organized under the laws of the State of Delaware, having a place of business at 1310 Electronics Drive, Carrollton, Texas 75006 (hereinafter referred to as "Assignor") has rights in and to the following:

LISTED IN EXHIBIT A

(said patents, patent applications and invention disclosures listed in Exhibit A being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Assignor is a wholly owned subsidiary of STMicroelectronics NV, a Dutch corporation organized under the laws of The Netherlands, acting through its Swiss branch, having an office at 39 Chemin du Champ-des-Filles, 1228 Plan-les-Ouates, Geneva, Switzerland (hereinafter referred to as "Assignee");

WHEREAS, Assignee requested Assignor to develop certain technology for Assignee;

WHEREAS, Assignee sponsored, paid for and is the equitable and beneficial owner of all resulting intellectual property resulting from such technology development, including SAID PATENT RIGHTS and related rights;

WHEREAS, Assignee and Assignor recognize that Assignee already owns all equitable and beneficial ownership in SAID PATENT RIGHTS and related rights;

WHEREAS, Assignor now desires to transfer the legal title in and to SAID PATENT RIGHTS to Assignee and confirm all ownership in Assignee to SAID PATENT RIGHTS and related rights that may have unintentionally remained in Assignor; and

WHEREAS, Assignee desires to perfect registration in Assignee's name all the above mentioned rights, title and interest in and to SAID PATENT RIGHTS and related rights.

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby transfer and assign to Assignee legal title, and any and all rights it may own (beyond said legal title, if any) in and to SAID PATENT RIGHTS, together with any and all continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing (to the extent not already owned by Assignee), together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be executed by its duly authorized officer on this 27th day of January, 2008.

STMicroelectronics, Inc.

By: ____________________

Printed Name: Reza Kazerounian

Title: President & CEO North America

STMicroelectronics NV

By: ____________________

Printed Name: Pierre Ollivier

Title: Corporate General Counsel

EXHIBIT A

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
6300195	98-AG-247	US	Granted		09512900
	98-AG-247	IT	Granted	1032035	99830100.6
	98-AG-247	GB	Granted	1032035	99830100.6
6496439	00-C-131	US	Granted		09896491
5557124	00-WSI-172	US	Granted		08212176
6822919	01-C-124	US	Granted		10190917
6731550	01-C-151	US	Granted		10181501
	01-C-151	EP	Publication		03253297.0
6490197	01-FM-021	US	Granted		09922043
	01-FM-021	JP	Filing		2002224398
	01-FM-021	EP	Notification 51(4)		02255360.6
6552935	01-FM-022	US	Granted		09922044
6563732	01-FM-025	US	Granted		09922068
6594177	01-FM-026	US	Granted		09922176
	01-FM-026	EP	Publication		02255361.4
7064534	03-C-006	US	Granted		10695294
7012417	03-C-042	US	Granted		10690332
6975696	04-C-118	US	Granted		11076785
6373737	89-C-025	US	Granted		08478429
5005158	89-C-028	US	Granted		07464219
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5206817	89-C-032	US	Granted		07600201
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5072137	90-C-026	US	Granted		07569009
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5422591	92-C-046	US	Granted		08176960
5485430	92-C-058	US	Granted		08306527
5455802	92-C-059	US	Granted		07995581
5389577	92-C-060	US	Granted		08180692

US Pat.No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
5400007	92-C-061	US	Granted		08020045
	92-C-061	JP	Granted	3517442	622814
5424988	92-C-062	US	Granted		07954276
5644542	92-C-062	US	Granted		08460409
5300828	92-C-063	US	Granted		07938399
5365129	92-C-072	US	Granted		08055080
5339277	92-C-080	US	Granted		08056078
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5331228	92-C-24/25	US	Granted		07923135
5404331	92-C-27/28	US	Granted		08099606
	92-C-27/28	GB	Granted	637036	94305553.3
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5781043	93-C-007	US	Granted		08932930
	93-C-007	US	Request for Continued Examination		09616821
5471431	93-C-010	US	Granted		08051184
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6297996	95-C-137	US	Granted		09457558
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5798980	95-C-138	US	Granted		08878612

US Pat No.	ST Ref	Country	Status	Non-US Pat. number	Filing Number
5801563	95-C-139	US	Granted		08588988
5790462	95-C-143	US	Granted		08580827
5857292	95-C-144	US	Granted		08588662
5845059	95-C-145	US	Granted		08588762
5745432	95-C-147	US	Granted		08589141
5808960	95-C-148	US	Granted		08858295
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	95-C-149	JP	Filing		96236
5802004	95-C-150	US	Granted		08587728
5828622	95-C-150	US	Granted		08944649
5848018	95-C-151	US	Granted		08926833
5864696	95-C-156	US	Granted		08587711
6006339	95-C-156	US	Granted		09020430
	95-C-156	JP	Filing		96721
5619466	95-C-168	US	Granted		08589024
5853838	95-C-169	US	Granted		08929987
5896336	95-C-169	US	Granted		08974747
5633828	95-C-26/37	US	Granted		08519075
	95-C-26/37	JP	Filing		8222288
	95-C-26/37	GB	Granted	760518	96305856.5
	95-C-26/37	FR	Granted	760518	96305856.5
	95-C-26/37	DE	Granted	69617336	96305856.5
5557573	95-C-53/56	US	Granted		08484609
	95-C-53/56	JP	Filing		8219954
5783958	96-C-003	US	Granted		08588648
5784331	96-C-008	US	Granted		08775664
5805027	96-C-038	US	Granted		08642271
5764592	96-C-051	US	Granted		08771642
	96-C-051	JP	Filing		9345970
6037792	96-C-053	US	Granted		08771645
6310485	96-C-053	US	Granted		09491664
6518746	96-C-053	US	Granted		09931820
	96-C-053	JP	Filing		9346027
5719445	96-C-055	US	Granted		08780053
5959910	96-C-057	US	Granted		08844696
6037799	96-C-117	US	Granted		08758587
6059450	96-C-127	US	Granted		08771643
	96-C-127	US	Request for Continued Examination		10145606
5808947	96-C-52/54	US	Granted		08710357
5995444	97-C-002	US	Granted		08000547
6107865	97-C-009	US	Granted		09429964
5995421	97-C-178	US	Granted		09087399
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	97-LJ-186	GB	Granted	1006448	99309641.1
	97-LJ-186	FR	Granted	1006448	99309641.1
6118188	98-C-072	US	Granted		09217321
6084390	98-C-073	US	Granted		09217323
5946264	98-C-098	US	Granted		09183231
6034917	98-C-099	US	Granted		09183589
6081466	98-C-114	US	Granted		09183451
6041000	98-C-115	US	Granted		09183840
6,434,189	98-OT-144	US	Granted		09335024
	98-OT-144	GB	Granted	1061689	00305020
	98-OT-144	FR	Granted	1061689	00305020
6281734	99-C-131	US	Granted		09476036
6594192	99-C-180	US	Granted		09653495
6781916	99-C-180	US	Granted		10442844
4802124	86-P-005	US	Granted		07081032
4787066	86-P-007	US	Granted		07081037

EXHIBIT A

ST Ref.	Country	Filing Number	Status
03-C-041	US	10690368	Notice of Allowance
03-C-041	JP	2004307123	Filing
03-C-041	EP	04256444.3	Publication
03-C-046	US	10788581	Notice of Allowance
03-C-132	US	10999751	Publication
03-C-133	US	10783935	Notice of Allowance
03-C-134	US	11012693	Publication
03-C-135	US	11013123	Publication
03-C-136	US	11012533	Publication
03-C-137	US	11012712	Publication

EXHIBIT A

ST Ref - Invention Disclosure(s)
06-C-082

CONFIRMATION OF ASSIGNMENT

WHEREAS, STMicroelectronics NV, a Dutch corporation organized under the laws of The Netherlands, acting through its Swiss branch, having an office at 39 Chemin du Champ-des-Filles, 1228 Plan-les-Ouates, Geneva, Switzerland (hereinafter referred to as "Assignor") previously assigned its rights in and to the following:

LISTED IN EXHIBIT A

(said patents, patent applications and invention disclosures listed in Exhibit A being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Numonyx BV is a corporation having a principal place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte de l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignee") previously obtained rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, this agreement confirms that Assignor has sold, conveyed, transfered and assigned to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Confirmation of Assignment to be executed by its duly authorized officer on this 25 day of April, 2012.

ASSIGNOR
STMicroelectronics, NV
By:
Printed Name: Pierre OLLIVIER
Title: Corporate General Counsel

ASIGNEE
Numonyx, BV
By:
Printed Name: Thomas L. Lawson Jr.
Title: Director

Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
6639833	US		10076023
	EP		01830097.0
6724658	US		10133231
	EP		01830276.0
7017099	US		10159782
	IT		TO01A00529
7084032	US		10356351
	US		11476361
	EP		02425044.1
	US		10323615
	EP		02014408.5
6720271	US		10189152
6816001	US		10290030
	JP		02302131
	EP		01830695.1
6496439	US		09895491
6292405	US		09636363
6701419	US		09977561
	EP		00830675.5
6489807	US		09925842
	IT	VA20000027	VA00A00027
6650153	US		10033711
	IT	1220448	01130836.8
	GB	1220448	01130836.8
	FR	1220448	01130836.8
	DE	60102212	01130836.8
6650569	US		10076134
	IT		TO01A00143
6560145	US		10068560
	EP		01830070.7
6674666	US		10077687
	IT		TO01A00148
6917994	US		10115888
	IT		TO01A00333
6549486	US		09998903
6720822	US		09998902
6549048	US		09925979
	FR	2812984	0010554
6807103	US		10438733
	GB	1342244	01996861.9
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6928530	US		10081740
	JP		200246868
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6568510	US		09997214
6714453	US		10352581
	FR	2816750	0014742
5557124	US		08212176
6654287	US		10171078
	IT	1325862	MI01A01232
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	GB	1306852	02078984.8

Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
5031152	US		07414775
	KR	223623	9013107
5072137	US		07569009
5053996	US		07661555
5265100	US		07552567
5311473	US		08071448
5161159	US		07568968
	JP	3616564	2000317923
5408435	US		07984233
5706232	US		08424722
5119340	US		07588601
5128897	US		07588577
5072138	US		07570203
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5124951	US		07588600
5124584	US		07601287
	JP	3375143	3272191
	GB	482868	91309720.0
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5265054	US		07827403
5297090	US		07627050
5258952	US		07627236
	JP	3609837	3330242
	GB	490679	91311570.5
	FR	490679	91311570.5
5355340	US		08101347
5311477	US		07731803
5319768	US		07694451
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5339322	US		07677313
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	JP	3598119	514481
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5357235	US		07876959
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Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
5319347	US		07876851
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5355113	US		07901667
5381126	US		07923855
5508679	US		08303172
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5534448	US		08282408
5589701	US		08486747
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5389577	US		08180692
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	JP	3517442	622814
5424988	US		07954276
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5365129	US		08055080
5339277	US		08056078
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	FR	622803	94303082.5
5341336	US		08056376
5349246	US		07995666
5331228	US		07923135
5404331	US		08099606
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5627403	US		08473552
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	DE	69333722	93830243.7
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5612641	US		08380309
5659501	US		08639931
5818760	US		08871226
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5659509	US		08391999

Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
5493531	US		08350961
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5559743	US		08412550
5546054	US		08377524
	JP	2743853	720895
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	GB	0665485	94830022.3
	FR	0665485	94830022.3
	DE	69413793	94830022.3
5515332	US		08391160
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5717642	US		08803915
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5768115	US		08855922
5499217	US		08348461
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6392469	US		08347788
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5659516	US		08368211
5905677	US		08831046
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Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
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5835427	US		08172854
6101618	US		08172848
5568061	US		08492219
5708789	US		08717139
5396108	US		08129257
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5418756	US		08129763
5568073	US		08172853
5440178	US		08160608
5491444	US		08174139
5577051	US		08173197
5424985	US		08170612
5493537	US		08202830
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5555216	US		08361313
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	JP	2886472	33625894
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6265277	US		09220635
5900756	US		08767521
5579265	US		08393464
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	DE	0669576	95400420.6
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5729162	US		08811358
	JP	2919299	9956195
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5677870	US		08752814
5721706	US		08754432
5561621	US		08381529
5612611	US		08423398
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5796653	US		08394314
	JP	2833646	5972695
5644530	US		08418636
5651128	US		08484873
5594693	US		08408016
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Exhibit A

FMG with US Granted:

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	FR	768672	95830405.7
5905683	US		08940045
5517455	US		08220976
5471157	US		08220834
	JP	3573372	774338
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	FR	675597	94309218.9
5530674	US		08235161
5583816	US		08267667
5455799	US		08267666
5521880	US		08251718
5493532	US		08251337
5589783	US		08282177
5982188	US		08282047
5566112	US		08288334
5570316	US		08438276
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5568084	US		08357664
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5596297	US		08360228
5548241	US		08360229
	JP	3679178	7330189
5594373	US		08359397
5581209	US		08359927
	JP	3596637	33198395
5598122	US		08359926
5589794	US		08360227
5917353	US		08848342
5572470	US		08438349
6052806	US		08519192
	JP	2898230	21890495
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5841314	US		08663524
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Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
	IT	0856886	97830029.1
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	FR	0856886	97830029.1
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	GB	0805453	96830239.8
5878049	US		08840056
	GB	0805452	96830238.0
	FR	0805452	96830238.0
5777941	US		08853732
	GB	0810606	96830275.2
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5751654	US		08826223
	GB	0798742	96830166.3
6148413	US		08835030
	GB	0798726	96830161.4
5854770	US		08791746
5946237	US		08826489
6212096	US		09351603
	IT	0798727	96830162.2
	GB	0798727	96830162.2
5831891	US		08812595
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	FR	797208	96830128.3
5859810	US		08826009
	GB	0798730	96830169.7
	FR	0798730	96830169.7
5625603	US		08477244
5793247	US		08667481
5640122	US		08464551
	GB	747800	96303214.9
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5576656	US		08414103
RE37876	US		09568201
5768206	US		08484491
	GB	747824	96303448.3
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5592422	US		08476658
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	JP		8200145
5745420	US		08833582
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5648933	US		08509211
5629896	US		08521800
5612918	US		08582424
6144594	US		08587709
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5801563	US		08588988
5790462	US		08580827
5657292	US		08588662
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Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
5825691	US		08858788
	JP		96236
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5828622	US		08944649
5848018	US		08926833
5864696	US		08587711
6006339	US		09020430
	JP		96721
5619466	US		08589024
5883838	US		08929987
5896336	US		08974747
5633828	US		08519075
	JP		8222288
	GB	760518	96305856.5
	FR	760518	96305856.5
	DE	69617336	96305856.5
5557573	US		08484609
	JP		8219954
5812446	US		08623044
	JP	2727527	10419096
5760638	US		08664083
	FR	762428	96460031.6
5801577	US		08762677
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5950224	US		08797948
5886549	US		08792962
5850092	US		08833925
6074916	US		09164761
	GB	0802569	96830212.5
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5822259	US		08846755
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6094073	US		09432642
	GB	809254	96830299.2
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	JP		9161556
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	FR	814480	96830344.6
5903498	US		08877927
	GB		96830345.3
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Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
	JP		9346027
5719445	US		08780053
5959910	US		08844696
6037799	US		08758587
6059450	US		08771643
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5808947	US		08710357
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6049497	US		08957715
5859798	US		08934696
5787040	US		08916054
5903141	US		08791383
6119210	US		08915855
5870336	US		08957666
6281723	US		08993071
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	FR	2758022	9616355
6097631	US		09275327
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6178490	US		08991428
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5969961	US		09059088
6140876	US		09188071
	FR	2770947	9714018
5966034	US		09087478
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5953253	US		09035654
6011724	US		08980195
5862075	US		08970445
6104644	US		09013966
6205512	US		09056921
6279068	US		09746472
6434056	US		09746473
	GB	875899	98400661.9
	FR	875899	98400661.9
5991199	US		09012938
6141254	US		09355064
	IT	954865	98903102.6
	GB	954865	98903102.6
	FR	954865	98903102.6
	DE	954865	98903102.6
6060932	US		09118953
	FR	0898356	98401807.7
	DE	0898356	98401807.7
6888225	US		10323961
6061286	US		09053720
	IT	0869506	97830160.4
	GB	0869506	97830160.4
6075718	US		09039588
	IT	1291209	TO97A00227
6212128	US		09093382

Exhibit A

FMG with US Granted:

US Pat No.	Country	Non-US Pat. number	Filing Number
6091642	US		09234942
	IT	0932161	98830024.0
6266222	US		09223621
	IT	0932202	97830741.1
	FR	0932202	97830741.1
6242793	US		09231129
6372597	US		09837137
	JP		10370047
	EP		97830742.9
6147902	US		09320315
	IT	961287	98830324.4
	GB	961287	98830324.4
	FR	961287	98830324.4
6218265	US		09336089
	IT	0971415	98830388.9
	GB	0971415	98830388.9
	FR	0971415	98830388.9
	DE	0971415	98830388.9
6111791	US		09322644
	IT	1301398	MI98A01193
6208705	US		09262500
	IT	1298594	MI98A00454
5995444	US		08000547
6107865	US		09429964
5995421	US		09087399
6097633	US		09182843
	IT	1295910	MI97A02458
6867113	US		09191743
	EP		97830603.3
6133718	US		09246029
	IT	1298560	MI98A00219
	IT	1298938	MI98A00351
	IT	1298939	MI98A00352
6271571	US		09276263
	IT	1298807	MI98A00628
6466059	US		09249834
	IT	936627	98830064.6
	GB	936627	98830064.6
6130844	US		09257682
	IT	1302079	TO98A00165
6064594	US		09258224
	IT	1302080	T098A00166
6198672	US		09257684
	IT	1302081	T098A00167
6034889	US		09177899
	IT	918336	98402620.3
	GB	918336	98402620.3
	FR	918336	98402620.3
	DE	918336	98402620.3
6040994	US		09179621
5995416	US		09156945
	IT	903750	98460036.1
	GB	903750	98460036.1
	FR	903750	98460036.1
	DE	903750	98460036.1
6128222	US		09173136

Exhibit A

FMG Pending

Country	Filing Number	Patent number
US	10816271	
US	10816271	
US	10698747	
US	10698747	
US	11348513	
US	11348513	
EP	05425052.7	
EP	05425052.7	
US	11323937	
EP	05425001.4	
US	11437405	
EP	05425347.1	
US	11437268	
EP	05425348.9	
US	11300145	
IT	MI04A002374	
EP	05027286.3	
US		
EP	05425814.0	
US	11411982	
US	11411982	
EP	05425261.4	
EP	05425261.4	
US	11334205	
IT	VA05A000001	
US	11340414	
JP	06015176	
EP	05100551.0	
CN	0610059263.5	
US	11444892	
EP	05104656.3	
US	11411010	
IT	MI05A000780	
US	11304488	
EP	04106609.3	
US	11368363	
EP	05101659.0	
US		
IT	VA05A000061	
US	11432301	
IT	MI05A000838	
US	11495886	
EP	05106976.3	
US		
US		
EP	05425835.5	
EP	05425835.5	
US	11367707	
EP	05101660.8	
EP	06425223.2	
US		
EP	05425564.1	
US	11381426	
IT	VA05A000028	
US	11238137	
EP	04425724.4	

PATENT ASSIGNMENT AGREEMENT

WHEREAS, Numonyx B.V., a company organized under the laws of Netherlands, having a place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte. De l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignor") has rights in and to the following:

1. U.S. Letters Patents Listed on Exhibit A,
2. U.S. Patent Applications listed on Exhibit B,
3. Foreign patents and patent applications listed on Exhibit C.

(said patents and patent applications listed in Exhibits A, B, and C being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Micron Technology, Inc. is a corporation having a principal place of business at 8000 S. Federal Way, Boise, Idaho 83707, United States (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

NOW, THERFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuation-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WTINESS WHEREOF, Assignor has caused this Assignment to be executed by its duly authorized officer effective as of the 23 day of May, 2012

Numonyx B.V.
By: Thomas L. Laws Jr.
Printed Name: Thomas L. Laws Sr.
Title: Director

Micron Technology, Inc.
By: Roderic W. Lewis L.S.
Printed Name: Roderic W. Lewis
Title: V.P. of Legal Affairs

Exhibit A - US Issued Patents

MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0001.00/RE	Utility - REIS	Expired	METHOD TO INSTALL AN ELECTRONIC COMPONENT AND ITS ELECTRICAL CONNECTIONS ON A SUPPORT, AND PRODUCT OBTAINED THEREBY	12/12/1988	08/198,208	3/20/1990	RE35,578	US
NMXS-0001.00/US	Utility - ORG	Expired	METHOD TO INSTALL AN ELECTRONIC COMPONENT AND ITS ELECTRICAL CONNECTIONS ON A SUPPORT. AND PRODUCT OBTAINEDTHEREBY	12/12/1988	07/283,214	3/20/1990	4,908,937	US
NMXS-0002.00/US	Utility - ORG	Abandoned	METHOD FOR FIXING AN ELECTRONIC COMPONENT AND ITS CONTACTS TO A SUPPORT	12/12/1988	07/283,305	7/17/1990	4,941,257	US
NMXS-0002.01/RE	Utility - REIS	Expired	METHOD FOR FIXING AN ELECTRONIC COMPONENT AND ITS CONTACTS TO A SUPPORT	11/9/1994	08/336,560	7/17/1990	RE35,385	US
NMXS-0003.00/US	Utility - ORG	Abandoned	ELECTRICALLY PROGRAMMABLE MEMORY WITH SEVERAL INFORMATION BITS PER CELL	4/24/1989	07/342,476	10/16/1990	4,964,079	US
NMXS-0004.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY DEVICE WITH BALANCING CIRCUIT INCLUDING FOLLOWER AMPLIFIER COUPLED TO BIT LINE	7/23/1993	08/096,684	12/3/1996	5,581,511	US
NMXS-0004.01/US	Utility - CON	Issued	INTEGRATED CIRCUIT MEMORY DEVICE WITH BALANCING CIRCUIT INCLUDING FOLLOWER AMPLIFIER COUPLED TO BIT LINE	6/7/1995	08/486,363	6/10/1997	5,638,332	US
NMXS-0004.02/US	Utility - DIV	Issued	INTEGRATED CIRCUIT MEMORY DEVICE WITH BALANCING CIRCUIT INCLUDING FOLLOWER AMPLIFIER COUPLED TO BIT LINE	6/7/1995	08/478,463	8/6/1996	5,544,114	US
NMXS-0005.00/US	Utility - ORG	Issued	DEVICE FOR THE PROTECTION OF AN INTEGRATED CIRCUIT AGAINST POWER SUPPLY CUTS	8/2/1993	08/100,867	12/5/1995	5,473,496	US
NMXS-0006.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER CIRCUIT OF THE CHARGE-PUMP TYPE WITH BOOTSTRAPPED OSCILLATOR	9/29/1993	08/128,871	12/31/1996	5,589,793	US
NMXS-0006.01/US	Utility - DIV	Issued	VOLTAGE BOOSTER CIRCUIT OF THE CHARGE-PUMP TYPE WITH A BOOTSTRAPPED OSCILLATOR	6/6/1995	08/467,297	1/7/1997	5,592,115	US
NMXS-0007.01/US	Utility - CON	Issued	POWER-ON-RESET CIRCUIT PROVIDING PROTECTION AGAINST POWER SUPPLY INTERRUPTIONS	8/8/1997	08/908,583	9/12/2000	6,118,315	US
NMXS-0008.00/US	Utility - ORG	Expired	SAFETY DEVICE FOR ELECTRICALLY PROGRAMMABLE READ-ONLY MEMORY	5/21/1987	07/052,068	3/7/1989	4,811,291	US
NMXS-0009.00/US	Utility - ORG	Expired	NON-VOLATILE SHADOW STORAGE CELL WITH REDUCED TUNNEL DEVICE COUNT FOR IMPROVED RELIABILITY	8/3/1987	07/081,032	1/31/1989	4,802,124	US
NMXS-0010.00/US	Utility - ORG	Expired	NON-VOLATILE SHADOW STORAGE CELL WITH IMPROVED LEVEL SHIFTING CIRCUIT AND REDUCED TUNNEL DEVICE COUNT FOR IMPROVED RELIABILITY	8/3/1987	07/081,037	11/22/1988	4,787,066	US
NMXS-0011.03/US	Utility - CON	Issued	CONTENT ADDRESSABLE MEMORY	6/7/1995	08/478,429	4/16/2002	6,373,737	US
NMXS-0012.00/US	Utility - ORG	Expired	REDUNDANCY FOR SERIAL MEMORY	1/12/1990	07/464,219	4/2/1991	5,005,158	US
NMXS-0013.00/RE	Utility - REIS	Expired	METHODS FOR FABRICATING MEMORY CELLS AND LOAD ELEMENTS	9/29/1994	08/316,035	9/29/1992	RE37,769	US
NMXS-0013.00/US	Utility - ORG	Abandoned	POLYCRYSTALLINE SILICON CONTACT STRUCTURE	4/30/1990	07/516,272	9/29/1992	5,151,387	US
NMXS-0013.01/US	Utility - DIV	Expired	POLYCRYSTALLINE SILICON CONTACT STRUCTURE	2/28/1992	07/843,818	1/18/1994	5,279,887	US
NMXS-0014.00/US	Utility - CIP	Expired	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	12/21/1989	07/454,408	6/9/1992	5,121,346	US
NMXS-0014.03/US	Utility - CON	Issued	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	4/1/1994	08/221,986	3/26/1996	5,502,655	US
NMXS-0015.00/US	Utility - ORG	Expired	PIPELINED CIRCUITRY FOR ALLOWING THE COMPARISON OF THE RELATIVE DIFFERENCE BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	10/18/1990	07/600,201	4/27/1993	5,206,817	US
NMXS-0016.00/US	Utility - ORG	Expired	TEST CIRCUIT FOR NON-VOLATILE STORAGE CELL	9/29/1989	07/414,775	7/9/1991	5,031,152	US
NMXS-0017.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH A CLOCKED ACCESS CODE FOR TEST MODE ENTRY	8/17/1990	07/569,009	12/10/1991	5,072,137	US
NMXS-0018.00/US	Utility - ORG	Expired	DUAL STATE MEMORY STORAGE CELL WITH IMPROVED DATA TRANSFER CIRCUITRY	2/26/1991	07/661,555	10/1/1991	5,053,996	US
NMXS-0019.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH IMPROVED TEST MODE	7/13/1990	07/552,567	11/23/1993	5,265,100	US
NMXS-0019.01/US	Utility - DIV	Expired	SEMICONDUCTOR MEMORY WITH IMPROVED TEST MODE	6/2/1993	08/071,448	5/10/1994	5,311,473	US
NMXS-0020.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH MULTIPLE CLOCKING FOR TEST MODE ENTRY	8/17/1990	07/568,968	11/3/1992	5,161,159	US

Exhibit A - US Issued Patents

MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0021.01/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY WITH INHIBITED TEST MODE ENTRY DURING POWER-UP	11/20/1992	07/984,233	4/18/1995	5,408,435	US
NMXS-0021.02/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY WITH MULTIPLE CLOCKING FOR TEST MODE ENTRY	4/18/1995	08/424,722	1/6/1998	5,706,232	US
NMXS-0022.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY HAVING LATCHED REPEATERS FOR MEMORY ROW LINE SELECTION	9/26/1990	07/588,601	6/2/1992	5,119,340	US
NMXS-0023.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY HAVING IMPROVED LATCHED REPEATERS FOR MEMORY ROW LINE SELECTION	9/26/1990	07/588,577	7/7/1992	5,128,897	US
NMXS-0024.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH SEQUENTIAL CLOCKED ACCESS CODES FOR TEST MODE ENTRY	8/17/1990	07/570,203	12/10/1991	5,072,138	US
NMXS-0025.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT HAVING MULTIPLE DATA OUTPUTS SHARING A RESISTOR NETWORK	10/22/1990	07/601,288	3/24/1992	5,099,148	US
NMXS-0026.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH POWER-ON RESET CONTROLLED LATCHED ROW LINE REPEATERS	9/26/1990	07/588,609	6/9/1992	5,121,358	US
NMXS-0026.02/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY WITH POWER-ON RESET CONTROLLED LATCHED ROW LINE REPEATERS	1/19/1995	08/376,127	6/11/1996	5,526,318	US
NMXS-0027.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH SEQUENCED LATCHED ROW LINE REPEATERS	9/26/1990	07/588,600	6/23/1992	5,124,951	US
NMXS-0028.00/US	Utility - ORG	Expired	ADDRESS BUFFER CIRCUIT WITH TRANSITION-BASED LATCHING	10/22/1990	07/601,287	6/23/1992	5,124,584	US
NMXS-0029.00/US	Utility - ORG	Expired	VEHICLE SEAT ASSEMBLY WITH INTEGRATED CHILD SEAT	12/14/1990	07/827,403	11/23/1993	5,265,054	US
NMXS-0030.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH COLUMN DECODED BIT LINE EQUILIBRATE	12/13/1990	07/627,050	3/22/1994	5,297,090	US
NMXS-0031.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH SEPARATE TIME-OUT CONTROL FOR READ AND WRITE OPERATIONS	12/14/1990	07/627,236	11/2/1993	5,258,952	US
NMXS-0032.01/US	Utility - CON	Expired	SEMICONDUCTOR MEMORY WITH MULTIPLEXED REDUNDANCY	8/2/1993	08/101,347	10/11/1994	5,355,340	US
NMXS-0033.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT MEMORY DEVICE HAVING FLASH CLEAR	7/17/1991	07/731,803	5/10/1994	5,311,477	US
NMXS-0034.00/US	Utility - ORG	Expired	CONTROL CIRCUIT FOR RESETTING A SNOOP VALID BIT IN A DUAL PORT CACHE TAG MEMORY	5/1/1991	07/694,451	6/7/1994	5,319,768	US
NMXS-0035.00/US	Utility - ORG	Expired	CONTROL CIRCUIT FOR DUAL PORT MEMORY	4/30/1991	07/693,670	6/27/1995	5,428,632	US
NMXS-0036.00/US	Utility - ORG	Expired	CACHE TAG PARITY DETECT CIRCUIT	3/29/1991	07/677,313	8/16/1994	5,339,322	US
NMXS-0037.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT MEMORY DEVICE WITH REDUNDANT ROWS	7/17/1991	07/731,487	3/22/1994	5,297,094	US
NMXS-0038.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT DUAL-PART MEMORY DEVICE HAVING REDUCED CAPACITANCE	7/17/1991	07/731,802	2/15/1994	5,287,322	US
NMXS-0039.01/US	Utility - CON	Expired	SIGNAL LINE PULSE ENHANCING CIRCUIT FOR INTEGRATED CIRCUITS	8/20/1993	08/109,697	6/14/1994	5,321,314	US
NMXS-0040.00/US	Utility - ORG	Issued	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	11/30/1992	07/982,988	12/27/1994	5,377,153	US
NMXS-0040.02/US	Utility - DIV	Issued	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	5/22/1996	08/651,233	3/31/1998	5,734,602	US
NMXS-0040.03/US	Utility - CON	Issued	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	8/26/1996	08/697,482	4/22/1997	5,623,438	US
NMXS-0041.00/US	Utility - ORG	Expired	READ/WRITE MEMORY WITH IMPROVED TEST MODE DATA COMPARE	12/17/1991	07/809,393	2/8/1994	5,285,419	US
NMXS-0042.01/US	Utility - CON	Issued	TRISTATABLE DRIVER FOR INTERNAL DATA BUS LINES	12/31/1996	08/777,836	5/5/1998	5,748,556	US
NMXS-0043.00/US	Utility - ORG	Issued	MULTIPLEXING SENSE AMPLIFIER HAVING LEVEL SHIFTER CIRCUITS	3/31/1993	08/040,916	12/27/1994	5,377,143	US
NMXS-0043.01/US	Utility - DIV	Issued	MULTIPLEXING SENSE AMPLIFIER	9/16/1994	08/307,332	1/9/1996	5,483,489	US
NMXS-0044.00/US	Utility - ORG	Issued	MULTIPLEXING SENSE AMPLIFIER	3/31/1993	08/041,321	1/23/1996	5,487,048	US
NMXS-0045.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT WITH PRECHARGED INTERNAL DATA BUS	12/17/1991	07/809,735	3/15/1994	5,295,104	US
NMXS-0046.00/US	Utility - ORG	Issued	REDUNDANCY DECODER	1/31/1992	07/830,315	11/28/1995	5,471,426	US
NMXS-0047.00/US	Utility - ORG	Expired	COLUMN REDUNDANCY ARCHITECTURE FOR A READ/WRITE MEMORY	1/31/1992	07/830,314	10/26/1993	5,257,229	US
NMXS-0048.00/US	Utility - ORG	Issued	SELECTIVE BULK WRITE OPERATION	4/7/1992	07/864,481	5/10/1994	5,311,467	US
NMXS-0049.00/US	Utility - ORG	Issued	PARALLELIZED MAGNITUDE COMPARATOR	4/30/1992	07/876,959	10/18/1994	5,357,235	US
NMXS-0050.00/US	Utility - ORG	Issued	PARALLELIZED MAGNITUDE COMPARATOR FOR COMPARING A BINARY NUMBER TO A FIXED VALUE	4/30/1992	07/876,851	6/7/1994	5,319,347	US
NMXS-0051.01/US	Utility - CON	Issued	PARALLELIZED BORROW LOOK AHEAD SUBTRACTOR	9/16/1993	08/122,804	1/31/1995	5,386,377	US
NMXS-0052.00/US	Utility - ORG	Issued	PARALLELIZED DIFFERENCE FLAG LOGIC	5/29/1992	07/890,919	10/18/1994	5,357,236	US

Exhibit A - US Issued Patents

MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0053.00/US	Utility - ORG	Issued	SERIALIZED DIFFERENCE FLAG CIRCUIT	6/22/1992	07/901,667	10/11/1994	5,355,113	US
NMXS-0054.00/US	Utility - ORG	Issued	PROGRAMMABLE DIFFERENCE FLAG LOGIC	7/31/1992	07/923,855	1/10/1995	5,381,126	US
NMXS-0054.01/US	Utility - CON	Issued	FIFO WITH ADAPTABLE FLAGS FOR CHANGING SYSTEM SPEED REQUIREMENTS	9/8/1994	08/303,172	4/16/1996	5,508,679	US
NMXS-0055.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT WITH SELF-BIASED DIFFERENTIAL DATA LINES	12/17/1991	07/809,733	10/26/1993	5,257,226	US
NMXS-0056.00/US	Utility - ORG	Issued	PROCESS FOR REALIZING P-CHANNEL MOS TRANSISTORS HAVING A LOW THRESHOLD VOLTAGE IN SEMICONDUCTOR INTEGRATED CIRCUITS FOR ANALOG APPLICATIONS	7/28/1994	08/282,408	7/9/1996	5,534,448	US
NMXS-0056.01/US	Utility - DIV	Issued	PROCESS FOR REALIZING P-CHANNEL MOS TRANSISTORS HAVING A LOW THRESHOLD VOLTAGE IN SEMICONDUCTOR INTEGRATED CIRCUITS FOR ANALOG APPLICATIONS	6/7/1995	08/486,747	12/31/1996	5,589,701	US
NMXS-0057.02/US	Utility - CON	Issued	INPUT BUFFER CIRCUIT IMMUNE TO COMMON MODE POWER SUPPLY FLUCTUATIONS	6/7/1995	08/477,283	11/3/1998	5,831,457	US
NMXS-0058.00/US	Utility - ORG	Issued	STRUCTURE FOR USING A PORTION OF AN INTEGRATED CIRCUIT DIE	11/13/1992	07/975,628	10/11/1994	5,355,344	US
NMXS-0058.01/US	Utility - CON	Issued	STRUCTURE FOR USING A PORTION OF AN INTEGRATED CIRCUIT DIE	6/21/1994	08/263,048	6/11/1996	5,526,317	US
NMXS-0059.01/US	Utility - CON	Issued	INTEGRATED CIRCUIT OUTPUT DRIVER	12/30/1994	08/367,681	7/17/2001	6,262,617	US
NMXS-0060.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUIT WITH BODY BIAS CONTROL FOR MULTIPLE POWER SUPPLY OPERATION	1/3/1994	08/176,960	6/6/1995	5,422,591	US
NMXS-0061.01/US	Utility - CON	Issued	MULTIPLE CLOCKED DYNAMIC SENSE AMPLIFIER	9/15/1994	08/306,527	1/16/1996	5,485,430	US
NMXS-0062.00/US	Utility - ORG	Issued	DUAL DYNAMIC SENSE AMPLIFIERS FOR A MEMORY ARRAY	12/22/1992	07/995,581	10/3/1995	5,455,802	US
NMXS-0063.01/US	Utility - CON	Issued	LEADFRAME FOR INTEGRATED CIRCUITS	1/13/1994	08/180,692	2/14/1995	5,389,577	US
NMXS-0064.00/US	Utility - CIP	Issued	MULTIPLE LEVEL PARALLEL MAGNITUDE COMPARATOR	2/19/1993	08/020,045	3/21/1995	5,400,007	US
NMXS-0065.00/US	Utility - ORG	Issued	STRESS TEST FOR MEMORY ARRAYS IN INTEGRATED CIRCUITS	9/30/1992	07/954,276	6/13/1995	5,424,988	US
NMXS-0065.01/US	Utility - CON	Issued	STRESS TEST FOR MEMORY ARRAYS IN INTEGRATED CIRCUITS	6/2/1995	08/460,409	7/1/1997	5,644,542	US
NMXS-0066.00/US	Utility - ORG	Issued	SLEW RATE LIMITED OUTPUT BUFFER WITH BYPASS CIRCUITRY	8/31/1992	07/938,399	4/5/1994	5,300,828	US
NMXS-0067.00/US	Utility - ORG	Issued	TEMPERATURE-COMPENSATED VOLTAGE LEVEL SENSE CIRCUIT	4/29/1993	08/055,080	11/15/1994	5,365,129	US
NMXS-0068.00/US	Utility - ORG	Issued	ADDRESS BUFFER	4/30/1993	08/056,078	8/16/1994	5,339,277	US
NMXS-0069.00/US	Utility - ORG	Issued	METHOD FOR STRESS TESTING DECODERS AND PERIPHERY CIRCUITS	4/30/1993	08/056,376	8/23/1994	5,341,336	US
NMXS-0070.00/US	Utility - ORG	Issued	INPUT BUFFER WITH HYSTERESIS CHARACTERISTICS	12/21/1992	07/995,666	9/20/1994	5,349,246	US
NMXS-0071.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUIT	7/31/1992	07/923,135	7/19/1994	5,331,228	US
NMXS-0072.00/US	Utility - ORG	Issued	REDUNDANCY ELEMENT CHECK IN IC MEMORY WITHOUT PROGRAMMING SUBSTITUTION OF REDUNDANT ELEMENTS	7/30/1993	08/099,606	4/4/1995	5,404,331	US
NMXS-0073.00/US	Utility - ORG	Issued	TEMPERATURE-STABLE CURRENT SOURCE	5/31/1995	08/454,926	7/1/1997	5,644,216	US
NMXS-0074.01/US	Utility - DIV	Issued	PROCESS FOR IMPROVING THE INTERFACE UNION AMONG DIELECTRIC MATERIALS IN AN INTEGRATED CIRCUIT MANUFACTURE	6/6/1995	08/473,552	5/6/1997	5,627,403	US
NMXS-0074.02/US	Utility - CON	Issued	PROCESS FOR IMPROVING THE INTERFACE UNION AMONG DIELECTRIC MATERIALS IN AN INTEGRATED CIRCUIT MANUFACTURE	11/18/1996	08/751,244	8/18/1998	5,795,821	US
NMXS-0075.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR CIRCUIT PROVIDING POTENTIALS OF OPPOSITE POLARITY	9/26/1994	08/311,941	8/13/1996	5,546,044	US
NMXS-0076.00/US	Utility - ORG	Abandoned	PROCESS FOR THE MANUFACTURE OF AN INTEGRATED VOLTAGE LIMITER AND STABILIZER IN FLASH EEPROM MEMORY DEVICES	9/7/1994	08/301,792	1/23/1996	5,486,486	US
NMXS-0076.01/US	Utility - DIV	Issued	PROCESS FOR THE MANUFACTURE OF AN INTEGRATED VOLTAGE LIMITER AND STABILIZER IN FLASH EEPROM MEMORY DEVICES	6/7/1995	08/477,302	2/4/1997	5,600,590	US
NMXS-0077.00/US	Utility - ORG	Issued	MONOLITHICALLY INTEGRATED STORAGE DEVICE	11/30/1994	08/347,653	7/9/1996	5,535,157	US
NMXS-0078.00/US	Utility - ORG	Issued	CIRCUIT FOR COVERING INITIAL CONDITIONS WHEN STARTING-UP AN INTEGRATED CIRCUIT DEVICE	1/30/1995	08/380,309	3/18/1997	5,612,641	US
NMXS-0079.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR SUPPLYING NEGATIVE PROGRAMMING VOLTAGES TO NON-VOLATILE MEMORY CELLS IN A NON-VOLATILE MEMORY DEVICE	11/23/1994	08/344,232	6/18/1996	5,528,536	US
NMXS-0079.01/US	Utility - DIV	Issued	METHOD AND DEVICE FOR SUPPLYING NEGATIVE PROGRAMMING VOLTAGES TO NON-VOLATILE MEMORY CELLS IN A NON-VOLATILE MEMORY DEVICE	4/26/1996	08/639,931	8/19/1997	5,659,501	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0079.02/US	Utility - CON	Issued	METHOD AND DEVICE FOR SUPPLYING NEGATIVE PROGRAMMING VOLTAGES TO NON-VOLATILE MEMORY CELLS IN A NON-VOLATILE MEMORY DEVICE	6/9/1997	08/871,226	10/6/1998	5,818,760	US
NMXS-0080.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUITRY FOR A SEMICONDUCTOR MEMORY DEVICE	12/6/1994	08/349,783	10/15/1996	5,566,114	US
NMXS-0081.01/US	Utility - CON	Issued	INTEGRATED CIRCUIT FOR THE PROGRAMMING OF A MEMORY CELL IN A NON-VOLATILE MEMORY REGISTER	4/18/1996	08/635,455	7/1/1997	5,644,529	US
NMXS-0082.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING REDUNDANCY REGISTERS IN A ROW REDUNDANCY INTEGRATED CIRCUITRY FOR A SEMICONDUCTOR MEMORY DEVICE, AND ROW REDUNDANCY INTEGRATED CIRCUITRY	2/16/1995	08/391,999	8/19/1997	5,659,509	US
NMXS-0083.00/US	Utility - ORG	Issued	INTEGRATED CIRCUITRY FOR CHECKING THE UTILIZATION RATE OF REDUNDANCY MEMORY ELEMENTS IN A SEMICONDUCTOR MEMORY DEVICE	12/7/1994	08/350,961	2/20/1996	5,493,531	US
NMXS-0083.01/US	Utility - CIP	Issued	INTEGRATED CIRCUITRY FOR CHECKING THE UTILIZATION RATE OF REDUNDANCY MEMORY ELEMENTS IN A SEMICONDUCTOR MEMORY DEVICE	2/16/1996	08/602,237	1/13/1998	5,708,601	US
NMXS-0084.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUITRY LAYOUT FOR A SEMICONDUCTOR MEMORY DEVICE	3/29/1995	08/412,550	9/24/1996	5,559,743	US
NMXS-0085.00/US	Utility - ORG	Issued	CURRENT SOURCE HAVING VOLTAGE STABILIZING ELEMENT	1/20/1995	08/377,524	8/13/1996	5,546,054	US
NMXS-0086.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TIMING THE LOADING OF NONVOLATILE-MEMORY OUTPUT DATA	2/21/1995	08/391,160	5/7/1996	5,515,332	US
NMXS-0087.00/US	Utility - ORG	Issued	INTERNAL TIMING METHOD AND CIRCUIT FOR PROGRAMMABLE MEMORIES	2/21/1995	08/391,159	9/2/1997	5,663,921	US
NMXS-0088.01/US	Utility - CON	Issued	LOAD SIGNAL GENERATING METHOD AND CIRCUIT FOR NONVOLATILE MEMORIES	2/25/1997	08/803,915	2/10/1998	5,717,642	US
NMXS-0089.01/US	Utility - CON	Issued	COUNT UNIT FOR NONVOLATILE MEMORIES	8/20/1996	08/700,126	11/11/1997	5,687,135	US
NMXS-0090.00/US	Utility - ORG	Issued	END-OF-COUNT DETECTING DEVICE FOR NONVOLATILE MEMORIES	12/28/1994	08/365,510	1/14/1997	5,594,703	US
NMXS-0091.01/US	Utility - CON	Issued	VOLTAGE BOOSTER WITH AN ACCELERATION CIRCUIT	5/14/1997	08/855,922	6/16/1998	5,768,115	US
NMXS-0092.00/US	Utility - ORG	Issued	BIAS CIRCUIT FOR A MEMORY LINE DECODER DRIVER OF NONVOLATILE MEMORIES	12/2/1994	08/348,461	3/12/1996	5,499,217	US
NMXS-0093.00/US	Utility - ORG	Issued	READING CIRCUIT FORAN INTEGRATED SEMICONDUCTOR MEMORY DEVICE	3/22/1995	08/408,589	5/6/1997	5,627,790	US
NMXS-0094.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR TESTING A NETWORK WITH A PROGRAMMABLE LOGIC MATRIX	11/28/1994	08/345,530	2/10/1998	5,717,698	US
NMXS-0095.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING AND TESTING A NONVOLATILE MEMORY	1/31/1995	08/381,530	2/4/1997	5,600,600	US
NMXS-0096.00/US	Utility - ORG	Issued	THRESHOLD VOLTAGE MEASURING DEVICE FOR MEMORY CELLS	3/31/1995	08/412,326	2/4/1997	5,600,594	US
NMXS-0097.00/US	Utility - ORG	Issued	CIRCUIT FOR IDENTIFYING A MEMORY CELL HAVING ERRONEOUS DATA STORED THEREIN	8/30/1995	08/521,304	11/11/1997	5,687,124	US
NMXS-0098.00/US	Utility - ORG	Issued	DEVICE FOR DETECTING A REDUCTION IN A SUPPLY VOLTAGE	12/29/1994	08/366,211	12/10/1996	5,583,820	US
NMXS-0098.01/US	Utility - DIV	Issued	DEVICE FOR DETECTING A REDUCTION IN A SUPPLY VOLTAGE	9/9/1996	08/711,132	5/5/1998	5,748,548	US
NMXS-0099.00/US	Utility - ORG	Issued	STABLE REFERENCE VOLTAGE GENERATOR CIRCUIT	11/30/1994	08/347,788	5/21/2002	6,392,469	US
NMXS-0100.00/US	Utility - ORG	Issued	REGULATION CIRCUIT AND METHOD FOR THE ERASING PHASE OF NON-VOLATILE MEMORY CELLS	2/21/1995	08/395,361	4/1/1997	5,617,356	US
NMXS-0101.00/US	Utility - ORG	Issued	CIRCUIT DEVICE AND CORRESPONDING METHOD FOR RESETTING NON-VOLATILE AND ELECTRICALLY PROGRAMMABLE MEMORY DEVICES	12/29/1994	08/366,212	12/17/1996	5,586,077	US
NMXS-0102.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR PROGRAMMING NON-VOLATILE AND ELECTRICALLY PROGRAMMABLE MEMORY CELLS	12/29/1994	08/366,259	5/21/1996	5,519,656	US
NMXS-0103.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR NON-VOLATILE SEMICONDUCTOR ELECTRICALLY PROGRAMMABLE MEMORY DEVICES	1/3/1995	08/368,211	8/19/1997	5,659,516	US
NMXS-0103.01/US	Utility - DIV	Issued	VOLTAGE REGULATOR FOR NON-VOLATILE SEMICONDUCTOR ELECTRICALLY PROGRAMMABLE MEMORY DEVICES	4/1/1997	08/831,046	5/18/1999	5,905,677	US
NMXS-0104.00/US	Utility - ORG	Issued	INTEGRATED PROGRAMMING CIRCUITRY FOR AN ELECTRICALLY PROGRAMMABLE SEMICONDUCTOR MEMORY DEVICE WITH REDUNDANCY	12/28/1994	08/365,154	8/20/1996	5,548,554	US
NMXS-0105.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR SUPPRESSING DATA LOADING NOISE IN NONVOLATILE MEMORIES	2/21/1995	08/391,147	7/30/1996	5,541,884	US
NMXS-0106.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TIMING THE READING OF NONVOLATILE MEMORIES	2/21/1995	08/391,920	7/2/1996	5,532,972	US

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NMXS-0107.00/US	Utility - ORG	Issued	MEMORY ARRAY CELL READING CIRCUIT WITH EXTRA CURRENT BRANCH	4/17/1995	08/422,813	10/8/1996	5,563,826	US
NMXS-0108.00/US	Utility - ORG	Issued	FLASH-EEPROM MEMORY ARRAY AND METHOD FOR BIASING THE SAME	3/28/1995	08/412,162	6/10/1997	5,638,327	US
NMXS-0109.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING REDUNDANCY REGISTERS IN A COLUMN REDUNDANCY INTEGRATED CIRCUITRY FOR A SEMICONDUCTOR MEMORY DEVICE, AND COLUMN REDUNDANCY INTEGRATED CIRCUITRY	2/16/1995	08/389,599	2/11/1997	5,602,786	US
NMXS-0110.00/US	Utility - ORG	Issued	REFERENCE SIGNAL GENERATING METHOD AND CIRCUIT FOR DIFFERENTIAL EVALUATION OF THE CONTENT OF NONVOLATILE MEMORY CELLS	3/28/1995	08/411,904	7/30/1996	5,541,880	US
NMXS-0111.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR NON-VOLATILE SEMICONDUCTOR MEMORY DEVICES	1/3/1995	08/367,538	11/19/1996	5,576,990	US
NMXS-0112.00/RE	Utility - REIS	Issued	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	7/12/2000	09/616,821	7/14/1998	RE39,918	US
NMXS-0112.02/US	Utility - CON	Abandoned	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	9/18/1997	08/932,930	7/14/1998	5,781,043	US
NMXS-0113.00/US	Utility - ORG	Issued	STRUCTURE TO RECOVER A PORTION OF A PARTIALLY FUNCTIONAL EMBEDDED MEMORY	4/22/1993	08/051,184	11/28/1995	5,471,431	US
NMXS-0114.00/RE	Utility - REIS	Issued	STRUCTURE FOR DESELECTIVE BROKEN SELECT LINES IN MEMORY ARRAYS	11/7/1997	08/966,042	11/7/1995	RE36,319	US
NMXS-0114.00/US	Utility - ORG	Abandoned	STRUCTURE FOR DESELECTIVE BROKEN SELECT LINES IN MEMORY ARRAYS	5/28/1993	08/069,025	11/7/1995	5,465,233	US
NMXS-0115.00/US	Utility - ORG	Issued	LATCH CONTROLLED OUTPUT DRIVER	6/30/1993	08/085,760	9/20/1994	5,349,243	US
NMXS-0116.00/US	Utility - ORG	Issued	FUSE CURCUITRY TO CONTROL THE PROPAGATION DELAY OF AN IC	6/30/1993	08/085,580	6/27/1995	5,428,311	US
NMXS-0117.01/US	Utility - CON	Issued	METHOD AND APPARATUS FOR PARALLEL TESTING OF MEMORY	3/27/1995	08/410,383	8/13/1996	5,546,537	US
NMXS-0118.01/US	Utility - CON	Issued	CLOCK GENERATION CIRCUIT HAVING COMPENSATION FOR SEMICONDUCTOR MANUFACTURING PROCESS VARIATIONS	3/30/1995	08/413,789	5/6/1997	5,627,793	US
NMXS-0119.00/US	Utility - ORG	Issued	CLOCK GENERATOR CIRCUIT WITH LOW CURRENT FREQUENCY DIVIDER	1/27/1994	08/188,153	11/21/1995	5,469,116	US
NMXS-0120.00/US	Utility - CIP	Issued	DISABLING SENSE AMPLIFIER	7/30/1993	08/099,947	12/27/1994	5,377,150	US
NMXS-0120.01/US	Utility - DIV	Issued	DISABLING SENSE AMPLIFIER	8/31/1994	08/298,766	12/5/1995	5,473,567	US
NMXS-0121.00/US	Utility - ORG	Issued	PRECHARGE DEVICE FOR AN INTEGRATED CIRCUIT INTERNAL BUS	8/31/1993	08/114,749	3/28/1995	5,402,379	US
NMXS-0122.00/US	Utility - ORG	Issued	APPARATUS FOR TESTING SIGNAL TIMING AND PROGRAMMING DELAY	1/31/1994	08/189,589	11/26/1996	5,579,326	US
NMXS-0122.01/US	Utility - DIV	Lapsed	APPARATUS FOR TESTING SIGNAL TIMING AND PROGRAMMING DELAY	5/9/1996	08/647,222	11/24/1998	5,841,789	US
NMXS-0123.00/US	Utility - ORG	Issued	DISTRIBUTED NOR TAG MATCH APPARATUS	8/31/1993	08/114,747	11/5/1996	5,572,456	US
NMXS-0124.02/US	Utility - CON	Issued	VARIABLE IMPEDANCE DELAY ELEMENTS	2/18/1997	08/801,452	1/11/2000	6,014,050	US
NMXS-0124.03/US	Utility - DIV	Issued	VARIABLE IMPEDANCE DELAY ELEMENTS	8/25/1998	09/139,225	6/28/2001	6,252,447	US
NMXS-0125.00/US	Utility - ORG	Issued	TESTING CIRCUITRY OF INTERNAL PERIPHERAL BLOCKS IN A SEMICONDUCTOR MEMORY DEVICE AND METHOD OF TESTING THE SAME	1/27/1994	08/188,158	8/8/1995	5,440,516	US
NMXS-0126.00/US	Utility - ORG	Issued	STRESS TEST MODE	12/22/1993	08/172,854	11/10/1998	5,835,427	US
NMXS-0127.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR ACQUIRING REDUNDANCY INFORMATION FROM A PACKAGED MEMORY CHIP	12/22/1993	08/172,848	8/8/2000	6,101,618	US
NMXS-0128.01/US	Utility - CON	Issued	REDUNDANT LINE DECODER MASTER ENABLE	6/19/1995	08/492,219	10/22/1996	5,568,061	US
NMXS-0129.01/US	Utility - CON	Issued	STRUCTURE TO UTILIZE A PARTIALLY FUNCTIONAL CACHE MEMORY BY INVALIDATION OF FAULTY CACHE MEMORY LOCATIONS	9/20/1996	08/717,139	1/13/1998	5,708,789	US
NMXS-0130.00/US	Utility - ORG	Issued	LATCH CONTROLLED OUTPUT DRIVER	9/30/1993	08/129,257	3/7/1995	5,396,108	US
NMXS-0131.00/US	Utility - ORG	Issued	EDGE TRANSITION DETECTION DISABLE CIRCUIT TO ALTER MEMORY DEVICE OPERATING CHARACTERISTICS	9/30/1993	08/129,763	5/23/1995	5,418,756	US
NMXS-0132.00/US	Utility - ORG	Issued	DATA COMPARING SENSE AMPLIFIER	12/22/1993	08/172,853	10/22/1996	5,568,073	US
NMXS-0133.00/US	Utility - ORG	Issued	STATIC TEST MODE NOISE FILTER	11/30/1993	08/160,608	8/8/1995	5,440,178	US
NMXS-0134.00/US	Utility - ORG	Issued	FUSE CIRCUIT WITH FEEDBACK DISCONNECT	12/28/1993	08/174,139	2/13/1996	5,491,444	US
NMXS-0135.00/US	Utility - ORG	Issued	STATIC MEMORY LONG WRITE TEST	12/22/1993	08/173,197	11/19/1996	5,577,051	US
NMXS-0136.00/US	Utility - CIP	Issued	COMPENSATING DELAY ELEMENT FOR CLOCK GENERATION IN A MEMORY DEVICE	12/20/1993	08/170,612	6/13/1995	5,424,985	US
NMXS-0137.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH EDGE TRANSITION DETECTION PULSE DISABLE	2/28/1994	08/202,830	2/20/1996	5,493,537	US
NMXS-0138.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY WITH WRITE CHECKING	11/17/1994	08/340,940	4/21/1998	5,742,548	US

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NMXS-0139.00/US	Utility - ORG	Issued	RESISTOR-CAPACITOR-TRANSISTOR TYPE INTEGRATED CIRCUIT, METHOD FOR THE MANUFACTURE OF SUCH A CIRCUIT AND APPLICATION TO AN OSCILLATOR	5/19/1994	08/246,070	7/4/1995	5,430,319	US
NMXS-0139.01/US	Utility - CON	Issued	RELAXATION OSCILLATOR USING INTEGRATED RTC STRUCTURE	3/8/1995	08/399,900	8/26/1997	5,661,324	US
NMXS-0140.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT CONTAINING A PROTECTED MEMORY AND SECURED SYSTEM USING SAID INTEGRATED CIRCUIT	10/27/1994	08/329,975	1/14/1997	5,594,793	US
NMXS-0141.00/US	Utility - ORG	Issued	LINE DECODER CIRCUIT FOR A MEMORY WORKING AT LOW SUPPLY VOLTAGES	12/21/1994	08/361,313	9/10/1996	5,555,216	US
NMXS-0142.00/US	Utility - ORG	Issued	MEMORY IN INTEGRATED CIRCUIT FORM WITH IMPROVED READING TIME	12/21/1994	08/361,996	7/16/1996	5,537,349	US
NMXS-0143.00/US	Utility - ORG	Issued	METHOD FOR MAKING A BIPOLAR TRANSISTOR FOR THE PROTECTION OF AN INTEGRATED CIRCUIT AGAINST ELECTROSTATIC DISCHARGES	1/27/1995	08/379,337	3/23/1999	5,886,386	US
NMXS-0143.01/US	Utility - DIV	Issued	METHOD FOR MAKING A BIPOLAR TRANSISTOR FOR THE PROTECTION OF AN INTEGRATED CIRCUIT AGAINST ELECTROSTATIC DISCHARGES	12/24/1998	09/220,635	7/24/2001	6,265,277	US
NMXS-0144.02/US	Utility - CON	Issued	BIAS CIRCUIT FOR TRANSISTOR OF A STORAGE CELL	12/16/1996	08/767,521	5/4/1999	5,900,756	US
NMXS-0145.00/US	Utility - ORG	Issued	MEMORY REDUNDANCY CIRCUIT	2/27/1995	08/393,464	11/26/1996	5,579,265	US
NMXS-0146.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY WITH COLUMN VOLTAGE HOLDING CIRCUIT	3/10/1995	08/402,519	3/11/1997	5,610,860	US
NMXS-0146.01/US	Utility - DIV	Issued	INTEGRATED CIRCUIT MEMORY WITH COLUMN VOLTAGE HOLDING CIRCUIT	3/4/1997	08/811,358	3/17/1998	5,729,162	US
NMXS-0147.00/US	Utility - ORG	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR IN PREDEFINED INITIAL STATE FOR MEMORY REDUNDANCY CIRCUIT	1/30/1995	08/380,737	2/25/1997	5,606,523	US
NMXS-0148.00/US	Utility - ORG	Issued	NON-VOLATILE-PROGRAMMABLE BISTABLE MULTIVIBRATOR, PROGRAMMABLE BY THE SOURCE, FOR MEMORY REDUNDANCY CIRCUIT	1/30/1995	08/380,738	1/7/1997	5,592,417	US
NMXS-0148.01/US	Utility - DIV	Issued	NON-VOLATILE-PROGRAMMABLE BISTABLE MULTIVIBRATOR, PROGRAMMABLE BY THE SOURCE, FOR MEMORY REDUNDANCY CIRCUIT	11/21/1996	08/752,814	10/14/1997	5,677,870	US
NMXS-0148.02/US	Utility - DIV	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR, PROGRAMMABLE BY THE SOURCE, FOR MEMORY REDUNDANCY CIRCUIT	11/21/1996	08/754,432	2/24/1998	5,721,706	US
NMXS-0149.00/US	Utility - ORG	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR WITH REDUCED PARASITICS IN READING MODE NOTABLY FOR MEMORY REDUNDANCY CIRCUIT	1/31/1995	08/381,529	10/1/1996	5,561,621	US
NMXS-0150.00/US	Utility - ORG	Issued	SWITCHING REGULATOR WITH DYSSYMETRICAL DIFFERENTIAL INPUT STAGE	4/19/1995	08/423,398	3/18/1997	5,612,611	US
NMXS-0151.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER CIRCUIT	4/19/1995	08/423,396	6/3/1997	5,636,115	US
NMXS-0152.00/US	Utility - ORG	Abandoned	METHOD FOR THE ERASURE OF A MEMORY, AND CIRCUITS FOR THE IMPLEMENTATION THEREOF	4/12/1995	08/421,671	4/8/1997	5,619,451	US
NMXS-0153.00/US	Utility - ORG	Issued	CIRCUIT FOR THE SELECTION OF REDUNDANT MEMORY ELEMENTS AND FLASH EEPROM MEMORY COMPRISING SAID CIRCUIT	2/22/1995	08/394,314	8/18/1998	5,796,653	US
NMXS-0154.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY INCORPORATING TEST FUNCTIONS	4/7/1995	08/418,636	7/1/1997	5,644,530	US
NMXS-0155.00/US	Utility - ORG	Issued	PROGRAMMABLE INTEGRATED CIRCUIT MEMORY COMPRISING EMULATION MEANS	6/7/1995	08/484,873	7/22/1997	5,651,128	US
NMXS-0156.00/US	Utility - ORG	Issued	MATRIX DEVICE WITH REDUNDANCY FUSES FOR INTEGRATED MEMORY	3/21/1995	08/408,016	1/14/1997	5,594,693	US
NMXS-0157.00/US	Utility - ORG	Issued	METHOD FOR TESTING AN ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY DEVICE	6/7/1995	08/479,081	12/31/1996	5,590,075	US
NMXS-0158.01/US	Utility - CON	Issued	PROGRAMMABLE MEMORY ELEMENT	2/4/1997	08/794,965	12/9/1997	5,696,716	US
NMXS-0159.00/US	Utility - ORG	Issued	SENSE AMPLIFIER WITH HYSTERESIS	5/2/1995	08/432,838	2/3/1998	5,715,204	US
NMXS-0160.01/US	Utility - DIV	Issued	HIGHLY-PLANAR INTERLAYER DIELECTRIC THIN FILMS IN INTEGRATED CIRCUITS	6/6/1995	08/468,282	1/28/1997	5,598,028	US
NMXS-0161.00/US	Utility - ORG	Issued	PROCESS FOR THE PRODUCTION OF A SEMICONDUCTOR DEVICE HAVING BETTER INTERFACE ADHESION BETWEEN DIELECTRIC LAYERS	12/22/1995	08/577,125	11/28/2000	6,153,537	US
NMXS-0161.01/US	Utility - DIV	Issued	PROCESS FOR THE PRODUCTION OF A SEMICONDUCTOR DEVICE HAVING BETTER INTERFACE ADHESION BETWEEN DIELECTRIC LAYERS	12/14/1998	09/211,065	3/11/2003	6,531,714	US
NMXS-0162.00/US	Utility - ORG	Issued	FAILURE TOLERANT MEMORY DEVICE, IN PARTICULAR OF THE FLASH EEPROM TYPE	5/31/1995	08/454,650	10/28/1997	5,682,349	US

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NMXS-0163.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY CELL AND A METHOD FOR FORMING THE SAME	7/18/1995	08/503,303	1/27/1998	5,712,814	US
NMXS-0163.01/US	Utility - DIV	Issued	METHOD OF MAKING ASYMMETRIC NONVOLATILE MEMORY CELL	9/11/1996	08/712,373	7/6/1999	5,920,776	US
NMXS-0164.00/US	Utility - ORG	Issued	MEMORY DEVICE HAVING ERROR DETECTION AND CORRECTION FUNCTION, AND METHODS FOR READING, WRITING AND ERASING THE MEMORY DEVICE	10/2/1995	08/538,161	6/2/1998	5,761,222	US
NMXS-0165.00/US	Utility - ORG	Issued	SENSE AMPLIFIER HAVING CAPACITIVELY COUPLED INPUT FOR OFFSET COMPENSATION	4/26/1996	08/639,192	3/17/1998	5,729,492	US
NMXS-0165.01/US	Utility - CON	Issued	SENSE AMPLIFIER CIRCUIT FOR SEMICONDUCTOR MEMORY DEVICES	1/26/1998	09/013,141	11/9/1999	5,982,666	US
NMXS-0166.01/US	Utility - CON	Issued	MONOLITHICALLY INTEGRATED GENERATOR OF A PLURALITY OF VOLTAGE VALUES	9/10/1998	09/150,802	11/7/2000	6,144,588	US
NMXS-0167.00/US	Utility - ORG	Issued	HIGH-VOLTAGE N-CHANNEL MOS TRANSISTOR AND ASSOCIATED MANUFACTURING PROCESS	2/27/1996	08/607,779	12/15/1998	5,850,360	US
NMXS-0168.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH IMPROVED IMMUNITY TO LARGE METALLIZATION DEFECTS	10/2/1995	08/538,302	7/1/1997	5,644,526	US
NMXS-0169.00/US	Utility - ORG	Issued	READING CIRCUIT FOR MEMORY CELLS	6/6/1995	08/476,547	1/20/1998	5,710,739	US
NMXS-0170.00/US	Utility - ORG	Issued	SYNCHRONIZATION DEVICE FOR OUTPUT STAGES, PARTICULARLY FOR ELECTRONIC MEMORIES	11/17/1995	08/560,324	5/27/1997	5,633,834	US
NMXS-0171.00/US	Utility - ORG	Issued	PROGRAMMABLE MEMORY WITH SINGLE BIT ENCODING	4/4/1996	08/628,587	12/15/1998	5,850,361	US
NMXS-0172.00/US	Utility - ORG	Issued	DECODER WITH REDUCED ARCHITECTURE	11/17/1995	08/560,090	4/21/1998	5,742,187	US
NMXS-0173.00/US	Utility - ORG	Issued	OUTPUT STAGE FOR INTEGRATED CIRCUITS, PARTICULARLY FOR ELECTRONIC MEMORIES	5/17/1996	08/649,468	8/12/1997	5,657,276	US
NMXS-0174.00/US	Utility - ORG	Issued	PROGRAMMABLE MULTIBIT REGISTER FOR COINCIDENCE AND JUMP OPERATIONS AND COINCIDENCE FUSE CELL	1/26/1996	08/592,122	3/24/1998	5,731,716	US
NMXS-0176.00/US	Utility - ORG	Issued	EEPROM MEMORY WITH CONTACTLESS MEMORY CELLS	5/3/1996	08/642,325	2/10/1998	5,717,636	US
NMXS-0177.00/US	Utility - ORG	Issued	BYTE ERASABLE EEPROM FULLY COMPATIBLE WITH A SINGLE POWER SUPPLY FLASH-EPROM PROCESS	9/25/1995	08/533,631	3/18/1997	5,612,913	US
NMXS-0178.00/US	Utility - ORG	Issued	POWER-ON RESET CIRCUIT	8/31/1995	08/521,666	12/9/1997	5,696,461	US
NMXS-0179.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE HAVING SECTORS OF SELECTABLE SIZE AND NUMBER	5/3/1996	08/649,857	8/11/1998	5,793,676	US
NMXS-0179.01/US	Utility - CON	Issued	NONVOLATILE MEMORY DEVICE HAVING SECTORS OF SELECTABLE SIZE AND NUMBER	6/15/1998	09/094,916	9/7/1999	5,949,713	US
NMXS-0180.00/US	Utility - ORG	Issued	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON VOLATILE MEMORY CELLS AND SIDE TRANSISTORS AND CORRESPONDING IC	6/20/1996	08/667,097	12/21/1999	6,004,847	US
NMXS-0180.01/US	Utility - CON	Issued	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS AND CORRESPONDING IC	4/27/1999	09/300,029	6/19/2001	6,248,630	US
NMXS-0181.00/US	Utility - ORG	Issued	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS OF AT LEAST TWO DIFFERENT TYPES, AND CORRESPONDING IC	6/20/1996	08/670,179	1/5/1999	5,856,221	US
NMXS-0182.00/US	Utility - ORG	Issued	METHOD OF MANUFACTURING A MOS INTEGRATED CIRCUIT HAVING COMPONENTS WITH DIFFERENT DIELECTRICS	5/10/1996	08/644,892	9/5/2000	6,114,203	US
NMXS-0183.00/US	Utility - ORG	Issued	METHOD FOR IMPROVING THE PROFILE OF THE INTERMEDIATE DIELECTRIC, PARTICULARLY FOR NON VOLATILE MEMORIES	2/19/1997	08/802,619	4/13/1999	5,894,065	US
NMXS-0183.01/US	Utility - DIV	Issued	METHOD FOR IMPROVING THE INTERMEDIATE DIELECTRIC PROFILE, PARTICULARLY FOR NON-VOLATILE MEMORIES	7/22/1997	08/898,155	8/15/2000	6,104,058	US
NMXS-0184.00/US	Utility - ORG	Issued	CHARGE PUMP VOLTAGE MULTIPLIER CIRCUIT	12/5/1995	08/567,328	9/22/1998	5,812,017	US
NMXS-0185.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR-BOOSTER FOR SUPPLYING A PULSATING VOLTAGE HAVING APPROXIMATELY CONSTANT VOLTAGE LEVELS	4/12/1996	08/631,574	6/22/1999	5,914,867	US
NMXS-0186.00/US	Utility - ORG	Issued	CURRENT DETECTING CIRCUIT	8/2/1996	08/691,796	6/9/1998	5,764,570	US
NMXS-0187.00/US	Utility - ORG	Issued	HIERARCHIC MEMORY DEVICE HAVING AUXILIARY LINES CONNECTED TO WORD LINES	9/30/1996	08/724,495	11/24/1998	5,841,728	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0188.04/US	Utility - CON	Issued	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	5/15/1997	08/940,045	5/18/1999	5,905,683	US
NMXS-0189.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH FUSE CIRCUITRY SIMULATING FUSE BLOWING	3/31/1994	08/220,976	5/14/1996	5,517,455	US
NMXS-0190.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH CENTRALIZED CONTROL OF EDGE TRANSITION DETECTION PULSE GENERATION	3/31/1994	08/220,834	11/28/1995	5,471,157	US
NMXS-0191.00/US	Utility - ORG	Issued	STRUCTURE CAPABLE OF SIMULTANEOUSLY TESTING REDUNDANT AND NON-REDUNDANT MEMORY ELEMENTS DURING STRESS TESTING OF AN INTEGRATED CIRCUIT MEMORY DEVICE	4/29/1994	08/235,161	6/25/1996	5,530,674	US
NMXS-0192.00/US	Utility - ORG	Issued	LONG WRITE TEST	6/29/1994	08/267,667	12/10/1996	5,583,816	US
NMXS-0193.00/US	Utility - ORG	Issued	CIRCUIT WHICH PROVIDES POWER ON RESET DISABLE DURING A TEST MODE	6/29/1994	08/267,666	10/3/1995	5,455,799	US
NMXS-0194.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY HAVING CONTROL CIRCUITRY FOR SHARED DATA BUS	5/31/1994	08/251,718	5/28/1996	5,521,880	US
NMXS-0195.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY WITH DISABLED EDGE TRANSITION PULSE GENERATION DURING SPECIAL TEST MODE	5/31/1994	08/251,337	2/20/1996	5,493,532	US
NMXS-0196.00/US	Utility - ORG	Issued	VARIABLE INPUT THRESHOLD ADJUSTMENT	7/29/1994	08/282,177	12/31/1996	5,589,783	US
NMXS-0197.00/US	Utility - ORG	Issued	TEST MODE CONTROL CIRCUIT OF AN INTEGRATED CIRCUIT DEVICE	7/29/1994	08/282,047	11/9/1999	5,982,188	US
NMXS-0198.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR ENABLING A BUS DRIVER WHEN A DATA SIGNAL IS VALID	8/10/1994	08/288,334	10/15/1996	5,566,112	US
NMXS-0198.01/US	Utility - DIV	Issued	APPARATUS AND METHOD FOR ENABLING A BUS DRIVER WHEN A DATA SIGNAL IS VALID	5/10/1995	08/438,276	10/29/1996	5,570,316	US
NMXS-0199.00/US	Utility - ORG	Issued	PRE-CHARGED EXCLUSIONARY WIRED-CONNECTED PROGRAMMED REDUNDANT SELECT	9/30/1994	08/316,087	2/27/1996	5,495,446	US
NMXS-0200.00/US	Utility - ORG	Issued	FULL MEMORY CHIP LONG WRITE TEST MODE	9/30/1994	08/315,337	3/26/1996	5,502,678	US
NMXS-0201.00/US	Utility - ORG	Issued	LOW CURRENT DIFFERENTIAL LEVEL SHIFTER	10/31/1994	08/331,892	5/14/1996	5,517,148	US
NMXS-0202.00/US	Utility - ORG	Issued	CIRCUIT STRUCTURE AND METHOD FOR STRESS TESTING OF BIT LINES	10/31/1994	08/331,699	3/11/1997	5,610,866	US
NMXS-0203.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR MAPPING A REDUNDANT MEMORY COLUMN TO A DEFECTIVE MEMORY COLUMN	5/10/1995	08/438,903	11/12/1996	5,574,688	US
NMXS-0204.00/US	Utility - ORG	Issued	POST-FABRICATION SELECTABLE REGISTERED AND NON-REGISTERED MEMORY	12/22/1994	08/362,187	11/26/1996	5,579,263	US
NMXS-0205.00/US	Utility - ORG	Issued	CIRCUIT FOR PROVIDING A COMPENSATED BIAS VOLTAGE	12/16/1994	08/357,664	10/22/1996	5,568,084	US
NMXS-0205.01/US	Utility - CON	Issued	CIRCUIT FOR PROVIDING A COMPENSATED BIAS VOLTAGE	4/12/1996	08/631,063	8/5/1997	5,654,663	US
NMXS-0206.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUITRY WITH LIMITED OUTPUT HIGH VOLTAGE	12/20/1994	08/360,228	1/21/1997	5,596,297	US
NMXS-0207.00/US	Utility - ORG	Issued	VOLTAGE REFERENCE CIRCUIT USING AN OFFSET COMPENSATING CURRENT SOURCE	12/20/1994	08/360,229	8/20/1996	5,548,241	US
NMXS-0208.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUITRY WITH SELECTIVE LIMITED OUTPUT HIGH VOLTAGE	12/20/1994	08/359,397	1/14/1997	5,594,373	US
NMXS-0209.00/US	Utility - ORG	Issued	ADJUSTABLE CURRENT SOURCE	12/20/1994	08/359,927	12/3/1996	5,581,209	US
NMXS-0210.00/US	Utility - ORG	Issued	VOLTAGE REFERENCE CIRCUIT HAVING A THRESHOLD VOLTAGE SHIFT	12/20/1994	08/359,926	1/28/1997	5,598,122	US
NMXS-0211.00/US	Utility - ORG	Issued	DYNAMICALLY CONTROLLED VOLTAGE REFERENCE CIRCUIT	12/20/1994	08/360,227	12/31/1996	5,589,794	US
NMXS-0212.01/US	Utility - CON	Issued	CLOCK PULSE EXTENDER MODE FOR CLOCKED MEMORY DEVICES HAVING PRECHARGED DATA PATHS	4/30/1997	08/848,342	6/29/1999	5,917,353	US
NMXS-0213.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR MAPPING A REDUNDANT MEMORY COLUMN TO A DEFECTIVE MEMORY COLUMN	5/10/1995	08/438,349	11/5/1996	5,572,470	US
NMXS-0214.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR TESTING AN INTEGRATED CIRCUIT DEVICE	8/25/1995	08/519,192	4/18/2000	6,052,806	US
NMXS-0215.00/US	Utility - ORG	Issued	RESIDUAL CHARGE ELIMINATION FOR A MEMORY DEVICE	12/29/1995	08/580,549	3/31/1998	5,734,608	US
NMXS-0216.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TESTING MEMORIES IN INTEGRATED CIRCUIT FORM	12/21/1995	08/575,953	10/7/1997	5,675,539	US
NMXS-0217.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR ADDRESS DECODING IN AN INTEGRATED CIRCUIT MEMORY	9/11/1995	08/526,500	4/21/1998	5,742,546	US
NMXS-0218.00/US	Utility - ORG	Issued	CURRENT DETECTION CIRCUIT FOR READING A MEMORY IN INTEGRATED CIRCUIT FORM	5/17/1996	08/649,282	12/16/1997	5,699,295	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0219.00/US	Utility - ORG	Issued	AN ELECTRICALLY PROGRAMMABLE MEMORY WITH IMPROVED RETENTION OF DATA AND A METHOD OF WRITING DATA IN SAID MEMORY	12/18/1995	08/573,897	7/29/1997	5,652,720	US
NMXS-0220.00/US	Utility - ORG	Issued	CHARGE PUMP TYPE OF NEGATIVE VOLTAGE GENERATOR CIRCUIT AND METHOD	6/13/1996	08/663,524	11/24/1998	5,841,314	US
NMXS-0221.00/US	Utility - ORG	Issued	RESISTANCE REFERENCE CIRCUIT	12/21/1995	08/576,881	2/10/1998	5,717,323	US
NMXS-0222.00/US	Utility - ORG	Issued	DIGITAL RAMP GENERATOR	5/30/1996	08/652,710	6/30/1998	5,774,390	US
NMXS-0223.00/US	Utility - ORG	Issued	SERIAL DICHOTOMIC METHOD FOR SENSING MULTIPLE-LEVEL NON-VOLATILE MEMORY CELLS, AND SENSING CIRCUIT IMPLEMENTING SUCH METHOD	1/29/1996	08/593,650	12/23/1997	5,701,265	US
NMXS-0224.01/US	Utility - CON	Issued	READING CIRCUIT FOR MULTILEVEL NON VOLATILE MEMORY CELL DEVICES	6/4/1997	08/869,072	11/17/1998	5,838,612	US
NMXS-0225.00/US	Utility - ORG	Issued	CIRCUIT FOR READING NON-VOLATILE MEMORIES	7/31/1996	08/690,530	3/31/1998	5,734,610	US
NMXS-0226.00/RE	Utility - REIS	Issued	CIRCUIT AND METHOD FOR READING A MEMORY CELL THAT CAN STORE MULTIPLE BITS OF DATA	9/30/1999	09/410,164	9/30/1997	RE38,166	US
NMXS-0226.00/US	Utility - ORG	Abandoned	SENSE CIRCUIT FOR SERIES BINARY SENSING OF MULTILEVEL NONVOLATILE STORAGE ELEMENT	1/29/1996	08/592,939	9/30/1997	5,673,221	US
NMXS-0227.00/US	Utility - ORG	Issued	READING CIRCUIT FOR MEMORY CELL DEVICES HAVING A LOW SUPPLY VOLTAGE	4/25/1996	08/638,976	12/2/1997	5,694,363	US
NMXS-0228.00/US	Utility - ORG	Issued	MODULATED SLOPE SIGNAL GENERATION CIRCUIT, PARTICULARLY FOR LATCH DATA SENSING ARRANGEMENTS	7/19/1996	08/684,431	4/7/1998	5,737,268	US
NMXS-0229.00/US	Utility - ORG	Issued	THRESHOLD DETECTING DEVICE	7/31/1996	08/688,956	12/8/1998	5,847,584	US
NMXS-0230.00/US	Utility - ORG	Issued	CHARGE PUMP CIRCUIT WITH MULTIPLE BOOST STAGES	7/19/1996	08/684,192	6/2/1998	5,760,497	US
NMXS-0231.00/US	Utility - ORG	Issued	FLASH EEPROM WITH INTEGRATED DEVICE FOR LIMITING THE ERASE SOURCE VOLTAGE	7/31/1996	08/692,936	1/14/2003	6,507,067	US
NMXS-0232.00/US	Utility - ORG	Issued	NEGATIVE WORD LINE VOLTAGE REGULATION CIRCUIT FOR ELECTRICALLY ERASABLE SEMICONDUCTOR MEMORY DEVICES	6/19/1996	08/665,862	8/19/1997	5,659,502	US
NMXS-0232.01/US	Utility - CON	Issued	NEGATIVE WORD LINE VOLTAGE REGULATION CIRCUIT FOR ELECTRICALLY ERASABLE SEMICONDUCTOR MEMORY DEVICES	6/23/1997	08/881,713	7/6/1999	5,920,505	US
NMXS-0233.00/US	Utility - ORG	Issued	PARALLEL PROGRAMMING METHOD OF MEMORY WORDS AND CORRESPONDING CIRCUIT	9/30/1996	08/722,378	7/14/1998	5,781,474	US
NMXS-0234.00/US	Utility - ORG	Issued	METHOD FOR SETTING THE THRESHOLD VOLTAGE OF A REFERENCE MEMORY CELL	7/12/1996	08/679,656	7/21/1998	5,784,314	US
NMXS-0235.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR SEMICONDUCTOR NON-VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY DEVICE	9/30/1996	08/720,491	12/1/1998	5,844,404	US
NMXS-0236.00/US	Utility - ORG	Issued	ROW DECODING CIRCUIT FOR SEMICONDUCTOR NON-VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY AND CORRESPONDING METHOD	3/27/1997	08/824,616	12/8/1998	5,848,013	US
NMXS-0236.01/US	Utility - DIV	Issued	ROW DECODING CIRCUIT FOR A SEMICONDUCTOR NON VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY AND CORRESPONDING METHOD	12/2/1998	09/203,937	10/24/2000	6,137,725	US
NMXS-0236.02/US	Utility - DIV	Issued	ROW DECODING CIRCUIT FOR A SEMICONDUCTOR NON VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY AND CORRESPONDING METHOD	8/7/2000	09/633,334	11/20/2001	6,320,792	US
NMXS-0237.00/US	Utility - ORG	Issued	DEVICE FOR GENERATING AND REGULATING A GATE VOLTAGE IN A NON-VOLATILE MEMORY	12/30/1996	08/775,111	10/13/1998	5,822,247	US
NMXS-0238.00/US	Utility - ORG	Issued	SYSTEM FOR DETERMINING THE PROGRAMMED/NON PROGRAMMED STATUS OF A MEMORY CELL	3/27/1997	08/827,409	1/26/1999	5,864,513	US
NMXS-0239.00/US	Utility - ORG	Issued	BOOST REGULATOR	1/21/1997	08/787,494	10/13/1998	5,821,806	US
NMXS-0240.00/US	Utility - ORG	Issued	UNBALANCED LATCH AND FUSE CIRCUIT INCLUDING THE SAME	7/19/1996	08/684,406	8/19/1997	5,659,498	US
NMXS-0241.00/US	Utility - ORG	Issued	REFERENCE WORD LINE AND DATA PROPAGATION REPRODUCTION CIRCUIT FOR MEMORIES PROVIDED WITH HIERARCHICAL DECODERS	3/27/1997	08/835,033	5/19/1998	5,754,483	US
NMXS-0242.00/US	Utility - ORG	Issued	TIMESHARING INTERNAL BUS, PARTICULARLY FOR NON-VOLATILE MEMORIES	3/7/1997	08/813,687	8/20/2002	6,438,669	US
NMXS-0243.00/US	Utility - ORG	Issued	PARALLEL-DICHOTOMIC SERIAL SENSING METHOD FOR SENSING MULTIPLE-LEVEL NON VOLATILE MEMORY CELLS, AND SENSING CIRCUIT FOR ACTUATING SUCH METHOD	7/31/1996	08/690,059	3/17/1998	5,729,490	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0244.01/US	Utility - CON	Issued	SUPPLY VOLTAGES SWITCHES CIRCUIT	7/2/1998	09/109,630	3/21/2000	6,040,734	US
NMXS-0245.00/US	Utility - ORG	Abandoned	METHOD AND PROGRAMMING DEVICE FOR DETECTING AN ERROR IN A MEMORY	12/24/1996	08/774,110	3/3/1998	5,724,290	US
NMXS-0246.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR FOR ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS	10/23/1996	08/735,709	8/11/1998	5,793,679	US
NMXS-0246.01/US	Utility - DIV	Issued	VOLTAGE GENERATOR FOR ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS	2/27/1998	09/032,179	12/5/2000	6,157,054	US
NMXS-0247.00/US	Utility - ORG	Issued	FLASH EEPROM WITH ON-CHIP ERASE SOURCE VOLTAGE GENERATOR	7/24/1996	08/687,145	2/27/2001	6,195,291	US
NMXS-0247.01/US	Utility - DIV	Issued	FLASH EEPROM WITH ON-CHIP ERASE SOURCE VOLTAGE GENERATOR	1/23/2001	09/768,744	11/19/2002	6,483,750	US
NMXS-0248.00/US	Utility - ORG	Issued	NEGATIVE CHARGE PUMP CIRCUIT FOR ELECTRICALLY ERASABLE SEMICONDUCTOR MEMORY DEVICES	10/31/1996	08/751,299	5/19/1998	5,754,476	US
NMXS-0249.00/US	Utility - ORG	Issued	ANALOG VOLTAGE-SIGNAL SELECTOR DEVICE	11/1/1996	08/742,978	5/18/1999	5,905,387	US
NMXS-0250.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH IMPROVED YIELD AND RELIABILITY	6/28/1996	08/671,848	7/7/1998	5,778,012	US
NMXS-0251.00/US	Utility - ORG	Issued	CIRCUIT FOR THE GENERATION OF A VOLTAGE AS A FUNCTION OF THE CONDUCTIVITY OF AN ELEMENTARY CELL OF A NON-VOLATILE MEMORY	3/27/1997	08/835,031	9/1/1998	5,801,988	US
NMXS-0252.00/US	Utility - ORG	Issued	ZERO CONSUMPTION POWER-ON-RESET	1/30/1997	08/790,832	10/13/1998	5,821,788	US
NMXS-0253.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR REDUNDANCY MANAGEMENT OF NON-VOLATILE MEMORIES	3/27/1997	08/828,039	11/17/1998	5,838,619	US
NMXS-0254.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING A MICROTIP CATHODE ASSEMBLY FOR A FIELD EMISSION DISPLAY PANEL	12/13/1996	08/807,113	12/14/1999	6,000,980	US
NMXS-0255.00/US	Utility - ORG	Issued	HIGH-VOLTAGE-RESISTANT MOS TRANSISTOR, AND CORRESPONDING MANUFACTURING PROCESS	3/18/1997	08/824,888	11/2/1999	5,977,591	US
NMXS-0256.01/US	Utility - CON	Issued	NONVOLATILE FLOATING-GATE MEMORY DEVICES, AND PROCESS OF FABRICATION	4/13/2000	09/548,782	9/10/2002	6,448,138	US
NMXS-0257.00/US	Utility - ORG	Issued	HIGH CAPACITY CAPACITOR AND CORRESPONDING MANUFACTURING PROCESS	10/30/1996	08/739,997	4/24/2001	6,222,245	US
NMXS-0258.00/US	Utility - ORG	Issued	ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS DEVICE FOR A REDUCED NUMBER OF PROGRAMMING CYCLES	2/27/1997	08/807,574	7/20/1999	5,926,416	US
NMXS-0259.00/US	Utility - ORG	Issued	PROCESS OF FABRICATING TUNNEL-OXIDE NONVOLATILE MEMORY DEVICES	1/31/1997	08/792,893	10/6/1998	5,817,557	US
NMXS-0260.00/US	Utility - ORG	Issued	FABRICATION OF NATURAL TRANSISTORS IN A NONVOLATILE MEMORY PROCESS	1/16/1997	08/784,967	7/13/1999	5,923,975	US
NMXS-0261.00/US	Utility - ORG	Issued	MULTI-LEVEL MEMORY CIRCUITS AND CORRESPONDING READING AND WRITING METHODS	1/30/1997	08/791,348	1/12/1999	5,859,795	US
NMXS-0262.00/US	Utility - NSPCT	Issued	MULTI-LEVEL MEMORY CIRCUIT WITH REGULATED READING VOLTAGE	10/30/1996	09/202,657	8/8/2000	6,101,121	US
NMXS-0263.00/US	Utility - ORG	Issued	CLOCK CIRCUIT FOR READING A MUTILEVEL NON VOLATILE MEMORY CELLS DEVICE	6/27/1997	08/883,822	11/16/1999	5,986,921	US
NMXS-0264.00/US	Utility - NSPCT	Issued	MULTI-LEVEL MEMORY CIRCUIT WITH REGULATED WRITING VOLTAGE	10/30/1996	09/202,656	8/1/2000	6,097,628	US
NMXS-0265.00/US	Utility - ORG	Issued	SECTORIZED ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY DEVICE WITH REDUNDANCY	3/21/1997	08/821,804	12/29/1998	5,854,764	US
NMXS-0266.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY DEVICE WITH TESTABLE REDUNDANCY CIRCUITS	5/8/1997	08/853,756	12/7/1999	5,999,450	US
NMXS-0267.00/US	Utility - ORG	Issued	SENSING CIRCUITRY FOR READING AND VERIFYING THE CONTENTS OF ELECTRICALLY PROGRAMMABLE/ERASABLE NON-VOLATILE MEMORY CELLS	4/29/1997	08/845,916	6/29/1999	5,917,753	US
NMXS-0267.01/US	Utility - CON	Issued	SENSING CIRCUITRY FOR READING AND VERIFYING THE CONTENTS OF ELECTRICALLY PROGRAMMABLE/ERASABLE NON-VOLATILE MEMORY CELLS	12/9/1998	09/209,319	4/25/2000	6,055,187	US
NMXS-0268.00/US	Utility - ORG	Issued	ERASING METHOD FOR A NON-VOLATILE MEMORY	12/24/1996	08/774,860	10/6/1998	5,818,763	US
NMXS-0269.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR GENERATING A SYNCHRONIZING ATD SIGNAL	11/26/1997	08/978,665	3/23/1999	5,886,949	US
NMXS-0270.01/US	Utility - DIV	Issued	METHOD OF AVOIDING DISTURBANCE DURING THE STEP OF PROGRAMMING AND ERASING AN ELECTRICALLY PROGRAMMABLE, SEMICONDUCTOR NON-VOLATILE STORAGE DEVICE	9/27/1999	09/407,135	2/27/2001	6,195,290	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0271.00/US	Utility - ORG	Issued	ERASE VOLTAGE CONTROL CIRCUIT FOR AN ELECTRICALLY ERASABLE NON-VOLATILE MEMORY CELL	1/23/1997	08/787,907	2/24/1998	5,721,707	US
NMXS-0272.00/US	Utility - ORG	Issued	METHOD FOR ERASING AN ELECTRICALLY PROGRAMMABLE AND ERASABLE NON-VOLATILE MEMORY CELL	1/24/1997	08/788,530	7/21/1998	5,784,319	US
NMXS-0273.00/US	Utility - ORG	Issued	MONOLITHICALLY INTEGRATED PROGRAMMABLE DEVICE HAVING ELEMENTARY MODULES CONNECTED ELECTRICALLY BY MEANS OF MEMORY CELLS OF THE FLASH TYPE	12/27/1996	08/777,296	12/21/1999	6,005,411	US
NMXS-0274.00/US	Utility - ORG	Issued	PAGE-MODE MEMORY DEVICE WITH MULTIPLE-LEVEL MEMORY CELLS	6/5/1997	08/869,208	5/26/1998	5,757,719	US
NMXS-0275.00/US	Utility - ORG	Issued	HIGH VOLTAGES DETECTOR CIRCUIT AND INTEGRATED CIRCUIT USING SAME	1/30/1997	08/791,700	8/18/1998	5,796,275	US
NMXS-0276.00/US	Utility - ORG	Issued	PROCESS FOR FORMING AN EDGE STRUCTURE TO SEAL INTEGRATED ELECTRONIC DEVICES, AND CORRESPONDING DEVICE	1/27/1998	09/014,437	5/2/2000	6,057,591	US
NMXS-0276.01/US	Utility - DIV	Issued	PROCESS FOR FORMING AN EDGE STRUCTURE TO SEAL INTEGRATED ELECTRONIC DEVICES	3/24/2000	09/534,675	4/3/2001	6,210,994	US
NMXS-0277.00/US	Utility - ORG	Issued	MEMORY ARCHITECTURE FOR FLEXIBLE READING MANAGEMENT, PARTICULARLY FOR NON-VOLATILE MEMORIES, HAVING NOISE-IMMUNITY FEATURES, MATCHING DEVICE PERFORMANCE, AND HAVING OPTIMIZED THROUGHOUT	4/24/1997	08/847,385	6/29/1999	5,917,768	US
NMXS-0278.00/US	Utility - ORG	Issued	CIRCUITS AND METHODS FOR READ-ENABLING MEMORY DEVICES SYNCHRONOUSLY WITHTHE REACHING OF THE MINIMUM FUNCTIONALITY CONDITIONS OF THE MEMORY CELLS AND READING CIRCUITS, PARTICULARLY FOR NON-VOLATILE MEMORIES	4/24/1997	08/840,056	3/2/1999	5,878,049	US
NMXS-0279.00/US	Utility - ORG	Issued	COLUMN MULTIPLEXER	5/9/1997	08/853,732	7/7/1998	5,777,941	US
NMXS-0280.00/US	Utility - ORG	Issued	DRIVER DEVICE FOR SELECTION LINES FOR A MULTIPLEXER, TO BE USED IN A WIDE RANGE OF SUPPLY VOLTAGES, PARTICULARLY FOR NON-VOLATILE MEMORIES	3/27/1997	08/826,223	5/12/1998	5,751,654	US
NMXS-0281.00/US	Utility - ORG	Issued	A MEMORY UNDER TEST PROGRAMMING AND READING DEVICE	3/28/1997	08/835,030	11/14/2000	6,148,413	US
NMXS-0282.00/US	Utility - ORG	Issued	DECODING HIERARCHICAL ARCHITECTURE FOR HIGH INTEGRATION MEMORIES	1/30/1997	08/791,746	12/29/1998	5,854,770	US
NMXS-0283.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE CAPABLE OF READING DATA WITH AN APPROPRIATE SELF-TIMING AND A REDUCED NUMBER OF REFERENCE LINES	3/27/1997	08/826,489	8/31/1999	5,946,237	US
NMXS-0283.01/US	Utility - DIV	Issued	DATA READING PATH MANAGEMENT ARCHITECTURE FOR A MEMORY DEVICE, PARTICULARLY FOR NON-VOLATILE MEMORIES	7/12/1999	09/351,603	4/3/2001	6,212,096	US
NMXS-0284.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE HAVING OPTIMIZED MANAGEMENT OF DATA TRANSMISSION LINES	3/7/1997	08/812,595	11/3/1998	5,831,891	US
NMXS-0285.00/US	Utility - ORG	Issued	PULSE GENERATION CIRCUIT AND METHOD FOR SYNCHRONIZED DATA LOADING IN AN OUTPUT PRE-BUFFER	3/27/1997	08/826,009	1/12/1999	5,859,810	US
NMXS-0286.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH UNEQUALLY-SIZED, PAIRED MEMORY COUPLED TO ODD NUMBER OF INPUT/OUTPUT PADS	6/7/1995	08/477,244	4/29/1997	5,625,603	US
NMXS-0287.01/US	Utility - CON	Issued	CONSTANT CURRENT SOURCE WITH REDUCED SENSITIVITY TO SUPPLY VOLTAGE AND PROCESS VARIATION	6/24/1996	08/667,481	8/11/1998	5,793,247	US
NMXS-0288.00/US	Utility - ORG	Issued	CIRCUIT FOR PROVIDING A BIAS VOLTAGE COMPENSATED FOR P-CHANNEL TRANSISTOR VARIATIONS	6/5/1995	08/464,551	6/17/1997	5,640,122	US
NMXS-0289.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR AN OUTPUT DRIVER WITH REDUCED OUTPUT IMPEDANCE	3/31/1995	08/414,103	11/19/1996	5,576,656	US
NMXS-0290.00/RE	Utility - REIS	Issued	POWER SUPPLY SWITCH REFERENCE CIRCUITRY	5/5/2000	09/568,201	5/5/1998	RE37,876	US
NMXS-0290.00/US	Utility - ORG	Abandoned	POWER SUPPLY SWITCH REFERENCE CIRCUITRY	6/5/1995	08/464,082	5/5/1998	5,747,890	US
NMXS-0291.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR BIASING BIT LINES	6/7/1995	08/484,491	6/16/1998	5,768,206	US
NMXS-0292.00/US	Utility - ORG	Issued	REDUCED PIN COUNT STRESS TEST CIRCUIT FOR INTEGRATED MEMORY DEVICES AND METHOD THEREFOR	6/7/1995	08/476,658	1/7/1997	5,592,422	US
NMXS-0293.00/US	Utility - ORG	Issued	FAULT DETECTION FOR ENTIRE WAFER STRESS TEST	7/31/1995	08/509,158	4/8/1997	5,619,462	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0294.01/US	Utility - CON	Issued	INTEGRATED MEMORY CIRCUIT WITH SEQUENCED BITLINES FOR STRESS TEST	4/7/1997	08/833,582	4/28/1998	5,745,420	US
NMXS-0295.01/US	Utility - CON	Issued	METHOD AND APPARATUS FOR TEST MODE ENTRY DURING POWER UP	9/12/1996	08/712,960	12/30/1997	5,703,512	US
NMXS-0296.00/US	Utility - ORG	Issued	COLUMN REDUNDANCY OF A MULTIPLE BLOCK MEMORY ARCHITECTURE	7/31/1995	08/509,351	3/4/1997	5,608,678	US
NMXS-0297.00/US	Utility - CIP	Issued	STRUCTURE FOR DESELECTING BROKEN SELECT LINES IN MEMORY ARRAYS	7/31/1995	08/509,211	7/15/1997	5,648,933	US
NMXS-0298.00/US	Utility - ORG	Issued	WRITE CONTROLLED ADDRESS BUFFER	8/31/1995	08/521,800	5/13/1997	5,629,896	US
NMXS-0299.00/US	Utility - ORG	Issued	REDUNDANCY ARCHITECTURE	12/29/1995	08/582,424	3/18/1997	5,612,918	US
NMXS-0300.00/US	Utility - ORG	Issued	TEST MODE ACTIVATION AND DATA OVERRIDE	1/19/1996	08/587,709	11/7/2000	6,144,594	US
NMXS-0300.02/US	Utility - CON	Issued	TEST MODE ACTIVATION AND DATA OVERRIDE	12/9/1999	09/457,558	10/2/2001	6,297,996	US
NMXS-0301.00/US	Utility - ORG	Issued	PIPELINED CHIP ENABLE CONTROL CIRCUITRY AND METHODOLOGY	1/19/1996	08/588,730	12/23/1997	5,701,275	US
NMXS-0301.01/US	Utility - DIV	Issued	PIPELINED CHIP ENABLE CONTROL CIRCUITRY AND METHODOLOGY	6/19/1997	08/878,612	8/25/1998	5,798,980	US
NMXS-0302.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUITRY HAVING A SINGLE SLEW RATE RESISTOR	1/19/1996	08/588,988	9/1/1998	5,801,563	US
NMXS-0303.00/US	Utility - ORG	Issued	REDUNDANCY CONTROL	12/29/1995	08/580,827	8/4/1998	5,790,462	US
NMXS-0304.00/US	Utility - ORG	Issued	WRITE PASS THROUGH CIRCUIT	1/19/1996	08/588,662	8/12/1997	5,657,292	US
NMXS-0305.00/US	Utility - ORG	Issued	DATA-INPUT DEVICE FOR GENERATING TEST SIGNALS ON BIT AND BIT-COMPLEMENT LINES	1/19/1996	08/588,762	12/1/1998	5,845,059	US
NMXS-0306.00/US	Utility - ORG	Issued	WRITE DRIVER HAVING A TEST FUNCTION	1/19/1996	08/589,141	4/28/1998	5,745,432	US
NMXS-0307.01/US	Utility - CON	Issued	CIRCUIT AND METHOD FOR TRACKING THE START OF A WRITE TO A MEMORY CELL	5/19/1997	08/858,295	9/15/1998	5,808,960	US
NMXS-0308.01/US	Utility - CON	Issued	CIRCUIT AND METHOD FOR TERMINATING A WRITE TO A MEMORY CELL	5/19/1997	08/858,788	10/20/1998	5,825,691	US
NMXS-0309.00/US	Utility - ORG	Issued	CLOCKED SENSE AMPLIFIER WITH WORDLINE TRACKING	1/19/1996	08/587,728	9/1/1998	5,802,004	US
NMXS-0309.01/US	Utility - DIV	Issued	CLOCKED SENSE AMPLIFIER WITH WORDLINE TRACKING	10/6/1997	08/944,649	10/27/1998	5,828,622	US
NMXS-0310.01/US	Utility - CON	Lapsed	MEMORY-ROW SELECTOR HAVING A TEST FUNCTION	9/10/1997	08/926,833	12/8/1998	5,848,018	US
NMXS-0311.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR SETTING THE TIME DURATION OF A WRITE TO A MEMORY CELL	1/19/1996	08/587,711	1/26/1999	5,864,696	US
NMXS-0311.01/US	Utility - DIV	Issued	CIRCUIT AND METHOD FOR SETTING THE TIME DURATION OF A WRITE TO A MEMORY CELL	2/9/1998	09/020,430	12/21/1999	6,006,339	US
NMXS-0312.00/US	Utility - ORG	Issued	LOW-POWER READ CIRCUIT AND METHOD FOR CONTROLLING A SENSE AMPLIFIER	1/19/1996	08/589,024	4/8/1997	5,619,466	US
NMXS-0313.01/US	Utility - DIV	Issued	DEVICE AND METHOD FOR DRIVING A CONDUCTIVE PATH WITH A SIGNAL	9/15/1997	08/929,987	3/16/1999	5,883,838	US
NMXS-0313.02/US	Utility - CON	Issued	DEVICE AND METHOD FOR DRIVING A CONDUCTIVE PATH WITH A SIGNAL	11/19/1997	08/974,747	4/20/1999	5,896,336	US
NMXS-0314.00/US	Utility - ORG	Issued	CIRCUITRY AND METHODOLOGY TO TEST SINGLE BIT FAILURES OF INTEGRATED CIRCUIT MEMORY DEVICES	8/24/1995	08/519,075	5/27/1997	5,633,828	US
NMXS-0315.00/US	Utility - ORG	Issued	ENTIRE WAFER STRESS TEST METHOD FOR INTEGRATED MEMORY DEVICES AND CIRCUIT THEREFOR	8/21/1995	08/484,609	9/17/1996	5,557,573	US
NMXS-0316.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR THE PROTECTION OF NON-VOLATILE MEMORY ZONES	3/28/1996	08/623,044	9/22/1998	5,812,446	US
NMXS-0317.00/US	Utility - ORG	Issued	PHASE GENERATOR CIRCUIT FOR CHARGE PUMP TYPE OF NEGATIVE SUPPLY CIRCUIT	6/13/1996	08/664,083	6/2/1998	5,760,638	US
NMXS-0318.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR THE PROGRAMMING AND ERASURE OF A MEMORY	8/27/1996	08/703,811	3/16/1999	5,883,833	US
NMXS-0318.01/US	Utility - DIV	Issued	METHOD AND CIRCUIT FOR THE PROGRAMMING AND ERASURE OF A MEMORY	11/24/1998	09/198,431	3/7/2000	6,034,895	US
NMXS-0319.00/US	Utility - ORG	Issued	HIGH VOLTAGE GENERATOR	12/11/1996	08/762,677	9/1/1998	5,801,577	US
NMXS-0320.00/US	Utility - ORG	Issued	METHODS AND APPARATUS FOR PROGRAMING CONTENT-ADDRESSABLE MEMORIES USING FLOATING-GATE MEMORY CELLS	10/25/1996	08/738,231	4/7/1998	5,737,266	US
NMXS-0321.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	2/12/1997	08/797,948	9/7/1999	5,950,224	US
NMXS-0322.00/US	Utility - ORG	Issued	DEVICE TO NEUTRALIZE AN ELECTRONIC CIRCUIT WHEN IT IS BEING POWERED OR DISCONNECTED	1/24/1997	08/792,962	3/23/1999	5,886,549	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0322.01/US	Utility - CON	Issued	DEVICE TO NEUTRALIZE AN ELECTRONIC CIRCUIT WHEN IT IS BEING POWERED OR DISCONNECTED	11/17/1998	09/193,735	9/26/2000	6,125,022	US
NMXS-0323.00/US	Utility - ORG	Issued	FLASH-EPROM WITH EMBEDDED EEPROM	4/10/1997	08/833,925	12/15/1998	5,850,092	US
NMXS-0323.01/US	Utility - DIV	Issued	FLASH-EPROM WITH EMBEDDED EEPROM	10/1/1998	09/164,761	6/13/2000	6,074,916	US
NMXS-0324.00/US	Utility - ORG	Issued	DATA SENSING TIMING MODULATING CIRCUIT, PARTICULARLY FOR NON-VOLATILE MEMORIES	3/27/1997	08/825,098	3/9/1999	5,881,001	US
NMXS-0325.00/US	Utility - ORG	Issued	SYNCHRONIZATION SIGNAL GENERATION CIRCUIT AND METHOD	5/30/1997	08/865,748	9/28/1999	5,959,935	US
NMXS-0326.00/US	Utility - ORG	Issued	POWER ON RESET CIRCUIT WITH AUTO TURN OFF	4/30/1997	08/846,757	7/27/1999	5,929,674	US
NMXS-0327.00/US	Utility - ORG	Issued	UPROM CELL FOR LOW VOLTAGE SUPPLY	4/30/1997	08/846,755	10/13/1998	5,822,259	US
NMXS-0328.00/US	Utility - ORG	Issued	BIASING CIRCUIT FOR UPROM CELLS WITH LOW VOLTAGE SUPPLY	4/30/1997	08/846,753	1/12/1999	5,859,797	US
NMXS-0329.00/US	Utility - ORG	Issued	LINE DECODER FOR MEMORY DEVICES	5/23/1997	08/862,563	1/25/2000	6,018,255	US
NMXS-0329.01/US	Utility - DIV	Issued	LINE DECODER FOR MEMORY DEVICES	11/2/1999	09/432,642	7/25/2000	6,094,073	US
NMXS-0330.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR READING LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY CELLS	6/18/1997	08/879,017	10/3/2000	6,128,225	US
NMXS-0331.00/US	Utility - ORG	Issued	LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY DEVICE WITH VOLTAGE BOOSTING	6/18/1997	08/877,927	5/11/1999	5,903,498	US
NMXS-0332.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR GENERATING A READ REFERENCE SIGNAL FOR NONVOLATILE MEMORY CELLS	6/18/1997	08/877,921	9/8/1998	5,805,500	US
NMXS-0333.01/US	Utility - CON	Issued	MOS CAPACITOR WITH WIDE VOLTAGE AND FREQUENCY OPERATING RANGES	7/27/2001	09/916,954	7/8/2003	6,590,247	US
NMXS-0334.00/US	Utility - ORG	Issued	BIDIRECTIONAL CHARGE PUMP GENERATING EITHER A POSITIVE OR NEGATIVE VOLTAGE	7/28/1997	08/900,165	2/6/2001	6,184,741	US
NMXS-0335.00/US	Utility - ORG	Issued	BICMOS NEGATIVE CHARGE PUMP	11/5/1997	08/965,068	1/18/2000	6,016,073	US
NMXS-0336.00/US	Utility - ORG	Issued	POSITIVE CHARGE PUMP	10/8/1997	08/946,727	6/13/2000	6,075,402	US
NMXS-0337.00/US	Utility - ORG	Issued	OUTPUT STAGE FOR A MEMORY DEVICE AND FOR LOW VOLTAGE APPLICATIONS	7/23/1997	08/899,228	4/10/2001	6,215,329	US
NMXS-0338.00/US	Utility - ORG	Issued	LOW NOISE OUTPUT BUFFER FOR SEMICONDUCTOR ELECTRONIC CIRCUITS	7/8/1997	08/889,653	5/9/2000	6,060,753	US
NMXS-0339.00/US	Utility - ORG	Issued	HIGH VOLTAGE TOLERANCE OUTPUT STAGE	7/23/1997	08/898,811	11/21/2000	6,150,844	US
NMXS-0340.00/US	Utility - CON	Issued	SINGLE-CELL REFERENCE SIGNAL GENERATING CIRCUIT FOR READING NONVOLATILE MEMORY	6/17/1997	08/877,066	8/31/1999	5,946,238	US
NMXS-0341.00/US	Utility - ORG	Issued	INTEGRATED DEVICE WITH PADS	3/5/1997	08/811,577	7/13/1999	5,923,076	US
NMXS-0342.00/US	Utility - ORG	Issued	NMOS NEGATIVE CHARGE PUMP	1/23/1998	09/012,331	10/10/2000	6,130,572	US
NMXS-0343.00/US	Utility - ORG	Issued	ADDRESS TRANSITION DETECTION CIRCUIT	3/5/1997	08/811,869	9/29/1998	5,815,464	US
NMXS-0344.00/US	Utility - ORG	Issued	METHOD FOR RECOVERING FAILED MEMORY DEVICES	3/18/1997	08/816,766	4/25/2000	6,055,665	US
NMXS-0345.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR GENERATING A POWER-ON RESET SIGNAL	3/27/1997	08/828,791	9/28/1999	5,959,476	US
NMXS-0346.00/US	Utility - ORG	Issued	GAIN MODULATED SENSE AMPLIFIER	3/27/1997	08/828,790	5/4/1999	5,901,087	US
NMXS-0346.01/US	Utility - DIV	Issued	GAIN MODULATED SENSE AMPLIFIER, PARTICULARLY FOR MEMORY DEVICES	3/26/1998	09/048,941	6/20/2000	6,078,523	US
NMXS-0347.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD TO ADJUST MEMORY TIMING	3/28/1997	08/825,138	3/23/1999	5,886,945	US
NMXS-0348.00/US	Utility - ORG	Issued	STANDBY VOLTAGE BOOSTING STAGE AND METHOD FOR A MEMORY DEVICE	3/27/1997	08/826,008	3/30/1999	5,889,723	US
NMXS-0349.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER FOR MEMORY DEVICES	3/27/1997	08/824,958	9/8/1998	5,805,435	US
NMXS-0350.00/US	Utility - ORG	Issued	CIRCUIT FOR THE SWITCHING OF SUPPLY VOLTAGES IN ELECTRICALLY PROGRAMMABLE AND CANCELABLE NON-VOLATILE SEMICONDUCTOR MEMORY DEVICES	4/7/1997	08/835,294	5/19/1998	5,754,473	US
NMXS-0351.00/US	Utility - ORG	Issued	AUTO-SAVING CIRCUIT FOR PROGRAMMING CONFIGURATION ELEMENTS IN NON-VOLATILE MEMORY DEVICES	4/7/1997	08/835,296	1/26/1999	5,864,500	US
NMXS-0352.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR PROGRAMMING ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS IN A CELL MATRIX	4/4/1997	08/833,336	1/16/2001	6,175,521	US
NMXS-0353.00/US	Utility - ORG	Issued	CIRCUIT FOR DETECTING THE COINCIDENCE BETWEEN A BINARY INFORMATION UNIT STORED THEREIN AND AN EXTERNAL DATUM	3/17/1997	08/819,519	9/28/1999	5,959,917	US
NMXS-0354.00/US	Utility - ORG	Issued	METHOD FOR DETECTING REDUNDED DEFECTIVE ADDRESSES IN A MEMORY DEVICE WITH REDUNDANCY	4/17/1997	08/841,904	11/17/1998	5,838,623	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0354.01/US	Utility - CON	Issued	METHOD FOR DETECTING REDUNDED DEFECTIVE ADDRESSES IN A MEMORY DEVICE WITH REDUNDANCY	10/30/1998	09/183,469	8/10/1999	5,936,907	US
NMXS-0355.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH ROW REDUNDANCY	4/17/1997	08/842,835	3/30/1999	5,889,710	US
NMXS-0356.00/US	Utility - ORG	Issued	REDUNDANCY MEMORY REGISTER	4/17/1997	08/841,903	9/22/1998	5,812,467	US
NMXS-0357.00/US	Utility - ORG	Issued	CIRCUIT FOR TRANSFERRING REDUNDANCY DATA OF A REDUNDANCY CIRCUIT INSIDE A MEMORY DEVICE BY MEANS OF A TIME-SHARED APPROACH	6/5/1997	08/869,859	1/26/1999	5,864,562	US
NMXS-0358.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH ROW AND COLUMN REDUNDANCY CIRCUITS AND A TIME-SHARED REDUNDANCY CIRCUIT TEST ARCHITECTURE	6/5/1997	08/869,367	2/2/1999	5,867,504	US
NMXS-0359.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH CLOCKED COLUMN REDUNDANCY AND TIME-SHARED REDUNDANCY DATA TRANSFER APPROACH	6/3/1997	08/868,214	10/19/1999	5,968,183	US
NMXS-0360.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH CLOCKED COLUMN REDUNDANCY	6/3/1997	08/868,213	11/3/1998	5,831,915	US
NMXS-0361.00/US	Utility - ORG	Issued	PRE-CHARGE STEP DETERMINING CIRCUIT OF A GENERIC BIT LINE PARTICULARLY FOR NON-VOLATILE MEMORIES	4/7/1997	08/835,347	5/9/2000	6,061,273	US
NMXS-0362.00/US	Utility - ORG	Issued	READ CIRCUIT AND METHOD FOR NONVOLATILE MEMORY CELLS WITH AN EQUALIZING STRUCTURE	6/18/1997	08/877,922	3/23/1999	5,886,925	US
NMXS-0363.00/US	Utility - ORG	Issued	MULTILEVEL NON VOLATILE MEMORY DEVICE	8/22/1997	08/916,874	12/7/1999	5,999,445	US
NMXS-0364.00/US	Utility - ORG	Issued	FLOATING GATE MOS TRANSISTOR CHARGE INJECTION CIRCUIT AND COMPUTATION DEVICES INCORPORATING IT	9/30/1997	08/940,278	8/31/1999	5,946,235	US
NMXS-0364.01/US	Utility - CON	Issued	FLOATING GATE MOS TRANSISTOR CHARGE INJECTION CIRCUIT AND COMPUTATION DEVICES INCORPORATING IT	8/13/1999	09/373,813	5/22/2001	6,236,592	US
NMXS-0364.02/US	Utility - CON	Issued	FLOATING GATE MOS TRANSISTOR CHARGE INJECTION CIRCUIT AND COMPUTATION DEVICES INCORPORATING IT	8/9/2000	09/636,397	6/26/2001	6,252,802	US
NMXS-0365.00/US	Utility - ORG	Issued	ASYMMETRICAL PULSIVE DELAY NETWORK	7/25/1997	08/900,424	4/4/2000	6,046,619	US
NMXS-0366.00/US	Utility - ORG	Issued	CONTROL CIRCUIT OF AN OUTPUT BUFFER	9/19/1997	08/934,499	5/18/1999	5,905,678	US
NMXS-0367.00/US	Utility - ORG	Issued	PROTECTION CIRCUIT FOR REDUNDANCY REGISTER SET-UP CELLS OF ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY DEVICES	10/30/1997	08/961,368	12/29/1998	5,854,762	US
NMXS-0368.00/US	Utility - ORG	Issued	PROCESS FOR REALIZING AN INTERMEDIATE DIELECTRIC LAYER FOR ENHANCING THE PLANARITY IN SEMICONDUCTOR ELECTRONIC DEVICES	12/9/1997	08/987,454	5/29/2001	6,239,042	US
NMXS-0368.01/US	Utility - DIV	Issued	PROCESS FOR REALIZING AN INTERMEDIATE DIELECTRIC LAYER FOR ENHANCING THE PLANARITY IN SEMICONDUCTOR ELECTRONIC DEVICES	8/24/2000	09/645,711	10/29/2002	6,472,750	US
NMXS-0369.00/US	Utility - ORG	Issued	FLOATING GATE NON-VOLATILE MEMORY CELL WITH LOW ERASING VOLTAGE AND MANUFACTURING METHOD	9/29/1997	08/940,856	4/25/2000	6,054,731	US
NMXS-0369.01/US	Utility - DIV	Issued	Floating gate non-volatile memory cell with low erasing voltage and having different potential barriers	3/8/2000	09/521,867	11/14/2000	6,147,380	US
NMXS-0369.02/US	Utility - DIV	Issued	METHOD OF MAKING FLOATING GATE NON-VOLATILE MEMORY CELL WITH LOW ERASING VOLTAGE	3/8/2000	09/521,876	6/4/2002	6,399,444	US
NMXS-0369.03/US	Utility - DIV	Issued	METHOD OF MAKING FLOATING GATE NON-VOLATILE MEMORY CELL WITH LOW ERASING VOLTAGE HAVING DOUBLE LAYER GATE DIELECTRIC	5/30/2002	10/158,706	3/23/2004	6,710,394	US
NMXS-0369.04/US	Utility - DIV	Issued	METHOD OF MAKING FLOATING GATE NON-VOLATILE MEMORY CELL WITH LOW ERASING VOLTAGE HAVING DOUBLE LAYER GATE DIELECTRIC	3/3/2004	10/792,308	1/11/2005	6,841,445	US
NMXS-0370.00/US	Utility - ORG	Issued	PROCESS FOR DEPOSITING A STRATIFIED DIELECTRIC STRUCTURE FOR ENHANCING THE PLANARITY OF SEMICONDUCTOR ELECTRONIC DEVICES	12/23/1997	08/996,920	11/30/1999	5,994,231	US
NMXS-0371.00/US	Utility - ORG	Abandoned	METHOD OF PLANARIZING A SEMICONDUCTOR DEVICE BY DEPOSITING A DIELECTRIC PLY STRUCTURE	12/23/1997	08/997,403	12/5/2000	6,156,637	US
NMXS-0371.01/US	Utility - DIV	Abandoned	PLANARIZATION STRUCTURE AND METHOD FOR DIELECTRIC LAYERS	10/13/2000	09/687,217	10/7/2003	6,630,739	US
NMXS-0372.00/US	Utility - ORG	Issued	NEADING CIRCUIT FOR SEMICONDUCTOR MEMORY CELLS	9/29/1997	08/940,115	3/16/1999	5,883,837	US
NMXS-0372.01/US	Utility - DIV	Issued	READING CIRCUIT FOR SEMICONDUCTOR MEMORY CELLS	12/1/1998	09/203,798	10/26/1999	5,973,966	US
NMXS-0373.00/US	Utility - ORG	Issued	STAIRCASE ADAPTIVE VOLTAGE GENERATOR CIRCUIT	2/26/1998	09/032,282	9/7/1999	5,949,666	US
NMXS-0374.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT ARCHITECTURE FOR TESTING A NON-VOLATILE MEMORY DEVICE	5/30/1997	08/865,642	6/27/2000	6,081,911	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0375.00/US	Utility - ORG	Issued	SELF-REGULATED EQUALIZER, PARTICULARLY FOR SENSE AMPLIFIERS	10/30/1997	08/960,926	11/16/1999	5,986,954	US
NMXS-0376.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT FOR GENERATING INITIALIZATION SIGNALS FOR MEMORY CELL SENSING CIRCUITS	5/30/1997	08/866,283	6/22/1999	5,914,901	US
NMXS-0377.00/US	Utility - ORG	Issued	CIRCUIT FOR IMMUNIZING AN INTEGRATED CIRCUIT FROM NOISE AFFECTING ENABLE SIGNALS OF THE INTEGRATED CIRCUIT	3/4/1997	08/811,548	5/11/1999	5,903,166	US
NMXS-0378.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH A MEMORY CELL ARRAY IN TRIPLE WELL, AND RELATED MANUFACTURING PROCESS	2/20/1998	09/027,343	11/23/1999	5,990,526	US
NMXS-0378.01/US	Utility - DIV	Issued	MEMORY DEVICE WITH A MEMORY CELL ARRAY IN TRIPLE WELL, AND RELATED MANUFACTURING PROCESS	9/3/1999	09/389,955	6/6/2000	6,071,778	US
NMXS-0379.00/US	Utility - ORG	Issued	HIGH VOLTAGE CAPACITOR	7/20/1998	09/119,115	2/13/2001	6,188,121	US
NMXS-0380.00/US	Utility - ORG	Issued	CIRCUIT FOR THE GENERATION AND RESET OF TIMING SIGNAL USED FOR READING A MEMORY DEVICE	3/4/1997	08/811,386	8/11/1998	5,793,699	US
NMXS-0381.00/US	Utility - ORG	Issued	CIRCUIT FOR SELECTIVELY ENABLING ONE AMONG A PLURALITY OF CIRCUIT ALTERNATIVES OF AN INTEGRATED CIRCUIT	10/24/1997	08/957,685	8/8/2000	6,100,740	US
NMXS-0381.01/US	Utility - CON	Issued	CIRCUIT FOR SELECTIVELY ENABLING ONE AMONG A PLURALITY OF CIRCUIT ALTERNATIVES OF AN INTEGRATED CIRCUIT	4/25/2000	09/558,956	2/25/2003	6,525,591	US
NMXS-0381.02/US	Utility - DIV	Issued	METHOD FOR SELECTIVELY ENABLING ALTERNATIVE CIRCUITS	7/6/2000	09/611,258	11/20/2001	6,320,440	US
NMXS-0382.00/US	Utility - ORG	Issued	METHOD FOR VERIFYING ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY DEVICE AFTER PROGRAMMING	5/30/1997	08/866,531	1/26/1999	5,864,503	US
NMXS-0383.00/US	Utility - ORG	Issued	AUTOALIGNED ETCHING PROCESS FOR REALIZING WORD LINES IN MEMORY DEVICES INTEGRATED SEMICONDUCTOR SUBSTRATES	12/23/1997	08/997,499	10/10/2000	6,130,165	US
NMXS-0383.01/US	Utility - DIV	Issued	AUTOALIGNED ETCHING PROCESS FOR REALIZING WORD LINES IN MEMORY DEVICES INTEGRATED SEMICONDUCTOR SUBSTRATES	3/17/2000	09/528,406	4/30/2002	6,380,582	US
NMXS-0384.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY DEVICE HAVING BIT LINES WITH BLOCK SELECTOR SWITCHES	12/29/1997	08/998,854	10/19/1999	5,969,977	US
NMXS-0384.01/US	Utility - CON	Issued	MEMORY BLOCK FOR REALIZING SEMICONDUCTOR MEMORY DEVICES AND CORRESPONDING MANUFACTURING PROCESS	6/7/1999	09/327,111	8/8/2000	6,101,124	US
NMXS-0384.02/US	Utility - DIV	Issued	ELECTRONIC MEMORY DEVICE HAVING BIT LINES WITH BLOCK SELECTOR SWITCHES	8/13/1999	09/387,103	9/4/2001	6,284,585	US
NMXS-0385.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING AN INTEGRATED CIRCUIT COMPRISING AN ARRAY OF MEMORY CELLS	7/21/1997	08/897,799	11/2/1999	5,976,933	US
NMXS-0385.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING AN INTEGRATED CIRCUIT COMPRISING AN ARRAY OF MEMORY CELLS	7/16/1999	09/356,080	3/5/2002	6,353,243	US
NMXS-0386.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR INCREASING THE INTERNAL ADDRESS OF A MEMORY DEVICE USING MULTIFUNCTIONAL TERMINALS	3/27/1998	09/049,858	9/5/2000	6,115,801	US
NMXS-0387.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR CHECKING MULTILEVEL PROGRAMMING OF FLOATING-GATE NONVOLATILE MEMORY CELLS PARTICULARLY FLASH CELLS	9/30/1997	08/941,882	3/9/1999	5,880,993	US
NMXS-0388.00/US	Utility - ORG	Abandoned	METHOD OF PRODUCING MOSFET TRANSISTORS BY MEANS OF TILTED IMPLANTS	4/20/1998	09/062,859	6/22/1999	5,915,185	US
NMXS-0389.00/US	Utility - ORG	Issued	METHOD FOR SAVING DATA IN THE EVENT OF UNWANTED INTERRUPTIONS IN THE PROGRAMMING CYCLE OF A NONVOLATILE MEMORY, AND A NONVOLATILE MEMORY	4/29/1999	09/302,231	9/5/2000	6,115,313	US
NMXS-0390.00/US	Utility - ORG	Issued	PROCESS FOR THE REPAIR OF FLOATING-GATE NON-VOLATILE MEMORIES DAMAGED BY PLASMA TREATMENT	12/15/1997	08/990,328	3/30/1999	5,888,836	US
NMXS-0391.00/US	Utility - ORG	Issued	VOLTAGE LEVEL SHIFTER DEVICE, PARTICULARY FOR A NONVOLATILE MEMORY	2/25/1998	09/030,604	9/28/1999	5,959,902	US
NMXS-0392.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING A NATIVE MOS P-CHANNEL TRANSISTOR WITH A PROCESS FOR MANUFACTURING NON-VOLATILE MEMORIES	8/26/1998	09/139,909	5/16/2000	6,063,663	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0394.00/US	Utility - ORG	Issued	METHOD OF MANUFACTURING AN EPROM MEMORY DEVICE HAVING MEMORY CELLS ORGANIZED IN A TABLECLOTH MATRIX	8/27/1998	09/141,849	12/4/2001	6,326,266	US
NMXS-0395.00/US	Utility - ORG	Issued	SWITCHING MASTER SLAVE CIRCUIT	1/19/1996	08/588,648	7/21/1998	5,783,958	US
NMXS-0396.00/US	Utility - ORG	Issued	MULTIPLE ACCESS MEMORY DEVICE	12/31/1996	08/775,664	7/21/1998	5,784,331	US
NMXS-0397.00/US	Utility - CIP	Issued	LOW CURRENT CRYSTAL OSCILLATOR WITH FAST START-UP TIME	5/3/1996	08/642,271	9/8/1998	5,805,027	US
NMXS-0398.00/US	Utility - ORG	Issued	EXTERNAL WRITE PULSE CONTROL METHOD AND STRUCTURE	12/21/1996	08/771,642	6/9/1998	5,764,592	US
NMXS-0399.00/US	Utility - ORG	Issued	BURN-IN STRESS TEST MODE	12/21/1996	08/771,645	3/14/2000	6,037,792	US
NMXS-0399.01/US	Utility - DIV	Issued	INTEGRATED CIRCUIT DEVICE HAVING A BURN-IN MODE FOR WHICH ENTRY INTO AND EXIT FROM CAN BE CONTROLLED	1/27/2000	09/491,664	10/30/2001	6,310,485	US
NMXS-0399.02/US	Utility - DIV	Issued	INTEGRATED CIRCUIT DEVICE HAVING A BURN-IN MODE FOR WHICH ENTRY INTO AND EXIT FROM CAN BE CONTROLLED	8/17/2001	09/931,820	2/11/2003	6,518,746	US
NMXS-0400.00/US	Utility - ORG	Issued	INPUT DELAY CONTROL	12/23/1996	08/780,053	2/17/1998	5,719,445	US
NMXS-0401.00/US	Utility - ORG	Issued	SENSE AMPLIFIER CONTROL OF A MEMORY DEVICE	4/25/1997	08/844,696	9/28/1999	5,959,910	US
NMXS-0402.00/US	Utility - CIP	Issued	CIRCUIT AND METHOD FOR SELECTING A SIGNAL	11/27/1996	08/758,587	3/14/2000	6,037,799	US
NMXS-0403.00/RE	Utility - REIS	Issued	EDGE TRANSITION DETECTION CIRCUITRY FOR USE WITH TEST MODE OPERATION OF AN INTEGRATED CIRCUIT MEMORY DEVICE	5/9/2002	10/145,606	5/9/2000	RE40,282	US
NMXS-0403.00/US	Utility - ORG	Issued	EDGE TRANSITION DETECTION CIRCUITRY FOR USE WITH TEST MODE OPERATION OF AN INTEGRATED CIRCUIT MEMORY DEVICE	12/21/1996	08/771,643	5/9/2000	6,059,450	US
NMXS-0404.00/US	Utility - CIP	Issued	INTEGRATED CIRCUIT THAT SUPPORTS AND METHOD FOR WAFER-LEVEL TESTING	9/17/1996	08/710,357	9/15/1998	5,808,947	US
NMXS-0405.00/US	Utility - ORG	Issued	DEVICE FOR READING CELLS OF A MEMORY	6/12/1997	08/873,502	7/13/1999	5,923,590	US
NMXS-0406.00/US	Utility - ORG	Issued	PARALLEL-ACCESS MEMORY AND METHOD	5/9/1997	08/853,526	7/4/2000	6,085,280	US
NMXS-0407.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE MULTILEVEL NON-VOLATILE MEMORY COMPRISING INTERNAL REFRESH MEANS	10/24/1997	08/957,715	4/11/2000	6,049,497	US
NMXS-0408.00/US	Utility - ORG	Issued	READ CIRCUIT FOR NON-VOLATILE MEMORY WORKING WITH A LOW SUPPLY VOLTAGE	9/22/1997	08/934,696	1/12/1999	5,859,798	US
NMXS-0409.00/US	Utility - ORG	Issued	DEVICE FOR THE PROTECTION OF STORED DATA	8/21/1997	08/916,054	7/28/1998	5,787,040	US
NMXS-0410.00/US	Utility - ORG	Issued	CURRENT REFERENCE DEVICE IN INTEGRATED CIRCUIT FORM	1/30/1997	08/791,383	5/11/1999	5,903,141	US
NMXS-0411.00/US	Utility - ORG	Issued	DEVICE FOR THE PROTECTION OF STORED DATA USING A TIME DELAY CIRCUIT	8/21/1997	08/915,855	9/12/2000	6,119,210	US
NMXS-0412.00/US	Utility - ORG	Issued	MEMORY WITH IMPROVED READING TIME	10/24/1997	08/957,666	2/9/1999	5,870,336	US
NMXS-0413.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR POWER-ON/POWER-OFF CHECKING OF AN INTEGRATED CIRCUIT	12/18/1997	08/993,071	8/28/2001	6,281,723	US
NMXS-0414.00/US	Utility - ORG	Issued	OSCILLATOR AND SWITCH-OVER CONTROL CIRCUIT FOR A HIGH-VOLTAGE GENERATOR	12/22/1997	08/995,667	11/14/2000	6,147,566	US
NMXS-0414.01/US	Utility - DIV	Issued	OSCILLATOR AND SWITCH-OVER CONTROL CIRCUIT FOR A HIGH-VOLTAGE GENERATOR.	5/25/2000	09/578,778	4/16/2002	6,373,311	US
NMXS-0415.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE FLOATING-GATE MEMORY ORGANIZED IN WORDS	3/24/1999	09/275,327	8/1/2000	6,097,631	US
NMXS-0416.00/US	Utility - ORG	Abandoned	METHOD AND DEVICE FOR THE PREDICTIVE READING OF A MEMORY	12/16/1997	08/991,693	10/24/2000	6,138,220	US
NMXS-0417.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR THE INCREMENTAL READING OF A MEMORY	12/16/1997	08/991,428	1/23/2001	6,178,490	US
NMXS-0418.00/US	Utility - ORG	Issued	MEMORY WITH READ PROTECTED ZONES	12/19/1997	08/994,889	12/14/1999	6,002,619	US
NMXS-0419.00/US	Utility - ORG	Issued	A READ CIRCUIT FOR MEMORY ADAPTED TO THE MEASUREMENT OF LEAKAGE CURRENTS	2/27/1998	09/031,748	3/30/1999	5,889,702	US
NMXS-0420.00/US	Utility - ORG	Issued	LOAD PUMP TYPE OF VOLTAGE GENERATOR CIRCUIT	4/13/1998	09/059,088	10/19/1999	5,969,961	US
NMXS-0421.00/US	Utility - ORG	Issued	DIFFERENTIAL AMPLIFIER IN A MOS TRANSISTORS STRUCTURE	11/6/1998	09/188,071	10/31/2000	6,140,876	US
NMXS-0422.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR THE FILTERING OF A PULSE SIGNAL	5/29/1998	09/087,478	10/12/1999	5,966,034	US
NMXS-0423.00/US	Utility - ORG	Issued	RING OSCILLATOR USING CMOS TECHNOLOGY	1/12/1998	09/005,722	10/3/2000	6,127,898	US
NMXS-0424.00/US	Utility - ORG	Issued	PAGE-WRITE INDICATOR FOR NON-VOLATILE MEMORY	1/20/1998	09/009,088	9/28/1999	5,959,886	US
NMXS-0425.00/US	Utility - ORG	Issued	WORD ADDRESSABLE FLOATING-GATE MEMORY COMPRISING A REFERENCE VOLTAGE GENERATOR CIRCUIT FOR THE VERIFICATION OF THE CONTENTS OF A WORD	3/6/1998	09/035,654	9/14/1999	5,953,253	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0426.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR THE ERASURE OF A NON-VOLATILE AND ELECTRICALLY ERASABLE MEMORY	11/26/1997	08/980,195	1/4/2000	6,011,724	US
NMXS-0427.00/US	Utility - ORG	Issued	DEVICE FOR PROTECTION AFTER A PAGE-WRITE OPERATION IN AN ELECTRICALLY PROGRAMMABLE MEMORY	11/14/1997	08/970,445	1/19/1999	5,862,075	US
NMXS-0428.00/US	Utility - ORG	Issued	CIRCUIT FOR THE DETECTION OF CHANGES OF ADDRESS	1/27/1998	09/013,966	8/15/2000	6,104,644	US
NMXS-0429.00/US	Utility - ORG	Abandoned	SET OF TWO MEMORIES ON THE SAME MONOLITHIC INTEGRATED CIRCUIT	4/8/1998	09/056,921	3/20/2001	6,205,512	US
NMXS-0429.01/US	Utility - DIV	Abandoned	SET OF TWO MEMORIES ON THE SAME MONOLITHIC INTEGRATED CIRCUIT	12/21/2000	09/746,473	8/13/2002	6,434,056	US
NMXS-0429.02/US	Utility - DIV	Abandoned	SET OF TWO MEMORIES ON THE SAME MONOLITHIC INTEGRATED CIRCUIT	12/21/2000	09/746,472	8/21/2001	6,279,068	US
NMXS-0430.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR THE PROGRAMMING OF A MEMORY	1/22/1998	09/012,938	11/23/1999	5,991,199	US
NMXS-0430.02/US	Utility - NSPCT	Issued	METHOD FOR PROGRAMMING AN EPROM-FLASH TYPE MEMORY	1/22/1998	09/355,064	10/31/2000	6,141,254	US
NMXS-0431.00/US	Utility - ORG	Issued	VARIABLE FREQUENCY CHARGE PUMP	7/17/1998	09/118,953	5/9/2000	6,060,932	US
NMXS-0432.02/US	Utility - CON	Issued	PROCESS OF FINAL PASSIVATION OF AN INTEGRATED CIRCUIT DEVICE	12/18/2002	10/323,961	5/3/2005	6,888,225	US
NMXS-0433.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH REDUCED POWER DISSIPATION	4/1/1998	09/053,720	5/9/2000	6,061,286	US
NMXS-0434.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR READING A NON-ERASABLE MEMORY CELL	3/16/1998	09/039,588	6/13/2000	6,075,718	US
NMXS-0435.00/US	Utility - ORG	Issued	SEMICONDUCTOR INTEGRATED CIRCUIT	10/6/1999	09/093,382	4/3/2001	6,212,128	US
NMXS-0436.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR GENERATING A GATE VOLTAGE IN NON-VOLATILE MEMORY DEVICES	8/27/1998	09/141,250	1/18/2000	6,016,271	US
NMXS-0437.00/US	Utility - ORG	Issued	PROCESS FOR THE MANUFACTURING OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY DEVICE	8/6/1998	09/130,720	2/27/2001	6,194,270	US
NMXS-0437.01/US	Utility - DIV	Issued	PROCESS FOR THE MANUFACTURING OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY DEVICE	2/1/2001	09/776,105	8/20/2002	6,437,395	US
NMXS-0438.00/US	Utility - ORG	Issued	METHOD FOR FINAL PASSIVATION OF INTEGRATED CIRCUIT	4/14/1998	09/059,740	2/13/2001	6,187,683	US
NMXS-0439.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR ANALOG PROGRAMMING OF NON-VOLATILE MEMORY CELLS	5/11/1998	09/076,013	2/27/2001	6,195,283	US
NMXS-0440.00/US	Utility - ORG	Issued	MEMORY DEVICE GENERATOR FOR GENERATING MEMORY DEVICES WITH REDUNDANCY	10/19/1998	09/175,220	7/22/2003	6,598,190	US
NMXS-0441.00/US	Utility - ORG	Issued	METHOD FOR FORMING NON VOLATILE MEMORY STRUCTURES ON A SEMICONDUCTOR SUBSTRATE	12/10/1999	09/459,754	4/23/2002	6,376,306	US
NMXS-0442.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR READING A NON-VOLATILE MEMORY	7/14/1999	09/353,756	2/27/2001	6,195,286	US
NMXS-0443.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR ANALOG PROGRAMMING OF FLASH EEPROM MEMORY CELLS WITH AUTOVERIFY	9/28/1998	09/162,639	6/27/2000	6,081,448	US
NMXS-0444.00/US	Utility - ORG	Abandoned	METHOD FOR LOCALIZING POINT DEFECTS CAUSING LEAKAGE CURRENTS IN A NON-VOLATILE MEMORY DEVICE	5/13/1999	09/311,258	4/9/2002	6,369,406	US
NMXS-0445.00/US	Utility - ORG	Issued	DATA READ CIRCUIT FOR NON VOLATILE MEMORY CELLS	10/6/1999	09/413,383	12/11/2001	6,330,188	US
NMXS-0446.00/US	Utility - ORG	Issued	ROW DECODER FOR A FLASH-EEPROM MEMORY DEVICE WITH THE POSSIBILITY OF SELECTIVE ERASING OF A SUB-GROUP OF ROWS OF A SECTOR	11/25/1998	09/200,002	9/19/2000	6,122,200	US
NMXS-0447.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD OF READING CELLS OF AN ANALOG MEMORY ARRAY, IN PARTICULAR OF THE FLASH TYPE	7/22/1998	09/121,024	10/26/1999	5,973,959	US
NMXS-0448.00/US	Utility - ORG	Issued	DECODER FOR MEMORIES HAVING OPTIMIZED CONFIGURATION	11/20/2000	09/716,747	3/26/2002	6,362,658	US
NMXS-0449.00/US	Utility - ORG	Issued	METHOD FOR PARALLEL PROGRAMMING OF NONVOLATILE MEMORY DEVICES, IN PARTICULAR FLASH MEMORIES AND EEPROMS	10/27/1998	09/181,230	5/30/2000	6,069,822	US
NMXS-0450.00/US	Utility - ORG	Issued	METHOD OF MEASURING THE THICKNESS OF A LAYER OF SILICON DAMAGED BY PLASMA ETCHING	6/29/1999	09/343,207	5/15/2001	6,233,046	US
NMXS-0451.00/US	Utility - ORG	Issued	SECTORED SEMICONDUCTOR MEMORY DEVICE WITH CONFIGURABLE MEMORY SECTOR ADDRESSES	9/23/1998	09/159,322	6/4/2002	6,401,164	US
NMXS-0452.00/US	Utility - ORG	Issued	STACKED CHARGE PUMP CIRCUIT	8/27/1997	08/927,391	7/20/1999	5,926,059	US
NMXS-0453.00/US	Utility - ORG	Issued	CMOS TWIN-TUB NEGATIVE VOLTAGE SWITCHING ARCHITECTURE	8/27/1997	08/921,930	11/30/1999	5,994,948	US
NMXS-0454.00/US	Utility - ORG	Abandoned	DETERMINATION OF THE THICKNESS OF A DENUDED ZONE IN A SILICON WAFER	7/1/1998	09/108,439	3/6/2001	6,197,606	US

Exhibit A - US Issued Patents

MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0455.00/US	Utility - ORG	Issued	HIGH VOLTAGE DRIVER CIRCUIT FOR A DECODING CIRCUIT IN MULTILEVEL NON-VOLATILE MEMORY DEVICES	12/31/1998	09/224,330	2/22/2000	6,028,793	US
NMXS-0456.00/US	Utility - ORG	Issued	MONOLITHICALLY INTEGRATED SELECTOR FOR ELECTRICALLY PROGRAMMABLE MEMORY CELL DEVICES	5/28/1999	09/321,961	6/5/2001	6,242,971	US
NMXS-0457.00/US	Utility - ORG	Issued	CONTROLLED HOT-ELECTRON WRITING METHOD FOR NON-VOLATILE MEMORY CELLS	10/8/1998	09/169,239	1/9/2001	6,172,908	US
NMXS-0458.00/US	Utility - ORG	Issued	SELF-TEST AND CORRECTION OF LOSS OF CHARGE ERRORS IN A FLASH MEMORY, ERASABLE AND PROGRAMMABLE BY SECTORS THEREOF	12/16/1998	09/212,896	8/14/2001	6,275,960	US
NMXS-0459.00/US	Utility - ORG	Issued	COMPENSATED VOLTAGE REGULATOR	9/30/1998	09/163,755	11/9/1999	5,982,677	US
NMXS-0460.00/US	Utility - ORG	Issued	METHOD FOR MULTILEVEL PROGRAMMING OF A NONVOLATILE MEMORY, AND A MULTILEVEL NONVOLATILE MEMORY	11/3/1998	09/185,906	1/4/2000	6,011,715	US
NMXS-0461.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR GENERATING AN ATD SIGNAL TO REGULATE THE ACCESS TO A NON-VOLATILE MEMORY	11/4/1998	09/186,497	6/13/2000	6,075,750	US
NMXS-0462.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR REGULATING THE LENGTH OF AN ATD PULSE SIGNAL	11/4/1998	09/186,496	1/2/2001	6,169,423	US
NMXS-0463.00/US	Utility - ORG	Issued	BOOSTING CIRCUIT PARTICULARY FOR A MEMORY DEVICE	11/5/1998	09/186,758	11/7/2000	6,144,589	US
NMXS-0464.00/US	Utility - ORG	Issued	IMPROVED OUTPUT CIRCUIT FOR INTEGRATED CIRCUITS	10/13/1998	09/170,788	11/28/2000	6,153,914	US
NMXS-0465.00/US	Utility - ORG	Issued	MEMORY CELL VOLTAGE REGULATOR WITH TEMPERATURE CORRELATED VOLTAGE GENERATOR CIRCUIT	11/4/1998	09/186,498	2/6/2001	6,184,670	US
NMXS-0467.00/US	Utility - ORG	Issued	HIGH-VOLTAGE PUMP ARCHITECTURE FOR INTEGRATED ELECTRONIC DEVICES	1/29/1999	09/239,908	8/14/2001	6,275,099	US
NMXS-0468.00/US	Utility - ORG	Issued	CIRCUIT FOR DISCHARGING A NEGATIVE POTENTIAL NODE TO GROUND, INCLUDING CONTROL OF THE DISCHARGE CURRENT	3/24/1999	09/275,200	6/19/2001	6,249,172	US
NMXS-0469.00/US	Utility - ORG	Issued	SWITCHING CIRCUIT	3/24/1999	09/275,694	5/16/2000	6,064,598	US
NMXS-0470.00/US	Utility - ORG	Issued	DATA PROTECTION METHOD FOR A SEMICONDUCTOR MEMORY AND CORRESPONDING PROTECTED MEMORY DEVICE	12/23/1998	09/220,127	9/4/2001	6,286,086	US
NMXS-0471.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR SINGLE FEED VOLTAGE MEMORY CIRCUITS, AND FLASH TYPE MEMORY IN PARTICULAR	11/20/1998	09/196,204	8/8/2000	6,101,118	US
NMXS-0471.01/US	Utility - DIV	Issued	VOLTAGE REGULATOR FOR SINGLE FEED VOLTAGE MEMORY CIRCUITS, AND FLASH TYPE MEMORIES IN PARTICULAR	6/26/2000	09/602,669	9/4/2001	6,285,614	US
NMXS-0472.00/US	Utility - ORG	Issued	ELECTRONIC CIRCUIT FOR GENERATING A STABLE VOLTAGE SIGNAL FOR POLARIZING DURING A READING STEP UPROM MEMORY CELLS OPERATING AT LOW FEED VOLTAGE	12/21/1998	09/218,796	3/20/2001	6,204,722	US
NMXS-0473.00/US	Utility - ORG	Issued	ROW DECODER CIRCUIT FOR AN ELECTRONIC MEMORY DEVICE, PARTICULARLY FOR LOW VOLTAGE APPLICATIONS	12/29/1998	09/222,022	5/30/2000	6,069,837	US
NMXS-0474.00/US	Utility - ORG	Issued	METHOD AND A RELATED CIRCUIT FOR ADJUSTING THE DURATION OF A SYNCHRONIZATION SIGNAL ATD FOR TIMING THE ACCESS TO A NON-VOLATILE MEMORY	12/29/1998	09/222,070	5/22/2001	6,237,104	US
NMXS-0475.00/US	Utility - ORG	Issued	DRIVING CIRCUIT WITH THREE OUTPUT LEVELS, ONE OUTPUT LEVEL BEING A BOOSTED LEVEL	1/19/1999	09/234,016	12/5/2000	6,157,225	US
NMXS-0476.00/US	Utility - ORG	Abandoned	MEMORY CELL INTEGRATED STRUCTURE AND CORRESPONDING BIASING DEVICE	4/21/1999	09/295,667	11/21/2000	6,151,251	US
NMXS-0476.01/US	Utility - DIV	Abandoned	MEMORY CELL INTEGRATED STRUCTURE WITH CORRESPONDING BIASING DEVICE	9/29/2000	09/675,985	10/16/2001	6,304,490	US
NMXS-0477.00/US	Utility - ORG	Issued	SWITCHING CIRCUIT HAVING AN OUTPUT VOLTAGE VARYING BETWEEN A REFERENCE VOLTAGE AND A NEGATIVE VOLTAGE	3/24/1999	09/275,255	2/29/2000	6,031,761	US
NMXS-0478.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TRIMMING THE INTERNAL TIMING CONDITIONS OF A SEMICONDUCTOR MEMORY DEVICE	2/26/1998	09/032,272	12/28/1999	6,009,041	US
NMXS-0479.00/US	Utility - ORG	Issued	STRUCTURE AND METHOD FOR EVALUATING AN INTEGRATED ELECTRONIC DEVICE	12/9/1998	09/209,049	11/6/2001	6,313,480	US
NMXS-0480.00/US	Utility - ORG	Issued	LOW CONSUMPTION TTL-CMOS INPUT BUFFER STAGE	12/30/1998	09/231,130	10/23/2001	6,307,396	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0481.00/US	Utility - NSPCT	Issued	MONOLITHICALLY INTEGRATED SELECTOR FOR ELECTRICALLY PROGRAMMABLE MEMORY CELLS DEVICES	6/30/1999	09/674,395	9/11/2001	6,288,594	US
NMXS-0482.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY CAPABLE OF AUTONOMOUSLY EXECUTING A PROGRAM	7/8/1999	09/349,702	7/1/2003	6,587,914	US
NMXS-0483.00/US	Utility - ORG	Issued	OPERATING VOLTAGE SELECTION CIRCUIT FOR NON-VOLATILE SEMICONDUCTOR MEMORIES	1/13/1999	09/229,474	8/8/2000	6,101,127	US
NMXS-0484.00/US	Utility - ORG	Issued	METHOD FOR CONTROLLED ERASING MEMORY DEVICES, IN PARTICULAR ANALOG AND MULTI-LEVEL FLASH-EEPROM DEVICES	1/21/1999	09/234,942	7/18/2000	6,091,642	US
NMXS-0485.00/US	Utility - ORG	Issued	ESD PROTECTION NETWORK FOR CIRCUIT STRUCTURES FORMED IN A SEMICONDUCTOR	12/30/1998	09/223,621	7/24/2001	6,266,222	US
NMXS-0486.00/US	Utility - ORG	Issued	METHOD AND A CIRCUIT FOR IMPROVING THE EFFECTIVENESS OF ESD PROTECTION IN CIRCUIT STRUCTURES FORMED IN A SEMICONDUCTOR	12/30/1998	09/231,129	6/5/2001	6,242,793	US
NMXS-0486.01/US	Utility - DIV	Issued	METHOD FOR IMPROVING THE EFFECTIVENESS OF ESD PROTECTION IN CIRCUIT STRUCTURES FORMED IN A SEMICONDUCTOR SUBSTRATE	4/17/2001	09/837,137	4/16/2002	6,372,597	US
NMXS-0487.00/US	Utility - ORG	Issued	HIGH STORAGE CAPACITY NON-VOLATILE MEMORY	5/26/1999	09/320,315	11/14/2000	6,147,902	US
NMXS-0488.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING A SEMICONDUCTOR NON-VOLATILE MEMORY DEVICE WITH SHALLOW TRENCH ISOLATION (STI)	6/18/1999	09/336,089	4/17/2001	6,218,265	US
NMXS-0489.00/US	Utility - ORG	Issued	CIRCUIT DEVICE AND CORRESPONDING METHOD FOR PROGRAMMING A NONVOLATILE MEMORY CELL HAVING A SINGLE VOLTAGE SUPPLY	5/28/1999	09/322,644	8/29/2000	6,111,791	US
NMXS-0490.00/US	Utility - ORG	Issued	ELECTRONIC COUNTER FOR A NON-VOLATILE MEMORY DEVICE INTEGRATED ON A SEMICONDUCTOR	3/4/1999	09/262,500	3/27/2001	6,208,705	US
NMXS-0491.00/US	Utility - ORG	Issued	EDGE TRANSITION DETECTION CONTROL OF A MEMORY DEVICE	12/30/1997	08/000,547	11/30/1999	5,995,444	US
NMXS-0492.01/US	Utility - CON	Abandoned	VSS SWITCHING SCHEME FOR BATTERY BACKED-UP SEMICONDUCTOR DEVICES	10/29/1999	09/429,964	8/22/2000	6,107,865	US
NMXS-0493.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR READING A MEMORY CELL	5/29/1998	09/087,399	11/30/1999	5,995,421	US
NMXS-0494.00/US	Utility - ORG	Issued	READ CIRCUIT FOR NON VOLATILE MEMORIES	10/29/1998	09/182,843	8/1/2000	6,097,633	US
NMXS-0495.00/US	Utility - ORG	Issued	IN-SITU DEPOSITION AND DOPING PROCESS FOR POLYCRYSTALLINE SILICON LAYERS AND THE RESULTING DEVICE	11/13/1998	09/191,743	3/15/2005	6,867,113	US
NMXS-0496.00/US	Utility - ORG	Issued	TEMPERATURE-STABLE CURRENT GENERATION	2/5/1999	09/246,029	10/17/2000	6,133,718	US
NMXS-0497.00/US	Utility - ORG	Issued	BITLINE BIAS CIRCUIT FOR NON-VOLATILE MEMORY DEVICES	2/23/1999	09/256,648	4/11/2000	6,049,491	US
NMXS-0498.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR NON-VOLATILE MEMORY DEVICES	2/23/1999	09/256,603	7/25/2000	6,094,394	US
NMXS-0499.00/US	Utility - ORG	Issued	UPROM MEMORY CELLS FOR NON-VOLATILE MEMORY DEVICES INTEGRATED ON SEMICONDUCTORS	3/25/1999	09/276,263	8/7/2001	6,271,571	US
NMXS-0500.00/US	Utility - ORG	Issued	SENSE AMPLIFER FOR LOW VOLTAGE MEMORIES	2/12/1999	09/249,834	10/15/2002	6,466,059	US
NMXS-0501.00/US	Utility - ORG	Issued	LOW CONSUMPTION BOOSTED VOLTAGE DRIVING CIRCUIT	2/26/1999	09/257,682	10/10/2000	6,130,844	US
NMXS-0502.00/US	Utility - ORG	Issued	VOLTAGE BOOSTING CIRCUIT FOR GENERATING BOOSTED VOLTAGE PHASES	2/26/1999	09/258,224	5/16/2000	6,064,594	US
NMXS-0503.00/US	Utility - ORG	Issued	VOLTAGE PHASE GENERATOR WITH INCREASED DRIVING CAPACITY	2/26/1999	09/257,684	3/6/2001	6,198,672	US
NMXS-0504.00/US	Utility - ORG	Issued	PIPELINED NON-BLOCKING LEVEL TWO CACHE SYSTEM WITH INHERENT TRANSACTION COLLISION-AVOIDANCE	12/4/1998	09/205,598	2/11/2003	6,519,682	US
NMXS-0505.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY HAVING A PROTECTABLE ZONE AND AN ELECTRONIC SYSTEM INCLUDING THE MEMORY	10/23/1998	09/177,899	3/7/2000	6,034,889	US
NMXS-0506.00/US	Utility - ORG	Issued	METHOD FOR WRITING IN AN ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY	10/27/1998	09/179,621	3/21/2000	6,040,994	US
NMXS-0507.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR THE GENERATION OF PROGRAMMING AND ERASURE VOLTAGE IN A NON-VOLATILE MEMORY	9/18/1998	09/156,945	11/30/1999	5,995,416	US
NMXS-0508.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORIES PROGRAMMABLE BY ""HOT CARRIER"" TUNNEL EFFECT AND ERASABLE BY TUNNEL EFFECT	10/15/1998	09/173,136	10/3/2000	6,128,222	US
NMXS-0509.00/US	Utility - ORG	Issued	NON-VIOLATILE ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY	11/25/1998	09/199,671	12/7/1999	5,999,447	US
NMXS-0510.00/US	Utility - ORG	Issued	DETECTOR OF RADIO FREQUENCY SIGNALS FOR CONTACTLESS CHIP CARDS	9/18/1998	09/157,060	11/28/2000	6,152,373	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0511.00/US	Utility - ORG	Issued	CIRCUIT FOR THE GENERATION OF VOLTAGE FOR THE PROGRAMMING OR ERASURE OF A MEMORY THAT USES FLOATING-GATE TRANSISTORS	10/27/1998	09/179,635	11/2/1999	5,978,268	US
NMXS-0512.00/US	Utility - ORG	Issued	DEVICE FOR DEMODULATING A BINARY PHASE-SHIFT KEYED SIGNAL	1/20/1999	09/234,194	10/31/2000	6,140,869	US
NMXS-0513.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE AND METHOD FOR THE PROGRAMMING OF THE SAME	11/18/1998	09/195,276	7/18/2000	6,091,641	US
NMXS-0514.00/US	Utility - ORG	Abandoned	DEVICE AND METHOD FOR THE READING OF EEPROM CELLS	4/27/1999	09/300,527	4/17/2001	6,219,277	US
NMXS-0515.00/US	Utility - ORG	Issued	LINE DECODER FOR A LOW SUPPLY VOLTAGE MEMORY DEVICE	6/1/1999	09/324,087	8/29/2000	6,111,809	US
NMXS-0516.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR READING NONVOLATILE MEMORY CELLS	5/28/1999	09/322,460	1/30/2001	6,181,602	US
NMXS-0517.00/US	Utility - ORG	Issued	CIRCUIT FOR PROVIDING A READING PHASE AFTER POWER-ON-RESET	4/20/1999	09/295,263	12/5/2000	6,157,579	US
NMXS-0519.00/US	Utility - ORG	Issued	SWITCHING CIRCUIT WITH AN OUTPUT VOLTAGE CHANGING AMONG FOUR POSSIBLE VALUES	3/24/1999	09/275,691	8/1/2000	6,097,213	US
NMXS-0520.00/US	Utility - ORG	Issued	PROCESS FOR THE MANUFACTURE OF INTEGRATED DEVICES WITH GATE OXIDE PROTECTION FROM MANUFACTURING PROCESS DAMAGE, AND PROTECTION STRUCTURE THEREFOR	6/25/1999	09/344,426	8/21/2001	6,278,159	US
NMXS-0520.01/US	Utility - DIV	Issued	PROCESS FOR THE MANUFACTURE OF INTEGRATED DEVICES WITH GATE OXIDE PROTECTION FROM MANUFACTURING PROCESS DAMAGE, AND PROTECTION STRUCTURE THEREFOR	6/25/2001	09/891,438	4/22/2003	6,551,892	US
NMXS-0521.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY CIRCUIT AND RELATED MANUFACTURING METHOD	7/30/1999	09/364,766	4/10/2001	6,215,688	US
NMXS-0521.01/US	Utility - DIV	Issued	ELECTRONIC MEMORY CIRCUIT AND RELATED MANUFACTURING METHOD	2/9/2001	09/779,956	2/8/2005	6,852,596	US
NMXS-0521.02/US	Utility - DIV	Issued	ELECTRONIC MEMORY CIRCUIT AND RELATED MANUFACTURING METHOD	1/12/2005	11/033,776	10/13/2009	7,601,590	US
NMXS-0522.00/US	Utility - ORG	Issued	METHOD FOR READING A MULTIPLE-LEVEL MEMORY CELL	3/25/1999	09/276,214	7/4/2000	6,084,797	US
NMXS-0523.01/US	Utility - CON	Issued	CIRCUIT STRUCTURE FOR PROVIDING A HIERARCHICAL DECODING IN SEMICONDUCTOR MEMORY DEVICES	6/27/2001	09/894,975	2/4/2003	6,515,911	US
NMXS-0524.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES HAVING HV TRANSISTORS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	7/22/1999	09/359,923	6/26/2001	6,251,728	US
NMXS-0524.01/US	Utility - DIV	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES HAVING HV TRANSISTORS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	5/18/2001	09/861,404	7/16/2002	6,420,769	US
NMXS-0525.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING AN ELECTRONIC DEVICE INCLUDING MOS TRANSISTORS WITH SALICIDED JUNCTIONS AND NON-SALICIDED RESISTORS	7/21/1999	09/358,075	10/9/2001	6,300,181	US
NMXS-0526.00/US	Utility - ORG	Issued	METHOD FOR THE INTEGRATION OF RESISTORS AND ESD SELF-PROTECTED TRANSISTORS IN AN INTEGRATED DEVICE WITH A MEMORY MATRIX MANUFACTURED BY MEANS OF A PROCESS FEATURING SELF-ALIGNED SOURCE (SAS) FORMATION AND JUNCTION SALICIDATION	12/15/1999	09/464,066	10/8/2002	6,461,922	US
NMXS-0527.01/US	Utility - DIV	Issued	METHOD FOR ENHANCING SELECTIVITY BETWEEN A FILM OF A LIGHT-SENSITIVE MATERIAL AND A LAYER TO BE ETCHED IN ELECTRONIC SEMICONDUCTOR DEVICE FABRICATION PROCESSES	4/17/2001	09/836,937	12/17/2002	6,495,455	US
NMXS-0528.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SELECTION TRANSISTORS FOR NONVOLATILE SERIAL-FLASH, EPROM, EEPROM AND FLASH-EEPROM MEMORIES IN STANDARD OR AMG CONFIGURATION	6/11/1999	09/330,540	7/3/2001	6,255,163	US
NMXS-0529.00/US	Utility - ORG	Issued	METHOD OF MAKING HIGH-VOLTAGE HV TRANSISTORS WITH DRAIN EXTENSION IN A CMOS PROCESS OF THE DUAL GATE TYPE WITH SILICIDE	10/1/1999	09/411,134	9/4/2001	6,284,607	US
NMXS-0530.00/US	Utility - ORG	Issued	METHOD FOR FAST PROGRAMMING FLOATING GATE MEMORIES BY TUNNEL EFFECT	6/25/1999	09/344,425	1/9/2001	6,172,913	US
NMXS-0531.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING OF A NON VOLATILE MEMORY WITH REDUCED RESISTANCE OF THE COMMON SOURCE LINES	6/21/1999	09/337,051	1/30/2001	6,180,460	US
NMXS-0532.00/US	Utility - ORG	Issued	SIMPLIFIED PROCESS FOR DEFINING THE TUNNEL AREA IN NON-ALIGNED, NON-VOLATILE SEMICONDUCTOR MEMORY CELLS	10/14/1999	09/419,403	9/3/2002	6,444,526	US
NMXS-0533.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH ROW REDUNDANCY	5/12/2000	09/570,332	10/9/2001	6,301,152	US
NMXS-0534.00/US	Utility - ORG	Issued	METHOD FOR FORMING CONTACTLESS MOS TRANSISTORS AND RESULTING DEVICES, ESPECIALLY FOR USE IN NON-VOLATILE MEMORY ARRAYS	12/28/1999	09/473,368	6/26/2001	6,251,736	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0535.00/US	Utility - ORG	Abandoned	PROCESS FOR THE DEFINITION OF OPENINGS IN A DIELECTRIC LAYER	9/28/1999	09/406,903	11/6/2001	6,313,040	US
NMXS-0536.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NON-VOLATILE MEMORY CELLS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	9/9/1999	09/392,937	8/28/2001	6,281,077	US
NMXS-0536.01/US	Utility - DIV	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NON-VOLATILE MEMORY CELLS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	4/16/2001	09/836,590	5/28/2002	6,396,101	US
NMXS-0537.00/US	Utility - ORG	Issued	SELECTIVE SALICIDATION PROCESS FOR ELECTRONIC DEVICES INTEGRATED IN A SEMICONDUCTOR SUBSTRATE	12/28/1999	09/473,367	8/5/2003	6,602,774	US
NMXS-0538.00/US	Utility - ORG	Issued	METHOD FOR CORRECTION OF ERRORS IN A BINARY WORD STORED IN MULTILEVEL MEMORY CELLS, NOT REQUIRING ADDITIONAL CELLS	2/24/2000	09/513,286	3/4/2003	6,530,058	US
NMXS-0539.00/US	Utility - ORG	Issued	NONVOLATILE MULTILEVEL MEMORY AND READING METHOD THEREFORE	2/9/2000	09/501,131	9/24/2002	6,456,527	US
NMXS-0540.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TESTING MEMORY CELLS IN A MULTILEVEL MEMORY DEVICE	10/7/1999	09/415,024	10/9/2001	6,301,157	US
NMXS-0541.00/US	Utility - ORG	Issued	DEVICE FOR READING NON-VOLATILE MEMORY CELLS, IN PARTICULAR ANALOG FLASH MEMORY CELLS	10/19/1999	09/421,616	11/27/2001	6,323,799	US
NMXS-0542.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR PROGRAMMING NONVOLATILE MEMORY CELLS WITH AUTOMATIC GENERATION OF PROGRAMMING VOLTAGE	11/12/1999	09/438,232	10/15/2002	6,466,481	US
NMXS-0543.00/US	Utility - ORG	Issued	CIRCUIT FOR PARALLEL PROGRAMMING NONVOLATILE MEMORY CELLS, WITH ADJUSTABLE PROGRAMMING SPEED	11/23/1999	09/447,531	12/19/2000	6,163,483	US
NMXS-0544.00/US	Utility - ORG	Issued	CIRCUIT FOR HIGH-PRECISION ANALOG READING OF NONVOLATILE MEMORY CELLS, IN PARTICULAR ANALOG OR MULTILEVEL FLASH OR EEPROM MEMORY CELLS	11/12/1999	09/438,823	10/3/2000	6,128,228	US
NMXS-0545.00/US	Utility - ORG	Issued	PROCESS FOR INTEGRATING IN A SAME CHIP A NON-VOLATILE MEMORY AND A HIGH-PERFORMANCE LOGIC CIRCUITRY	11/24/1999	09/449,044	6/25/2002	6,410,387	US
NMXS-0545.01/US	Utility - DIV	Issued	PROCESS FOR INTEGRATING IN A SAME CHIP A NON-VOLATILE MEMORY AND A HIGH-PERFORMANCE LOGIC CIRCUITRY	5/29/2002	10/158,424	3/30/2004	6,713,347	US
NMXS-0546.00/US	Utility - ORG	Issued	METHOD FOR MAINTAINING THE MEMORY CONTENT OF NON-VOLATILE MEMORY CELLS	9/15/1999	09/397,387	1/2/2001	6,169,691	US
NMXS-0547.00/US	Utility - ORG	Issued	METHOD OF FABRICATION OF A NO-FIELD MOS TRANSISTOR	4/5/2000	09/543,400	2/26/2002	6,350,637	US
NMXS-0548.00/US	Utility - CIP	Issued	DEVICE FOR READING ANALOG NONVOLATILE MEMORY CELLS, IN PARTICULAR FLASH CELLS	10/22/1999	09/425,446	2/27/2001	6,195,289	US
NMXS-0549.00/US	Utility - ORG	Issued	CAPACITIVE BOOSTING CIRCUIT FOR THE REGULATION OF THE WORD LINE READING VOLTAGE IN NON-VOLATILE MEMORIES	1/19/2000	09/491,476	7/10/2001	6,259,635	US
NMXS-0550.00/US	Utility - ORG	Issued	CAPACITIVE COMPENSATION CIRCUIT FOR THE REGULATION OF THE WORD LINE READING VOLTAGE IN NON-VOLATILE MEMORIES	1/19/2000	09/491,475	7/10/2001	6,259,632	US
NMXS-0551.00/US	Utility - ORG	Issued	CHIP OUTLINE BAND (COB) STRUCTURE FOR INTEGRATED CIRCUITS	1/14/2000	09/483,656	10/8/2002	6,462,400	US
NMXS-0552.00/US	Utility - ORG	Issued	IMPROVED FIELD-EFFECT TRANSISTOR AND CORRESPONDING MANUFACTURING METHOD	11/18/1999	09/442,834	5/14/2002	6,387,763	US
NMXS-0552.01/US	Utility - DIV	Issued	FIELD-EFFECT TRANSISTOR AND CORRESPONDING MANUFACTURING METHOD	3/4/2002	10/090,978	5/18/2004	6,737,715	US
NMXS-0553.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR DRIVING PLURAL LOADS BASED ON THE NUMBER OF LOADS BEING DRIVEN	12/20/1999	09/467,726	5/15/2001	6,232,753	US
NMXS-0554.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES, COMPRISING NON-SALICIDED NON-VOLATILE MEMORY CELLS, NON-SALICIDED HV TRANSISTORS, AND LV TRANSISTORS WITH SALICIDED JUNCTIONS WITH FEW MASKS	12/21/1999	09/469,849	8/14/2001	6,274,411	US
NMXS-0555.00/US	Utility - ORG	Issued	METHOD FOR HIGH PRECISION PROGRAMMING NONVOLATILE MEMORY CELLS, WITH OPTIMIZED PROGRAMMING SPEED	11/24/1999	09/449,168	5/21/2002	6,392,931	US
NMXS-0556.00/US	Utility - ORG	Issued	PROCESS OF MANUFACTURE OF A NON VOLATILE MEMORY WITH ELECTRIC CONTINUITY OF THE COMMON SOURCE LINES	4/12/2000	09/547,520	9/25/2001	6,294,431	US
NMXS-0557.00/US	Utility - ORG	Issued	FLASH COMPATIBLE EEPROM	6/21/2000	09/599,356	4/24/2001	6,222,775	US

Exhibit A - US Issued Patents

MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0558.00/US	Utility - ORG	Issued	METHOD FOR READING A MEMORY, PARTICULARLY A NON-VOLATILE MEMORY	12/29/1999	09/474,932	11/5/2002	6,477,625	US
NMXS-0559.00/US	Utility - ORG	Issued	SYNCHRONOUS OUTPUT BUFFER, PARTICULARLY FOR NON-VOLATILE MEMORIES	11/20/2000	09/716,759	7/23/2002	6,424,575	US
NMXS-0560.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SEMICONDUCTOR INTEGRATED MEMORY DEVICES WITH CELLS MATRIX HAVING VIRTUAL GROUND	2/18/2000	09/507,777	4/2/2002	6,365,456	US
NMXS-0561.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SEMICONDUCTOR INTEGRATED MEMORY DEVICES WITH CELLS MATRIX HAVING VIRTUAL GROUND	2/25/2000	09/512,900	10/9/2001	6,300,195	US
NMXS-0562.00/US	Utility - ORG	Abandoned	BIT COUNTER STAGE, PARTICULARLY FOR MEMORIES	12/15/1999	09/464,631	11/27/2001	6,324,238	US
NMXS-0563.00/US	Utility - ORG	Issued	PULSE GENERATOR CIRCUIT, PARTICULARLY FOR NON-VOLATILE MEMORIES	12/27/1999	09/472,496	8/14/2001	6,275,416	US
NMXS-0564.00/US	Utility - ORG	Issued	PRIORITY DETERMINING CIRCUIT FOR NON-VOLATILE MEMORY	2/8/2000	09/499,799	4/16/2002	6,373,781	US
NMXS-0565.00/US	Utility - ORG	Issued	METHOD FOR AUTOALIGNING OVERLAPPED LINES OF A CONDUCTIVE MATERIAL PROVIDED IN INTEGRATED ELECTRONIC CIRCUITS	5/26/2000	09/579,778	2/26/2002	6,350,671	US
NMXS-0566.00/US	Utility - ORG	Issued	METHOD OF ADJUSTING PROGRAM VOLTAGE IN NON-VOLATILE MEMORIES, AND PROCESS FOR FABRICATING A NON-VOLATILE MEMORY DEVICE	4/20/2000	09/552,933	3/18/2003	6,535,431	US
NMXS-0566.01/US	Utility - DIV	Issued	METHOD OF ADJUSTING PROGRAM VOLTAGE IN NON-VOLATILE MEMORIES, AND PROCESS FOR FABRICATING A NON-VOLATILE MEMORY DEVICE	1/24/2003	10/350,745	10/12/2004	6,803,630	US
NMXS-0567.00/US	Utility - ORG	Issued	ADDRESS LATCH ENABLE SIGNAL CONTROL CIRCUIT FOR ELECTRONIC MEMORIES	12/8/1999	09/457,500	6/19/2001	6,249,463	US
NMXS-0569.00/US	Utility - ORG	Issued	SEMICONDUCTOR DEVICE WITH IMPROVED INTERCONNECTIONS BETWEEN THE CHIP AND THE TERMINALS, AND PROCESS FOR ITS MANUFACTURE	2/10/2000	09/502,070	5/28/2002	6,396,132	US
NMXS-0570.00/US	Utility - ORG	Issued	Method for reading a multilevel nonvolatile memory and multilevel nonvolatile memory	2/25/2000	09/513,598	10/9/2001	6,301,149	US
NMXS-0571.00/US	Utility - ORG	Issued	VOLTAGE REGULATING CIRCUIT FOR A CAPACITIVE LOAD	6/29/2000	09/608,445	6/19/2001	6,249,112	US
NMXS-0572.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR SWITCHING BETWEEN TWO POWER SUPPLIES OF AN INTEGRATED CIRCUIT	12/21/1998	09/217,321	9/12/2000	6,118,188	US
NMXS-0573.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR INCREASING COMPARATOR GAIN WITHOUT AFFECTING STANDBY CURRENT	12/21/1998	09/217,323	7/4/2000	6,084,390	US
NMXS-0574.00/US	Utility - ORG	Issued	METHOD AND STRUCTURE FOR ENHANCING THE ACCESS TIME OF INTEGRATED CIRCUIT MEMORY DEVICES	10/30/1998	09/183,231	8/31/1999	5,946,264	US
NMXS-0575.00/US	Utility - ORG	Issued	CONTROL CIRCUIT FOR TERMINATING A MEMORY ACCESS CYCLE IN A MEMORY BLOCK OF AN ELECTRONIC STORAGE DEVICE	10/30/1998	09/183,589	3/7/2000	6,034,917	US
NMXS-0576.00/US	Utility - ORG	Issued	STRESS TEST MODE ENTRY AT POWER UP FOR LOW/ZERO POWER MEMORIES	10/30/1998	09/183,451	6/27/2000	6,081,466	US
NMXS-0577.00/US	Utility - ORG	Issued	INITIALIZATION FOR FUSE CONTROL	10/30/1998	09/183,840	3/21/2000	6,041,000	US
NMXS-0578.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT STRUCTURE COMPRISING CAPACITOR ELEMENT AND CORRESPONDING MANUFACTURING PROCESS	10/12/1999	09/415,992	9/25/2001	6,294,798	US
NMXS-0578.01/US	Utility - DIV	Issued	INTEGRATED CIRCUIT STRUCTURE COMPRISING CAPACITOR ELEMENT AND CORRESPONDING MANUFACTURING PROCESS	7/24/2001	09/912,638	1/28/2003	6,511,874	US
NMXS-0579.00/US	Utility - ORG	Issued	CONTENT ADDRESSABLE MEMORY PROTECTION CIRCUIT AND METHOD	4/28/2000	09/561,271	5/29/2001	6,240,002	US
NMXS-0580.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR FAST ADDRESSING REDUNDANt COLUMNS IN A NON VOLATILE MEMORY	4/13/2000	09/548,783	10/30/2001	6,310,801	US
NMXS-0581.00/US	Utility - ORG	Issued	READING CIRCUIT FOR NONVOLATILE MEMORY CELLS WITHOUT LIMITATIONOF THE SUPPLY VOLTAGE	4/11/2000	09/546,957	11/27/2001	6,324,098	US
NMXS-0582.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR AUTOMATICALLY REGULATING THE EQUALIZATION DURATION WHEN READING A NONVOLATILE MEMORY	4/11/2000	09/546,975	6/5/2001	6,243,310	US
NMXS-0583.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SOI INTEGRATED CIRCUITS AND CIRCUITS MADE THEREBY	4/28/2000	09/561,272	6/25/2002	6,410,404	US
NMXS-0584.00/US	Utility - ORG	Issued	COMMUNICATIONS SYSTEM AND METHOD FOR REDUCING THE EFFECTS OF TRANSMITTER NON-LINEAR DISTORTION ON A RECEIVED SIGNAL	6/17/1999	09/335,024	8/13/2002	6,434,189	US
NMXS-0585.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT COMPRISING AT LEAST TWO MEMORIES	5/18/1999	09/313,926	7/23/2002	6,425,066	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0586.00/US	Utility - ORG	Issued	MULTIPLE LEVEL FLOATING-GATE MEMORY	12/2/1999	09/453,753	6/11/2002	6,404,679	US
NMXS-0587.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR GENERATING A HIGH VOLTAGE	8/10/1999	09/371,549	12/5/2000	6,157,243	US
NMXS-0588.00/US	Utility - ORG	Abandoned	METHOD FOR VERIFYING THE MEMORY CIRCUIT INTEGRITY	12/2/1999	09/452,446	4/3/2001	6,212,112	US
NMXS-0589.00/US	Utility - ORG	Issued	SECURED EEPROM MEMORY COMPRISING MEANS FOR THE DETECTION OF ERASURE BY ULTRAVIOLET RADIATION	11/24/1999	09/448,632	6/19/2001	6,249,456	US
NMXS-0590.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR THE PROGRAMMING OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY FOR IMPROVING RELIABILITY	4/21/2000	09/556,476	9/18/2001	6,292,396	US
NMXS-0591.00/US	Utility - ORG	Issued	METHOD FOR ERASING AND REWRITING NON VOLATILE MEMORY CELLS AND PARTICULARLY FLASH CELLS	4/20/2000	09/553,526	8/28/2001	6,282,125	US
NMXS-0592.00/US	Utility - ORG	Issued	READING METHOD FOR NON-VOLATILE MEMORIES WITH SENSING RATIO VARIABLE WITH THE READING VOLTAGE, AND DEVICE TO REALIZE SAID METHOD	6/8/2000	09/589,723	3/26/2002	6,363,015	US
NMXS-0593.00/US	Utility - ORG	Issued	ERASING AND PARALLEL REWRITING CIRCUIT FOR MEMORY CELL BLOCKS, PARTICULARLY FOR ANALOG FLASH CELLS, AND RELATED OPERATING METHOD	6/13/2000	09/592,796	11/6/2001	6,314,043	US
NMXS-0594.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE, IN PARTICULAR A FLASH-EEPROM	4/20/2000	09/552,945	2/26/2002	6,351,413	US
NMXS-0595.00/US	Utility - ORG	Issued	METHOD FOR THE IN-WRITING VERIFICATION OF THE THRESHOLD VALUE IN NON-VOLATILE MEMORIES	5/11/2000	09/569,232	9/18/2001	6,292,398	US
NMXS-0596.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE WITH HIERARCHICAL SECTOR DECODING	6/26/2000	09/602,680	9/24/2002	6,456,530	US
NMXS-0597.00/US	Utility - ORG	Issued	SYNCHRONOUS MULTILEVEL NON-VOLATILE MEMORY AND RELATED READING METHOD	5/17/2000	09/572,127	3/6/2001	6,198,660	US
NMXS-0598.00/US	Utility - ORG	Issued	CIRCUIT FOR READING A SEMICONDUCTOR MEMORY	6/26/2000	09/603,275	12/25/2001	6,333,885	US
NMXS-0599.00/US	Utility - ORG	Issued	BIASING STAGE FOR BIASING THE DRAIN TERMINAL OF A NONVOLATILE MEMORY CELL DURING THE READ PHASE	6/2/2000	09/586,399	11/20/2001	6,320,790	US
NMXS-0600.00/US	Utility - ORG	Issued	METHOD AND A DEVICE FOR MEASURING AN ANALOG VOLTAGE IN A NON-VOLATILE MEMORY	6/29/2000	09/608,847	1/14/2003	6,507,183	US
NMXS-0601.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR GENERATING FROM A SINGLE SUPPLY LINE VOLTAGES INTERNAL TO A FLASH MEMORY WITH REDUCED SETTLING TIMES	6/30/2000	09/608,239	5/21/2002	6,392,936	US
NMXS-0602.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH LOW POWER CONSUMPTION AND AND RELATIVE WRITING, READING AND ERASING METHODS	7/21/2000	09/620,765	9/18/2001	6,292,400	US
NMXS-0603.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING MULTI-LEVEL NON-VOLATILE MEMORIES BY CONTROLLING THE GATE VOLTAGE	8/2/2000	09/631,187	4/2/2002	6,366,496	US
NMXS-0604.00/US	Utility - ORG	Issued	CMOS SWITCH CIRCUIT FOR TRANSFERRING HIGH VOLTAGES, IN PARTICULAR FOR LINE	6/2/2000	09/585,916	8/13/2002	6,433,583	US
NMXS-0605.00/US	Utility - ORG	Abandoned	ROW DECODER FOR SELECTIVELY BIASING A WORDLINE TO A POSITIVE OR NEGATIVE VOLTAGE	6/16/2000	09/595,054	3/12/2002	6,356,481	US
NMXS-0606.00/US	Utility - ORG	Issued	METHOD FOR READING DATA FROM A NON-VOLATILE MEMORY DEVICE WITH AUTODETECT BURST MODE READING AND CORRESPONDING READING CIRCUIT	11/20/2000	09/716,746	2/19/2002	6,349,059	US
NMXS-0607.00/US	Utility - ORG	Issued	SOFT PROGRAMMING METHOD FOR NON-VOLATILE MEMORY CELLS	12/14/2000	09/738,253	10/22/2002	6,469,934	US
NMXS-0608.01/US	Utility - DIV	Issued	BIPOLAR TRANSISTOR PRODUCED USING PROCESSES COMPATIBLE WITH THOSE EMPLOYED IN THE MANUFACTURE OF MOS DEVICES	2/15/2002	10/077,288	12/30/2003	6,670,229	US
NMXS-0610.00/US	Utility - ORG	Issued	BUFFER DEVICE WITH DUAL SUPPLY VOLTAGE FOR LOW SUPPLY VOLTAGE APPLICATIONS	12/13/2000	09/736,984	11/20/2001	6,320,361	US
NMXS-0611.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH FUNCTIONAL CAPABILITY OF SIMULTANEOUS MODIFICATION OF THE CONTENT AND BURST MODE READ OR PAGE MODE READ	7/28/2000	09/627,703	6/28/2005	6,912,598	US
NMXS-0612.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH FUNCTIONAL CAPABILITY OF BURST MODE READ AND PAGE MODE READ DURING SUSPENSION OF AN OPERATION OF ELECTRICAL ALTERATION	7/19/2000	09/619,589	8/27/2002	6,442,068	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0613.00/US	Utility - ORG	Issued	METHOD FOR CONTROLLED SOFT PROGRAMMING OF NON-VOLATILE MEMORY CELLS, IN PARTICULAR OF THE FLASH EEPROM AND EPROM TYPE	10/27/2000	09/699,309	4/30/2002	6,381,177	US
NMXS-0614.00/US	Utility - ORG	Issued	MANUFACTURE OF AN HV-MOS TRANSISTOR WITH LOW MULTIPLICATION FACTOR IN A DUAL-GATE CMOS PROCESS	11/14/2000	09/713,144	12/31/2002	6,501,147	US
NMXS-0615.00/US	Utility - ORG	Issued	READ CIRCUIT FOR A NONVOLATILE MEMORY	7/21/2000	09/621,019	12/4/2001	6,327,184	US
NMXS-0617.00/US	Utility - ORG	Issued	ENABLING CIRCUIT FOR OUTPUT DEVICES IN ELECTRONIC MEMORIES	7/31/2000	09/629,229	8/20/2002	6,438,040	US
NMXS-0618.00/US	Utility - ORG	Issued	PROCESS FOR THE FABRICATION OF INTEGRATED DEVICES WITH REDUCTION OF DAMAGE FROM PLASMA	3/9/2001	09/804,693	10/28/2003	6,638,833	US
NMXS-0619.00/US	Utility - ORG	Issued	NON VOLATILE MEMORY WITH DETECTION OF SHORT CIRCUITS BETWEEN WORD LINES	9/8/2000	09/658,236	10/23/2001	6,307,778	US
NMXS-0620.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR TIMING MULTI-LEVEL NON-VOLATILE MEMORIES	12/15/2000	09/738,889	6/3/2003	6,574,146	US
NMXS-0621.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH BURST MODE READING AND CORRESPONDING READING METHOD	11/21/2000	09/717,938	2/8/2005	6,854,040	US
NMXS-0622.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR SWITCHING BETWEEN ALTERNATING, FIRST AND SECOND VOLTAGE VALUES, IN PARTICULAR FOR PROGRAMMING MULTILEVEL CELLS	11/15/2000	09/714,837	7/23/2002	6,424,121	US
NMXS-0623.00/US	Utility - ORG	Lapsed	ANTI-DECIPHERING CONTACTS	10/1/2001	09/968,682	3/4/2003	6,528,885	US
NMXS-0624.00/US	Utility - ORG	Issued	READING CIRCUIT FOR A NON-VOLATILE MEMORY	10/27/2000	09/699,304	6/4/2002	6,400,607	US
NMXS-0625.00/US	Utility - ORG	Issued	READING METHOD AND CIRCUIT FOR A NON-VOLATILE MEMORY	10/27/2000	09/699,043	10/29/2002	6,473,340	US
NMXS-0626.00/US	Utility - ORG	Issued	COLUMN DECODER CIRCUIT FOR PAGE READING OF A SEMICONDUCTOR MEMORY	2/27/2001	09/797,171	1/14/2003	6,507,534	US
NMXS-0627.00/US	Utility - ORG	Issued	FLOW PROCESS FOR PRODUCING NON-VOLATILE MEMORIES WITH DIFFERENTIATED REMOVAL OF SACRIFICAL OXIDE	8/2/2000	09/630,933	3/26/2002	6,362,053	US
NMXS-0628.00/US	Utility - ORG	Issued	ARCHITECTURE FOR HANDLING INTERNAL VOLTAGES IN A NON-VOLATILE MEMORY, PARTICULARLY IN A SINGLE-VOLTAGE SUPPLY TYPE OF DUAL-WORK FLASH MEMORY	11/9/2000	09/710,067	5/7/2002	6,385,107	US
NMXS-0629.00/US	Utility - ORG	Issued	CONTROL AND TIMING STRUCTURE FOR A MEMORY	10/5/2001	09/972,753	4/15/2003	6,549,485	US
NMXS-0630.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NONVOLATILE MEMORY CELLS OF REDUCED DIMENSIONS	11/22/2000	09/718,971	1/21/2003	6,509,222	US
NMXS-0630.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NONVOLATILE MEMORY CELLS OF REDUCED DIMENSIONS	8/20/2002	10/225,315	8/5/2003	6,603,171	US
NMXS-0631.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR NON-VOLATILE FLOATING GATE MEMORY CELLS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND COMPRISED IN A CELL MATRIX WITH AN ASSOCIATED CONTROL CIRCUITRY	12/5/2000	09/730,518	7/16/2002	6,420,223	US
NMXS-0632.00/US	Utility - ORG	Issued	SINGLE SUPPLY VOLTAGE NONVOLATILE MEMORY DEVICE WITH ROW DECODING	7/28/2000	09/627,273	4/16/2002	6,373,780	US
NMXS-0633.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY TEST STRUCTURE AND NONVOLATILE MEMORY RELIABILITY TEST METHOD	10/27/1999	09/428,683	10/3/2000	6,128,219	US
NMXS-0635.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR PROGRAMMING A MULTILEVEL NON-VOLATILE MEMORY	9/12/2001	09/952,957	2/25/2003	6,525,961	US
NMXS-0637.00/US	Utility - ORG	Issued	SERIAL-FLASH, EPROM, EEPROM AND FLASH EEPROM NONVOLATILE MEMORY IN AMG CONFIGURATION	10/10/2000	09/686,362	4/30/2002	6,381,173	US
NMXS-0638.00/US	Utility - ORG	Issued	LOW CONSUMPTION VOLTAGE BOOST DEVICE	12/22/2000	09/747,312	8/20/2002	6,437,636	US
NMXS-0639.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH CONFIGURABLE ROW REDUNDANCY	2/14/2001	09/785,079	7/9/2002	6,418,051	US
NMXS-0640.00/US	Utility - ORG	Abandoned	INTEGRATED DEVICE WITH OPERATIVITY TESTING	3/2/2001	09/798,347	5/24/2005	6,898,745	US
NMXS-0641.00/US	Utility - ORG	Issued	VOLTAGE BOOSTING DEVICE, IN PARTICULAR FOR SPEEDING POWER-UP OF MULTILEVEL NONVOLATILE MEMORIES	2/6/2001	09/778,330	8/6/2002	6,429,634	US
NMXS-0642.00/US	Utility - ORG	Issued	NON-VOLATILE HIGH-PERFORMANCE MEMORY DEVICE AND RELATIVE MANUFACTURING PROCESS	12/19/2000	09/740,407	1/13/2004	6,677,206	US
NMXS-0643.00/US	Utility - ORG	Issued	SYNCHRONOUS COUNTER FOR ELECTRONIC MEMORIES	1/23/2001	09/767,762	2/26/2002	6,351,434	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0644.00/US	Utility - ORG	Issued	SYNCHRONIZATION CIRCUIT FOR READ PATHS OF AN ELETRONIC MEMORY	4/5/2001	09/827,369	10/12/2004	6,804,756	US
NMXS-0645.00/US	Utility - ORG	Issued	INTERNAL ADDRESSING STRUCTURE OF A SEMICONDUCTOR MEMORY	10/9/2001	09/974,737	12/31/2002	6,501,700	US
NMXS-0646.00/US	Utility - ORG	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	3/20/2001	09/817,363	7/2/2002	6,414,875	US
NMXS-0646.01/US	Utility - DIV	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	6/24/2002	10/179,553	1/27/2004	6,683,808	US
NMXS-0646.02/US	Utility - DIV	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	12/19/2003	10/742,429	7/4/2006	7,072,212	US
NMXS-0646.03/US	Utility - DIV	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	12/19/2003	10/742,181	10/11/2005	6,954,395	US
NMXS-0647.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR THE INTEGRATION IN A SEMICONDUCTOR CHIP OF AN INTEGRATED CIRCUIT INCLUDING A HIGH-DENSITY INTEGRATED CIRCUIT COMPONENTS PORTION AND A HIGH-PERFORMANCE LOGIC INTEGRATED CIRCUIT COMPONENTS PORTION	5/15/2001	09/858,335	9/17/2002	6,451,653	US
NMXS-0648.00/US	Utility - ORG	Issued	LOGIC PARTITIONING OF A NONVOLATILE MEMORY ARRAY	3/26/2001	09/817,804	6/17/2003	6,581,134	US
NMXS-0649.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY MATRIX ARCHITECTURE WITH VERTICAL INSULATION STRIPS BETWEEN ADJACENT MEMORY BLOCKS	7/3/2001	09/898,744	10/15/2002	6,466,479	US
NMXS-0650.00/US	Utility - ORG	Issued	PROGRAMMABLE VOLTAGE GENERATOR	11/15/2000	09/714,852	11/18/2003	6,650,173	US
NMXS-0651.00/US	Utility - ORG	Issued	METHOD OF FORMING LOW-RESISTIVITY CONNECTIONS IN NON-VOLATILE MEMORIES	3/2/2001	09/798,778	2/3/2004	6,686,241	US
NMXS-0652.00/US	Utility - ORG	Issued	METHOD AND A DEVICE FOR TESTING ELECTRONIC MEMORY DEVICES	3/30/2001	09/823,926	1/23/2007	7,168,016	US
NMXS-0653.00/US	Utility - ORG	Issued	CIRCUITAL STRUCTURE FOR PROGRAMMING DATA IN A NON-VOLATILE MEMORY DEVICE	5/30/2001	09/871,235	1/14/2003	6,507,517	US
NMXS-0654.00/US	Utility - ORG	Issued	CIRCUITAL STRUCTURE FOR READING DATA IN A NON-VOLATILE MEMORY DEVICE	5/30/2001	09/871,234	4/15/2003	6,549,473	US
NMXS-0655.00/US	Utility - ORG	Abandoned	INTEGRATED DEVICE WITH VOLTAGE SELECTOR	3/29/2001	09/823,262	11/5/2002	6,476,664	US
NMXS-0656.00/US	Utility - ORG	Abandoned	LOW-CONSUMPTION CHARGE PUMP FOR A NONVOLATILE MEMORY	3/28/2001	09/821,827	5/6/2003	6,559,709	US
NMXS-0657.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING A NON-VOLATILE MEMORY CELL WITH A FLOATING GATE REGION AUTOALIGNED TO THE ISOLATION AND WITH A HIGH COUPLING COEFFICIENT	7/6/2001	09/900,501	3/25/2003	6,537,879	US
NMXS-0657.01/US	Utility - DIV	Issued	NON-VOLATILE MEMORY CELL WITH A FLOATING GATE REGION AUTOALIGNED TO THE ISOLATION AND WITH A HIGH COUPLING COEFFICIENT	1/7/2003	10/337,556	6/15/2004	6,750,505	US
NMXS-0658.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY ARCHITECTURE	10/5/2001	09/972,769	6/17/2003	6,580,637	US
NMXS-0659.00/US	Utility - ORG	Issued	REFERENCE VOLTAGE ADJUSTMENT	12/31/1999	09/476,036	8/28/2001	6,281,734	US
NMXS-0659.01/US	Utility - DIV	Issued	REFERENCE VOLTAGE ADJUSTMENT	7/10/2001	09/902,206	11/5/2002	6,476,669	US
NMXS-0660.00/US	Utility - ORG	Issued	INTEGRATED VOLATILE AND NON-VOLATILE MEMORY	8/31/2000	09/653,495	7/15/2003	6,594,192	US
NMXS-0660.01/US	Utility - DIV	Issued	INTEGRATED VOLATILE AND NON-VOLATILE MEMORY	5/20/2003	10/442,844	8/24/2004	6,781,916	US
NMXS-0661.00/US	Utility - ORG	Issued	CMOS TECHNOLOGY VOLTAGE BOOSTER	12/15/2000	09/738,890	7/16/2002	6,420,926	US
NMXS-0662.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY AND HIGH SPEED MEMORY TEST METHOD	9/26/2000	09/670,248	8/20/2002	6,438,048	US
NMXS-0663.00/US	Utility - ORG	Issued	Memory test method and nonvolatile memory with low error masking probability	9/26/2000	09/670,471	8/28/2001	6,282,134	US
NMXS-0664.00/US	Utility - ORG	Issued	ROW SELECTION CIRCUIT FOR FAST MEMORY DEVICES	12/21/2000	09/745,286	8/27/2002	6,442,072	US
NMXS-0665.00/US	Utility - ORG	Issued	INTERLEAVED DATA PATH AND OUTPUT MANAGEMENT ARCHITECTURE FOR AN INTERLEAVED MEMORY AND LOAD PULSER CIRCUIT FOR OUTPUTTING THE READ DATA	1/31/2001	09/774,542	10/22/2002	6,470,431	US
NMXS-0666.00/US	Utility - ORG	Issued	INTERLEAVED MEMORY DEVICE FOR SEQUENTIAL ACCESS SYNCHRONOUS READING WITH SIMPLIFIED ADDRESS COUNTERS	1/31/2001	09/774,539	9/17/2002	6,452,864	US
NMXS-0667.00/US	Utility - ORG	Issued	REDUNDANCY ARCHITECTURE FOR AN INTERLEAVED MEMORY	1/31/2001	09/773,272	10/29/2002	6,473,339	US
NMXS-0668.00/US	Utility - ORG	Issued	ACCELERATED CARRY GENERATION	1/31/2001	09/774,878	4/2/2002	6,366,634	US
NMXS-0669.00/US	Utility - ORG	Issued	ATD GENERATION IN A SYNCHRONOUS MEMORY	1/31/2001	09/773,760	9/23/2003	6,625,706	US
NMXS-0670.00/US	Utility - ORG	Issued	INTERLEAVED MEMORY DEVICE FOR BURST TYPE ACCESS IN SYNCHRONOUS READ MODE WITH THE TWO SEMI-ARRAYS INDEPENDENTLY READABLE IN RANDOM ACCESS ASYNCHRONOUS MODE	1/31/2001	09/773,300	7/1/2003	6,587,913	US
NMXS-0671.00/US	Utility - ORG	Issued	COLOR IMAGE RESTORATION WITH ANTI-ALIAS	11/2/2000	09/705,145	1/11/2005	6,842,191	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0672.00/US	Utility - NSPCT	Issued	VOLTAGE PRODUCTION CIRCUIT	6/26/2000	10/019,771	9/16/2003	6,621,720	US
NMXS-0673.00/US	Utility - ORG	Issued	METHOD FOR PAGE MODE WRITING IN AN ELECTRICALLY ERASABLE/PROGRAMMABLE NON-VOLATILE MEMORY AND CORRESPONDING ARCHITECTURE	9/12/2000	09/660,303	1/7/2003	6,504,791	US
NMXS-0674.00/US	Utility - ORG	Issued	MEMORY INCORPORATING COLUMN REGISTER AND METHOD OF WRITING IN SAID MEMORY	9/29/2000	09/675,366	10/23/2001	6,307,792	US
NMXS-0674.01/US	Utility - DIV	Issued	MEMORY INCORPORATING COLUMN REGISTER AND METHOD OF WRITING IN SAID MEMORY	9/13/2001	09/952,904	5/7/2002	6,385,096	US
NMXS-0675.00/US	Utility - ORG	Issued	ARCHITECTURE OF A NON-VOLATILE ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY	9/7/2000	09/657,319	8/14/2001	6,275,413	US
NMXS-0677.00/US	Utility - ORG	Issued	PAGE BY PAGE PROGRAMMABLE FLASH MEMORY	12/14/2000	09/737,170	1/4/2005	6,839,285	US
NMXS-0678.00/US	Utility - ORG	Issued	METHOD FOR THE CORRECTION OF A BIT IN A STRING OF BITS	12/15/2000	09/737,827	5/11/2004	6,735,733	US
NMXS-0679.00/US	Utility - ORG	Issued	HIGH-EFFICIENCY BIDIRECTIONAL VOLTAGE BOOSTING DEVICE	2/13/2001	09/788,623	9/10/2002	6,448,842	US
NMXS-0680.00/US	Utility - ORG	Issued	CHARGE PUMP BOOSTER DEVICE WITH TRANSFER AND RECOVERY OF THE CHARGE	2/13/2001	09/785,042	6/11/2002	6,404,272	US
NMXS-0681.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY CELL WITH HIGH PROGRAMMING EFFICIENCY	7/30/2001	09/919,341	5/11/2004	6,734,490	US
NMXS-0682.00/US	Utility - ORG	Issued	METHOD AND A CIRCUIT STRUCTURE FOR MODIFYING THE THRESHOLD VOLTAGES OF NON-VOLATILE MEMORY CELLS	7/18/2001	09/908,986	2/11/2003	6,519,183	US
NMXS-0683.00/US	Utility - ORG	Issued	CONTROL CIRCUIT FOR A VARIABLE-VOLTAGE REGULATOR OF A NONVOLATILE MEMORY WITH HIERARCHICAL ROW DECODING	9/21/2001	09/960,851	1/7/2003	6,504,758	US
NMXS-0684.00/US	Utility - ORG	Issued	CIRCUIT ARRANGEMENT FOR THE LOWERING OF THE THRESHOLD VOLTAGE OF A DIODE CONFIGURED TRANSISTOR	6/13/2001	09/881,661	12/31/2002	6,501,673	US
NMXS-0685.00/US	Utility - ORG	Issued	DIRECT-COMPARISON READING CIRCUIT FOR A NONVOLATILE MEMORY ARRAY	8/15/2001	09/930,875	10/8/2002	6,462,987	US
NMXS-0686.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH A CHARGE PUMP WITH REGULATED VOLTAGE	7/19/2001	09/909,467	11/12/2002	6,480,436	US
NMXS-0687.00/US	Utility - ORG	Issued	CHARGE PUMP WITH EFFICIENT SWITCHING TECHNIQUES	12/11/2000	09/735,277	3/19/2002	6,359,500	US
NMXS-0688.00/US	Utility - ORG	Issued	High-voltage bidirectional switch made using high-voltage MOS transistors	6/16/2000	09/595,680	9/11/2001	6,288,603	US
NMXS-0689.00/US	Utility - ORG	Issued	READING CIRCUIT FOR SEMICONDUCTOR NON-VOLATILE MEMORIES	9/13/2001	09/953,070	5/13/2003	6,563,737	US
NMXS-0691.00/US	Utility - ORG	Issued	LATERAL DMOS TRANSISTOR WITH FIRST AND SECOND DRAIN ELECTRODES IN RESPECTIVE CONTACT WITH HIGH-AND LOW-CONCENTRATION PORTIONS OF A DRAIN REGION	9/20/2001	09/960,254	9/23/2003	6,624,471	US
NMXS-0692.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER WITH A LOW OUTPUT RESISTANCE	2/13/2001	09/785,083	6/11/2002	6,404,273	US
NMXS-0693.00/US	Utility - ORG	Issued	MULTIPURPOSE METHOD FOR CONSTRUCTING AN ERROR-CONTROL CODE FOR MULTILEVEL MEMORY CELLS OPERATING WITH A VARIABLE NUMBER OF STORAGE LEVELS, AND MULTIPURPOSE ERROR-CONTROL METHOD USING SAID ERROR-CONTROL CODE	11/2/2001	10/015,949	5/16/2006	7,047,478	US
NMXS-0694.00/US	Utility - ORG	Issued	FOLDING TOP FOR A MOTOR VEHICLE	10/31/2001	09/002,599	8/5/2003	6,603,681	US
NMXS-0695.00/US	Utility - ORG	Issued	NONVOLATILE SEMICONDUCTOR MEMORY CAPABLE OF SELECTIVELY ERASING A PLURALITY OF ELEMENTAL MEMORY UNITS	7/31/2001	09/919,789	3/11/2003	6,532,171	US
NMXS-0696.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE, NON-VOLATILE, SEMICONDUCTOR MEMORY WHICH CAN KEEP A DATUM STORED UNTIL AN OPERATION TO MODIFY THE DATUM IS COMPLETED	12/28/2001	10/036,088	1/4/2005	6,839,818	US
NMXS-0697.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR LOW-CONSUMPTION CIRCUITS	11/7/2001	09/008,540	5/6/2003	6,559,627	US
NMXS-0698.00/US	Utility - ORG	Issued	SMALL SIZE, LOW CONSUMPTION, MULTILEVEL NONVOLATILE MEMORY	10/4/2001	09/972,726	4/1/2003	6,542,404	US
NMXS-0699.00/US	Utility - ORG	Issued	METHOD FOR STORING AND READING DATA IN A MULTILEVEL NONVOLATILE MEMORY	10/11/2001	09/976,473	11/11/2003	6,646,913	US
NMXS-0700.00/US	Utility - ORG	Abandoned	NONVOLATILE MEMORY DEVICE, HAVING PARTS WITH DIFFERENT ACCESS TIME, RELIABLITY, AND CAPACITY	9/19/2001	09/957,628	12/10/2002	6,493,260	US
NMXS-0701.00/US	Utility - ORG	Issued	CONTROL CIRCUIT FOR AN OUTPUT DRIVING STAGE OF AN INTEGRATED CIRCUIT	11/21/2001	09/991,493	5/20/2003	6,567,318	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0702.00/US	Utility - ORG	Issued	BIASING CIRCUIT FOR MULTI-LEVEL MEMORY CELLS	6/14/2001	09/882,535	3/18/2003	6,535,428	US
NMXS-0703.00/US	Utility - ORG	Issued	CIRCUIT DEVICE FOR PERFORMING HIERARCHIC ROW DECODING IN NON-VOLATILE MEMORY DEVICES	7/12/2001	09/905,163	12/10/2002	6,493,268	US
NMXS-0704.01/US	Utility - CON	Issued	ANALOG-TO-DIGITAL CONVERSION METHOD AND DEVICE, IN HIGH-DENSITY MULTILEVEL NON-VOLATILE MEMORY DEVICES	1/29/2002	10/060,076	1/6/2004	6,674,385	US
NMXS-0705.00/US	Utility - ORG	Issued	POWER-ON RESET CIRCUIT FOR DUAL SUPPLY VOLTAGES	10/25/2001	09/003,474	11/12/2002	6,480,421	US
NMXS-0706.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING NONVOLATILE MEMORY CELLS WITH PROGRAM AND VERIFY ALGORITHM USING A STAIRCASE VOLTAGE WITH VARYING STEP AMPLITUDE	4/9/2002	10/119,523	9/7/2004	6,788,579	US
NMXS-0707.00/US	Utility - ORG	Issued	CONDUCTION LINE DECOUPLING CIRCUIT	7/26/2001	09/917,613	9/7/2004	6,788,517	US
NMXS-0708.00/US	Utility - ORG	Issued	MULTI-EMITTER BIPOLAR TRANSISTOR FOR BANDGAP REFERENCE CIRCUITS	12/27/2001	10/035,006	3/2/2004	6,700,226	US
NMXS-0710.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY SYSTEM	5/30/2002	10/158,554	7/15/2003	6,594,180	US
NMXS-0711.00/US	Utility - ORG	Issued	OUTPUT BUFFER FOR A NONVOLATILE MEMORY WITH OUTPUT SIGNAL SWITCHING NOISE REDUCTION, AND NONVOLATILE MEMORY COMPRISING THE SAME	5/30/2002	10/161,053	9/7/2004	6,788,586	US
NMXS-0712.00/US	Utility - ORG	Issued	OUTPUT BUFFER FOR A NONVOLATILE MEMORY WITH OPTIMIZED SLEW-RATE CONTROL	5/30/2002	10/161,055	12/7/2004	6,829,177	US
NMXS-0713.00/US	Utility - ORG	Issued	EEPROM FLASH MEMORY ERASABLE LINE BY LINE	8/20/2002	10/225,513	2/3/2004	6,687,167	US
NMXS-0714.00/US	Utility - ORG	Issued	METHOD OF PROGRAMMING A PLURALITY OF MEMORY CELLS CONNECTED IN PARALLEL, AND A PROGRAMMING CIRCUIT THEREFOR	12/19/2001	10/036,337	2/3/2004	6,687,159	US
NMXS-0715.00/US	Utility - ORG	Issued	COLUMN MULTIPLEXER FOR SEMICONDUCTOR MEMORIES	5/30/2002	10/158,553	4/22/2003	6,552,952	US
NMXS-0716.00/US	Utility - ORG	Issued	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	12/14/2001	10/017,502	12/23/2003	6,667,903	US
NMXS-0717.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR DYNAMIC READING OF A MEMORY CELL, IN PARTICULAR A MULTI-LEVEL NONVOLATILE MEMORY CELL	1/14/2002	10/047,918	11/4/2003	6,643,179	US
NMXS-0718.00/US	Utility - ORG	Issued	METHOD FOR STORING DATA IN A NONVOLATILE MEMORY	12/19/2001	10/035,909	12/2/2003	6,655,758	US
NMXS-0720.00/US	Utility - ORG	Issued	AUTOTESTING METHOD OF A MEMORY CELL MATRIX, PARTICULARLY OF THE NON-VOLATILE TYPE	12/23/2002	10/328,721	11/8/2005	6,963,512	US
NMXS-0721.00/US	Utility - ORG	Issued	READING CIRCUIT AND METHOD FOR A MULTILEVEL NON-VOLATILE MEMORY	4/8/2002	10/118,660	12/2/2003	6,657,895	US
NMXS-0722.00/US	Utility - ORG	Abandoned	METHOD OF REFRESHING AN ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY	6/20/2002	10/176,387	5/4/2004	6,731,557	US
NMXS-0723.00/US	Utility - ORG	Issued	HIGH-EFFICIENCY POWER CHARGE PUMP SUPPLYING HIGH DC OUTPUT CURRENTS	6/3/2002	10/162,135	8/12/2003	6,605,985	US
NMXS-0724.00/US	Utility - ORG	Issued	NON-VOLATILE, ELECTRICALLY ALTERABLE SEMICONDUCTOR MEMORY	1/24/2002	10/057,769	9/9/2003	6,618,315	US
NMXS-0725.00/US	Utility - ORG	Issued	METHOD FOR ERASING AN ELECTRICALLY ERASABLE NONVOLATILE MEMORY DEVICE, IN PARTICULAR AN EEPROM-FLASH MEMORY DEVICE, AND AN ELECTRICALLY ERASABLE NONVOLATILE MEMORY DEVICE, IN PARTICULAR AN EEPROM-FLASH MEMORY DEVICE	5/30/2002	10/159,780	3/22/2005	6,871,258	US
NMXS-0726.00/US	Utility - ORG	Issued	METHOD FOR REFRESHING STORED DATA IN AN ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY	1/24/2002	10/057,768	12/23/2003	6,668,303	US
NMXS-0727.00/US	Utility - ORG	Issued	MEMORY WITH IMPROVED DIFFERENTIAL READING SYSTEM	6/20/2002	10/176,954	3/2/2004	6,700,819	US
NMXS-0728.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TIMING DYNAMIC READING OF A MEMORY CELL WITH CONTROL OF THE INTEGRATION TIME	4/16/2002	10/123,874	4/27/2004	6,728,141	US
NMXS-0729.00/US	Utility - CIP	Issued	METHOD AND CIRCUIT FOR DYNAMIC READING OF A MEMORY CELL AT LOW SUPPLY VOLTAGE AND WITH LOW OUTPUT DYNAMICS	2/13/2002	10/076,023	10/28/2003	6,639,833	US
NMXS-0730.00/US	Utility - CIP	Issued	METHOD AND CIRCUIT FOR GENERATING REFERENCE VOLTAGES FOR READING A MULTILEVEL MEMORY CELL	4/26/2002	10/133,231	4/20/2004	6,724,658	US
NMXS-0731.00/US	Utility - ORG	Issued	METHOD FOR ERROR CONTROL IN MULTILEVEL CELLS WITH CONFIGURABLE NUMBER OF STORED BITS	5/30/2002	10/159,782	3/21/2006	7,017,099	US
NMXS-0732.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS OF AN INTERPOLY DIELECTRIC STRUCTURE FOR NON-VOLATILE SEMICONDUCTOR INTEGRATED MEMORIES	1/30/2003	10/356,351	8/1/2006	7,084,032	US

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NMXS-0733.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS OF A SEMICONDUCTOR NON-VOLATILE MEMORY CELL	12/18/2002	10/323,615	8/28/2007	7,262,098	US
NMXS-0734.00/US	Utility - ORG	Issued	PROCESS FOR REMOVING POLYMERS DURING THE FABRICATION OF SEMICONDUCTOR DEVICES	7/2/2002	10/189,152	4/13/2004	6,720,271	US
NMXS-0735.00/US	Utility - ORG	Issued	LOW POWER CHARGE PUMP CIRCUIT	11/7/2002	10/290,030	11/9/2004	6,816,001	US
NMXS-0736.00/US	Utility - ORG	Issued	CONTENT ADDRESSABLE MEMORY (CAM) WITH BATTERY BACK-UP	6/29/2001	09/895,491	12/17/2002	6,496,439	US
NMXS-0737.00/US	Utility - ORG	Issued	Data output buffer with precharge	8/11/2000	09/636,363	9/18/2001	6,292,405	US
NMXS-0738.00/US	Utility - ORG	Issued	INTERLACED MEMORY DEVICE WITH RANDOM OR SEQUENTIAL ACCESS	10/15/2001	09/977,561	3/2/2004	6,701,419	US
NMXS-0739.00/US	Utility - ORG	Issued	OUTPUT BUFFER AND METHOD OF DRIVING	8/8/2001	09/925,842	12/3/2002	6,489,807	US
NMXS-0740.00/US	Utility - ORG	Issued	GENERATOR CIRCUIT FOR VOLTAGE RAMPS AND CORRESPONDING VOLTAGE GENERATION METHOD	12/28/2001	10/033,711	11/18/2003	6,650,153	US
NMXS-0741.00/US	Utility - ORG	Issued	BOOST DEVICE FOR NONVOLATILE MEMORIES WITH AN INTEGRATED STAND-BY CHARGE PUMP	2/13/2002	10/076,134	11/18/2003	6,650,569	US
NMXS-0742.00/US	Utility - ORG	Abandoned	CHARGE PUMP FOR A NONVOLATILE MEMORY WITH READ VOLTAGE REGULATION IN THE PRESENCE OF ADDRESS SKEW, AND NONVOLATILE MEMORY COMPRISING SUCH A CHARGE PUMP	2/5/2002	10/068,560	5/6/2003	6,560,145	US
NMXS-0743.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR TIMING THE READING OF A NONVOLATILE MEMORY WITH REDUCED SWITCHING NOISE	2/15/2002	10/077,687	1/6/2004	6,674,666	US
NMXS-0744.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR AUTOMATICALLY GENERATING AN APPROPRIATE NUMBER OF WAIT CYCLES WHILE READING A NONVOLATILE MEMORY	4/3/2002	10/115,888	7/12/2005	6,917,994	US
NMXS-0745.00/US	Utility - ORG	Issued	CIRCUIT FOR GENERATING A PULSE SIGNAL INDEPENDENT OF VOLTAGE AND TEMPERATURE VARIATIONS	10/31/2001	09/998,903	4/15/2003	6,549,486	US
NMXS-0746.00/US	Utility - ORG	Issued	NEGATIVE CHARGE PUMP ARCHITECTURE WITH SELF-GENERATED BOOSTED PHASES	10/31/2001	09/998,902	4/13/2004	6,720,822	US
NMXS-0748.00/US	Utility - ORG	Issued	THRESHOLD AMPLIFIER	8/9/2001	09/925,979	4/15/2003	6,549,048	US
NMXS-0749.00/US	Utility - NSPCT	Issued	PAGE-ERASABLE FLASH MEMORY	11/14/2001	10/438,733	10/19/2004	6,807,103	US
NMXS-0750.00/US	Utility - ORG	Issued	CIRCUIT FOR THE DETECTION OF A DEFECTIVE POWER SUPPLY CONNECTION	1/29/2002	10/060,105	8/10/2004	6,774,663	US
NMXS-0751.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR SEQUENTIAL READOUT OF A MEMORY WITH ADDRESS JUMP	2/22/2002	10/081,740	8/9/2005	6,928,530	US
NMXS-0752.00/US	Utility - ORG	Issued	FLASH MEMORY INCLUDING MEANS OF CHECKING MEMORY CELL THRESHOLD VOLTAGES	11/15/2001	09/997,214	5/27/2003	6,568,510	US
NMXS-0752.01/US	Utility - DIV	Issued	FLASH MEMORY INCLUDING MEANS OF CHECKING MEMORY CELL THRESHOLD VOLTAGES	1/28/2003	10/352,581	3/30/2004	6,714,453	US
NMXS-0753.00/US	Utility - NSPCT	Issued	CONTACT LESS IC CARD WITH OPERATING SYSTEM USED IN CONTACT TYPE CARDS AND READER FOR SUCH CONTACTLESS CARDS	2/14/2002	10/471,883	1/16/2007	7,163,154	US
NMXS-0754.00/US	Utility - ORG	Issued	FLASH EEPROM AND EPROM ARRAYS WITH SELECT TRANSISTORS WITHIN THE BIT LINE PITCH	3/11/1994	08/212,176	9/17/1996	5,557,124	US
NMXS-0755.00/US	Utility - ORG	Issued	METHOD OF RE-PROGRAMMING AN ARRAY OF NON-VOLATILE MEMORY CELLS, IN PARTICULAR OF THE NOR ARCHITECTURE FLASH TYPE, AFTER AN ERASE OPERATION, AND A CORRESPONDING MEMORY DEVICE	6/12/2002	10/171,078	11/25/2003	6,654,287	US
NMXS-0756.00/US	Utility - ORG	Issued	MEMORY DEVICE	10/24/2001	10/041,684	5/20/2003	6,567,296	US
NMXS-0757.00/US	Utility - ORG	Issued	WORD LINE SELECTOR FOR A SEMICONDUCTOR MEMORY	2/20/2003	10/372,626	3/8/2005	6,865,114	US
NMXS-0758.00/US	Utility - ORG	Issued	DESIGN FAILURE MODE EFFECT ANALYSIS (DFMEA)	12/26/2002	10/330,485	4/25/2006	7,035,769	US
NMXS-0759.00/US	Utility - ORG	Issued	POWER SUPPLY CIRCUIT STRUCTURE FOR A ROW DECODER OF A MULTILEVEL NON-VOLATILE MEMORY DEVICE	12/30/2002	10/334,126	12/7/2004	6,829,168	US
NMXS-0760.00/US	Utility - ORG	Issued	BINARY ENCODING CIRCUIT	12/20/2002	10/325,707	2/24/2004	6,696,990	US
NMXS-0761.00/US	Utility - ORG	Issued	REGULATION METHOD FOR THE DRAIN, BODY AND SOURCE TERMINALS VOLTAGES IN A NON-VOLATILE MEMORY CELL DURING A PROGRAM PHASE AND CORRESPONDING PROGRAM CIRCUIT	12/27/2002	10/331,116	10/26/2004	6,809,961	US

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NMXS-0762.00/US	Utility - ORG	Issued	METHOD FOR STORING AND READING DATA IN A MULTILEVEL NONVOLATILE MEMORY, AND ARCHITECTURE THEREFOR	9/26/2002	10/259,252	11/9/2004	6,816,407	US
NMXS-0762.01/US	Utility - DIV	Issued	MULTILEVEL MEMORY DEVICE WITH MEMORY CELLS STORING NON-POWER OF TWO VOLTAGE LEVELS	10/12/2004	10/964,160	11/15/2005	6,965,523	US
NMXS-0763.00/US	Utility - ORG	Issued	ELECTRICALLY-PROGRAMMABLE NON-VOLATILE MEMORY CELL	2/20/2003	10/372,044	4/19/2005	6,882,001	US
NMXS-0764.00/US	Utility - ORG	Issued	DECODING STRUCTURE FOR A MEMORY DEVICE WITH A CONTROL CODE	12/27/2002	10/331,177	10/25/2005	6,958,949	US
NMXS-0765.00/US	Utility - ORG	Issued	SENSE AMPLIFIER STRUCTURE FOR MULTILEVEL NON-VOLATILE MEMORY DEVICES AND CORRESPONDING READING METHOD	12/26/2002	10/331,158	11/23/2004	6,822,906	US
NMXS-0766.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING A DUAL CHARGE STORAGE LOCATION MEMORY CELL	11/14/2002	10/294,999	10/25/2005	6,958,510	US
NMXS-0767.00/US	Utility - ORG	Issued	FINITE STATE MACHINE INTERFACE HAS ARBITRATION STRUCTURE TO STORE COMMAND GENERATED BY INTERNAL CIRCUITS DURING EVALUATION PHASE OF STATE MACHINE FOR FLASH EEPROM DEVICE	12/4/2002	10/309,759	10/16/2007	7,284,144	US
NMXS-0768.00/US	Utility - ORG	Issued	SINGLE SUPPLY VOLTAGE, NONVOLATILE PHASE CHANGE MEMORY DEVICE WITH CASCODED COLUMN SELECTION AND SIMULTANEOUS WORD READ/WRITE OPERATIONS	12/27/2002	10/331,185	6/22/2004	6,754,107	US
NMXS-0769.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING A DUAL CHARGE STORAGE LOCATION MEMORY CELL	10/7/2002	10/267,033	11/30/2004	6,825,523	US
NMXS-0769.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING A DUAL CHARGE STORAGE LOCATION MEMORY CELL	10/12/2004	10/964,049	10/3/2006	7,115,472	US
NMXS-0770.00/US	Utility - ORG	Issued	ARCHITECTURE FOR A FLASH-EEPROM SIMULTANEOUSLY READABLE IN OTHER SECTORS WHILE ERASING AND/OR PROGRAMMING ONE OR MORE SECTORS	1/10/2003	10/340,207	5/10/2005	6,891,755	US
NMXS-0771.00/US	Utility - ORG	Issued	FAST PROGRAMMING METHOD FOR NONVOLATILE MEMORIES, IN PARTICULAR FLASH MEMORIES, AND RELATIVE MEMORY ARCHITECTURE	10/24/2002	10/281,078	12/27/2005	6,981,107	US
NMXS-0772.00/US	Utility - ORG	Issued	ARCHITECTURE OF A PHASE-CHANGE NONVOLATILE MEMORY ARRAY	12/12/2002	10/319,439	11/9/2004	6,816,404	US
NMXS-0773.00/US	Utility - ORG	Issued	MEMORY SYSTEM COMPRISING A SEMICONDUCTOR MEMORY	12/12/2003	10/735,250	5/22/2007	7,221,602	US
NMXS-0774.00/US	Utility - ORG	Issued	SELF-REPAIR METHOD FOR NONVOLATILE MEMORY DEVICES WITH ERASING/PROGRAMMING FAILURE, AND RELATIVE NONVOLATILE MEMORY DEVICE	5/15/2003	10/440,043	9/13/2005	6,944,072	US
NMXS-0775.00/US	Utility - ORG	Issued	INTEGRATED RESISTOR, PHASE-CHANGE MEMORY ELEMENT INCLUDING THIS RESISTOR, AND PROCESS FOR THE FABRICATION THEREOF	1/14/2003	10/345,129	9/20/2005	6,946,673	US
NMXS-0776.00/US	Utility - ORG	Issued	METHOD FOR AVOIDING THE EFFECTS OF LACK OF UNIFORMITY IN TRENCH ISOLATED INTEGRATED CIRCUITS	11/29/2001	09/997,227	7/1/2003	6,586,313	US
NMXS-0777.00/US	Utility - ORG	Issued	SMALL AREA CONTACT REGION, HIGH EFFICIENCY PHASE CHANGE MEMORY CELL AND FABRICATION METHOD THEREOF	12/5/2002	10/313,991	6/5/2007	7,227,171	US
NMXS-0778.00/US	Utility - ORG	Issued	PROGRAMMING METHOD OF THE MEMORY CELLS IN A MULTILEVEL NON-VOLATILE MEMORY DEVICE	5/13/2003	10/438,175	7/19/2005	6,920,066	US
NMXS-0779.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE	3/14/2003	10/390,556	12/12/2006	7,149,844	US
NMXS-0780.00/US	Utility - CIP	Issued	PHASE CHANGE MEMORY CELL AND MANUFACTURING METHOD THEREOF USING MINITRENCHES	2/20/2003	10/372,761	5/10/2005	6,891,747	US
NMXS-0780.01/US	Utility - DIV	Issued	PHASE CHANGE MEMORY CELL AND MANUFACTURING METHOD THEREOF USING MINITRENCHES	1/27/2005	11/045,170	8/9/2011	7,993,957	US
NMXS-0782.00/US	Utility - CIP	Issued	SUBLITHOGRAPHIC CONTACT STRUCTURE, PHASE CHANGE MEMORY CELL WITH OPTIMIZED HEATER SHAPE, AND MANUFACTURING METHOD THEREOF	2/20/2003	10/371,154	12/6/2005	6,972,430	US
NMXS-0782.01/US	Utility - DIV	Issued	SUBLITHOGRAPHIC CONTACT STRUCTURE, PHASE CHANGE MEMORY CELL WITH OPTIMIZED HEATER SHAPE, AND MANUFACTURING METHOD THEREOF	10/24/2005	11/258,340	5/13/2008	7,372,166	US
NMXS-0783.00/US	Utility - CIP	Issued	CONTACT STRUCTURE, PHASE CHANGE MEMORY CELL, AND MANUFACTURING METHOD THEREOF WITH ELIMINATION OF DOUBLE CONTACTS	2/20/2003	10/372,639	8/16/2005	6,930,913	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0783.01/US	Utility - DIV	Issued	MANUFACTURING METHOD OF A CONTACT STRUCTURE AND PHASE CHANGE MEMORY CELL WITH ELIMINATION OF DOUBLE CONTACTS	6/20/2005	11/156,989	7/22/2008	7,402,455	US
NMXS-0784.00/US	Utility - ORG	Issued	BASIC STAGE FOR A CHARGE PUMP CIRCUIT	3/28/2003	10/402,852	2/21/2006	7,002,399	US
NMXS-0785.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY CELL	6/25/2003	10/606,164	4/5/2005	6,876,033	US
NMXS-0786.00/US	Utility - ORG	Issued	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	12/26/2002	10/331,161	8/8/2006	7,088,614	US
NMXS-0787.00/US	Utility - ORG	Issued	ADJUSTABLE FREQUENCY OSCILLATOR WITH PROGRAMMABLE CALIBRATING CIRCUIT AND RELATED SYSTEM AND METHOD	4/2/2003	10/406,628	4/19/2005	6,882,231	US
NMXS-0788.00/US	Utility - ORG	Issued	TEST STRUCTURE FOR THE MEASUREMENT OF CONTACT TO GATE DISTANCE IN NON-VOLATILE MEMORY DEVICES AND CORRESPONDING TEST METHOD	5/30/2003	10/449,761	6/7/2005	6,903,995	US
NMXS-0789.00/US	Utility - ORG	Abandoned	METHOD FOR MANUFACTURING ELECTRONIC CIRCUITS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE	5/1/2003	10/428,338	2/14/2006	6,998,348	US
NMXS-0790.00/US	Utility - ORG	Issued	SINGLE ENDED OUTPUT SENSE AMPLIFIER CIRCUIT WITH REDUCED POWER CONSUMPTION AND NOISE	7/8/2002	10/190,917	11/23/2004	6,822,919	US
NMXS-0791.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUIT AND METHOD FOR SEMICONDUCTOR MEMORY DEVICES	5/31/2002	10/161,501	5/4/2004	6,731,550	US
NMXS-0792.00/US	Utility - ORG	Issued	CIRCUIT FOR CONTROLLING A REFERENCE NODE IN A SENSE AMPLIFIER	12/27/2002	10/331,147	10/5/2004	6,801,466	US
NMXS-0793.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE WITH DOUBLE SERIAL/PARALLEL COMMUNICATION INTERFACE	10/15/2002	10/271,352	5/10/2005	6,892,269	US
NMXS-0794.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH SELF-REFRESH CAPABILITY	3/11/2003	10/387,141	5/8/2007	7,216,212	US
NMXS-0796.00/US	Utility - ORG	Issued	MEMORY DEVICE OUTPUTTING READ DATA IN A TIME STARTING FROM A RISING EDGE OF AN EXTERNAL CLOCK THAT IS SHORTER THAN THAT OF KNOWN DEVICES	12/19/2002	10/325,486	1/24/2006	6,990,596	US
NMXS-0797.00/US	Utility - ORG	Issued	DEVICE INTEGRATING A NONVOLATILE MEMORY ARRAY AND A VOLATILE MEMORY ARRAY	2/7/2003	10/360,840	5/23/2006	7,050,322	US
NMXS-0798.00/US	Utility - ORG	Issued	METHOD OF WRITING A GROUP OF DATA BYTES IN A MEMORY AND MEMORY DEVICE	2/21/2003	10/371,221	2/7/2006	6,996,697	US
NMXS-0799.00/US	Utility - ORG	Issued	NONVOLATILE STORAGE DEVICE AND SELF-REDUNDANCY METHOD FOR THE SAME	8/11/2003	10/639,240	4/8/2008	7,355,908	US
NMXS-0800.00/US	Utility - ORG	Issued	BUILT-IN TESTING METHODOLOGY IN FLASH MEMORY	2/27/2004	10/789,443	5/23/2006	7,050,343	US
NMXS-0801.00/US	Utility - ORG	Issued	SECTOR PROTECTION CIRCUIT AND METHOD FOR FLASH MEMORY DEVICES	8/2/2001	09/922,043	12/3/2002	6,490,197	US
NMXS-0802.00/US	Utility - ORG	Issued	DUAL BANK FLASH MEMORY DEVICE AND METHOD	8/2/2001	09/922,044	4/22/2003	6,552,935	US
NMXS-0803.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUIT AND METHOD FOR FLASH MEMORY DEVICES	8/2/2001	09/922,068	5/13/2003	6,563,732	US
NMXS-0804.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUIT AND METHOD FOR REPLACING DEFECTIVE MEMORY CELLS IN A FLASH MEMORY DEVICE	8/2/2001	09/922,176	7/15/2003	6,594,177	US
NMXS-0805.00/US	Utility - ORG	Issued	CIRCUIT AND ASSOCIATED METHOD FOR THE ERASURE OR PROGRAMMING OF A MEMORY CELL	3/11/2002	10/096,531	9/16/2003	6,621,737	US
NMXS-0806.00/US	Utility - ORG	Issued	EEPROM MEMORY COMPRISING MEANS FOR SIMULTANEOUS READING OF SPECIAL BITS OF A FIRST AND SECOND TYPE	10/21/2002	10/277,183	5/18/2004	6,738,286	US
NMXS-0807.00/US	Utility - ORG	Issued	EEPROM MEMORY PROTECTED AGAINST THE EFFECTS FROM A BREAKDOWN OF AN ACCESS TRANSISTOR	6/24/2002	10/178,796	8/23/2005	6,934,192	US
NMXS-0808.00/US	Utility - ORG	Issued	READ AMPLIFIER WITH A LOW CURRENT CONSUMPTION DIFFERENTIAL OUTPUT STAGE	11/19/2002	10/299,965	7/6/2004	6,760,265	US
NMXS-0809.00/US	Utility - ORG	Issued	METHOD FOR ERASING NON-VOLATILE MEMORY CELLS AND CORRESPONDING MEMORY DEVICE	9/30/2003	10/675,221	2/27/2007	7,184,319	US
NMXS-0810.00/US	Utility - ORG	Issued	NON-VOLATILE LATCH CIRCUIT	5/7/2003	10/434,395	4/18/2006	7,031,189	US
NMXS-0811.00/US	Utility - ORG	Issued	METHOD FOR REDUCING SPURIOUS ERASING DURING PROGRAMMING OF A NONVOLATILE NROM	4/29/2003	10/426,924	11/30/2004	6,826,083	US
NMXS-0812.00/US	Utility - CIP	Issued	METHOD FOR POST ETCH CLEANS	5/1/2002	10/137,096	6/24/2008	7,390,755	US

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NMXS-0813.00/US	Utility - ORG	Issued	REGULATION METHOD FOR THE SOURCE TERMINAL VOLTAGE IN A NON-VOLATILE MEMORY CELL DURING A PROGRAM PHASE AND CORRESPONDING PROGRAM CIRCUIT	12/27/2002	10/331,106	11/23/2004	6,822,905	US
NMXS-0815.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT STRUCTURES	7/30/2003	10/631,463	1/16/2007	7,163,898	US
NMXS-0816.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR WITH VERY QUICK RESPONSE	6/26/2003	10/608,998	6/21/2005	6,909,264	US
NMXS-0817.00/US	Utility - ORG	Issued	ARRAY OF CELLS INCLUDING A SELECTION BIPOLAR TRANSISTOR AND FABRICATION METHOD THEREOF	10/7/2003	10/680,727	11/14/2006	7,135,756	US
NMXS-0817.01/US	Utility - DIV	Issued	ARRAY OF CELLS INCLUDING A SELECTION BIPOLAR TRANSISTOR AND FABRICATION METHOD THEREOF	10/19/2006	11/551,170	11/4/2008	7,446,011	US
NMXS-0818.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING AN ARRAY OF CELLS INCLUDING SELECTION BIPOLAR JUNCTION TRANSISTORS	10/7/2003	10/680,721	1/24/2006	6,989,580	US
NMXS-0818.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING AN ARRAY OF CELLS INCLUDING SELECTION BIPOLAR JUNCTION TRANSISTORS	11/1/2005	11/264,084	7/21/2009	7,563,684	US
NMXS-0819.00/US	Utility - ORG	Issued	SELF-REPAIR METHOD VIA ECC FOR NONVOLATILE MEMORY DEVICES, AND RELATIVE NONVOLATILE MEMORY DEVICE	4/15/2003	10/417,416	5/31/2005	6,901,011	US
NMXS-0820.00/US	Utility - ORG	Issued	SELF-REPAIR METHOD FOR NONVOLATILE MEMORY DEVICES USING A SUPERSECURE ARCHITECTURE, AND NONVOLATILE MEMORY DEVICE	4/24/2003	10/423,845	7/26/2005	6,922,366	US
NMXS-0821.00/US	Utility - ORG	Issued	LINE SELECTOR FOR A MATRIX OF MEMORY ELEMENTS	7/8/2003	10/616,414	8/16/2005	6,930,927	US
NMXS-0822.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR A HIGH VOLTAGE TRANSISTOR INTEGRATED ON A SEMICONDUCTOR SUBSTRATE WITH NON-VOLATILE MEMORY CELLS AND CORRESPONDING TRANSISTOR	9/29/2003	10/675,245	9/27/2005	6,949,803	US
NMXS-0823.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING A BYTE SELECTION TRANSISTOR FOR A MATRIX OF NON VOLATILE MEMORY CELLS AND CORRESPONDING STRUCTURE	11/18/2003	10/715,887	12/6/2005	6,972,454	US
NMXS-0823.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING A BYTE SELECTION TRANSISTOR FOR A MATRIX OF NON VOLATILE MEMORY CELLS AND CORRESPONDING STRUCTURE	10/25/2005	11/258,675	11/25/2008	7,456,467	US
NMXS-0824.00/US	Utility - ORG	Issued	ION-IMPLANTATION MACHINE, CONTROL METHOD THEREOF, AND PROCESS FOR MANUFACTURING INTEGRATED DEVICES	9/8/2003	10/657,801	6/13/2006	7,060,995	US
NMXS-0826.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR PROGRAMMING AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE SEMICONDUCTOR MEMORY	12/5/2003	10/729,875	4/18/2006	7,031,193	US
NMXS-0827.00/US	Utility - ORG	Issued	CIRCUIT FOR PROGRAMMING A NON-VOLATILE MEMORY DEVICE WITH ADAPTIVE PROGRAM LOAD CONTROL	11/12/2003	10/706,306	10/18/2005	6,956,773	US
NMXS-0828.00/US	Utility - ORG	Issued	SINGLE CELL ERASING METHOD FOR RECOVERING MEMORY CELLS UNDER PROGRAMMING DISTURBS IN NON VOLATILE SEMICONDUCTOR MEMORY DEVICES	11/26/2003	10/724,022	9/13/2005	6,944,061	US
NMXS-0829.00/US	Utility - ORG	Issued	CIRCUIT FOR BIASING AN INPUT NODE OF A SENSE AMPLIFIER WITH A PRE-CHARGE STAGE	9/16/2003	10/664,606	2/15/2005	6,856,547	US
NMXS-0830.00/US	Utility - ORG	Issued	INTEGRATED RESISTIVE ELEMENTS WITH SILICIDATION PROTECTION	9/26/2003	10/672,293	2/13/2007	7,176,553	US
NMXS-0830.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING INTEGRATED RESISTIVE ELEMENTS WITH SILICIDATION PROTECTION	1/30/2006	11/343,593	7/28/2009	7,566,610	US
NMXS-0831.00/US	Utility - ORG	Issued	SELF-ALIGNED INTEGRATED ELECTRONIC DEVICES	11/14/2003	10/713,538	12/23/2008	7,468,535	US
NMXS-0831.01/US	Utility - DIV	Issued	PROCESS FOR SELF-ALIGNED MANUFACTURE OF INTEGRATED ELECTRONIC DEVICES	1/4/2008	12/006,706	8/10/2010	7,772,084	US
NMXS-0832.00/US	Utility - ORG	Issued	VOLTAGE BOOST DEVICE AND MEMORY SYSTEM	7/7/2003	10/614,693	12/28/2004	6,836,442	US
NMXS-0833.00/US	Utility - ORG	Issued	METHOD OF PROGRAMMING A MULTI-LEVEL, ELECTRICALLY PROGRAMMABLE NON-VOLATILE SEMICONDUCTOR MEMORY	2/19/2004	10/782,725	9/19/2006	7,110,300	US
NMXS-0834.00/US	Utility - ORG	Issued	STRUCTURE AND METHOD FOR DETECTING ERRORS IN A MULTILEVEL MEMORY DEVICE WITH IMPROVED PROGRAMMING GRANULARITY	12/18/2003	10/741,815	1/13/2009	7,478,292	US

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NMXS-0834.01/US	Utility - DIV	Issued	STRUCTURE AND METHOD FOR DETECTING ERRORS IN A MULTILEVEL MEMORY DEVICE WITH IMPROVED PROGRAMMING GRANULARITY	10/14/2008	12/251,289	6/1/2010	7,730,383	US
NMXS-0835.00/US	Utility - ORG	Issued	METHOD FOR CONTROLLING PROGRAMMING VOLTAGE LEVELS OF NON-VOLATILE MEMORY CELLS, THE METHOD TRACKING THE CELL FEATURES, AND CORRESPONDING VOLTAGE REGULATOR	8/28/2003	10/651,019	11/22/2005	6,967,876	US
NMXS-0836.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY SYSTEM INCLUDING SELECTION TRANSISTORS	12/24/2003	10/746,555	4/4/2006	7,023,728	US
NMXS-0837.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR TIMING RANDOM READING OF A MEMORY DEVICE	11/3/2003	10/700,322	10/18/2005	6,956,787	US
NMXS-0838.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH IMPROVED SEQUENTIAL PROGRAMMING SPEED	12/18/2003	10/739,928	9/6/2005	6,940,756	US
NMXS-0839.00/US	Utility - ORG	Issued	MEMORY DEVICE COMPOSED OF A PLURALITY OF MEMORY CHIPS IN A SINGLE PACKAGE	12/3/2003	10/727,150	9/27/2005	6,950,324	US
NMXS-0840.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE WITH SIMULTANEOUS READ/WRITE	11/21/2003	10/719,650	9/27/2005	6,950,337	US
NMXS-0842.00/US	Utility - ORG	Issued	NON VOLATILE MEMORY DEVICE INCLUDING A PREDETERMINED NUMBER OF SECTORS	12/30/2003	10/748,696	4/25/2006	7,035,142	US
NMXS-0843.00/US	Utility - ORG	Issued	PROGRAMMING METHOD FOR NON VOLATILE MULTILEVEL MEMORY CELLS AND CORRESPONDING PROGRAMMING CIRCUIT	11/26/2003	10/724,023	8/23/2005	6,934,185	US
NMXS-0844.00/US	Utility - ORG	Issued	STABILIZATION METHOD FOR DRAIN VOLTAGE IN NON-VOLATILE MULTI-LEVEL MEMORY CELLS AND RELATED MEMORY DEVICE	12/30/2003	10/748,701	4/18/2006	7,031,191	US
NMXS-0845.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH ACCESS PROTECTION SCHEME	2/18/2004	10/781,974	7/24/2007	7,249,231	US
NMXS-0849.00/US	Utility - ORG	Issued	ANALYSIS OF THE QUALITY OF CONTACTS AND VIAS IN MULTI-METAL FABRICATION PROCESSES OF SEMICONDUCTOR DEVICES, METHOD AND TEST CHIP ARCHITECTURE	5/21/2004	10/850,834	12/4/2007	7,304,485	US
NMXS-0850.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING NON-VOLATILE MEMORY CELLS ON A SEMICONDUCTIVE SUBSTRATE	12/29/2003	10/749,020	10/24/2006	7,125,808	US
NMXS-0851.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING NON-VOLATILE MEMORY CELLS ON A SEMICONDUCTOR SUBSTRATE	12/23/2003	10/746,878	10/24/2006	7,125,807	US
NMXS-0853.00/US	Utility - ORG	Issued	METHOD FOR DETECTING A RESISTIVE PATH OR A PREDETERMINED POTENTIAL IN NON-VOLATILE MEMORY ELECTRONIC DEVICES	9/30/2003	10/675,805	9/20/2005	6,947,329	US
NMXS-0854.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY CELL SENSING CIRCUIT, PARTICULARLY FOR LOW POWER SUPPLY VOLTAGES AND HIGH CAPACITIVE LOAD VALUES	12/4/2003	10/728,372	5/17/2005	6,894,934	US
NMXS-0855.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH EMBEDDED DRAM	11/20/2003	10/720,013	4/11/2006	7,027,317	US
NMXS-0857.00/US	Utility - ORG	Issued	REDUNDANCY SCHEME FOR A MEMORY INTEGRATED CIRCUIT	7/16/2004	10/893,760	12/26/2006	7,154,803	US
NMXS-0858.00/US	Utility - ORG	Issued	FULL-SWING WORDLINE DRIVING CIRCUIT	4/29/2004	10/835,538	4/4/2006	7,023,738	US
NMXS-0859.00/US	Utility - ORG	Issued	METHOD FOR GENERATING A REFERENCE CURRENT FOR SENSE AMPLIFIERS AND CORRESPONDING GENERATOR	6/4/2004	10/861,340	2/28/2006	7,006,025	US
NMXS-0860.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR PROGRAMMING AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE SEMICONDUCTOR MEMORY	12/5/2003	10/729,829	6/27/2006	7,068,540	US
NMXS-0861.00/US	Utility - ORG	Issued	HIGH-DENSITY PLASMA PROCESS FOR DEPOSITING A LAYER OF SILICON NITRIDE	10/14/2003	10/686,556	10/11/2005	6,953,609	US
NMXS-0862.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY CELLS	2/12/2004	10/777,457	3/24/2009	7,508,716	US
NMXS-0862.01/US	Utility - CON	Issued	SENSE AMPLIFIER FOR LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY CELLS	2/9/2009	12/368,271	2/8/2011	7,885,116	US
NMXS-0863.00/US	Utility - ORG	Issued	VOLTAGE SUPPLY DISTRIBUTION ARCHITECTURE FOR A PLURALITY OF MEMORY MODULES	12/30/2003	10/748,697	5/24/2005	6,897,710	US
NMXS-0864.00/US	Utility - ORG	Issued	NONLITHOGRAPHIC METHOD OF DEFINING GEOMETRIES FOR PLASMA AND/OR ION IMPLANTATION TREATMENTS ON A SEMICONDUCTOR WAFER	3/25/2005	11/089,942	10/30/2007	7,288,008	US
NMXS-0865.00/US	Utility - ORG	Issued	PARALLEL SENSE AMPLIFIER WITH MIRRORING OF THE CURRENT TO BE MEASURED INTO EACH REFERENCE BRANCH	1/20/2004	10/762,195	7/11/2006	7,075,844	US
NMXS-0866.00/US	Utility - ORG	Issued	METHOD FOR THE FABRICATION OF ISOLATION STRUCTURES	8/2/2004	10/909,749	5/8/2007	7,214,596	US
NMXS-0869.00/US	Utility - ORG	Issued	MOS DEVICE AND PROCESS FOR MANUFACTURING MOS DEVICES USING DUAL-POLYSILICON LAYER TECHNOLOGY	12/23/2003	10/745,295	4/4/2006	7,023,047	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0870.00/US	Utility - ORG	Issued	MOS DEVICE AND A PROCESS FOR MANUFACTURING MOS DEVICES USING A DUAL-POLYSILICON LAYER TECHNOLOGY WITH SIDE CONTACT	12/23/2003	10/745,297	8/15/2006	7,091,570	US
NMXS-0871.00/US	Utility - ORG	Issued	FAST READING, LOW CONSUMPTION MEMORY DEVICE AND READING METHOD THEREOF	12/20/2004	11/018,550	4/10/2007	7,203,087	US
NMXS-0872.00/US	Utility - ORG	Issued	METHOD FOR READING A NONVOLATILE MEMORY DEVICE AND NONVOLATILE MEMORY DEVICE IMPLEMENTING THE READING METHOD	4/8/2004	10/820,458	5/30/2006	7,054,197	US
NMXS-0873.00/US	Utility - ORG	Issued	TESTING METHOD AND DEVICE FOR NON-VOLATILE MEMORIES HAVING A LPC (LOW PIN COUNT) COMMUNICATION SERIAL INTERFACE	5/28/2003	10/447,293	8/31/2004	6,785,174	US
NMXS-0874.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR READING NON-VOLATILE MEMORIES HAVING AT LEAST ONE PSEUDO-PARALLEL COMMUNICATION INTERFACE	5/30/2003	10/452,762	12/13/2005	6,975,559	US
NMXS-0875.00/US	Utility - ORG	Issued	AUTOMATIC DECODING METHOD FOR MAPPING AND SELECTING A NON-VOLATILE MEMORY DEVICE HAVING A LPC SERIAL COMMUNICATION INTERFACE IN THE AVAILABLE ADDRESSING AREA ON MOTHERBOARDS	7/18/2003	10/623,474	6/12/2007	7,231,487	US
NMXS-0876.00/US	Utility - ORG	Issued	STRUCTURE FOR UPDATING A BLOCK OF MEMORY CELLS IN A FLASH MEMORY DEVICE WITH ERASE AND PROGRAM OPERATION REDUCTION	10/15/2003	10/686,552	7/26/2005	6,922,362	US
NMXS-0877.00/US	Utility - ORG	Issued	VOLTAGE REGULATION SYSTEM FOR A MULTIWORD PROGRAMMING OF A LOW INTEGRATION AREA NON VOLATILE MEMORY	2/26/2004	10/789,351	11/21/2006	7,139,197	US
NMXS-0878.00/US	Utility - ORG	Issued	GATE VOLTAGE REGULATION SYSTEM FOR A NON VOLATILE MEMORY CELLS PROGRAMMING AND/OR SOFT PROGRAMMING PHASE	2/26/2004	10/788,525	8/1/2006	7,085,163	US
NMXS-0879.00/US	Utility - ORG	Issued	METHOD FOR SOFT-PROGRAMMING AN ELECTRICALLY ERASABLE NONVOLATILE MEMORY DEVICE, AND AN ELECTRICALLY ERASABLE NONVOLATILE MEMORY DEVICE IMPLEMENTING THE SOFT-PROGRAMMING METHOD	2/17/2004	10/779,856	10/10/2006	7,120,062	US
NMXS-0880.00/US	Utility - ORG	Issued	MEMORY DEVICE ACCESSIBLE WITH DIFFERENT COMMUNICATION PROTOCOLS	9/2/2003	10/653,459	8/9/2005	6,927,991	US
NMXS-0881.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE ARCHITECTURE, FOR INSTANCE A FLASH KIND, HAVING A SERIAL COMMUNICATION INTERFACE	11/26/2003	10/727,341	12/19/2006	7,151,705	US
NMXS-0882.00/US	Utility - ORG	Issued	FAST PAGE PROGRAMMING ARCHITECTURE AND METHOD IN A NON-VOLATILE MEMORY DEVICE WITH AN SPI INTERFACE	12/30/2003	10/748,447	4/26/2005	6,885,584	US
NMXS-0883.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY COMPRISING AN INTERNAL SUPPLY VOLTAGE MANAGEMENT DEVICE	4/22/2003	10/420,533	12/7/2004	6,829,169	US
NMXS-0884.00/US	Utility - ORG	Issued	PROGRAMMABLE POR CIRCUIT WITH TWO SWITCHING THRESHOLDS	8/14/2003	10/641,337	5/24/2005	6,897,689	US
NMXS-0885.00/US	Utility - NSPCT	Issued	EEPROM MEMORY COMPRISING A NON-VOLATILE REGISTER INTEGRATED INTO THE MEMORY ARRAY THEREOF	8/21/2003	11/067,345	11/21/2006	7,139,195	US
NMXS-0886.00/US	Utility - ORG	Issued	DOUBLE READ STAGE SENSE AMPLIFIER	4/1/2004	10/816,204	4/18/2006	7,031,212	US
NMXS-0887.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR A FLASH MEMORY AND FLASH MEMORY THUS PRODUCED	1/22/2004	10/763,044	2/27/2007	7,183,160	US
NMXS-0888.00/US	Utility - ORG	Issued	FLASH MEMORY COMPRISING AN ERASE VERIFY ALGORITHM INTEGRATED INTO A PROGRAMMING ALGORITHM	2/26/2004	10/789,449	5/10/2005	6,891,756	US
NMXS-0889.00/US	Utility - ORG	Issued	NEGATIVE VOLTAGE WORD LINE DECODER HAVING COMPACT TERMINATING ELEMENTS	1/20/2004	10/760,631	5/16/2006	7,046,577	US
NMXS-0890.00/US	Utility - ORG	Issued	SECTORED FLASH MEMORY COMPRISING MEANS FOR CONTROLLING AND FOR REFRESHING MEMORY CELLS	2/9/2004	10/775,032	11/15/2005	6,965,526	US
NMXS-0891.00/US	Utility - ORG	Issued	METHOD FOR ERASING/PROGRAMMING A NON-VOLATILE ELECTRICALLY ERASABLE MEMORY	7/31/2004	10/903,927	3/14/2006	7,012,837	US
NMXS-0892.00/US	Utility - ORG	Issued	WORD-PROGRAMMABLE FLASH MEMORY	6/14/2004	10/867,381	7/18/2006	7,079,448	US
NMXS-0893.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING DIFFERENTIAL ISOLATION STRUCTURES IN A SEMICONDUCTOR ELECTRONIC DEVICE AND CORRESPONDING STRUCTURE	7/12/2004	10/890,529	10/26/2010	7,820,504	US
NMXS-0894.00/US	Utility - ORG	Issued	CHARGE PUMP CIRCUIT WITH A BRIEF SETTLING TIME AND HIGH OUTPUT VOLTAGE REGULATION PRECISION	11/5/2004	10/982,528	2/13/2007	7,176,748	US

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NMXS-0895.00/US	Utility - ORG	Issued	METHOD FOR REDUCING NON-UNIFORMITY OR TOPOGRAPHY VARIATION BETWEEN AN ARRAY AND CIRCUITRY IN A PROCESS FOR MANUFACTURING SEMICONDUCTOR INTEGRATED NON-VOLATILE MEMORY DEVICES	2/16/2005	11/059,294	3/20/2007	7,192,820	US
NMXS-0896.00/US	Utility - ORG	Issued	EXPLOITING A STATISTICAL DISTRIBUTION OF THE VALUES OF AN ELECTRICAL CHARACTERISTIC IN A POPULATION OF AUXILIARY MEMORY CELLS FOR OBTAINING REFERENCE CELLS	12/23/2005	11/318,053	12/8/2009	7,630,263	US
NMXS-0897.00/US	Utility - ORG	Issued	SENSE AMPLIFIER WITH EQUALIZER	8/6/2004	10/913,788	11/14/2006	7,136,305	US
NMXS-0898.00/US	Utility - ORG	Issued	METHOD FOR REDUCING DEFECTS AFTER A METAL ETCHING IN SEMICONDUCTOR DEVICES	12/10/2004	11/009,687	10/30/2007	7,288,427	US
NMXS-0899.00/US	Utility - ORG	Issued	METHOD FOR PERFORMING ERROR CORRECTIONS OF DIGITAL INFORMATION CODIFIED AS A SYMBOL SEQUENCE	3/19/2004	10/805,168	2/5/2008	7,328,397	US
NMXS-0900.00/US	Utility - ORG	Issued	INTEGRATED MEMORY SYSTEM	3/19/2004	10/805,182	6/1/2010	7,730,357	US
NMXS-0901.00/US	Utility - ORG	Issued	PHASE CHANGE MEMORY DEVICE WITH BIASING OF DESELECTED BIT LINES	8/25/2004	10/926,784	8/15/2006	7,092,277	US
NMXS-0902.00/US	Utility - ORG	Issued	MANUFACTURING METHOD FOR NON-ACTIVE ELECTRICALLY STRUCTURES IN ORDER TO OPTIMIZE THE DEFINITION OF ACTIVE ELECTRICALLY STRUCTURES IN AN ELECTRONIC CIRCUIT INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT	8/3/2004	10/911,220	2/21/2006	7,001,800	US
NMXS-0902.01/US	Utility - DIV	Issued	MANUFACTURING METHOD FOR NON-ACTIVE ELECTRICALLY STRUCTURES IN ORDER TO OPTIMIZE THE DEFINITION OF ACTIVE ELECTRICALLY STRUCTURES IN AN ELECTRONIC CIRCUIT INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT	1/19/2006	11/334,988	1/22/2008	7,320,904	US
NMXS-0903.00/US	Utility - ORG	Issued	SEALING METHOD FOR ELECTRONIC DEVICES FORMED ON A COMMON SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT STRUCTURE	10/22/2004	10/971,774	7/18/2006	7,078,294	US
NMXS-0904.00/US	Utility - ORG	Issued	SENSING CIRCUIT FOR A SEMICONDUCTOR MEMORY	8/6/2004	10/913,128	9/18/2007	7,272,059	US
NMXS-0905.00/US	Utility - ORG	Issued	BIT LINE DISCHARGE CONTROL METHOD AND CIRCUIT FOR A SEMICONDUCTOR MEMORY	10/22/2004	10/971,776	8/14/2007	7,257,039	US
NMXS-0906.00/US	Utility - ORG	Issued	SENSING CIRCUIT FOR A SEMICONDUCTOR MEMORY	8/1/2005	11/194,739	2/27/2007	7,184,348	US
NMXS-0908.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING NON-VOLATILE MEMORY DEVICES INTEGRATED IN A SEMICONDUCTOR SUBSTRATE	12/27/2005	11/319,798	9/2/2008	7,419,876	US
NMXS-0910.00/US	Utility - ORG	Issued	DISTRIBUTION OF AN ELECTRIC QUANTITY THROUGH A CIRCUIT	7/20/2005	11/185,905	4/21/2009	7,521,989	US
NMXS-0912.00/US	Utility - ORG	Issued	PAGE BUFFER CIRCUIT AND METHOD FOR A PROGRAMMABLE MEMORY DEVICE	6/24/2005	11/166,354	7/28/2009	7,567,456	US
NMXS-0913.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH INFORMATION LOSS SELF-DETECT CAPABILITY	5/1/2006	11/415,879	8/19/2008	7,414,902	US
NMXS-0914.00/US	Utility - ORG	Issued	NONVOLATILE PHASE CHANGE MEMORY DEVICE AND BIASING METHOD THEREFOR	8/2/2005	11/195,359	9/11/2007	7,269,080	US
NMXS-0916.00/US	Utility - ORG	Issued	PHASE CHANGE MEMORY CELL WITH JUNCTION SELECTOR AND MANUFACTURING METHOD THEREOF	12/19/2005	11/312,253	10/14/2008	7,436,692	US
NMXS-0917.00/US	Utility - ORG	Issued	READ/VERIFY CIRCUIT FOR MULTILEVEL MEMORY CELLS WITH RAMP READ VOLTAGE, AND READ/VERIFY METHOD THEREOF	7/8/2005	11/178,240	7/8/2008	7,397,702	US
NMXS-0919.00/US	Utility - ORG	Issued	METHOD OF MAKING A FLOATING GATE NON-VOLATILE MOS SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CAPACITIVE COUPLING AND DEVICE THUS OBTAINED	12/22/2005	11/317,641	8/30/2011	8,008,701	US
NMXS-0920.00/US	Utility - ORG	Issued	PROGRAMMABLE NAND MEMORY	7/14/2005	11/183,229	6/8/2010	7,733,697	US
NMXS-0921.00/US	Utility - ORG	Issued	DATA CONTROL UNIT CAPABLE OF CORRECTING BOOT ERRORS, AND CORRESPONDING SELF-CORRECTION METHOD	6/9/2005	11/149,948	10/28/2008	7,444,543	US
NMXS-0923.00/US	Utility - ORG	Issued	TRIMMING FUNCTIONAL PARAMETERS IN INTEGRATED CIRCUITS	4/25/2005	11/113,818	5/22/2007	7,221,212	US
NMXS-0924.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC NON-VOLATILE MEMORY DEVICES INTEGRATED IN A SEMICONDUCTOR SUBSTRATE	12/27/2005	11/319,750	2/5/2008	7,326,615	US

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NMXS-0926.00/US	Utility - ORG	Issued	FLASH MEMORY DEVICE WITH NAND ARCHITECTURE WITH REDUCED CAPACITIVE COUPLING EFFECT	5/31/2006	11/445,491	7/1/2008	7,394,694	US
NMXS-0928.00/US	Utility - ORG	Issued	INTEGRATED ELECTRONIC DEVICE HAVING A LOW VOLTAGE ELECTRIC SUPPLY	4/11/2006	11/401,523	11/10/2009	7,616,515	US
NMXS-0929.00/US	Utility - ORG	Issued	REGULATOR CIRCUITRY AND METHOD	10/27/2003	10/695,294	6/20/2006	7,064,534	US
NMXS-0930.00/US	Utility - ORG	Issued	MAGNITUDE CONTENT ADDRESSABLE MEMORY	10/21/2003	10/690,368	6/12/2007	7,230,839	US
NMXS-0931.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR WITH STRESS MODE	10/21/2003	10/690,332	3/14/2006	7,012,417	US
NMXS-0932.00/US	Utility - ORG	Issued	USER RAM FLASH CLEAR	2/27/2004	10/788,581	2/6/2007	7,173,845	US
NMXS-0933.00/US	Utility - ORG	Issued	RESET INITIALIZATION	11/30/2004	10/999,751	4/24/2007	7,208,987	US
NMXS-0934.00/US	Utility - ORG	Issued	TAMPER MEMORY CELL	2/20/2004	10/783,935	5/29/2007	7,224,600	US
NMXS-0935.00/US	Utility - ORG	Issued	RESET RAMP CONTROL	12/15/2004	11/012,693	8/12/2008	7,411,433	US
NMXS-0936.00/US	Utility - ORG	Issued	ESD BONDING PAD	12/15/2004	11/013,123	2/19/2008	7,333,310	US
NMXS-0937.00/US	Utility - ORG	Issued	VOLTAGE TRANSLATING CONTROL STRUCTURE	12/15/2004	11/012,533	5/6/2008	7,368,947	US
NMXS-0938.00/US	Utility - ORG	Issued	RESET CIRCUIT	12/15/2004	11/012,712	10/2/2007	7,276,948	US
NMXS-0939.00/US	Utility - ORG	Abandoned	SENSE AMPLIFIER FOR READING A CELL OF A NON-VOLATILE MEMORY DEVICE	5/4/2005	11/121,616	1/23/2007	7,167,394	US
NMXS-0940.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR VERIFYING AND EVENTUALLY SUBSTITUTING DEFECTIVE REFERENCE CELLS OF A MEMORY	5/4/2005	11/121,615	2/13/2007	7,177,217	US
NMXS-0941.00/US	Utility - ORG	Issued	CIRCUIT FOR SELECTING/DESELECTING A BITLINE OF A NON-VOLATILE MEMORY	5/3/2005	11/120,766	8/7/2007	7,254,062	US
NMXS-0943.01/US	Utility - CON	Issued	METHOD FOR MANAGING BAD MEMORY BLOCKS IN A NONVOLATILE-MEMORY DEVICE, AND NONVOLATILE-MEMORY DEVICE IMPLEMENTING THE MANAGEMENT METHOD	2/19/2008	12/033,855	10/13/2009	7,603,593	US
NMXS-0944.00/US	Utility - ORG	Issued	ROW DECODER FOR NAND MEMORIES	8/11/2005	11/202,632	2/20/2007	7,180,786	US
NMXS-0945.00/US	Utility - ORG	Issued	METHOD OF GENERATING AN ENABLE SIGNAL OF A STANDARD MEMORY CORE AND RELATIVE MEMORY DEVICE	7/7/2004	10/886,003	4/14/2009	7,519,751	US
NMXS-0946.00/US	Utility - ORG	Issued	INTEGRATED MEMORY DEVICE WITH MULTI-SECTOR SELECTION COMMANDS	11/8/2004	10/984,372	11/25/2008	7,457,908	US
NMXS-0947.00/US	Utility - ORG	Issued	SYNCHRONOUS MEMORY DEVICE WITH REDUCED POWER CONSUMPTION	5/25/2005	11/138,884	4/29/2008	7,366,012	US
NMXS-0948.00/US	Utility - ORG	Issued	FLASH MEMORY COMPRISING MEANS FOR CHECKING AND REFRESHING MEMORY CELLS IN THE ERASED STATE	6/14/2004	10/867,378	5/23/2006	7,050,335	US
NMXS-0949.00/US	Utility - ORG	Issued	MEMORY CIRCUIT WITH NON-VOLATILE IDENTIFICATION MEMORY AND ASSOCIATED METHOD	8/18/2004	10/921,365	6/27/2006	7,068,538	US
NMXS-0950.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR A CONTINUOUS READ COMMAND IN AN EXTENDED MEMORY ARRAY	12/9/2004	11/008,586	2/12/2008	7,330,381	US
NMXS-0952.00/US	Utility - ORG	Issued	SERIAL MEMORY COMPRISING MEANS FOR PROTECTING AN EXTENDED MEMORY ARRAY DURING A WRITE OPERATION	12/9/2004	11/008,588	10/30/2007	7,290,078	US
NMXS-0952.01/US	Utility - DIV	Issued	SERIAL MEMORY COMPRISING MEANS FOR PROTECTING AN EXTENDED MEMORY ARRAY DURING A WRITE OPERATION	9/10/2007	11/852,937	9/7/2010	7,793,033	US
NMXS-0953.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH UNIPOLAR AND BIPOLAR SELECTORS	9/22/2005	11/233,464	1/27/2009	7,483,296	US
NMXS-0954.00/US	Utility - ORG	Issued	ELECTRONIC NON-VOLATILE MEMORY DEVICE HAVING A CNAND STRUCTURE AND BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/401,521	3/9/2010	7,675,788	US
NMXS-0957.00/US	Utility - ORG	Issued	MEMORY DEVICE	10/13/2005	11/249,763	3/27/2007	7,196,943	US
NMXS-0958.00/US	Utility - ORG	Issued	CHARGE-PUMP DEVICE WITH INCREASED CURRENT OUTPUT	11/9/2005	11/270,308	5/12/2009	7,532,060	US
NMXS-0959.00/US	Utility - ORG	Issued	INTEGRATED ELECTRONIC NON-VOLATILE MEMORY DEVICE HAVING NAND STRUCTURE	4/11/2006	11/279,384	11/13/2007	7,295,472	US
NMXS-0961.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/279,378	9/25/2007	7,274,594	US
NMXS-0963.00/US	Utility - ORG	Issued	PROGRAMMING METHOD OF MULTILEVEL MEMORIES AND CORRESPONDING CIRCUIT	10/27/2005	11/261,903	1/8/2008	7,317,637	US
NMXS-0964.00/US	Utility - ORG	Issued	MEMORY DEVICE	10/13/2005	11/250,176	4/1/2008	7,352,645	US
NMXS-0965.00/US	Utility - ORG	Issued	MEMORY DEVICE AND METHOD FOR OPERATING THE SAME WITH HIGH REJECTION OF THE NOISE ON THE HIGH-VOLTAGE SUPPLY LINE	9/30/2005	11/241,729	1/29/2008	7,324,379	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0966.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY DEVICE HAVING HIGH DENSITY NON-VOLATILE MEMORY CELLS AND A REDUCED CAPACITIVE INTERFERENCE CELL-TO-CELL	12/14/2005	11/300,053	1/15/2008	7,319,604	US
NMXS-0968.00/US	Utility - ORG	Issued	ROW DECODER CIRCUIT AND RELATED SYSTEM AND METHOD	5/15/2006	11/434,564	4/28/2009	7,525,851	US
NMXS-0969.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/279,381	7/8/2008	7,397,700	US
NMXS-0971.00/US	Utility - ORG	Issued	FLASH MEMORY DEVICE WITH REDUCED DRAIN STRESSES	2/6/2007	11/703,535	5/19/2009	7,535,770	US
NMXS-0972.00/US	Utility - ORG	Issued	REMOVABLE DATA STORAGE DEVICE AND RELATED ASSEMBLING METHOD	1/31/2006	11/344,519	2/2/2010	7,656,676	US
NMXS-0974.00/US	Utility - ORG	Issued	METHOD FOR CONFIGURING A VOLTAGE REGULATOR	11/16/2005	11/280,803	6/17/2008	7,388,793	US
NMXS-0974.01/US	Utility - DIV	Issued	VOLTAGE REGULATOR FOR THE PROGRAMMING CIRCUIT OF A MEMORY CELL	12/26/2007	11/964,250	4/27/2010	7,706,193	US
NMXS-0975.00/US	Utility - ORG	Issued	PROCESS FOR DIGGING A DEEP TRENCH IN A SEMICONDUCTOR BODY AND SEMICONDUCTOR BODY SO OBTAINED	12/28/2006	11/648,838	6/9/2009	7,544,620	US
NMXS-0976.00/US	Utility - ORG	Issued	METHOD AND SYSTEM FOR CONTROLLING MRAM WRITE CURRENT TO REDUCE POWER CONSUMPTION	3/31/2004	10/816,271	9/19/2006	7,110,289	US
NMXS-0977.00/US	Utility - ORG	Issued	MAGNETIC MEMORY CELL WITH PLURAL READ TRANSISTORS	10/31/2003	10/698,747	3/14/2006	7,012,832	US
NMXS-0978.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING A MEMORY DEVICE SUITABLE TO MINIMIZE THE LATERAL COUPLING EFFECTS BETWEEN MEMORY CELLS	2/6/2006	11/348,513	12/30/2008	7,471,571	US
NMXS-0979.00/US	Utility - ORG	Issued	DUAL RESISTANCE HEATER FOR PHASE CHANGE DEVICES AND MANUFACTURING METHOD THEREOF	12/19/2005	11/312,231	2/1/2011	7,880,123	US
NMXS-0983.00/US	Utility - ORG	Issued	HIGH-VOLTAGE SWITCH WITH LOW OUTPUT RIPPLE FOR NON-VOLATILE FLOATING-GATE MEMORIES	5/19/2006	11/437,405	4/21/2009	7,521,983	US
NMXS-0984.00/US	Utility - ORG	Issued	CHARGE-PUMP TYPE, VOLTAGE-BOOSTING DEVICE WITH REDUCED RIPPLE, IN PARTICULAR FOR NON-VOLATILE FLASH MEMORIES	5/19/2006	11/437,268	5/12/2009	7,532,061	US
NMXS-0985.00/US	Utility - ORG	Issued	PHASE CHANGE MEMORY CELL WITH TUBULAR HEATER AND MANUFACTURING METHOD THEREOF	4/6/2006	11/398,858	10/21/2008	7,439,536	US
NMXS-0986.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY DEVICE HAVING HIGH INTEGRATION DENSITY NON-VOLATILE MEMORY CELLS AND A REDUCED CAPACITIVE COUPLING	12/14/2005	11/300,145	9/22/2009	7,593,247	US
NMXS-0987.00/US	Utility - ORG	Issued	METHOD FOR ACCESSING IN READING, WRITING AND PROGRAMMING TO A NAND NON-VOLATILE MEMORY ELECTRONIC DEVICE MONOLITHICALLY INTEGRATED ON SEMICONCTOR	11/20/2006	11/561,799	5/5/2009	7,529,881	US
NMXS-0987.01/US	Utility - DIV	Issued	METHOD FOR ACCESSING IN READING, WRITING AND PROGRAMMING TO A NAND NON-VOLATILE MEMORY ELECTRONIC DEVICE MONOLITHICALLY INTEGRATED ON SEMICONCTOR	3/24/2009	12/409,740	1/19/2010	7,649,778	US
NMXS-0988.00/US	Utility - ORG	Issued	VERTICAL MOSFET TRANSISTOR, IN PARTICULAR OPERATING AS A SELECTOR IN NONVOLATILE MEMORY DEVICES	4/26/2006	11/411,982	12/7/2010	7,847,329	US
NMXS-0989.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY IMPLEMENTED WITH LOW-VOLTAGES TRANSISTORS AND RELATED SYSTEM AND METHOD	11/27/2006	11/605,209	3/3/2009	7,499,345	US
NMXS-0990.00/US	Utility - ORG	Issued	CONTROL OF VOLTAGES DURING ERASE AND RE-PROGRAM OPERATIONS OF MEMORY CELLS	1/18/2006	11/334,205	10/30/2007	7,289,368	US
NMXS-0991.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH A RAMP-LIKE VOLTAGE BIASING STRUCTURE BASED ON A CURRENT GENERATOR	1/26/2006	11/340,414	4/15/2008	7,359,246	US
NMXS-0992.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR RETRIEVING DATA STORED IN SEMICONDUCTOR MEMORY CELLS	5/31/2006	11/444,892	1/6/2009	7,474,577	US
NMXS-0992.01/US	Utility - DIV	Issued	CIRCUIT AND METHOD FOR RETRIEVING DATA STORED IN SEMICONDUCTOR MEMORY CELLS	10/9/2008	12/248,843	2/1/2011	7,889,586	US
NMXS-0993.00/US	Utility - ORG	Issued	DATA STORING METHOD FOR A NON-VOLATILE MEMORY CELL ARRAY HAVING AN ERROR CORRECTION CODE	4/25/2006	11/411,010	1/26/2010	7,653,863	US
NMXS-0995.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH A RAMP-LIKE VOLTAGE BIASING STRUCTURE AND REDUCED NUMBER OF REFERENCE CELLS	3/3/2006	11/368,363	6/30/2009	7,554,861	US
NMXS-0996.00/US	Utility - ORG	Issued	METHOD OF MANAGING FAILS IN A NON-VOLATILE MEMORY DEVICE AND RELATIVE MEMORY DEVICE	11/8/2006	11/557,786	8/4/2009	7,571,362	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-0997.00/US	Utility - NSPCT	Issued	METHOD FOR MULTILEVEL PROGRAMMING OF PHASE CHANGE MEMORY CELLS USING A PERCOLATION ALGORITHM	6/1/2006	11/949,598	12/29/2009	7,639,526	US
NMXS-0998.00/US	Utility - ORG	Issued	FLASH MEMORY DEVICE WITH IMPROVED MANAGEMENT OF PROTECTION INFORMATION	5/10/2006	11/432,301	7/20/2010	7,761,675	US
NMXS-0999.00/US	Utility - ORG	Issued	NAND FLASH MEMORY WITH ERASE VERIFY BASED ON SHORTER EVALUATION TIME	7/28/2006	11/495,886	4/22/2008	7,362,616	US
NMXS-1000.00/US	Utility - ORG	Issued	TRANSISTOR STRUCTURE WITH HIGH INPUT IMPEDANCE AND HIGH CURRENT CAPABILITY	11/27/2006	11/605,190	7/14/2009	7,560,782	US
NMXS-1001.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH TIME-SHIFTING BASED EMULATION OF REFERENCE CELLS	3/2/2006	11/367,707	3/18/2008	7,345,905	US
NMXS-1002.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING A MEMORY DEVICE SUITABLE TO MINIMIZE FLOATING GATE COUPLING AND MEMORY DEVICE	4/2/2007	11/732,486	3/30/2010	7,688,633	US
NMXS-1003.00/US	Utility - ORG	Issued	NON VOLATILE MEMORY DEVICE ARCHITECTURE AND CORRESPONDING PROGRAMMING METHOD	2/28/2007	11/713,081	1/19/2010	7,649,791	US
NMXS-1004.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH MULTIPLE REFERENCES AND CORRESPONDING CONTROL METHOD	7/27/2006	11/460,531	1/12/2010	7,646,644	US
NMXS-1006.00/US	Utility - ORG	Issued	RAMP GENERATOR AND RELATIVE ROW DECODER FOR FLASH MEMORY DEVICE	5/3/2006	11/381,426	1/22/2008	7,321,512	US
NMXS-1007.00/US	Utility - ORG	Issued	ROW SELECTOR WITH REDUCED AREA OCCUPATION FOR SEMICONDUCTOR MEMORY DEVICES	2/8/2007	11/672,857	6/1/2011	7,965,561	US
NMXS-1008.00/US	Utility - ORG	Issued	BUILT-IN SELF TEST FOR A COUNTER SYSTEM	3/10/2005	11/076,785	12/13/2005	6,975,696	US
NMXS-1009.00/US	Utility - ORG	Issued	READING CIRCUIT AND METHOD FOR A NONVOLATILE MEMORY DEVICE	9/28/2005	11/238,137	7/10/2007	7,242,619	US
NMXS-1009.01/US	Utility - CON	Issued	READING CIRCUIT AND METHOD FOR A NONVOLATILE MEMORY DEVICE	6/8/2007	11/811,394	11/11/2008	7,450,428	US
NMXS-1010.00/US	Utility - ORG	Issued	SELF-ADAPTIVE OUTPUT BUFFER BASED ON CHARGE SHARING	7/7/2006	11/482,524	9/8/2009	7,586,331	US
NMXS-1011.00/US	Utility - ORG	Issued	CIRCUIT FOR GENERATING AN INTERNAL ENABLING SIGNAL FOR AN OUTPUT BUFFER OF A MEMORY	1/20/2006	11/337,030	5/19/2009	7,535,774	US
NMXS-1014.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR SIMULTANEOUSLY PROGRAMMING MEMORY CELLS	4/13/2006	11/279,663	12/4/2007	7,304,896	US
NMXS-1016.00/US	Utility - ORG	Issued	SEQUENTIAL PROGRAM-VERIFY METHOD WITH RESULT BUFFERING	3/29/2005	11/093,012	2/27/2007	7,184,310	US
NMXS-1017.00/US	Utility - ORG	Issued	OPTIMIZING WRITE/ERASE OPERATIONS IN MEMORY DEVICES	1/13/2005	11/035,013	4/22/2008	7,363,421	US
NMXS-1018.00/US	Utility - ORG	Issued	CONTROLLING OPERATION OF FLASH MEMORIES	1/14/2005	11/036,179	11/25/2008	7,457,909	US
NMXS-1026.00/US	Utility - ORG	Issued	MEMORY ARCHITECTURE	9/1/2006	11/469,754	7/28/2009	7,567,475	US
NMXS-1033.00/US	Utility - ORG	Issued	MEMORY ARCHITECURE WITH SERIAL PERIPHERAL INTERFACE	9/8/2006	11/530,199	9/7/2010	7,793,031	US
NMXS-1038.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR OR NON-VOLATILE MEMORIES IMPLEMENTED WITH LOW-VOLTAGE TRANSISTORS	8/24/2007	11/844,470	8/17/2010	7,777,466	US
NMXS-1040.00/US	Utility - ORG	Issued	READING CIRCUIT FOR SEMICONDUCTOR MEMORY	4/27/2007	11/740,941	3/24/2009	7,508,717	US
NMXS-1043.00/US	Utility - ORG	Issued	SENSING CIRCUIT FOR SEMICONDUCTOR MEMORIES	4/24/2007	11/739,167	4/7/2009	7,515,493	US
NMXS-1044.00/US	Utility - ORG	Issued	ELECTRONIC DEVICE CONTAINING SEMICONDUCTOR POLYMERS AND CORRESPONDING MANUFACTURING PROCESS	1/30/2007	11/700,541	10/18/2011	8,039,831	US
NMXS-1046.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH A PAGE BUFFER HAVING AN IMPROVED LAYOUT ARRANGEMENT	7/25/2006	11/459,831	8/5/2008	7,408,819	US
NMXS-1047.00/US	Utility - ORG	Issued	REDUCTION OF THE TIME FOR EXECUTING AN EXTERNALLY COMMANDED TRANSFER OF DATA IN AN INTEGRATED DEVICE	3/16/2007	11/687,353	7/28/2009	7,567,107	US
NMXS-1048.00/US	Utility - ORG	Issued	SYNCHONIZATION OF OPERATIONS IN DISTINCT MEMORY PARTITIONS	3/14/2007	11/686,133	4/7/2009	7,515,464	US
NMXS-1050.00/US	Utility - ORG	Issued	OPTIMIZED FLASH MEMORY ACCESS METHOD AND DEVICE	4/13/2007	11/787,101	9/6/2011	8,015,345	US
NMXS-1052.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR PROGRAMMING A MEMORY CELL, IN PARTICULAR OF THE NOR FLASH TYPE	4/16/2008	12/104,118	2/2/2010	7,656,712	US
NMXS-1053.00/US	Utility - ORG	Issued	HIGH VOLTAGE GENERATOR OF THE DAC-CONTROLLED TYPE	8/8/2006	11/463,260	4/27/2010	7,706,160	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-1054.00/US	Utility - ORG	Issued	READING METHOD OF A MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE AND MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE	3/1/2007	11/713,376	3/15/2011	7,908,543	US
NMXS-1055.00/US	Utility - ORG	Issued	FAST PROGRAMMING MEMORY DEVICE	5/19/2008	12/123,359	9/13/2011	8,018,771	US
NMXS-1056.00/US	Utility - ORG	Issued	OUTPUT BUFFER	12/13/2006	11/638,321	12/9/2008	7,463,051	US
NMXS-1056.01/US	Utility - DIV	Issued	OUTPUT BUFFER	10/30/2008	12/261,101	9/28/2010	7,804,322	US
NMXS-1062.00/US	Utility - ORG	Issued	REFERENCE CELL LAYOUT WITH ENHANCED RTN IMMUNITY	4/27/2007	11/741,462	6/23/2009	7,551,465	US
NMXS-1066.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE	10/12/2006	11/549,067	1/27/2009	7,483,300	US
NMXS-1067.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING OF MEMORY CELLS, IN PARTICULAR OF THE FLASH TYPE, AND CORRESPONDING PROGRAMMING ARCHITECTURE	12/8/2006	11/636,382	10/20/2009	7,606,078	US
NMXS-1067.01/US	Utility - DIV	Issued	METHOD FOR PROGRAMMING OF MEMORY CELLS, IN PARTICULAR OF THE FLASH TYPE, AND CORRESPONDING PROGRAMMING ARCHITECTURE	9/20/2009	12/556,970	5/17/2011	7,944,751	US
NMXS-1068.00/US	Utility - ORG	Issued	AUTOMATIC REGULATION METHOD FOR THE REFERENCE SOURCES IN A NON-VOLATILE MEMORY DEVICE AND CORRESPONDING MEMORY DEVICE	6/29/2007	11/771,677	7/28/2009	7,567,470	US
NMXS-1070.00/US	Utility - ORG	Issued	METHOD FOR ACCESSING A MULTILEVEL NONVOLATILE MEMORY DEVICE OF THE FLASH NAND TYPE	7/20/2006	11/458,904	6/3/2008	7,382,660	US
NMXS-1071.00/US	Utility - ORG	Issued	CONFIGURATION OF A MULTILEVEL FLASH MEMORY DEVICE	7/28/2006	11/460,777	5/3/2011	7,937,576	US
NMXS-1075.00/US	Utility - ORG	Issued	ELECTRONIC DEVICE COMPRISING NON VOLATILE MEMORY CELLS WITH OPTIMIZED PROGRAMMING AND CORRESPONDING PROGRAMMING METHOD	2/28/2007	11/713,074	3/31/2009	7,512,032	US
NMXS-1079.00/US	Utility - CIP	Issued	SEALING METHOD FOR ELECTRONIC DEVICES FORMED ON A COMMON SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT STRUCTURE	7/17/2006	11/457,966	8/12/2008	7,410,872	US
NMXS-1084.00/US	Utility - ORG	Issued	METHOD FOR COMPACTING THE ERASED THRESHOLD VOLTAGE DISTRIBUTION OF FLASH MEMORY DEVICES DURING WRITING OPERATIONS	8/24/2007	11/844,480	5/5/2009	7,529,136	US
NMXS-1085.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR ELECTRICALLY PROGRAMMING A NON-VOLATILE	6/21/2007	11/766,493	3/3/2009	7,499,332	US
NMXS-1089.00/US	Utility - ORG	Issued	METHOD OF MANAGING A MULTILEVEL MEMORY DEVICE AND RELATED DEVICE	4/25/2008	12/109,525	5/4/2010	7,710,772	US
NMXS-1090.00/US	Utility - ORG	Issued	METHOD OF MANAGING A MEMORY DEVICE EMPLOYING THREE-LEVEL CELLS	2/28/2008	12/039,268	8/24/2010	7,782,665	US
NMXS-1091.00/US	Utility - ORG	Issued	MEMORY DEVICE EMPLOYING THREE-LEVEL CELLS AND RELATED METHODS OF MANAGING	11/2/2007	11/934,144	9/29/2009	7,596,023	US
NMXS-1093.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH FAIL SEARCH AND REDUNDANCY	7/20/2007	11/780,581	1/12/2010	7,646,655	US
NMXS-1094.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE AND METHOD OF HANDLING A DATUM READ FROM A MEMORY CELL	8/3/2007	11/833,400	5/12/2009	7,532,512	US
NMXS-1095.00/US	Utility - ORG	Issued	ADDRESS COUNTER FOR NONVOLATILE MEMORY DEVICE	7/27/2007	11/829,527	7/7/2009	7,558,152	US
NMXS-1104.01/US	Utility - CON	Expired	REDUCED-BEAK PLANOX PROCESS FOR THE FORMATION OF INTEGRATED ELECTRONIC COMPONENTS	11/23/1987	07/124,440	1/30/1990	4,897,365	US
NMXS-1105.01/US	Utility - DIV	Expired	A METHOD FOR MANUFACTURING MOS/CMOS MONALITHIC INTEGRATED CIRCUITS INCLUDING SILICIDE AND POLYSILICON PATTERNING	12/15/1988	07/289,391	11/6/1990	4,968,645	US
NMXS-1106.00/US	Utility - ORG	Expired	ELECTRICALLY ALTERABLE, NONVOLATILE FLOATING GATE MEMORY DEVICE	5/27/1987	07/054,712	4/18/1989	4,823,175	US
NMXS-1107.00/US	Utility - ORG	Expired	NONVOLATILE, SEMICONDUCTOR MEMORY DEVICE	6/22/1987	07/064,480	3/28/1989	4,816,883	US
NMXS-1108.00/US	Utility - ORG	Expired	GENERATOR OF RESET PULSES UPON THE RISE OF THE POWER SUPPLY FOR CMOS-TYPE INTEGRATED CIRCUITS	4/28/1988	07/187,581	12/19/1989	4,888,497	US
NMXS-1109.00/US	Utility - ORG	Expired	PROCESS FOR MANUFACTURING CMOS INTEGRATED DEVICES WITH REDUCED GATE LENGTHS	12/14/1988	07/284,272	11/6/1990	4,968,639	US
NMXS-1110.00/US	Utility - ORG	Expired	CIRCUIT FOR SENSING THE STATE OF MATRIX CELLS IN MOS EPROM MEMORIES INCLUDING AN OFFSET CURRENT GENERATOR	1/18/1989	07/298,487	8/14/1990	4,949,307	US
NMXS-1111.00/US	Utility - ORG	Expired	CMOS VOLTAGE MULTIPLIER	6/28/1989	07/372,493	5/1/1990	4,922,402	US
NMXS-1112.00/US	Utility - ORG	Expired	CMOS LOGIC CIRCUIT FOR HIGH VOLTAGE OPERATION	6/30/1989	07/373,203	9/11/1990	4,956,569	US
NMXS-1113.00/US	Utility - ORG	Expired	FABRICATION OF CMOS DEVICES WITH REDUCED GATE LENGTH	7/18/1989	07/381,283	1/22/1991	4,987,088	US
NMXS-1114.00/US	Utility - ORG	Expired	FABRICATION OF CMOS INTEGRATED DEVICES WITH REDUCED GATE LENGTH AND LIGHTLY DOPED DRAIN	7/28/1989	07/386,189	3/5/1991	4,997,782	US

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MTI-DcktNo	MatterType	MatterStatus	Title	FileDate	SerialNo	IssueDate	PatentNo	Country
NMXS-1115.00/US	Utility - ORG	Expired	GRIPPER FOR THE ADVANTAGEOUSLY ROBOTIZED HANDLING OF ONE OR MORE SILICON WAFERS AND/OR OF A SUPPORT FOR SUCH WAFERS	12/5/1989	07/446,196	10/8/1991	5,054,834	US
NMXS-1116.00/US	Utility - ORG	Expired	PROGRAMMABLE LOGIC DEVICE HAVING A PLURALITY OF PROGRAMMABLE LOGIC ARRAYS ARRANGED IN A MOSAIC LAYOUT TOGETHER WITH A PLURALITY OF INTERMINGLINGLY ARRANGED INTERFACING BLOCKS	12/27/1989	07/456,782	2/12/1991	4,992,680	US
NMXS-1117.00/US	Utility - ORG	Expired	METHOD OF MANUFACTURING INTEGRATED CIRCUITS HAVING ELECTRONIC COMPONENTS OF TWO DIFFERENT TYPES EACH HAVING PAIRS OF ELECTRODES OBTAINED FROM THE SAME POLYCRYSTALLINE SILICON LAYERS AND SEPARATED BY DIFFERENT DIELECTRIC MATERIALS	12/13/1990	07/625,764	12/24/1991	5,075,246	US
NMXS-1118.00/US	Utility - ORG	Expired	VOLTAGE-BOOSTED PHASE OSCILLATOR FOR DRIVING A VOLTAGE MULTIPLIER	2/14/1991	07/655,049	3/17/1992	5,097,226	US
NMXS-1119.00/RE	Utility - REIS	Expired	SENSE CIRCUIT FOR READING DATA STORED IN NONVOLATILE MEMORY CELLS	6/8/1995	08/488,718	6/8/1993	RE36,579	US
NMXS-1119.00/US	Utility - ORG	Abandoned	READING CIRCUIT FOR NONVOLATILEMEMORY CELL.	2/19/1991	07/657,083	6/8/1993	5,218,570	US
NMXS-1120.01/US	Utility - CIP	Issued	METHOD FOR ADJUSTING THE THRESHOLD OF A READ-ONLY MEMORY TO ACHIEVE LOW CAPACITANCE AND HIGH BREAKDOWN VOLTAGE	10/19/1993	08/139,800	1/23/1996	5,486,487	US
NMXS-1121.02/US	Utility - CON	Issued	METHOD FOR CARRYING OUT A BOOLEAN OPERATION BETWEEN ANY TWO BITS OF ANY TWO REGISTERS	12/27/1994	08/364,505	8/12/1997	5,657,484	US
NMXS-1122.00/US	Utility - ORG	Expired	DATA OUTPUT STAGE HAVING FEEDBACK LOOPS TO PRECHARGE THE OUTPUT NODE	4/26/1991	07/691,768	1/12/1993	5,179,300	US
NMXS-1123.00/US	Utility - ORG	Expired	READING DEVICE FOR EPROM MEMORY CELLS WITH THE OPERATIONAL FIELD INDEPENDENT OF THE THRESHOLD JUMP OF THE WRITTEN CELLS WITH RESPECT TO THE VIRGIN CELLS	7/5/1991	07/725,987	11/30/1993	5,267,202	US
NMXS-1124.00/US	Utility - ORG	Expired	METHOD FOR FORMATION OF CONTACT PLUGS UTILIZING ETCHBACK	10/1/1991	07/769,600	7/27/1993	5,231,051	US
NMXS-1125.01/US	Utility - CON	Expired	MONOSTABILIZED DYNAMIC PROGRAMMABLE LOGIC ARRAY IN CMOS TECHNOLOGY	10/30/1992	07/970,609	12/28/1993	5,274,282	US
NMXS-1126.00/US	Utility - ORG	Abandoned	TWO PHASES SCAN PATH WITH SINGLECLOCK.	10/22/1991	07/781,360	7/6/1993	5,225,724	US
NMXS-1126.01/RE	Utility - REIS	Expired	OPERATIONAL ANALYSIS DEVICE OF THE SCAN PATH TYPE HAVING A SINGLE SCANNING CLOCK AND A SINGLE OUTPUT PHASE FOR AN INTEGRATED CIRCUIT	6/23/1997	08/876,130	7/6/1993	RE36,292	US
NMXS-1127.00/US	Utility - ORG	Expired	COMPENSATED SENSE CIRCUIT FOR STORAGE DEVICES	11/13/1991	07/791,973	1/25/1994	5,282,169	US
NMXS-1128.00/US	Utility - ORG	Expired	SENSE CIRCUIT FOR STORAGE DEVICES SUCH AS NON-VOLATILE MEMORIES, WITH COMPENSATED OFFSET CURRENT	11/13/1991	07/791,453	1/4/1994	5,276,644	US
NMXS-1129.00/US	Utility - ORG	Expired	SENSE CIRCUIT FOR STORAGE DEVICES SUCH A NON-VOLATILE MEMORIES, WITH ENHANCED SENSING DISCRIMINATION	12/12/1991	07/806,118	12/14/1993	5,270,590	US
NMXS-1130.00/US	Utility - ORG	Expired	FAST CAPACITIVE-LOAD DRIVING CIRCUIT PARTICULARLY MEMORIES	12/20/1991	07/811,323	2/1/1994	5,283,478	US
NMXS-1131.00/US	Utility - ORG	Expired	MEMORY CELL READING CIRCUIT	12/19/1991	07/810,480	11/2/1993	5,258,959	US
NMXS-1132.00/US	Utility - ORG	Issued	CURRENT OFFSET SENSE AMPLIFIER OF A MODULATED CURRENT OR CURRENT UNBALANCE TYPE FOR PROGRAMMABLE MEMORIES	5/4/1992	07/878,823	7/5/1994	5,327,379	US
NMXS-1132.01/US	Utility - CIP	Issued	CURRENT OFFSET SENSE AMPLIFIER OF A MODULATED CURRENT OR CURRENT UNBALANCE TYPE FOR PROGRAMMABLE MEMORIES	7/5/1994	08/270,498	10/24/1995	5,461,713	US
NMXS-1133.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR PROGRAMMABLE MEMORIES WITH A VIRTUALLY ENHANCED SOURCE OF SIGNAL	7/24/1992	07/919,606	4/18/1995	5,408,148	US
NMXS-1134.00/US	Utility - ORG	Issued	ZERO-CONSUMPTION POWER-ON RESET CIRCUIT	8/27/1992	07/936,857	6/14/1994	5,321,317	US
NMXS-1135.00/US	Utility - ORG	Issued	ROW DECODER FOR NAND-TYPE ROM	8/31/1992	07/938,731	9/13/1994	5,347,493	US
NMXS-1136.01/US	Utility - CON	Issued	METHOD OF FORMING HIGH-STABILITY METALLIC CONTACTS IN AN INTEGRATED CIRCUIT WITH ONE OR MORE METALLIZED LAYERS	3/28/1995	08/411,385	2/26/2002	6,350,676	US
NMXS-1137.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING MOS-TYPE INTEGRATED CIRCUITS	6/7/1995	08/475,555	12/9/1997	5,696,399	US
NMXS-1137.02/US	Utility - CON	Issued	PROCESS FOR MANUFACTURING MOS-TYPE INTEGRATED CIRCUITS	9/6/1995	08/524,080	9/2/1997	5,663,080	US
NMXS-1138.01/US	Utility - CON	Issued	DYNAMIC REFERENCE SYSTEM FOR SENSE AMPLIFIER	10/12/1993	08/135,842	10/11/1994	5,355,333	US
NMXS-1139.00/US	Utility - ORG	Issued	METALLIZATION OVER TUNGSTEN PLUGS	5/26/1993	08/068,139	4/18/1995	5,407,861	US
NMXS-1139.01/US	Utility - CON	Issued	METALLIZATION OVER TUNGSTEN PLUGS	4/18/1995	08/423,397	7/28/1998	5,786,272	US

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NMXS-1140.00/US	Utility - ORG	Issued	LOW SWITCHING NOISE OUTPUT BUFFER	6/28/1993	08/084,727	5/30/1995	5,420,525	US
NMXS-1141.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT ENTIRELY PROTECTED AGAINST ULTRAVIOLET RAYS	6/30/1993	08/086,342	8/8/1995	5,440,510	US
NMXS-1142.02/US	Utility - CON	Issued	VOLTAGE REGULATOR FOR MEMORY DEVICE	3/13/1996	08/615,727	1/6/1998	5,706,240	US
NMXS-1143.01/US	Utility - DIV	Issued	A NON-VOLATILE MEMORY IN AN INTEGRATED CIRCUIT	5/23/1995	08/447,772	10/22/1996	5,568,418	US
NMXS-1143.02/US	Utility - CON	Issued	METHOD OF FABRICATING NON-VOLATILE MEMORIES WITH OVERLAPPING LAYERS	6/6/1995	08/469,431	8/25/1998	5,798,279	US
NMXS-1144.00/US	Utility - ORG	Issued	METHOD OF MAKING NOR-TYPE ROM WITH LDD CELLS	6/28/1993	08/084,971	4/18/1995	5,407,852	US
NMXS-1144.02/US	Utility - CON	Issued	NOR-TYPE ROM WITH LDD CELLS AND PROCESS OF FABRICATION	12/23/1996	08/772,301	8/11/1998	5,793,086	US
NMXS-1145.01/US	Utility - DIV	Issued	METHOD OF EVALUATING THE GATE OXIDE OF NON-VOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	6/2/1995	08/460,540	5/7/1996	5,515,318	US
NMXS-1145.02/US	Utility - CON	Issued	METHOD OF EVALUATING THE GATE OXIDE OF NON-VOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	4/1/1997	08/829,935	8/11/1998	5,793,675	US
NMXS-1146.00/US	Utility - ORG	Issued	METHOD FOR MAKING DIRECT CONTACTS IN HIGH DENSITY MOS/CMOS PROCESSES	11/17/1993	08/153,620	12/13/1994	5,372,956	US
NMXS-1147.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR FILTERING HIGH RESOLUTION DIGITAL SIGNALS	1/28/1994	08/189,271	1/14/1997	5,594,677	US
NMXS-1148.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR FILTERING DIGITAL SIGNALS	1/28/1994	08/188,569	3/3/1998	5,724,395	US
NMXS-1149.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING INTEGRATED DEVICES INCLUDING FLASH-EEPROM MEMORIES AND TRANSISTORS	2/10/1994	08/195,369	6/10/1997	5,637,520	US
NMXS-1150.00/US	Utility - ORG	Abandoned	PROCESS FOR FABRICATING INTEGRATED DEVICES INCLUDING NONVOLATILE MEMORIES AND TRANSISTORS WITH TUNNEL OXIDE PROTECTION	2/15/1994	08/196,634	11/14/1995	5,466,622	US
NMXS-1150.01/US	Utility - CON	Issued	PROCESS FOR FABRICATING INTEGRATED DEVICES INCLUDING NONVOLATILE MEMORIES AND TRANSISTORS WITH TUNNEL OXIDE PROTECTION	7/13/1995	08/502,329	6/15/1999	5,913,120	US
NMXS-1151.00/US	Utility - ORG	Issued	NONVOLATILE EPROM, EEPROM OF FLASH-EEPROM MEMORY WITH TUNNEL OXIDE PROTECTION	2/15/1994	08/196,572	3/5/1996	5,497,345	US
NMXS-1152.00/US	Utility - ORG	Issued	NONVOLATILE FLASH-EEPROM MEMORY ARRAY WITH SOURCE CONTROL TRANSISTORS	3/15/1994	08/214,049	4/16/1996	5,508,956	US
NMXS-1153.00/US	Utility - ORG	Issued	METHOD OF READING, ERASING AND PROGRAMMING A NONVOLATILE FLASH-EEPROM MEMORY ARRAY USING SOURCE LINE SWITCHING TRANSISTORS	3/15/1994	08/212,907	12/24/1996	5,587,946	US
NMXS-1153.01/US	Utility - DIV	Issued	METHOD OF PROGRAMMINING A NONVOLATILE FLASH-EEPROM MEMORY ARRAY USING SOURCE LINE SWITCHING TRANSISTORS	6/2/1995	08/458,346	5/27/1997	5,633,822	US
NMXS-1154.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH MEMORY MATRIX COMPRISING REDUNDANCY CELL COLUMNS ASSOCIATED WITH SINGLE MATRIX SECTORS	3/29/1994	08/219,204	11/21/1995	5,469,389	US
NMXS-1155.01/US	Utility - DIV	Issued	METHOD OF EVALUATING THE DIELECTRIC LAYER OF NONVOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	5/31/1995	08/454,854	2/18/1997	5,604,699	US
NMXS-1155.02/US	Utility - CON	Issued	METHOD FOR EVALUATING THE DIELECTRIC LAYER OF NONVOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	7/24/1996	08/685,782	1/27/1998	5,712,816	US
NMXS-1157.00/US	Utility - NSPCT	Issued	METHOD AND APPARTUS FOR CHECKING POST-ERASURE CONTENTS OF AN ERASABLE PERMANENT MEMORY	10/9/1992	08/075,543	1/9/1996	5,483,492	US
NMXS-1158.00/US	Utility - ORG	Issued	SUPPLY VOLTAGE DISTRIBUTION SYSTEM WITH REDUCED RESISTANCE FOR SEMICONDUCTOR DEVICES	7/27/2007	11/881,505	9/27/2011	8,027,217	US