PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	50 Percent Interest

CONVEYING PARTY DATA

Name	Execution Date
Applied Micro Circuits Corporation	12/20/2012

RECEIVING PARTY DATA

Name:	Volex plc
Street Address:	10 Eastbourne Terrace
City:	London
State/Country:	UNITED KINGDOM
Postal Code:	W2 6LG

PROPERTY NUMBERS Total: 29

Property Type	Number
Patent Number:	8135281
Patent Number:	8200094
Application Number:	12483616
Patent Number:	8109677
Patent Number:	8113721
Patent Number:	8109676
Application Number:	12793513
Patent Number:	8109678
Patent Number:	8109675
Patent Number:	8061904
Patent Number:	8057106
Application Number:	13243439
Application Number:	13031196
Application Number:	13305471
Application Number:	13232919

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Application Number:	13278953
Application Number:	13355615
Application Number:	13654493
Application Number:	13230495
Application Number:	13252923
Application Number:	13299571
Application Number:	13344660
Application Number:	13657249
Application Number:	13657331
Application Number:	13661490
Application Number:	13660902
Application Number:	13645627
Application Number:	13645629
Application Number:	13662130

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	VOLX-0022 - VOLX-0050
NAME OF SUBMITTER:	Robin L. Parmelee

Total Attachments: 3
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Joint Ownership Assignment Agreement

Applied Micro Circuits Corporation, a Delaware corporation ("APM"), does hereby declare that:

A. Each of the inventors of the inventions listed in Annex I hereto and identified therein by APM case number, U.S. patent number or U.S. patent application number, filing date and title assigned to APM all right, title and interest of the inventor in the aforesaid corresponding U.S. patent or U.S. patent application (such patent applications and patents listed on Annex I hereto collectively the "Named Patents"), all priority rights derived from the Named Patents, all inventions disclosed in the Named Patents, all applications for patents for all such inventions in all countries, all resulting patents, the right to enforce such patents, and the right to collect damages for past infringement thereof; and

B. In accordance with the terms and conditions of a Technology and Asset Purchase Agreement entered into on December 4, 2012 by and between APM, Volex plc, a United Kingdom public limited company ("Volex") and Volex Inc., a Massachusetts corporation (the "TAPA") for valuable consideration the receipt and adequacy of which is hereby acknowledged and intending to be legally bound hereby, APM sells, assigns, and transfers unto Volex an undivided one-half portion of the entire right, title and interest, so as to effect joint ownership with Volex, in and to (1) each of Named Patents, (2) all priority rights derived from the Named Patents by virtue of the International Convention for the Protection of Industrial Property and any other treaty or understanding for intellectual property for any and all member countries of the aforesaid International Convention or other treaty or understanding, (3) any and all inventions disclosed in the Named Patents, (4) any and all applications for patents for any and all such inventions in any and all countries whatsoever, including all substitutes, divisionals, continuations, continuations-in-part, revisions, extensions, and reexaminations thereof, together with any foreign equivalents, (5) any and all patents for any such inventions in any country whatsoever, (6) the right to file such applications and obtain patents thereon in the name of both APM and Volex, all of which shall be subject to such undivided joint ownership rights of APM and Volex, and (7) the right to enforce said patents and to sue for and recover profits and damages for any and all infringements thereof whether past or future (collectively, the "Joint Patents"). APM and Volex further agree that their interests in the Joint Patents are subject in all respects to the terms and conditions set forth in Section 9.6(c) of the TAPA, all of which shall enure to the benefit of and be enforceable against all successors and assigns of the parties.

In accordance with the TAPA, APM and Volex do hereby agree that (i) Volex shall be responsible for the prosecution and maintenance of the Joint Patents, (ii) APM and Volex will provide reasonable joint cooperation toward the same and will execute the necessary legal documents to file for the Joint Patents referenced herein, to file assignments in relevant countries, and to establish joint ownership consistent herewith, and (iii) APM and Volex will provide reasonable assistance (including testifying in any legal proceeding respecting rights granted herein) to the other party, its successors and assigns in obtaining and enforcing Joint Patents.

IN WITNESS WHEREOF, APM and Volex have caused this instrument to be executed by its duly authorized representatives and which shall go into effect on the date last executed by a party below.

For Applied Micro Circuits Corporation

By: _____

Printed Name: _____

Title: _____

Date: December 20, 2012

For Volex plc

By: _____

Printed Name: _____

Title: _____

Date: December 20, 2012

Draft Dated 12.4.12

Annex I
Joint Patents

Inventor Last Names	APM Case No.	US Patent No.	US Application No.	Filing Date	Title
Zhovnirovsky Roy Conroy	applied_312	8,135,281		3/13/12	Free Space Optical Connector
Zhovnirovsky Roy	applied_313	8,200,094		6/12/12	System and Method for Free Space Optical Connector Alignment
Zhovnirovsky Roy Conroy	applied_321	n/a	US 12/483,616	6/12/09	Fiber Optic Cable Interface
Zhovnirovsky Roy Conroy	applied_321 CON1	8,109,677		2/7/12	Fiber Optic Cable Connector
Zhovnirovsky Roy Conroy	applied_321 CIP1	8,113,721		2/14/12	Off-axis Misalignment Compensating Fiber Optic Cable Interface
Zhovnirovsky Roy	applied_321 CIP2	8,109,676		2/7/12	Fiber Optic cable with High Interface Mismatch Tolerance
Zhovnirovsky Roy	applied_321 CIP3	n/a	US 12/793,513	6/3/10	Fiber Optic Jack with High Interface Mismatch Tolerance
Zhovnirovsky Roy Conroy	applied_352	8,109,678		2/7/12	Punch-down Fiber Optic Cable Termination
Zhovnirovsky Roy	applied_354	8,109,675		2/7/12	Connector Jack Processing Backcap
Zhovnirovsky Roy Greenberg	applied_405	8,061,904		11/22/11	Fiber Optic Connector Microlens with Self-aligning Optical Fiber Cavity
Zhovnirovsky Roy Greenberg	applied_407	8,057,106		11/15/11	Fiber Optic Connector Microlens with Focal Plane Aligning Fiber Trap
Zhovnirovsky Roy	applied_409	n/a	US 13/243,439	9/23/11	Optical Element Assembly With Integrally Formed Microlens
Zhovnirovsky Roy	applied_416	n/a	US 13/031,196	2/19/11	Test Method for Lens
Fanfelle	applied_467	n/a	US 13/305,471	9/28/11	System and Method for Aligning Surface Mount Devices on a Substrate

Roy Zhovnirovsky	applied_481	n/a	US 13/232,919	9/14/11	Optical Substrate Chip Carrier
Vinogradov	applied_483	n/a	13 US /278,953	10/21/11	Photodiode with Optical Path-Aligned Pillar Electrodes
Zhovnirovsky Roy	applied_483 CIP1	n/a	US 13/355,615	1/23/12	Photodetector and Method for Bandwidth Tuning a Honeycomb Cell Photodiode Structure
Sevigny	applied_484	n/a	US 13/654,493	10/18/12	Expanded Beam Interconnector
Roy Zhovnirovsky	applied_487	n/a	US 13/230,495	9/12/11	Multipath coupling lens system
Roy Zhovnirovsky	applied_487 CIP1	n/a	US 13/252,923	10/4/11	Free Space Multipath Lens System
Roy Zhovnirovsky	applied_487 CIP2	n/a	US 13/299,571	11/18/11	Multipath Coupling Lens System
Roy Zhovnirovsky	applied_487 CIP3	n/a	US 13/344,660	1/6/12	Photodiode Detector
Gold Sevigny	applied_516	n/a	US 13/657,249	10/22/12	Method for Alignment Between Two Optical Components
Gold Roy Zhovnirovsky	applied_517	n/a	US 13/657,331	10/22/12	Method for Passive Alignment of Optical Components for a Substrate
Sevigny Gold	applied_518	n/a	US 13/661,490	10/26/12	Imaging System for Passive Alignment of Engines
Kesiakov	applied_519	n/a	US 13/660,902	10/25/12	Mini SAS HD Connector
Sevigny	applied_520	n/a	US 13/645,627	10/5/12	Collimated Beam Channel with Four Lens Optical Surfaces
Sevigny	applied_521	n/a	US 13/645,629	10/5/12	High-Density Fiber Coupling and Emission Detection System
Sevigny	applied_522	n/a	US 13/662,130	10/26/12	Method and System for Marking Substrate and Placing components for High Accuracy Component Placement