

## PATENT ASSIGNMENT

Electronic Version v1.1  
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT						
NATURE OF CONVEYANCE:	ASSIGNMENT						
CONVEYING PARTY DATA							
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Fuchen Mu</td> <td>01/22/2013</td> </tr> <tr> <td>Chen He</td> <td>01/22/2013</td> </tr> </tbody> </table>		Name	Execution Date	Fuchen Mu	01/22/2013	Chen He	01/22/2013
Name	Execution Date						
Fuchen Mu	01/22/2013						
Chen He	01/22/2013						
RECEIVING PARTY DATA							
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Postal Code:	78735						
PROPERTY NUMBERS Total: 1							
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>13747504</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	13747504		
Property Type	Number						
Application Number:	13747504						
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NAME OF SUBMITTER:	J. Gustav Larson						
Total Attachments: 3 source=AI20131HC Assignment#page1.tif source=AI20131HC Assignment#page2.tif source=AI20131HC Assignment#page3.tif							

CH \$40.00 13747504

## PATENT

## ASSIGNMENT AND AGREEMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, we, **Fuchen Mu** of Austin, TX; and **Chen He** of Austin, TX; have sold, assigned and transferred, and do hereby sell, assign and transfer, unto **FREESCALE SEMICONDUCTOR, INC.** a corporation of the State of Delaware, having its principal office in Austin, State of Texas, United States of America, and its successors, assigns, and legal representatives, the entire right, title and interest for the United States of America in and to certain inventions relating to improvements in **DYNAMIC DETECTION METHOD FOR LATENT SLOW-TO-ERASE BIT FOR HIGH PERFORMANCE AND HIGH RELIABILITY FLASH MEMORY** (Docket No. **AI20131HC**), described, illustrated and claimed in an application for Letters Patent of the United States of America executed by us on the dates indicated by our signatures below, together with the entire right, title and interest in and to the application, and in and to Letters Patent which may be issued upon the application, and upon any division, extension, continuation or reissue thereof.

We hereby also sell, assign and transfer unto **FREESCALE SEMICONDUCTOR, INC.**, the entire right, title and interest in and to the invention and in and to applications for Letters Patent therefore in all countries foreign to the United States of America, including all rights under any and all international conventions and treaties in respect of the invention and the applications for Letters Patent in foreign countries, and we further authorize **FREESCALE SEMICONDUCTOR, INC.** to apply for Letters Patent in foreign countries directly in its own name, and to claim priority of the filing date of the application for Letters Patent of the United States of America under the provisions of any and all international conventions and treaties.

We hereby authorize and request the Commissioner of Patents of the United States of America to issue Letters Patent upon the aforesaid application, division, extension, continuation or reissue, to **FREESCALE SEMICONDUCTOR, INC.**, for the sole use and benefit of **FREESCALE SEMICONDUCTOR, INC.**, its successors, assigns and legal representatives, to the full end of the term for which Letters Patent may be granted, the same as they would have been held and enjoyed by me had this assignment not been made, and we hereby authorize and request the equivalent authorities in foreign countries to issue the patents of their respective countries to **FREESCALE SEMICONDUCTOR, INC.**



PATENT

Chen He

Chen He

January 22, 2013

Date

Witnesses for Second Inventor:

First Witness:

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Signature

1/22/13  
Date (Month-Day-Year)

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