

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT										
NATURE OF CONVEYANCE:	ASSIGNMENT										
CONVEYING PARTY DATA											
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Tetsuya HIROSE</td> <td>01/21/2013</td> </tr> <tr> <td>Yuji OSAKI</td> <td>01/17/2013</td> </tr> <tr> <td>Yumiko TSURUYA</td> <td>01/21/2013</td> </tr> <tr> <td>Osamu KOBAYASHI</td> <td>01/18/2013</td> </tr> </tbody> </table>		Name	Execution Date	Tetsuya HIROSE	01/21/2013	Yuji OSAKI	01/17/2013	Yumiko TSURUYA	01/21/2013	Osamu KOBAYASHI	01/18/2013
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<table border="1"> <tr> <td>Name:</td> <td>Semiconductor Technology Academic Research Center</td> </tr> <tr> <td>Street Address:</td> <td>3-17-2, Shin-Yokohama, Kohoku-ku, Yokohama-shi</td> </tr> <tr> <td>City:</td> <td>Kanagawa</td> </tr> <tr> <td>State/Country:</td> <td>JAPAN</td> </tr> <tr> <td>Postal Code:</td> <td>222-0033</td> </tr> </table>		Name:	Semiconductor Technology Academic Research Center	Street Address:	3-17-2, Shin-Yokohama, Kohoku-ku, Yokohama-shi	City:	Kanagawa	State/Country:	JAPAN	Postal Code:	222-0033
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PROPERTY NUMBERS Total: 1											
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CORRESPONDENCE DATA											
<p>Fax Number: 2027218250 <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i></p> <p>Phone: 2027218200 Email: wlp@wenderoth.com Correspondent Name: WENDEROTH, LIND & PONACK, L.L.P. Address Line 1: 1030 15th Street, N.W. Address Line 2: Suite 400 East Address Line 4: Washington, DISTRICT OF COLUMBIA 20005-1503</p>											
ATTORNEY DOCKET NUMBER:	2013_0106A										
NAME OF SUBMITTER:	Fatema Seraj										
<p>Total Attachments: 4 source=executed assignment#page1.tif source=executed assignment#page2.tif source=executed assignment#page3.tif source=executed assignment#page4.tif</p>											

OP \$40.00 13752919

ASSIGNMENT

In consideration of the sum of One Dollar (\$1.00) and other good and valuable consideration paid to each of the undersigned

Tetsuya HIROSE, Yuji OSAKI, Yumiko TSURUYA and Osamu KOBAYASHI

Insert Name(s)
of
Inventor(s)

Insert Name(s)
of
Assignee(s)

the undersigned hereby sell(s) and assign(s) to

Semiconductor Technology Academic Research Center

Address

of 3-17-2, Shin-Yokohama, Kohoku-ku, Yokohama-shi, Kanagawa 222-0033 Japan

Title of
Invention

(hereinafter designated as the Assignee) the entire right, title and interest for the United States as defined in 35 USC 100, in the invention known as

DIFFERENTIAL AMPLIFIER CIRCUIT WITH ULTRALOW POWER CONSUMPTION
PROVIDED WITH ADAPTIVE BIAS CURRENT GENERATOR CIRCUIT

Date of
Signing of
Application

for which an application for patent in the United States has been executed by the undersigned

on January 17, 2013, January 18, 2013 and January 21, 2013

The undersigned agree(s) to execute all papers necessary in connection with this application and any continuing, divisional or reissue applications thereof and also to execute separate assignments in connection with such applications as the Assignee may deem necessary or expedient.

The undersigned agree(s) to execute all papers necessary in connection with any interference which may be declared concerning this application or continuation, division or reissue thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference.

The undersigned agree(s) to execute all papers and documents and perform any act which may be necessary in connection with claims or provisions of the International Convention for Protection of Industrial Property or similar agreements.

The undersigned agree(s) to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee.

The undersigned hereby authorize(s) and request(s) the Commissioner of Patents to issue any and all Letters Patents of the United States resulting from said application or any division or divisions or continuing or reissue applications thereof to the said Assignee, as Assignee of the entire interest, and hereby covenants that he has (they have) full right to convey the entire interest herein assigned, and that he has (they have) not executed, and will not execute, any agreement in conflict herewith.

The undersigned hereby grant(s) the firm of WENDEROTH, LIND & PONACK, L.L.P., 1030 15th Street, N.W., Suite 400 East, Washington, D.C. 20005-1503, the power to insert on this assignment any further identification which may be necessary or desirable in order to comply with the rules of the United States Patent Office for recordal of this document.

In witness whereof, executed by the undersigned on the date(s) opposite the undersigned name(s).

Date January 21, 2013, Name of Inventor Tetsuya Hirose
Tetsuya HIROSE

Date Jan. 17, 2013, Name of Inventor Yuji Osaki
Yuji OSAKI

Date January 21, 2013, Name of Inventor Yumiko Tsuruya
Yumiko TSURUYA

Date January 16, 2013, Name of Inventor Osamu Kobayashi
Osamu KOBAYASHI

(This assignment should preferably be acknowledged before a United States Consul. If not, then the execution by the Inventor(s) should be witnessed by at least two witnesses who sign here.)

Witness Mizuho Tsukamoto

Witness Yusuke Saito

ACKNOWLEDGMENT

_____ } ss

This _____ day of _____, 20____,
before me personally came the above-named
_____ to me personally known as the individual(s) who executed the foregoing
assignment, who did acknowledge to me that he (they) executed the same of his (their) own free will for the
purposes therein set forth.

SEAL

Official Signature

Official Title

The above application may be more particularly identified as follows:

U.S. Application Serial No.: _____ Filing Date: _____

Applicant Reference Number: 565135 Attorney Docket No.: _____

Title of the Invention

DIFFERENTIAL AMPLIFIER CIRCUIT WITH ULTRALOW POWER CONSUMPTION PROVIDED WITH
ADAPTIVE BIAS CURRENT GENERATOR CIRCUIT