

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the Spelling of Assignee name previously recorded on Reel 014885 Frame 0260. Assignor(s) hereby confirms the Assignee name is Axcelis Technologies, Inc.
CONVEYING PARTY DATA	
Name	Execution Date
Matrix Integrated Systems, Inc.	11/30/2003
RECEIVING PARTY DATA	
Name:	Axcelis Technologies, Inc.
Street Address:	108 Cherry Hill Drive
City:	Beverly
State/Country:	MASSACHUSETTS
Postal Code:	01915
PROPERTY NUMBERS Total: 10	
Property Type	Number
Patent Number:	6263830
Patent Number:	6461801
Patent Number:	6273956
Patent Number:	6228773
Patent Number:	6412438
Patent Number:	6439155
Patent Number:	6352050
Patent Number:	6605226
Patent Number:	6409932
Patent Number:	6499777
CORRESPONDENCE DATA	
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ATTORNEY DOCKET NUMBER:

15545-500005

NAME OF SUBMITTER:

Stephanie Stevens

Total Attachments: 5

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SCHEDULE A – U.S. PATENTS

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PATENT NO.	ISSUE DATE	FILING DATE	USSN
6,352,936 Method for Stripping Ion Implanted Photoresist Layer Co-owned with IMEC. - vzw of Leuven, Belgium	05MR2002	25FE1999	09/257,626
6,461,801 Rapid Heating and Cooling of Work Piece Chucks	08OC2002	26MY2000	09/579,943
6,499,777 End Effector with Integrated Cooling Mechanism	31DE2002	05MY2000	09/565,833
6,263,830 Microwave Choke for Remote Plasma Generator	24JL2001	11AP2000	09/546,750
6,412,438 Downstream Sapphire Elbow Joint for Remote Plasma Generator	02JL2002	22DE2000	09/748,060
6,439,155 Remote Plasma Generator with Sliding Short Tuner	27AU2002	22DE2000	09/747,452
6,352,050 Remote Plasma Mixer	05MR2002	22DE2000	09/747,822
6,228,773 Synchronous Multiplexed Near Zero Overhead Architecture for Vacuum Processes	08MY2001	14AP1998	09/060,095
6,273,956 Synchronous Multiplexed Near Zero Overhead Architecture for Vacuum Processes	14AU2001	19MY1999	09/314,193
4,971,653 Temperature Controlled Chuck for Elevated Temperature Etch Processing	20NO1990	14MY1990	07/494,173
5,209,803 Parallel Plate Reactor and Method Use	11MY1993	18JA1991	07/643,197
6,409,932 Method and Apparatus for Increased Workpiece Throughput	25JE2002	27DE2000	09/749,648

PATENT NO.	ISSUE DATE	FILING DATE	USSN
6,605,226 Method for Increased Workpiece Throughput	12AU2003	10JE2002	10/170,621
6,660,975 Method for Producing Flat Wafer Chucks	09DE2003	16AU2002	10/223,226

SCHEDULE B - U.S. PATENT APPLICATIONS

(Page 1 of 1)

SERIAL NO.	FILING DATE	
10/167,937	10JE2002	Apparatus for Increased Workpiece Throughput
60/469,050	07MY2003	Wide Temperature Range Chuck System
60/469,056	07MY2003	Method and Apparatus for Reducing Wafer Warping on a Robotic End Effector
60/469,077	07MY2003	Apparatus for Supporting a Wafer in a Process Chamber
60/444,612	30JA2003	Helix Coupled Remote Plasma Source
10/660,359	10SE2003	Method of Heating a Substrate in a Variable Temperature Process Using a Fixed Temperature Chuck
10/651,433	29AU2003	Gas Inlet End Cap for Microwave Plasma Source
10/624,728	21JL2003	Dual Chamber Vacuum Processing System
09/534,657	24MR2000	Method for Etching Sidewall Polymer and Other Residues from the Surface of Semiconductor Devices

ASSIGNMENT OF U.S PATENTS AND PATENT APPLICATIONS

WHEREAS, Matrix Integrated Systems, Inc. a corporation under the laws of the State of California, having principal offices at 4050 Lakeside Drive, Richmond, California 94806 (hereinafter "Assignor"), is the owner of the United States Letters Patents and Patent Applications which are specifically identified and listed in Schedule A and Schedule B, respectively, attached hereto, incorporated herein and made a part hereof and all related inventions including any and all related patents and patent applications including provisionals, continuations, continuation-in-part, and divisionals, and


WHEREAS, Axcelis Technologies, Inc., a corporation organized under the laws of the State of Delaware, having principal offices at 108 Cherry Hill Drive, Beverly, Massachusetts 01915 (hereinafter "Assignee"), is desirous of acquiring all of Assignor's right, title and interest in the Patents and Patent Applications, specifically identified in Schedule A and Schedule B, respectively, including its right to sue for and collect for past infringement thereof;

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which the Assignor hereby acknowledges, and intending to be legally bound hereby, the Assignor does hereby sell, assign, transfer and set over unto the Assignee its entire right, title and interest in and to, respectively, the Patents identified in Schedule A hereto, and any and all reissues and extensions thereof, including Assignor's right to sue for and collect damages and other recoveries for past infringement thereof; and the Patent Applications identified in Schedule B hereto, and any and all related provisional applications, divisions, continuations and continuations-in-part thereof; the same to be held and enjoyed by the Assignee, its successors and assigns, as fully as the same would have been held and enjoyed by the Assignor had this assignment not been made.

And for the consideration aforesaid, Assignor agrees that it will, upon request, execute and deliver to Assignee any and all additional papers and generally do all other and further lawful acts deemed necessary by said Assignee to carry out the terms of this Agreement.

EXECUTED this 30th day of November 2003.

Matix Integrated Systems, Inc.

By: 

Lynnette C. Fallon

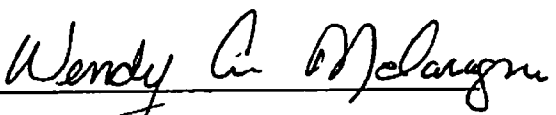
Title: Secretary

STATE OF MASSACHUSETTS :
: SS:
COUNTY OF ESSEX :

The foregoing instrument was acknowledged before me in the City of Beverly, this 30th day of November, 2003, by Lynnette C. Fallon, Secretary of Matrix Integrated, Inc., a California Corporation, on behalf of the corporation.

My Commission Expires:

February 19, 2010


(Notary Public)

(Notarial Seal)