

## PATENT ASSIGNMENT

Electronic Version v1.1  
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SUBMISSION TYPE:	NEW ASSIGNMENT												
NATURE OF CONVEYANCE:	ASSIGNMENT												
CONVEYING PARTY DATA													
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Peter J. Hopper</td> <td>01/31/2013</td> </tr> <tr> <td>Peter Smeys</td> <td>01/18/2013</td> </tr> <tr> <td>William French</td> <td>01/24/2013</td> </tr> <tr> <td>Andrei Papou</td> <td>01/18/2013</td> </tr> <tr> <td>Aditi Dutt Chaudhuri</td> <td>01/24/2013</td> </tr> </tbody> </table>		Name	Execution Date	Peter J. Hopper	01/31/2013	Peter Smeys	01/18/2013	William French	01/24/2013	Andrei Papou	01/18/2013	Aditi Dutt Chaudhuri	01/24/2013
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Aditi Dutt Chaudhuri	01/24/2013												
RECEIVING PARTY DATA													
<table border="1"> <tr> <td>Name:</td> <td>National Semiconductor Corporation</td> </tr> <tr> <td>Street Address:</td> <td>2900 Semiconductor Drive</td> </tr> <tr> <td>City:</td> <td>Santa Clara</td> </tr> <tr> <td>State/Country:</td> <td>CALIFORNIA</td> </tr> <tr> <td>Postal Code:</td> <td>95052</td> </tr> </table>		Name:	National Semiconductor Corporation	Street Address:	2900 Semiconductor Drive	City:	Santa Clara	State/Country:	CALIFORNIA	Postal Code:	95052		
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PROPERTY NUMBERS Total: 1													
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CORRESPONDENCE DATA													
<p>Fax Number: 2144791273  <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i></p> <p>Phone: 214 479 1180        Email: uspto@ti.com        Correspondent Name: Andrea Sarro        Address Line 1: 12500 TI Boulevard        Address Line 2: MS 3999        Address Line 4: Dallas, TEXAS 75243</p>													
ATTORNEY DOCKET NUMBER:	TI-88208												
NAME OF SUBMITTER:	Andrea Sarro												

CH \$40.00 13230171

**Total Attachments: 15**

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**ASSIGNMENT**

WHEREAS, I, the undersigned inventor (or one of the undersigned joint inventors), of residence as listed, having invented certain new and useful improvements as below entitled, for which application for United States Letters Patent is made.

WHEREAS, NATIONAL SEMICONDUCTOR CORPORATION, a corporation organized and existing under the laws of the State of Delaware, with a place of business at 2900 Semiconductor Drive, Santa Clara, California 95052, is desirous of acquiring my entire right, title and interest in and to the said invention, and to the said application and any Letters Patent that may issue thereon, as well as to U.S. Provisional Application No(s). \_\_\_\_\_, filed \_\_\_\_\_;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, I hereby sell and assign unto the said NATIONAL SEMICONDUCTOR CORPORATION, its successors and assigns, my entire right, title and interest in and to the said invention and in and to the said application(s) and all patents which may be granted therefor, and all divisions, reissues, substitutions, continuations, and extensions thereof; and I hereby authorize and request the Patent and Trademark Office to issue all patents for said invention, or patents resulting therefrom, to the said NATIONAL SEMICONDUCTOR CORPORATION, as assignee of my entire right, title and interest.

I also hereby sell and assign to NATIONAL SEMICONDUCTOR CORPORATION, its successors and assigns, my corresponding rights to the invention disclosed in said application in all other countries of the world, including the right to claim priority, file applications and obtain patents under the terms of the Paris Convention for the Protection of Industrial Property, and further agree to execute any and all patent applications, assignments, affidavits, and any other papers in connection therewith necessary to perfect such rights.

I hereby further agree that I will communicate to said NATIONAL SEMICONDUCTOR CORPORATION, or to its successors, assigns, and legal representatives, any facts known to me respecting said invention, and at the expense of said assignee company, testify in any legal proceedings, sign all lawful papers, execute all divisional, continuation, reissue and substitute applications, make all lawful oaths, and generally do everything possible to aid said NATIONAL SEMICONDUCTOR CORPORATION, its successors, assigns and nominees to obtain and enforce proper patent protection for said invention in all countries.

I authorize the later insertion of the nonprovisional application number and filing date hereinbelow.

IN WITNESS WHEREOF, I hereunto set hand and seal this day and year;

TITLE	System and Method of Galvanic Isolation in Digital Signal Transfer Integrated Circuits Utilizing Conductivity Modulation of Semiconductor Substrate		
NONPROVISIONAL APPLICATION NO.	13/230,171	FILING DATE	09/12/2011

SIGNATURE OF INVENTOR	
PRINTED NAME OF INVENTOR	Peter J. Hopper
DATE	
RESIDENCE (CITY AND STATE)	San Jose, CA

Application No. 13/230,171

Attorney Docket No. TI-88208


SIGNATURE OF INVENTOR	
PRINTED NAME OF INVENTOR	<b>Peter Smeys</b>
DATE	
RESIDENCE (CITY AND STATE)	San Jose, CA

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PRINTED NAME OF INVENTOR	<b>William French</b>
DATE	
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SIGNATURE OF INVENTOR	
PRINTED NAME OF INVENTOR	<b>Andrei Papou</b>
DATE	
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Application No. 13/230,171

Attorney Docket No. TI-88208

SIGNATURE OF INVENTOR	
PRINTED NAME OF INVENTOR	Aditi Dutt Chaudhuri
DATE	Jan. 24, 2013
RESIDENCE (CITY AND STATE)	Mountain View, CA

After recording, return Assignment to:

Andrea Sarro  
Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265

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SIGNATURE OF INVENTOR	<i>William French</i>
PRINTED NAME OF INVENTOR	William French
DATE	1/24/2013
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DATE	
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SIGNATURE OF INVENTOR	
PRINTED NAME OF INVENTOR	<b>Andrei Papou</b>
DATE	01/18/13
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PRINTED NAME OF INVENTOR	<b>Aditi Dutt Chaudhuri</b>
DATE	
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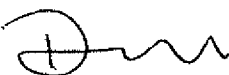
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PRINTED NAME OF INVENTOR	Peter J. Hopper
DATE	Jan 31, 2013
RESIDENCE (CITY AND STATE)	San Jose, CA

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Attorney Docket No. TI-88208

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