

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Fultec Semiconductor, Inc.	10/10/2008
RECEIVING PARTY DATA	
Name:	Bourns, Inc.
Street Address:	1200 COLUMBIA AVENUE
City:	Riverside
State/Country:	CALIFORNIA
Postal Code:	92507
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7324315
CORRESPONDENCE DATA	
Fax Number:	8655840104
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Email:	nsoon@pl-iplaw.com
Correspondent Name:	Pitts & Lake, PC
Address Line 1:	PO Box 51295
Address Line 2:	Andrew C. Lake
Address Line 4:	Knoxville, TENNESSEE 37950
ATTORNEY DOCKET NUMBER:	1576.3061300
NAME OF SUBMITTER:	Andrew C. Lake
Total Attachments: 5 source=00292458#page1.tif source=00292458#page2.tif source=00292458#page3.tif source=00292458#page4.tif source=00292458#page5.tif	

PATENT

PATENT ASSIGNMENT

THIS PATENT ASSIGNMENT (this "Assignment") is made and entered into as of October 10, 2008 (the "Effective Date") by and between **FULTEC SEMICONDUCTOR, INC.**, a Delaware corporation ("Assignor") and **BOURNS, INC.**, a California corporation ("Assignee").

WHEREAS, Assignor and Assignee are parties to that certain Asset Purchase Agreement, dated as of the date hereof (the "Purchase Agreement").

WHEREAS, pursuant to the Purchase Agreement, Assignor wishes to assign to Assignee, and Assignee wishes to acquire from Assignor, the patents and patent applications set forth on Schedule I attached hereto (collectively, the "Assigned Patents").

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor hereby sells, assigns, transfers and sets over to Assignee its entire right, title and interest in and to the Assigned Patents, including, without limitation, any continuations, divisions, continuations-in-part, reissues, reexaminations, extensions or foreign equivalents thereof, and all other corresponding rights that are or may be secured under the laws of the United States and any foreign country, now or hereafter in effect, for Assignee's own use and enjoyment, and for the use and enjoyment of Assignee's successors, assigns or other legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment had not been made, together with all income, royalties, damages or payments due or payable as of the Effective Date or thereafter, including, without limitation, all claims for damages by reason of past, present or future infringement or other unauthorized use of the Assigned Patents, with the right to sue for, and collect the same for its own use and enjoyment, and for the use and enjoyment of its successors, assigns, or other legal representatives.

Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks, and the corresponding entities or agencies in any applicable foreign countries, to record Assignee as the assignee and owner of the Assigned Patents.

This Assignment may be executed simultaneously in two or more counterparts (including facsimile versions), each of which shall be deemed an original, but all of which together shall constitute one and the same instrument.

This Assignment and the legal relations among the parties hereto shall be governed by and construed in accordance with the laws of the United States of America, without regard to its conflict of laws principles.

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IN WITNESS WHEREOF, Assignor and Assignee have caused this Assignment to be executed by their duly authorized representatives as of the Effective Date.

"ASSIGNOR"

FULTEC SEMICONDUCTOR, INC.,
a Delaware corporation

By: [Signature]
Name: DANIEL R. SACCAI
Title: CEO

"ASSIGNEE"

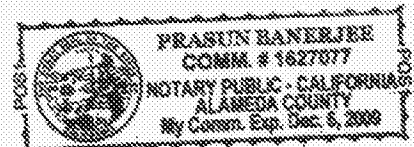
BOURNS, INC.,
a California corporation

By: [Signature]
Name: L. Erik Meijer
Title: Vice President / General Manager -
Circuit Protection Division

On 02-12-09, before me, PRASUN BANERJEE, a Notary Public in and for said State, personally appeared Daniel Robert Saccai and [Signature], each personally known to me (or proved to me on the basis of satisfactory evidence) to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

WITNESS my hand and official seal.

Signature Prasun Banerjee (Seal)



My commission expires December 6th, 2009

SCHEDULE 1

Type of IP	Title	Owner	Seller #	Number	Date Filed or Priority Date	Jurisdiction (PCT pub #)
Patent	A Protection Device	Seller	FUL-01	10/504,503 7324315	2/12/03	US
Patent Application	A Protection Device	Seller	FUL-01	3896127.9	2/12/03	China
Patent Application	A Protection Device	Seller	FUL-01	2003-568756	2/12/03	Japan
Patent Application	A Protection Device	Seller	FUL-01	EP03701348.9	2/12/03	Europe
Patent	A Protection and Indication Apparatus	Seller	FLT-136	11/028,277 7457093	1/3/05	US
Patent	Protection Devices and Methods for Preventing the Flow of Undesirable Differential Mode Transients	Seller	FLT-135	11/097,452 7372688	4/1/05	US
Patent Application	Protection Devices and Methods for Preventing the Flow of Undesirable Differential Mode Transients	Seller	FLT-135	2003-80101017.6	10/8/02	China
Patent Application	Protection Devices and Methods for Preventing the Flow of Undesirable Differential Mode Transients	Seller	FLT-135	2004-542092	10/8/02	Japan
Patent Application	Protection Devices and Methods for Preventing the Flow of Undesirable Differential Mode Transients	Seller	FLT-135	3747722.1	10/8/02	Europe
Patent	Integrated Electronic Disconnecting Circuits, Methods, and Systems	Seller	FLT-140	10/924,763 7262946	8/23/04	US
Patent Application	Integrated Electronic Disconnecting Circuits, Methods, and Systems	Seller	FLT-140	4761154.6	8/20/04	Europe
Patent Application	Integrated Electronic Disconnecting Circuits, Methods, and Systems	Seller	FLT-140	2006-523488	8/20/04	Japan
Patent Application	A Surge Protection Device	Seller	FLT-168	11/667,193 PCT/AU2005/001721	5/3/07 11/11/05	US
Patent Application	High-Voltage Transistor Fabrication with Trench Etching Technique	Seller	FLT-137	60/626,374 11/270,313	11/9/04 11/9/05	US
Patent	Apparatus and Method for Temperature- Dependant Blocking	Seller	FLT-108	11/270,874 7369387 PCT/US05/040555	11/8/05 11/8/05	US WO 2006/053025
Patent	Apparatus and Method for Enhanced Transient Blocking	Seller	FLT-109	11/270,862 7342433 PCT/US05/041159	11/8/05 11/9/05	US WO 2006/053292
Patent Application	Apparatus and Method for High-Voltage Transient Blocking Using Low-Voltage Elements	Seller	FLT-110	60/626,308 11/271,859 PCT/US05/041292	11/9/04 11/9/05 11/9/05	US WO 2006/053314

Type of IP	Title	Owner	Seller #	Number	Date Filed or Priority Date	Jurisdiction (PCT pub #)
Patent Application	Transient Blocking Unit Having Shunt for Over-Voltage Protection	Seller	FLT-116	60/644,478 60/693,693 60/733,667 11/331,836	1/14/03 6/30/03 11/16/05 1/12/06	US
Patent Application	Low Resistance Transient blocking Unit	Seller	FLT-118	60/644,410 60/677,168 11/332,787	1/14/03 5/3/05 1/12/06	US
Patent Application	Integrated Transient Blocking Unit Compatible with Very High Voltage	Seller	FLT-111	60/626,369 11/139,839 PCT/US2005/041416 JP 2007-540429 05826642.0	11/9/04 5/17/05 11/9/05	US, Japan, EP WO 2006/053326 (Japan) (Europe)
Patent Application	Apparatus and Method for Transient Blocking Employing Relays	Seller	FLT-125	60/663,130 60/675,075 11/419,575 PCT/US06/015841	3/18/03 4/23/03 4/24/06 4/25/06	US WO 2006/116517
Patent Application	Minimized Wire Bonds in Transient Blocking Unit Packaging	Seller	FLT-128	60/651,914 11/249,163 PCT/US06/004408	2/10/03 10/12/05 2/9/06	US WO 2006/086460
Patent Application	Transient Blocking Apparatus with enhanced Electrostatic Discharge Protection	Seller	FLT-134	60/631,917 60/678,632 11/429,661 PCT/US06/017776	2/10/03 5/6/05 5/5/06 5/3/06	US WO 2006/122058
Patent	Flexible Secondary Overcurrent Protection	Seller	FLT-126	60/651,715 11/292,336 7375942 PCT/US06/004606	2/10/03 12/1/03 7/7/06 2/9/06	US WO 2006/086564
Patent Application	Improved Transient Blocking Unit	Seller	FLT-103	60/707,602 11/503,337 PCT/US06/031742	8/11/03 8/19/06 8/11/06	US WO 2007/022136
Patent Application	A Transient Blocking Apparatus with Reset	Seller	FLT-102	60/691,435 11/445,774 PCT/US06/022010	6/16/03 6/2/06 6/5/06	US WO 2006/138119
Patent Application	Adaptive Transient Blocking Unit	Seller	FLT-154	60/837,236 11/891,145 PCT/US07/017856	8/10/06 8/8/07 8/9/07	US WO 2008/021279
Patent Application	Mitigation of Current Collapse in GaN Transient Blocking Units	Seller	FLT-159	60/854,816 11/977,008 PCT/US07/032559	10/27/06 10/22/07 10/23/07	US WO 2008/057222
Patent Application	Bi-Directional Transient Blocking Unit Having a Dual Gate Transistor	Seller	FLT-160	60/860,528 11/886,679 PCT/US07/034408	11/21/06 11/21/07 11/21/07	US WO 2008/063674
Patent Application	Device and Integrated Circuit Including Laterally Floating Capacitively Coupled Structures	Seller	FLT-167	60/932,851 12/156,670	5/31/07 6/2/08	US
Patent Application	Programmable Control IC for Circuit Protection	Seller	FLT-169	60/962,221 12/230,798 PCT/US08/009131	7/26/07 7/25/08 7/25/08	US & PCT
Patent Application	Common Gate Connected High Voltage Transient Blocking Unit	Seller	FLT-171 171A	60/993,222 61/601,658 12/283,430 PCT/US08/010627	9/10/07 11/2/07 9/10/08 9/10/08	US
Patent Application	Integrated Compound Semiconductor on Silicon Transient Blocking Unit	Seller	FLT-173	61/065,411	2/11/08	US
Patent Application	A Tightly Controlled Threshold Voltage DMOS Device	Seller	FLT-175	61/067,747	2/29/08	US
Patent Application	TBU Using Normally-Off Device to Detect the Current Trip Threshold	Seller	FLT-174	61/065,416	2/11/08	US

Type of IP	Title	Owner	Seller #	Number	Date Filed or Priority Date	Jurisdiction (PCT pub #)
Patent Application	Enhanced Break Current Clamp Circuit	Seller	FLT-172	61/003,340	11/15/07	US
Patent Application	A Current Limiting Surge Protection Device	Seller	FLT-176 176A	61/134,588 61/194,939	4/16/08 10/1/08	US
Patent Application	Voltage Triggered THU	Seller	FLT-184	61/133,400	6/26/08	US

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