

## PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT										
NATURE OF CONVEYANCE:	ASSIGNMENT										
CONVEYING PARTY DATA											
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Fulong Zhang</td> <td>12/20/2012</td> </tr> <tr> <td>Chien Kuang</td> <td>03/17/2013</td> </tr> </tbody> </table>		Name	Execution Date	Fulong Zhang	12/20/2012	Chien Kuang	03/17/2013				
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RECEIVING PARTY DATA											
<table border="1"> <tr> <td>Name:</td> <td>Lattice Semiconductor Corporation</td> </tr> <tr> <td>Street Address:</td> <td>5555 NE Moore Ct</td> </tr> <tr> <td>City:</td> <td>Hillsboro</td> </tr> <tr> <td>State/Country:</td> <td>OREGON</td> </tr> <tr> <td>Postal Code:</td> <td>97124</td> </tr> </table>		Name:	Lattice Semiconductor Corporation	Street Address:	5555 NE Moore Ct	City:	Hillsboro	State/Country:	OREGON	Postal Code:	97124
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CORRESPONDENCE DATA											
<p>Fax Number:</p> <p><i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i></p> <p>Phone: 503-268-8629</p> <p>Email: mark.becker@latticesemi.com</p> <p>Correspondent Name: LATTICE SEMICONDUCTOR CORPORATION</p> <p>Address Line 1: 5555 NE MOORE COURT</p> <p>Address Line 4: HILLSBORO, OREGON 97124-6421</p>											
ATTORNEY DOCKET NUMBER:	L12-006US										
NAME OF SUBMITTER:	Mark L Becker										
<p>Total Attachments: 2</p> <p>source=assignment#page1.tif</p> <p>source=assignment#page2.tif</p>											

CH \$40.00 13721867

ASSIGNMENT AND AGREEMENT

For value received, I, as a below-named inventor:

***Fulong Zhang of Cupertino*** in the State of California,  
***Chien Kuang Chen of San Jose*** in the State of California,

hereby sell, assign and transfer to Lattice Semiconductor Corporation, a corporation of the State of Delaware, having an office at 5555 N.E. Moore Ct., Hillsboro, Oregon 97124-6421, U.S.A., and its successors, assigns and legal representatives, the entire right, title and interest, for the United States of America, in and to certain inventions related to **IN-SYSTEM MARGIN MEASUREMENT CIRCUIT** described in an application for Letters Patent of the United States, executed by me of even date herewith, and all the rights and privileges in said application and under any and all Letters Patent that may be granted in the United States for said inventions; and I also concurrently hereby sell, assign and transfer to Lattice Semiconductor Corporation the entire right, title and interest in and to said inventions for all countries foreign to the United States, including all rights of priority arising from the application aforesaid, and all the rights and privileges under any and all forms of protection, including Letters Patent, that may be granted in said countries foreign to the United States for said inventions.

I authorize Lattice Semiconductor Corporation to make application for such protection in its own name and maintain such protection in any and all countries foreign to the United States, and to invoke and claim for any application for patent or other form of protection for said inventions, without further authorization from me, any and all benefits, including the right of priority provided by any and all treaties, conventions, or agreements.

I hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country in proof of the right of Lattice Semiconductor Corporation to apply for patent or other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.


I request that any and all patents for said inventions be issued to Lattice Semiconductor Corporation in the United States and in all countries foreign to the United States, or to such nominees as Lattice Semiconductor Corporation may designate.

Customer No.: 29416


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Attorney Docket No. L12-006

I agree that, when requested, I shall, without charge to Lattice Semiconductor Corporation but at its expense, sign all papers, and do all acts which may be necessary, desirable or convenient in connection with said applications, patents, or other forms of protection.

  
**Fulong Zhang**

Date: 12/20/2012

  
**Chien Kuang**

Date: 3-17 2013

Lattice Semiconductor Corporation  
5555 N.E. Moore Ct.  
Hillsboro, Oregon 97124-6421