

PATENT ASSIGNMENT

Electronic Version v1.1
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SUBMISSION TYPE:	NEW ASSIGNMENT										
NATURE OF CONVEYANCE:	ASSIGNMENT										
CONVEYING PARTY DATA											
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Madhur Bobde</td> <td>10/29/2010</td> </tr> <tr> <td>Anup Bhalla</td> <td>10/29/2010</td> </tr> </tbody> </table>		Name	Execution Date	Madhur Bobde	10/29/2010	Anup Bhalla	10/29/2010				
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RECEIVING PARTY DATA											
<table border="1"> <tr> <td>Name:</td> <td>Alpha and Omega Semiconductor Incorporated</td> </tr> <tr> <td>Street Address:</td> <td>475 Oakmead Pkwy</td> </tr> <tr> <td>City:</td> <td>Sunnyvale</td> </tr> <tr> <td>State/Country:</td> <td>CALIFORNIA</td> </tr> <tr> <td>Postal Code:</td> <td>94085</td> </tr> </table>		Name:	Alpha and Omega Semiconductor Incorporated	Street Address:	475 Oakmead Pkwy	City:	Sunnyvale	State/Country:	CALIFORNIA	Postal Code:	94085
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PROPERTY NUMBERS Total: 1											
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CORRESPONDENCE DATA											
Fax Number: 6509494118 <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i> Email: boinlin@gmail.com Correspondent Name: Bo-In Lin Address Line 1: 13445 Mandoli Drive Address Line 4: Los Altos Hills, CALIFORNIA 94022											
ATTORNEY DOCKET NUMBER:	AOS-1006D										
NAME OF SUBMITTER:	Bo-In Lin										
Signature:	/Bo-In Lin/										
Date:	05/15/2013										
Total Attachments: 2 source=AOS1006 Assignment#page1.tif source=AOS1006 Assignment#page2.tif											

OP \$40.00 13892259

ASSIGNMENT

INVENTOR
AND CITY

Whereas, I, Madhur Bobde of 770 Pellos Lane
San Jose, California 95127

have invented:

TITLE:

**NEW TOPSIDE STRUCTURES FOR AN INSULATED GATE BIPOLAR
TRANSISTOR (IGBT) DEVICE TO ACHIEVE IMPROVED DEVICE
PERFORMANCES**

DATE INVENTOR
SIGNED THE
DECLARATION

and executed a United States patent application therefor
on October 29th, 2010;

Whereas, Alpha and Omega Semiconductor Incorporated, a
California Company, having its registered address at 475 Oakmead
Parkway, Sunnyvale, CA 94085, USA, (hereinafter called AOS), desires
to acquire the entire right, title and interest of said application and
invention, and to any United States and foreign patents to be obtained
therefor;

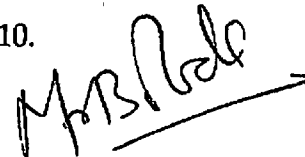
Now therefore, for a valuable consideration, receipt whereof is
hereby acknowledged, I, the above named, hereby sell, assign, and
transfer to AOS, its successors and assigns, the entire right, title and
interest in the said application and invention therein disclosed for the
United States and foreign countries, and all rights of priority resulting
from the filing said United States application, and I request the
Commissioner of Patents to issue any Letters Patent granted upon the
inventions set forth in said application to AOS its successors and
assigns; and I hereby agree that AOS may apply for foreign Letters
Patent on said invention and I will execute all papers necessary in
connection with the United States and foreign applications when called
upon to do so by

CITY AND

Signed and Sealed at Sunnyvale, California

DATE

on October 29th, 2010.



SIGNATURE OF INVENTOR

ASSIGNMENT

INVENTOR
AND CITYWhereas, I, Anup Bhalla of 1406 Block Drive
Santa Clara, California 95050

have invented:

TITLE:

**NEW TOPSIDE STRUCTURES FOR AN INSULATED GATE BIPOLAR
TRANSISTOR (IGBT) DEVICE TO ACHIEVE IMPROVED DEVICE
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Whereas, Alpha and Omega Semiconductor Incorporated, a California Company, having its registered address at 475 Oakmead Parkway, Sunnyvale, CA 94085, USA, (hereinafter called AOS), desires to acquire the entire right, title and interest of said application and invention, and to any United States and foreign patents to be obtained therefor;

Now therefore, for a valuable consideration, receipt whereof is hereby acknowledged, I, the above named, hereby sell, assign, and transfer to AOS, its successors and assigns, the entire right, title and interest in the said application and invention therein disclosed for the United States and foreign countries, and all rights of priority resulting from the filing said United States application, and I request the Commissioner of Patents to issue any Letters Patent granted upon the inventions set forth in said application to AOS its successors and assigns; and I hereby agree that AOS may apply for foreign Letters Patent on said invention and I will execute all papers necessary in connection with the United States and foreign applications when called upon to do so by

CITY AND

Signed and Sealed at Sunnyvale, California

DATE

on 10/29/, 2010.

SIGNATURE OF INVENTOR