PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	RELEASE BY SECURED PARTY	

CONVEYING PARTY DATA

Name	Execution Date
Kreos Capital III (Luxembourg) SARL	09/23/2011

RECEIVING PARTY DATA

Name:	Coreoptics GMBH
Street Address:	Nordostpark12-14 CO 03
City:	Nuremberg
State/Country:	GERMANY
Postal Code:	90411

PROPERTY NUMBERS Total: 2

Property Type	Number
Application Number:	13266138
Application Number:	13266281

CORRESPONDENCE DATA

Fax Number: 4085265952

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

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Address Line 1: 170 West Tasman Drive

Address Line 2: 10/2/1

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ATTORNEY DOCKET NUMBER:	COREOPTICS RELEASE3
NAME OF SUBMITTER:	Karen A. Hallock
Signature:	/Karen A Hallock/
Date:	05/17/2013

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Total Attachments: 8

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RELEASE OF GUARANTY AND SECURITY INTEREST

September 23, 2011

To Whom It May Concern:

Reference is made to that certain letter agreement dated July 13th, 2010, by and between Kreos Capital III (Luxembourg) SARL ("Kreos") and Coreoptics GmbH (the "Payoff Letter"), attached hereto as Schedule 1, regarding that certain Loan Agreement dated March 23, 2007, by and between Coreoptics GmbH ("Coreoptics") and European Ventures Partners III (Luxembourg) S.A.R.L. (the "Loan Agreement").

Reference is also made to that certain Guaranty and Security Agreement dated September 29, 2009 (the "Guaranty and Security Agreement"), by and between Kreos and Coreoptics, Inc. ("Guarantor"), attached hereto as Schedule 2, and that certain Patent Security Agreement dated September 29, 2009 (the "Patent Security Agreement"), by and between Guarantor and Kreos attached hereto as Schedule 3.

As signatory of the Payoff Letter, Kreos hereby acknowledges and agrees that it is the sole owner of and successor in interest to all rights and interests of European Ventures Partners III (Luxembourg) S.A.R.L. in the Loan Agreement, the Guaranty and Security Agreement, and the Patent Security Agreement, and as such that Kreos has full and complete legal rights and authority to bind European Ventures Partners III to the terms and obligations set forth in the Payoff Letter.

In the Payoff Letter, Kreos confirmed that upon receipt by Kreos of the Payoff Amount: (i) all indebtedness of Coreoptics for credit extended under the Loan Agreement shall be fully paid and discharged, (ii) all security interests and other liens of every type granted to or held by Kreos as security for such indebtedness shall be terminated, and (iii) all other obligations of Coreoptics under the Loan Agreement and any other related loan and collateral security agreements shall be terminated.

Kreos hereby confirms that it has received the Payoff Amount, and that all security interests under the Loan Agreement and other obligations of Coreoptics under the Loan Agreement and any other related loan and collateral security agreements have been terminated. Without limiting the foregoing, Kreos acknowledges and agrees that the Guaranty and Security Agreement and the Patent Security Agreement are "related loan and collateral security agreements" under the Loan Agreement, and therefor that all security interests and obligations of Coreoptics and Coreoptics Inc. under the Guarantee and Security Agreement have been terminated and are no longer of any force or effect.

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THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged:

- (1) Kreos hereby forever releases and discharges any and all obligations of Coreoptics, Inc. set forth in the Guarantee and Security Agreement and the Patent Security Agreement, including without limitation any and all Security Interests in and to the Collateral (each as defined in the Guarantee and Security Agreement); and
- (2) Kreos hereby forever releases and discharges any and all security interests set forth in the Patent Security Agreement, including without limitation any interest in or to the Patents (as defined in the Patent Security Agreement), including all patents and patent rights listed on Schedule A attached to the Patent Security Agreement.

IN WHITNESS WHEREOF, Kreos has caused this Release of Guaranty and Security Interest to be duly executed as a sealed instrument by its duly authorized representative effective as of the day and year first set forth above:

KREOSS CAPTIAL III (LUXEMOURG) S.A.R	L.
By: _ NON allegue	
Name: Ross Ahlganix/	
Title: Manager A	***********
Ву:	
Name:	
Title: Manager R	

THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged:

- (1) Kreos hereby forever releases and discharges any and all obligations of Coreoptics, Inc. set forth in the Guarantee and Security Agreement and the Patent Security Agreement, including without limitation any and all Security Interests in and to the Collateral (each as defined in the Guarantee and Security Agreement); and
- (2) Kreos hereby forever releases and discharges any and all security interests set forth in the Patent Security Agreement, including without limitation any interest in or to the Patents (as defined in the Patent Security Agreement), including all patents and patent rights listed on Schedule A attached to the Patent Security Agreement.

IN WHITNESS WHEREOF, Kreos has caused this Release of Guaranty and Security Interest to be duly executed as a sealed instrument by its duly authorized representative effective as of the day and year first set forth above:

KREOSS CAPTIAL III (LUXEMOURG) S.A.R.L.

By:	
By: _ Name	•
Title:	Manager A
By: Name	
Name	Georges/scheuer
Title:	Manager/R

SCHEDULE A PATENTS

U.S. Patent Registrations

Amplitude Detection for Controlling the Decision Instant for Sampling as a Detaflow	r	U. S. A.	Petent 6,930,628 of: 10/131,672 atfr: COUS6
Clock Recovery Circuit		U. S. A.	Patent 7,149,270 of:: 10/138,210 atfr: COUSS
Method and Apparatus for Compensating for Timing Variances in Digital Data Transmission Channels		U. S. A.	Petent 7,095,817 of:: 10/138,212 ath: COUS7
Method and Circuit for Controlling Amplification	uniformity parameter	U. S. A.	Patent 7,319,363 ofr: 10/554,889 agfr: CO2US

Foreign Patent Registrations

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1	CIRCUIT]	ofr: 02725899.5-2415	
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U.S. Patent Applications

CHANNEL ESTIMATION AND SEQUENCE ESTIMATION FOR THE RECEPTION OF OPTICAL SIGNAL	overall receiver design	U. S. A.	Patent application in U. S. A. ofr: 10/563,084 affr: CO4US
Self-timing method for adjustment of a sampling phase in an oversampling receiver and circuit	sampling phase control, population difference parameter	U. S. A.	Petent application in U. S. A. ofr: 10/547,169 atfr: CO3US
Amplitude Detection for Controlling the Decision Instant for Sampling as a Dataflow		U. S. A.	Application of:: 60/288 374 atfr: CO6USp

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Clock Recovery Circuit and Method	U. S. A.	Application of: 60/288 376 atfr: CO5USp
Apparatus and Method for Compensating for Timing Variances in N Digital Data Transmission Channels	U. S. A.	Application of: 60/288 375 atfr: CO7USp
Modification of error statistics behind equalizer to improve inter-working with different FEC codes.	U. S. A.	Patent application in U. S. A. ofr: 11/924,397 atfr: CO15US

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Porcion Patent Applications

	Key Words	State / Region	File Reference
Me HANNEL ESTIMATION IND SEQUENCE STIMATION FOR THE ECEPTION OF OPTICAL	overall receiver design		European Patent Application of: 03015024.7 ath: COEP4 cfr: CO-1949/1950
IGNAL CHANNEL ESTIMATION AND SEQUENCE ESTIMATION FOR THE RECEPTION OP OPTICAL SIGNAL	design	PCT member states	PCT application off: PCT/EP2004/007155 atfr: CO4WO European Patent Application
CHANNEL BSTIMATION AND SEQUENCE ESTIMATION FOR THE RECEPTION OF OPTICAL SIGNAL	overall receiver design	EPC member states	off: 04740522.0 atfr: CO4EP
CHANNEL ESTIMATION AND SEQUENCE ESTIMATION FOR THE RECEPTION OF OPTICAL SIGNAL	overall receiver design	Japan	Patent application in Japan off: 2006-521411 atfr: CO4JP European Patent Application
Solf-timing method for adjustment of a sampling phase in an oversampling	sampling phase control, population difference parameter		ofr: 03004079.4 etfr: COEP3
rectiver and circuit Self-timing method for adjustment of a sampling phase in an oversampling receiver and circuit	sampling phase control, population difference parameter	PCT member states	PCT application of: PCT/EP2004/001838 attr: COWO3 European Patent Application
SELF-TIMING METHOD FOR ADJUSTMENT OF A SAMPLING PHASE IN AN OVERSAMPLING RECEIVER AND CRECUIT	sampling phase control, population difference parameter	EPC member states	ofr: 04714323.5 atfr: CO3EP
Self-timing method for adjustment of a sampling phase in an oversampling receiver and mircuit	difference parameter	Japan	Patent application in Japan off: 2006-501948 attr: CO3JP European Patent Application
Error rate estimation method for a receiver and receiver apparatus	unreliable detection event		of: 03002172.9 atfr: COEP1 PCT application
Brow rate estimation method for a receiver	unreliable detection event	PCT member states	off: PCT/EP2004/000909

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PATENT REEL: 023438 FRAME: 0291

Title	Key Words	State / Region	File Reference
and receiver appearance			attr: COWOI
Tail Extrapolator and Method	tail extrapolation		European Patent Application off: 07102182.8
Tail Extrapolator and Method	tail extrapolation	PCT member states	PCT application of:: PCT/EP2008/051684 ath:: CO11WO
Tail Extrapolator and Method	tail extrapolation	EPC member states + AL, BA, MK, RS	European Patent Application off: 08708917.3 etff: CO11EPa
Amplitude Detection for Controlling the Decision Instant for Sampling as a Dataflow		PCT member states	PCT application off: PCT/US02/14030 atfr: COWO6
AMPLITUDE DETECTION POR CONTROLLING		EPC member states	European Patent Application of: 02734168.4 atfr: COEP6
Clock Recovery Circuit		PCT manber states	PCT application off: PCT/US02/13937 atfr: COWOS
Method and Apperatus for Compensating for Timing Variances in Digital Data Transmission Channels		PCT member states	PCT application of: PCT/US02/13936 stir: COWO7
METHOD AND APPARATUS FOR COMPENSATING FOR TIMING VARIANCES IN DIGITAL DATA TRANSMISSION CHANNELS		EPC member states	Buropean Patent Application of: 02769315.9-2210 stfr: COEP7
Method and circuit for controlling amplification	uniformity parameter		European Patent Application of:: 03009564.0 affr: COEP2
Method and Circuit for Corarolling	uniformity parameter	PCT member states	PCT application of: PCT/EP 2004/004337 atf: CO2WO
Amplification Method and Circuit for Controlling Amplification	uniformity parameter	EPC member states	European Patent Application ofr: 04729098.6 atfr: COZEP
Method and Circuit for Controlling Amplification	uniformity parameter	Japan	Patent application in Japan ofr: 2005-505244 atfr: CO2JP
Receiver, Interleaving and Deinterleaving Circuit and Method		EPC member states + AL, BA, HR, MK, RS	off: 07118489.9 atfr: CO15EP
Receiver, Interloaving and Deinterleaving Circuit and Method		PCT member states	PCT application ofr: PCT/EP2008/063631 atfr: CO15WO

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PATENT REEL: 023438 FRAME: 0292

Title	Key Words	State / Region	File Reference
Receiver, Interleaving and Deleterleaving Circuit and Method		BPC member states + AL, BA, MK, RS	European Patent Application off: 08138908.5 stfr: CO15EPs
Phase control circuit and method for optical receivers	minimize RF power for DQPSK phase control	EPC ntember states + AL, BA, MK, RS	European Patent Application of: 08102343,4 atfr: CO13EP apfr: CO13EP
Phase control circuit and sactacd for optical receivers	minimize RF power for DQPSK phase control	PCT member states	PCT application off: PCT/EP2009/052515 attr: CO13WO apfr: CO13EP
Inversion and swap tolerant coding and decoding vircuits and methods		EPC receiber states + AL, BA, RS	European Petent Application off: 09160058.5 atfr: CO12EP apfr: CO12EP
		EPC member states + AL, BA, RS	European Patent Application of:: 09160054.4 atfr: CO16EP



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