

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT										
NATURE OF CONVEYANCE:	ASSIGNMENT										
CONVEYING PARTY DATA											
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Javier Alejandro Salcedo</td> <td>05/21/2013</td> </tr> <tr> <td>Karl Sweetland</td> <td>05/23/2013</td> </tr> </tbody> </table>		Name	Execution Date	Javier Alejandro Salcedo	05/21/2013	Karl Sweetland	05/23/2013				
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Karl Sweetland	05/23/2013										
RECEIVING PARTY DATA											
<table border="1"> <tr> <td>Name:</td> <td>Analog Devices, Inc.</td> </tr> <tr> <td>Street Address:</td> <td>One Technology Way</td> </tr> <tr> <td>City:</td> <td>Norwood</td> </tr> <tr> <td>State/Country:</td> <td>MASSACHUSETTS</td> </tr> <tr> <td>Postal Code:</td> <td>02062</td> </tr> </table>		Name:	Analog Devices, Inc.	Street Address:	One Technology Way	City:	Norwood	State/Country:	MASSACHUSETTS	Postal Code:	02062
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PROPERTY NUMBERS Total: 1											
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>13902729</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	13902729						
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CORRESPONDENCE DATA											
Fax Number: 9497609502 <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i> Phone: 9497600404 Email: efiling@knobbe.com Correspondent Name: Knobbe, Martens, Olson & Bear LLP Address Line 1: 2040 Main Street, 14th Floor Address Line 4: Irvine, CALIFORNIA 92614											
ATTORNEY DOCKET NUMBER:	ADINC.204A										
NAME OF SUBMITTER:	Kyu Min										
Signature:	/Kyu Min/										
Date:	05/28/2013										
Total Attachments: 3 source=2013_05_23_Assignment_ADINC_204A#page1.tif source=2013_05_23_Assignment_ADINC_204A#page2.tif source=2013_05_23_Assignment_ADINC_204A#page3.tif											

OP \$40.00 13902729

COMBINED DECLARATION & ASSIGNMENT (37 CFR 1.63(e))

Application Data Sheet filed previously or concurrently

Docket No.: ADINC.204A

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Title: ANALOG SWITCH WITH HIGH BIPOLAR BLOCKING VOLTAGE IN LOW VOLTAGE CMOS PROCESS

Inventors: Javier Alejandro Salcedo, Karl Sweetland

Declaration

This Declaration is directed to the application identified above that:

Is attached, where "attached" means filed concurrently with.

As a named inventor, I declare that:

The above-identified application was made or authorized to be made by me.

I believe that I am the original inventor or an original joint inventor in the application.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 USC 1001 by fine or imprisonment of not more than five (5) years, or both.

I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

Assignment from Inventors

WHEREAS, **above-identified inventors** (individual(s) hereinafter "ASSIGNOR") invented certain new and useful improvements, technology, inventions, developments, ideas, ornamental designs, or discoveries, and hereby assign or are under an obligation to assign to the below identified Assignee the above-titled application (collectively hereinafter referred to as the "Work") for which an application for Letters Patent in the United States (identified above) has been prepared for filing with the United States Patent and Trademark Office (hereinafter the "Application").

AND WHEREAS, **ANALOG DEVICES, Inc.**, a Massachusetts Corporation with its principal place of business at One Technology Way, Norwood, Massachusetts 02062 (hereinafter the "ASSIGNEE"), desires to acquire the entire right, title, and interest in and to the Application and the Work.

NOW, THEREFORE, for good and valuable consideration of which receipt is hereby acknowledged, ASSIGNOR hereby acknowledges that ASSIGNOR has sold, assigned, transferred and set over, and by these presents does hereby sell, assign, transfer and set over, unto said ASSIGNEE, its successors, legal representatives and assigns, the entire right, title, and interest throughout the world in the Application and the Work, including all patent properties filed or issued upon the Application and the Work; where "Patent Properties" include, but are not limited to:

all provisional applications relating thereto;

all nonprovisional applications claiming priority to aforementioned provisional(s) and/or the present Application, including, all divisions, continuations, continuations-in-part, and reissues, and all Letters Patent of the United States which may be granted thereon and all reissues and extensions thereof; and

all rights of priority under International Conventions and any related Letters Patent which may hereafter be granted or filed in any country or countries foreign to the United States, all extensions, renewals and reissues thereof.

ASSIGNOR hereby acknowledges the ASSIGNEE as the Applicant for all aforementioned patent properties, and authorizes and requests the Commissioner of Patents of the United States, and any

PATENT

REEL: 030495 FRAME: 0433

COMBINED DECLARATION & ASSIGNMENT (37 CFR 1.63(e))*Application Data Sheet filed previously or concurrently*

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Title: ANALOG SWITCH WITH HIGH BIPOLAR BLOCKING VOLTAGE IN LOW VOLTAGE CMOS PROCESS

Inventors: Javier Alejandro Salcedo, Karl Sweetland

Official of any country or countries foreign to the United States, whose duty it is to issue patents on applications as aforesaid, to issue all related Letters Patent to the ASSIGNEE, its successors, legal representatives and assigns, in accordance with the terms of this instrument.

AND ASSIGNOR DOES HEREBY sell, assign, transfer, and convey to ASSIGNEE, its successors, legal representatives, and assigns all claims for damages and all remedies arising out of any violation of the rights assigned hereby that may have accrued prior to the date of assignment to ASSIGNEE, or may accrue hereafter, including, but not limited to, the right to sue for, collect, and retain damages for past infringements of said Letters Patent before or after issuance.

AND ASSIGNOR DOES HEREBY covenant and agree that ASSIGNOR will communicate to said ASSIGNEE, its successors, legal representatives and assigns, any facts known to ASSIGNOR respecting the Work, and testify in any legal proceeding, assist in the preparation of any other Patent Property relating to the Application and the Work or any improvements made thereto, sign/execute all lawful papers, execute and make all rightful oaths and/or declarations in connection with the Application and the Work including any improvements made thereto, any patent applications filed therefrom, and any continuing application filed from any of the aforementioned applications, and generally do everything possible to aid the ASSIGNEE, its successors, legal representatives and assigns, to obtain and enforce proper patent protection for the Work in all countries.

Legal Name of Inventor: Javier Alejandro Salcedo

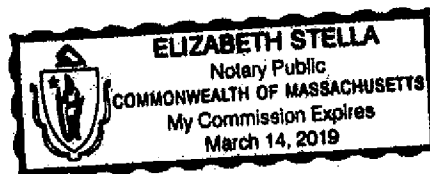
IN TESTIMONY WHEREOF, I hereunto set my hand and seal this 21 day of May, 2013.

Signature: 

Please Sign Before a Notary. Please attach the appropriate notarial documentation.

Elizabeth Stella

Commission Expires March 14, 2019

**PATENT****REEL: 030495 FRAME: 0434**

COMBINED DECLARATION & ASSIGNMENT (37 CFR 1.63(e))

Application Data Sheet filed previously or concurrently

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Title: ANALOG SWITCH WITH HIGH BIPOLAR BLOCKING VOLTAGE IN LOW VOLTAGE CMOS PROCESS

Inventors: Javier Alejandro Salcedo, Karl Sweetland

Legal Name of inventor: Karl Sweetland

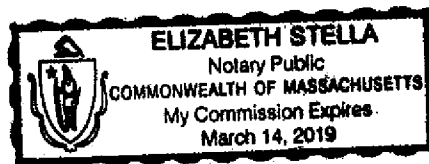
IN TESTIMONY WHEREOF, I hereunto set my hand and seal this 23 day of May, 2013.

Signature: Karl Sweetland

Please Sign Before a Notary. Please attach the appropriate notarial documentation.

Elizabeth Stella
Commission Expires March 14 2019

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