

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Elpida Memory, Inc.	05/30/2013
RECEIVING PARTY DATA	
Name:	Rambus Inc.
Street Address:	1050 Enterprise Way, Suite 700
City:	Sunnyvale
State/Country:	CALIFORNIA
Postal Code:	94089
PROPERTY NUMBERS Total: 12	
Property Type	Number
Patent Number:	7767569
Patent Number:	7585736
Patent Number:	7741215
Patent Number:	7439126
Patent Number:	7534695
Patent Number:	7863191
Patent Number:	7741176
Patent Number:	7708621
Patent Number:	7898877
Patent Number:	7715273
Patent Number:	6934212
Patent Number:	7371694
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	

CH \$480.00 7767569

Phone: 408-462-8000
Email: twain@rambus.com
Correspondent Name: Tarisa Wain
Address Line 1: 1050 Enterprise Way, Suite 700
Address Line 4: Sunnyvale, CALIFORNIA 94089

ATTORNEY DOCKET NUMBER:	ELPIDA GROUP D
-------------------------	----------------

NAME OF SUBMITTER:	Tarisa Wain
--------------------	-------------

Signature:	/Tarisa Wain/
------------	---------------

Date:	06/03/2013
-------	------------

Total Attachments: 2

source=2013-05-30 Elpida Assignment#page1.tif

source=2013-05-30 Elpida Assignment#page2.tif

ASSIGNMENT OF PATENT RIGHTS

Assignor: Elpida Memory, Inc.
Address: 2-1, Yaesu 2-chome
 Chuo-ku, Tokyo
 104-0028 JAPAN

Assignee: Rambus Inc.
Address: 1050 Enterprise Way, Suite 700
 Sunnyvale, CA 94089 USA

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, **Elpida Memory, Inc.** ("Assignor"), a Japanese corporation, and **Rambus Inc.**, ("Assignee"), a Delaware corporation, agree as follows:

Patents

"Patents" means the following patents and patent applications:

App. No.	Pat. No.	Cty.	Filing Date	Title
11/546986	7767569	US	10/13/2006	METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE
11/723092	7585736	US	3/16/2007	METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE WITH REGARD TO FILM THICKNESS OF GATE OXIDE FILM
11/809478	7741215	US	6/1/2007	SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME
11/601720	7439126	US	11/20/2006	METHOD FOR MANUFACTURING SEMICONDUCTOR MEMORY
11/890425	7534695	US	8/6/2007	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE
11/860579	7863191	US	9/25/2007	MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE
12/013509	7741176	US	1/14/2008	METHOD FOR FABRICATING A CYLINDRICAL CAPACITOR INCLUDING IMPLANTING IMPURITIES INTO THE UPPER SECTIONS OF THE LOWER ELECTRODE TO PREVENT THE FORMATION OF HEMISPHERICAL GRAIN SILICON ON THE UPPER SECTIONS
12/051156	7708621	US	3/19/2008	POLISHING APPARATUS AND METHOD OF RECONDITIONING POLISHING PAD
12/659521	7898877	US	3/11/2010	SYNCHRONOUS SEMICONDUCTOR DEVICE AND DATA PROCESSING SYSTEM INCLUDING THE SAME
12/216674	7715273	US	7/9/2008	SYNCHRONOUS SEMICONDUCTOR SEVICE AND DATA PROCESSING SYSTEM INCLUDING THE SAME
10/724684	6934212	US	12/2/2003	SEMICONDUCTOR APPARATUS
11/056128	7371694	US	2/14/2005	SEMICONDUCTOR DEVICE FABRICATINO METHOD AND FABRICATION APPARATUS

Assignment

Assignor hereby irrevocably assigns, transfers, and conveys to Assignee all of its right, title, and interest in and to the Patents, and all rights, claims, and privileges pertaining to, arising out of, or associated with, the Patents, including, without limitation, the right to the underlying inventions, the right to file, prosecute and maintain the Patents, and the right to sue, seek equitable relief, and recover damages for past, present, and future infringement thereof.

Further Assignment


Assignor further assigns to and empowers Assignee, its successors, assigns, or nominees, all rights to make applications for patents or other forms of protection for said inventions and to prosecute such applications and the Patents.

Assignor's representative signing below represents that he or she is duly authorized to sign documents on behalf of Assignor.

Assignor grants Assignee or its agents the power to insert on this Assignment any further identification that may be necessary or desirable to in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

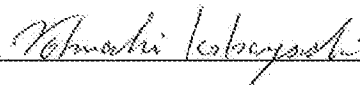
EXECUTED this 30th day of May, 2013.
day month

ELPIDA MEMORY, INC.

Signed 

Typed Name: Yukio Sakamoto

Title: Trustee

Signed 

Typed Name: Nobuaki Kobayashi

Title: Trustee