

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Sanmina Corporation	06/04/2013
RECEIVING PARTY DATA	
Name:	Memory Integrity, LLC
Street Address:	1220 N. Market Street, Suite 806
City:	Wilmington
State/Country:	DELAWARE
Postal Code:	19801
PROPERTY NUMBERS Total: 55	
Property Type	Number
Patent Number:	7921188
Patent Number:	7107409
Patent Number:	7103725
Patent Number:	7107408
Patent Number:	6986069
Patent Number:	6865595
Patent Number:	7395379
Patent Number:	7653790
Patent Number:	7103636
Patent Number:	7039740
Patent Number:	7155525
Patent Number:	7281055
Patent Number:	7251698
Patent Number:	7577755
Patent Number:	7418517

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Patent Number:	8185602
Patent Number:	6934814
Patent Number:	6920532
Patent Number:	6925536
Patent Number:	7162589
Patent Number:	6950913
Patent Number:	7003633
Patent Number:	7103726
Patent Number:	7047372
Patent Number:	7222262
Patent Number:	7337279
Patent Number:	7024521
Patent Number:	7272688
Patent Number:	7346744
Patent Number:	7249224
Patent Number:	7334089
Patent Number:	7103823
Patent Number:	7069392
Patent Number:	7159137
Patent Number:	7395347
Patent Number:	7117419
Patent Number:	7386626
Patent Number:	7577727
Patent Number:	7194660
Patent Number:	7719964
Patent Number:	7296121
Patent Number:	7080284
Patent Number:	7386796
Patent Number:	7225327
Patent Number:	7512971
Patent Number:	7908648
Patent Number:	7561572
Patent Number:	7877629
Patent Number:	7647596
Patent Number:	7694309

	7492716
Patent Number:	8411591
Application Number:	13327483
Application Number:	11258228
Application Number:	13783190

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Email: swood@farneydaniels.com

Correspondent Name: Stephanie Wood

Address Line 1: 800 S. Austin Ave., Unit 200

Address Line 4: Georgetown, TEXAS 78626

NAME OF SUBMITTER:	Stephanie R. Wood
Signature:	/s/ Stephanie R. Wood
Date:	06/11/2013
	This document serves as an Oath/Declaration (37 CFR 1.63).

Total Attachments: 7

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ASSIGNMENT

WHEREAS, Sanmina Corporation, with an office at 2700 North First Street, San Jose, California 95134, together with any successors, legal representatives or assigns thereof, called "Assignor") is the owner of and desires to assign the entire right, title, and interest in the Patents listed in the Patent List attached hereto (the "Patents");

AND WHEREAS, Memory Integrity, LLC, having a principal place of business at 4101 County Road 253, Stephenville, Texas 76401 (hereafter, together with any successors, legal representatives or assigns thereof, called "ASSIGNEE") wants to acquire the entire right, title and interest in and to said Patents and all the inventions therein, and Assignor is willing to enter into such assignment.

NOW, THEREFORE, effective on June 4, 2013 and in consideration of the sum of One Dollar (\$1.00) in hand paid and other good and valuable consideration the receipt of which from ASSIGNEE is hereby acknowledged, Assignor has sold, assigned, transferred and set over, and does hereby sell, assign, transfer and set over to ASSIGNEE the entire right, title and interest in and to the Patents, and any patents, patent applications, foreign patents, foreign patent applications, continuations, continuations-in-part, divisionals, extensions, renewals, reissues and re-examinations claiming priority directly from the Patents, including without limitation, all rights to claim priority directly therefrom, all rights to sue for past, present and future infringement, including the right to collect and receive any damages, royalties, or settlements for such infringements, and all rights to sue for injunctive or other equitable relief.

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IN TESTIMONY WHEREOF, I hereunto set my hand this 4th day of June, 2013.

Sanmina Corporation
(Assignor)

By [Signature]
Name Robert K Eulau
Title CEO

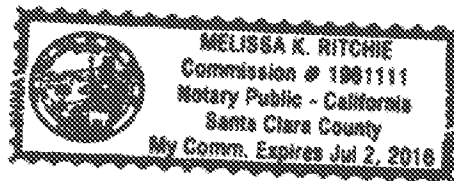
STATE OF)

COUNTY OF)

On 4 June 2013, before me Melissa K Ritchie,
Notary Public, personally appeared Robert Eulau
proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to
the within instrument and acknowledged to me that he executed the same in his authorized
capacity, and that by his signature on the instrument the person, or the entity upon behalf of
which the person acted, executed the instrument.

Signature of Notary

Melissa K Ritchie



PATENT LIST

Patent US	Patent Title	Filing Date
7,921,188	COMPUTER SYSTEM PARTITIONING USING DATA TRANSFER ROUTING MECHANISM	8/16/2001
7,107,409	METHODS AND APPARATUS FOR SPECULATIVE PROBING AT A REQUEST CLUSTER	3/22/2002
7,103,725	METHODS AND APPARATUS FOR SPECULATIVE PROBING WITH EARLY COMPLETION AND DELAYED REQUEST	3/22/2002
7,107,408	METHODS AND APPARATUS FOR SPECULATIVE PROBING WITH EARLY COMPLETION AND EARLY REQUEST	3/22/2002
6,986,069	METHODS AND APPARATUS FOR STATIC AND DYNAMIC POWER MANAGEMENT OF COMPUTER SYSTEMS	7/1/2002
6,865,595	METHODS AND APPARATUS FOR SPECULATIVE PROBING OF A REMOTE CLUSTER	5/28/2002
7,395,379	METHODS AND APPARATUS FOR RESPONDING TO A REQUEST CLUSTER	5/13/2002
7,653,790	METHODS AND APPARATUS FOR RESPONDING TO A REQUEST CLUSTER	5/13/2002
7,103,636	METHODS AND APPARATUS FOR SPECULATIVE PROBING OF A REMOTE CLUSTER	5/28/2002
7,039,740	INTERRUPT HANDLING IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS	7/19/2002
7,155,525	TRANSACTION MANAGEMENT IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS	5/28/2002
7,281,055	ROUTING MECHANISMS IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS	5/28/2002
7,251,698	ADDRESS SPACE MANAGEMENT IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS	5/28/2002
7,577,755	METHODS AND APPARATUS FOR DISTRIBUTING SYSTEM MANAGEMENT SIGNALS	11/19/2002
7,418,517	METHODS AND APPARATUS FOR DISTRIBUTING SYSTEM MANAGEMENT SIGNALS	1/30/2003

8,185,602	TRANSACTION PROCESSING USING MULTIPLE PROTOCOL ENGINES IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS	11/5/2002
6,934,814	CACHE COHERENCE DIRECTORY EVICTION MECHANISMS IN MULTIPROCESSOR SYSTEMS WHICH MAINTAIN TRANSACTION ORDERING	11/5/2002
6,920,532	CACHE COHERENCE DIRECTORY EVICTION MECHANISMS FOR MODIFIED COPIES OF MEMORY LINES IN MULTIPROCESSOR SYSTEMS	11/5/2002
6,925,536	CACHE COHERENCE DIRECTORY EVICTION MECHANISMS FOR UNMODIFIED COPIES OF MEMORY LINES IN MULTIPROCESSOR SYSTEMS	11/5/2002
7,162,589	METHODS AND APPARATUS FOR CANCELING A MEMORY DATA FETCH	12/16/2002
6,950,913	METHODS AND APPARATUS FOR MULTIPLE CLUSTER LOCKING	11/8/2002
7,003,633	METHODS AND APPARATUS FOR MANAGING PROBE REQUESTS	11/4/2002
7,103,726	METHODS AND APPARATUS FOR MANAGING PROBE REQUESTS	11/4/2002
7,047,372	MANAGING I/O ACCESSSES IN MULTIPROCESSOR SYSTEMS	4/15/2003
7,222,262	METHODS AND DEVICES FOR INJECTING COMMANDS IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS	8/5/2003
7,337,279	METHODS AND APPARATUS FOR SENDING TARGETED PROBES	6/27/2003
7,024,521	MANAGING SPARSE DIRECTORY EVICTIONS IN MULTIPROCESSOR SYSTEMS VIA MEMORY LOCKING	4/24/2003
7,272,688	METHODS AND APPARATUS FOR PROVIDING CACHE STATE INFORMATION	4/28/2003
7,346,744	METHODS AND APPARATUS FOR MAINTAINING REMOTE CLUSTER STATE INFORMATION	5/9/2003
7,249,224	METHODS AND APPARATUS FOR PROVIDING EARLY RESPONSES FROM A REMOTE DATA CACHE	8/5/2003

7,334,089	METHODS AND APPARATUS FOR PROVIDING CACHE STATE INFORMATION	5/20/2003
7,103,823	COMMUNICATION BETWEEN MULTI-PROCESSOR CLUSTERS OF MULTI-CLUSTER COMPUTER SYSTEMS	8/5/2003
7,069,392	METHODS AND APPARATUS FOR EXTENDED PACKET COMMUNICATIONS BETWEEN MULTIPROCESSOR CLUSTERS	6/12/2003
7,159,137	SYNCHRONIZED COMMUNICATION BETWEEN MULTI-PROCESSOR CLUSTERS OF MULTI-CLUSTER COMPUTER SYSTEMS	8/5/2003
7,395,347	COMMUNICATION BETWEEN AND WITHIN MULTI-PROCESSOR CLUSTERS OF MULTI-CLUSTER COMPUTER SYSTEMS	8/5/2003
7,117,419	RELIABLE COMMUNICATION BETWEEN MULTI-PROCESSOR CLUSTERS OF MULTI-CLUSTER COMPUTER SYSTEMS	8/5/2003
7,386,626	BANDWIDTH, FRAMING AND ERROR DETECTION IN COMMUNICATIONS BETWEEN MULTI-PROCESSOR CLUSTERS OF MULTI-CLUSTER COMPUTER SYSTEMS	6/23/2003
7,577,727	DYNAMIC MULTIPLE CLUSTER SYSTEM RECONFIGURATION	6/27/2003
7,194,660	MULTI-PROCESSING IN A BIOS ENVIRONMENT	6/23/2003
7,719,964	DATA CREDIT POOLING FOR POINT-TO-POINT LINKS	8/12/2004
7,296,121	REDUCING PROBE TRAFFIC IN MULTIPROCESSOR SYSTEMS	10/15/2004
7,080,284	COMPUTER SERVER ARCHITECTURE AND DIAGNOSTIC FRAMEWORK FOR TESTING SAME	7/19/2002
7,386,796	METHOD AND EQUIPMENT ADAPTED FOR MONITORING SYSTEM COMPONENTS OF A DATA PROCESSING SYSTEM	4/1/2003
7,225,327	METHOD, SYSTEM, SOFTWARE AND PROCESSOR FOR INITIALIZING INFORMATION SYSTEMS OPERATING IN HEADLESS AND NON-HEADLESS ENVIRONMENTS	4/11/2003
7,512,971	METHOD AND SYSTEM FOR ENABLING REMOTE ACCESS TO A COMPUTER SYSTEM	1/29/2004

7,908,648	METHOD AND SYSTEM FOR ENABLING REMOTE ACCESS TO A COMPUTER SYSTEM	2/17/2009
7,561,572	SYNTHESIS OF GLOBAL TRANSACTION TAGS	7/20/2005
7,877,629	FACILITATING HANDLING OF EXCEPTIONS IN A PROGRAM IMPLEMENTING A M-ON-N THREADING MODEL	6/1/2005
7,647,596	METHOD FOR SHARING A DATA STORE ACROSS EVENT MANAGEMENT FRAMEWORKS AND SYSTEM COMPRISING SAME	12/8/2005
7,694,309	METHOD FOR FACILITATING UNIFICATION OF EVENT FRAMEWORK VIEWS AND SYSTEM COMPRISING SAME	2/14/2006
7,492,716	METHOD FOR EFFICIENTLY RETRIEVING TOPOLOGY-SPECIFIC DATA FOR POINT-TO-POINT NETWORKS	10/26/2005
8,411,591	METHOD FOR EFFICIENTLY RETRIEVING TOPOLOGY-SPECIFIC DATA FOR POINT-TO-POINT NETWORKS	2/4/2009
Foreign		
Australia 2002324671	COMPUTER SYSTEM PARTITIONING USING DATA TRANSFER ROUTING MECHANISM	8/9/2002
Germany 1561162	METHODS AND APPARATUS FOR MULTIPLE CLUSTER LOCKING	11/4/2003
France 1561162	METHODS AND APPARATUS FOR MULTIPLE CLUSTER LOCKING	11/4/2003
United Kingdom 1561162	METHODS AND APPARATUS FOR MULTIPLE CLUSTER LOCKING	11/4/2003
Italy 1561162	METHODS AND APPARATUS FOR MULTIPLE CLUSTER LOCKING	11/4/2003
United Kingdom 1652091	METHODS AND APPARATUS FOR PROVIDING EARLY RESPONSES FROM A REMOTE DATA CACHE	7/29/2004
China 200480020505.9	METHODS AND APPARATUS FOR PROVIDING CACHE STATE INFORMATION	5/20/2004
China 200480024015.6	DYNAMIC MULTIPLE CLUSTER SYSTEM RECONFIGURATION	6/18/2004

Pending Patent Applications Application/ Serial Number	Patent Application Title	Filing Date
EP 02759329.2	COMPUTER SYSTEM PARTITIONING USING DATA TRANSFER ROUTING MECHANISM	8/9/2002
13/327,483	TRANSACTION PROCESSING USING MULTIPLE PROTOCOL ENGINES IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS	12/15/2011
11/258,228	METHOD FOR EFFICIENTLY CALCULATING DEADLOCK-FREE OPTIMAL ROUTING MATRICES FOR N-NODE POINT TO POINT NETWORKS	10/24/2005
13/783,190	METHOD FOR EFFICIENTLY RETRIEVING TOPOLOGY-SPECIFIC DATA FOR POINT-TO-POINT NETWORKS	3/1/2013

Memory Integrity-Sanmina Agreement