

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE BY SECURED PARTY
CONVEYING PARTY DATA	
Name	Execution Date
Citibank, N.A.	03/15/2013
RECEIVING PARTY DATA	
Name:	Freescale Semiconductor, Inc.
Street Address:	6501 William Cannon Drive West
City:	Austin
State/Country:	TEXAS
Postal Code:	78735
PROPERTY NUMBERS Total: 8	
Property Type	Number
Patent Number:	5951678
Patent Number:	5983338
Patent Number:	7124281
Patent Number:	6865667
Patent Number:	7444668
Patent Number:	7686000
Patent Number:	7584344
Patent Number:	6751724
CORRESPONDENCE DATA	
Fax Number:	2144364683
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	214-494-4805
Email:	ipadmin@mosaid.com
Correspondent Name:	Mosaid Corporation Ltd.
Address Line 1:	5700 Granite Parkway
Address Line 2:	Suite 960

CH \$320.00 5951678

Address Line 4: Plano, TEXAS 75024

ATTORNEY DOCKET NUMBER:

FREESCALE SECURITY AGREE

NAME OF SUBMITTER:

Julie McLeod

Signature:

/Julie McLeod/

Date:

07/02/2013

Total Attachments: 4

source=Feb_July_CAGR Patent Release 3.15.13 [Execution]#page1.tif

source=Feb_July_CAGR Patent Release 3.15.13 [Execution]#page2.tif

source=Feb_July_CAGR Patent Release 3.15.13 [Execution]#page3.tif

source=Feb_July_CAGR Patent Release 3.15.13 [Execution]#page4.tif

PATENT RELEASE

THIS RELEASE is effective as of March 15, 2013 by Citibank, N.A., as Collateral Agent (as defined below) for the Lenders. Capitalized terms used herein and not otherwise defined shall have the meanings assigned to such terms in the Credit Agreement and IP Agreement, in each case, referred to below.

A. Reference is made to (i) the THIRD AMENDED AND RESTATED CREDIT AGREEMENT entered into as of March 1, 2013 (as otherwise amended, supplemented or modified from time to time, the “**Credit Agreement**”), among FREESCALE SEMICONDUCTOR, INC. (the “**Borrower**”), a Delaware corporation, FREESCALE SEMICONDUCTOR HOLDINGS V, INC. (“**Holdings**”), a Delaware corporation, FREESCALE SEMICONDUCTOR HOLDINGS IV, LTD., a Bermuda exempted limited liability company, FREESCALE SEMICONDUCTOR HOLDINGS III, LTD. (“**Parent**”), a Bermuda exempted limited liability company, CITIBANK, N.A., as administrative agent, collateral agent (in such capacity, the “**Collateral Agent**”), swing line lender and L/C issuer, and each lender from time to time party thereto (collectively, the “**Lenders**”); (ii) the INTELLECTUAL PROPERTY SECURITY AGREEMENT (as amended, supplemented or modified from time to time, the “**IP Agreement**”) dated as of December 1, 2006, among the Borrower, Holdings, the Subsidiaries of Parent from time to time party thereto and CITIBANK, N.A., as Collateral Agent; (iii) the PATENT SECURITY AGREEMENT (as amended, supplemented or modified from time to time, “**March Patent Agreement**”) dated as of March 12, 2008, among the Borrower and CITIBANK, N.A., as Collateral Agent; (iv) the PATENT SECURITY AGREEMENT (as amended, supplemented or modified from time to time, “**July Patent Agreement**”) dated as of July 28, 2008, among the Borrower and CITIBANK, N.A., as Collateral Agent; (v) the SUPPLEMENT NO. 4 (the “**Supplement**”) dated as of June 5, 2008, to the IP Agreement, among the Borrower, Holdings, the Subsidiaries of Parent and CITIBANK, N.A., as Collateral Agent; (vi) the SECURITY AGREEMENT dated as of December 1, 2006 among Freescale Acquisition Corporation, Freescale Semiconductor, Inc., Freescale Acquisition Holdings Corp., Freescale Holdings (Bermuda) IV, Ltd., the Subsidiaries of Freescale Holdings (Bermuda) III, Ltd. from time to time party thereto and Citibank, N.A. as Collateral Agent; and (vii) the other Collateral Documents (as defined in the Credit Agreement).

B. The IP Agreement was recorded in the Patent Division of the United States Patent and Trademark Office on February 2, 2007, at Reel 018855 and Frame 0129. The Supplement was recorded in the Patent Division of the United States Patent and Trademark Office on July 9, 2008, at Reel 021212 and Frame 0372. The March Patent Agreement was recorded in the Patent Division of the United States Patent and Trademark Office on July 11, 2008, at Reel 021217 and Frame 0368. The July Patent Agreement was recorded in the Patent Division of the United States Patent and Trademark Office on September 24, 2008, at Reel 021570 and Frame 0449.

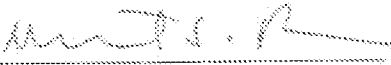
C. The Borrower has informed the Collateral Agent that it has disposed of the Patents listed on Schedule I attached hereto in a transaction permitted under the Credit Agreement.

Accordingly, the Collateral Agent's interest (including security interests) in and to the Patents listed on Schedule I attached hereto shall hereby terminate and the security interest recorded with the United States Patent & Trademark Office against such Patents shall hereby cease and become void.

[Remainder of page intentionally blank]

IN WITNESS WHEREOF, the Collateral Agent has caused this Release to be duly executed by its duly authorized officer.

CITIBANK, N.A., as Collateral Agent,

Signature: 

Print: Matthew Burke

Title: Vice President

Date: 3/15/2013

[Patent Release]

SCHEDULE I

I. Patents

Holder	Patent No.	Description/Title
Freescale Semiconductor, Inc.	7584344	INSTRUCTION FOR CONDITIONALLY YIELDING TO A READY THREAD BASED ON PRIORITY CRITERIA
Freescale Semiconductor, Inc.	8060730	SELECTIVE MISR DATA ACCUMULATION DURING EXCEPTION PROCESSING
Freescale Semiconductor, Inc.	7984337	ADDRESS TRANSLATION TRACE MESSAGE GENERATION FOR DEBUG
Freescale Semiconductor, Inc.	7992052	PROGRAM CORRELATION MESSAGE GENERATION FOR DEBUG
Freescale Semiconductor, Inc.	6751724	METHOD AND APPARATUS FOR INSTRUCTION FETCHING
Freescale Semiconductor, Inc.	7124281	PROCESSING SYSTEM HAVING SEQUENTIAL ADDRESS INDICATOR SIGNALS
Freescale Semiconductor, Inc.	6865667	DATA PROCESSING SYSTEM HAVING REDIRECTING CIRCUITRY AND METHOD THEREOF
Freescale Semiconductor, Inc.	7444668	METHOD AND APPARATUS FOR DETERMINING ACCESS PERMISSION
Freescale Semiconductor, Inc.	7686000	CONTROLLER AND METHOD FOR CONTROLLING AN IGNITION COIL
Freescale Semiconductor, Inc.	5951678	METHOD AND APPARATUS FOR CONTROLLING CONDITIONAL BRANCH EXECUTION IN A DATA PROCESSOR
Freescale Semiconductor, Inc.	5983338	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR FOR COMMUNICATING REGISTER WRITE INFORMATION