502414003 07/10/2013

PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
PLX Technology, Inc.	04/30/2013
Teranetics, Inc.	04/30/2013

RECEIVING PARTY DATA

Name:	Aquantia Corporation
Street Address:	700 Tasman Drive
City:	Milpitas
State/Country:	CALIFORNIA
Postal Code:	95035

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	13398748

CORRESPONDENCE DATA

Fax Number: 4082366641

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 4082366635

Email: assignment@mpkpatentlaw.com

Correspondent Name: Katayoun Ghazian

Address Line 1: 550 South Winchester Blvd., Suite 605

Address Line 4: San Jose, CALIFORNIA 95128

ATTORNEY DOCKET NUMBER:	AQUA.P205
NAME OF SUBMITTER:	Katayoun Ghazian
Signature:	/Katayoun Ghazian/
Date:	07/10/2013

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Total Attachments: 7

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AMENDMENT TO PATENT ASSIGNMENT

PLX Technology, Inc. and Teranetics, Inc. (collectively, "Assignor") on the one hand, and Aquantia Corp. ("Assignee") on the other hand, execute this Amendment to Patent Assignment effective as of April 2013 ("Amendment").

Assignor executed that certain Patent Assignment effective as of September 20, 2012 in favor of Assignee to evidence the transfer of certain patent rights from Assignor to Assignee in connection with the Asset Purchase Agreement dated September 20, 2012 between Assignor and Assignee (the "Patent Assignment").

Assignor and Assignee hereby agree as follows:

- 1. The Patent Assignment incorrectly indicated the Patent titled "Clock Recovery System" to have Serial Number 13/471,613.
- 2. The parties hereby amend the Patent Assignment to indicate the correct Serial Number for Clock Recovery System is 13/398,748.
- 3. Except as provided herein, the Patent Assignment remains in full force and effect.
- 4. This Amendment may be executed in one or more counterparts, including counterparts transmitted by facsimile or electronic transmission, all of which together shall be deemed to constitute one and the same instrument.

The parties have executed this Amendment to Patent Assignment effective April 30, 2013.

PLX Technology, Inc.:	Teranetics, Inc.
By: Cetta Carril	By: Cuth Comp
Name: HRTHUR OWHIPPLE	Name: ARTHUR O. WHIPPLE
Title: CFO	Title: <u>CFO</u>
Aquantia Corp.	
By: Faraj Aalae Digitally signed by Faraj Aalaei Oht: cn=5raj Aala	
Name:	
Title:	

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PATENT ASSIGNMENT

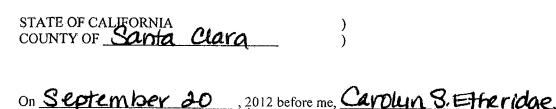
PLX Technology, Inc. and Teranetics, Inc. (collectively, "Assignor") execute this instrument effective as of September 20, 2012 in favor of Aquantia Corp. ("Assignee") to evidence the transfer of certain patent rights from Assignor to Assignee in connection with the Asset Purchase Agreement dated September 20, 2012 between Assignor and Assignee.

- 1. For good and valuable consideration, receipt of which the Assignor acknowledges the Assignor hereby sells, assigns and transfers to the Assignee all of the Assignor's right, title, and interest in and to:
- (a) the patents, patent applications and invention disclosures specifically listed in Annex A to this Patent Assignment (collectively, the "Assigned Patent Rights");
- (b) the inventions disclosed in the Assigned Patent Rights together with the right to file applications and obtain patents for said inventions in Assignee's own name throughout the world; and
- (c) the following properties and rights with respect to the Assigned Patent Rights:
- (i) any patents in the United States and anywhere else in the world and any patent applications that may be granted or filed, respectively, with respect to the Assigned Patent Rights;
- (ii) all rights to sue and recover damages and payments for past, present, and future infringements of any of the Assigned Patent Rights; and
- (iii) other than with respect to royalties and other payments due under license agreements executed by Assignor with third parties prior to the date hereof, all income, royalties, damages, and payments due or payable to the Assignor with respect to the Assigned Patent Rights.
- 2. This Patent Assignment (a) benefits and binds the parties and their respective successors and assignees, and (b) may be signed in counterparts.

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The undersigned have executed this Patent Assignment on September 20, 2012.

PLX Technology, Inc.:	Teranetics, Inc.
By: Cofaction	By: Cetalon
Name: ARTHUR DWHIPPLE	Name: ACTURE O. WHIPPLE
Title: CR	Title: DIRECTOR



I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature: (seal)



ANNEX A TO PATENT ASSIGNMENT

	Wiling Date	Serial###	Paten 2
A FULL DUPLEX	11/03/2003	10/699,761	7333448
TRANSCEIVER	11/05/2005	10/075,701	7555440
TRANSOBIVER			
A METHOD AND APPARATUS	10/28/2003	10/695,166	7227883
FOR DOMAIN			
TRANSFORMATION MULTIPLE			
SIGNAL PROCESSING	04/1/2004	30/916 400	7266021
SUB-BLOCK DOMAIN TRANSFORMATION MULTIPLE	04/1/2004	10/816,409	7366231
SIGNAL PROCESSING			
PARALLEL FEEDBACK	06/29,/2004	10/880,052	<u> </u>
PROCESSING	00/27,72004	10/000,032	
SINGLE AMPLIFIER	06/09/2005	11/148,988	7466746
PRESAMPLE PROCESSING		11	
CIRCUITRY			
LOW POWER RECEIVER	08/26,/2004	10/926,699	7747923
DECODING	,		
MAPPING BETWEEN	08/26/2004	10/927,463	
ENCODED DATA AND			
CONSTELLATION POINTS	03/25/2005	11/090,109	7461328
EFFICIENT DECODING	03/23/2003	11/090,109	/401328
RECEIVER ADC CLOCK	08/17/2005	11/205,615	7720015
DELAY BASED ON ECHO			
SIGNALS			
MULTIPLE MODULATION	10/11/2005	11/247,805	7782852
RATE 10GBASE-T			
TRANSMISSION			
TRANSCEIVER POWER	12/16/2005	11/303,364	7646699
BACKOFF	12/10/2003	11/202,204	,0,100//
TRANSMISSION PRE-CODING	12/21/2005	60/752,729	
AUTO-SEQUENCING	02/13/2006	11/352,669	
TRANSMISSION SPEED OF A			
DATA PORT		***************************************	
TRANSCEIVER NON-	03/13/2006	11/373,928	
LINEARITY CANCELLATION			
MASTER/SLAVE	05/22/2006	11/438,177	7860020
TRANSCEIVER POWER BACK-			
OFF			
Multiple Transmission Protocol	08/28/2006	11/510,934	7782929
Transceiver			<u> </u>

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and a second	Tiling Date	Serial#27.	Paten##
Aiding Synchronization Between	12/22/2006	11/644,598	7729464
Master and Slave Transceivers			
A METHOD AND APPARATUS	02/5, 2007	11/702,348	7362791
FOR DOMAIN			
TRANSFORMATION MULTIPLE			
SIGNAL PROCESSING			
SELECTION OF FILTER	3/19/2007	11/725,528	7957456
COEFFICIENTS FOR			·
TRANSCEIVER NON-	}		
LINEARITY SIGNAL	<u> </u>		
CANCELLATION	<u> </u>		
GENERATING AN ESTIMATED	3/20/2008	12/077,672	
NON-LINEAR ECHO SIGNAL			
EFFICIENT DECODING	7/10/2008	12/217,962	7634710
CONTROLLING ACTIVATION	10/28/2008	12/290,181	7881330
OF ELECTRONIC CIRCUITRY			
OF DATA PORTS OF A			
COMMUNICATION SYSTEM			
INTERFACING MEDIA ACCESS	04/02/2009	12/384,298	
CONTROL (MAC) WITH A			
LOW-POWER PHYSICAL			
LAYER (PHY) CONTROL			
EFFICIENT DECODING	11/06/2009	12/613,627	8234550
REDUCING TRANSMIT SIGNAL	04/22/2010	12/765,097	
COMPONENTS OF A RECEIVE			
SIGNAL OF A TRANSCEIVER			
REDUCING	05/28/2010	12/789,728	
ELECTROMAGNETIC			
INTERFERENCE IN A			
RECEIVED SIGNAL			
REDUCING	06/18/2010	12/818,549	1
ELECTROMAGNETIC			
INTERFERENCE IN A RECEIVE			
SIGNAL WITH AN ANALOG			
CORRECTION SIGNAL			
REDUCING TRANSMIT SIGNAL	07/19/2010	12/839,254	8254490
COMPONENTS OF A RECEIVE	0111714010	1.21037,237	0207770
SIGNAL OF A TRANSCEIVER			
USING A SHARED DAC			
ARCHITECTURE			
A METHOD AND APPARATUS	08/16/10	12/857,080	
FOR FAST LINK RECOVERY	00/10/10	127007,000	
TOK PAST LINK RECOVER I		L	l

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TIME TO SECURE A SECURITION OF THE SECURITIES.	Filing Date	Serial# 14	Patent###
A full duplex 10GBase-T Transmitter Hybrid with SFDR < - 65dBc over 400MHz in 40nm CMOS	09/10/10	61/381,911	
ADJUSTABLE LATENCY TRANSCEIVER PROCESSING	12/04/2010	12/960,474	
MASTER/SLAVE TRANSCEIVER POWER BACK- OFF	11/06/2010	12/941,039	
CONTROLLING ACTIVATION OF ELECTRONIC CIRCUITRY OF DATA PORTS OF A COMMUNICATION SYSTEM	12/04/2010	12/960,481	
TRANSCEIVER SPECTRUM CONTROL FOR CROSS-TALK MITIGATION	09/16/2011	13/235,283	
CLOCK RECOVERY SYSTEM	02/16/2012	13/471,613	
METHODS FOR DIGITAL AND ANALOG CALIBRATION OF AN ETHERNET TRANSCEIVER FOR IMPROVED LINK PERFORMANCE			

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RECORDED: 07/10/2013