Form <b>PTO-1595</b> (Rev. 03-11) DMB No. 0651-0027 (exp. 03/31/2015)	U.S. DEPARTMENT OF COMMERCE United States Patent and Trademark Office					
RECORDATION FOR						
PATENTS ONLY  To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.						
1. Name of conveying party(ies):	Name and address of receiving party(les)     Name: Round Rock Research, LLC					
Micron Technology, Inc.						
	Internal Address:					
Additional name(s) of conveying party(ies) attached? Yes X No	Street Address:					
3. Nature of conveyance/Execution Date(s):						
Execution Date(s): December 23, 2009	Suite 2100 101 Hudson Street					
X Assignment   Merger   Change of Name	10111000010000					
Security Agreement Joint Research Agreement	City Jorean City					
	City: Jersey City					
Government Interest Assignment	State: New Jersey					
Executive Order 9424, Confirmatory License	Country: United States of America ZIp: 07302					
Other	Additional name(s) & address(es) Yes X No attached?					
Additional numbers attached	? Yes X No					
<ol><li>Name and address to whom correspondence concerning document should be mailed:</li></ol>	6. Total number of applications and patents involved:					
Name: Richard J. Botos LERNER, DAVID, LITTENBERG, KRUMHOLZ & MENTLIK, LLP	7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00					
Internal Address: Atty. Dkt.:						
Street Address: 600 South Avenue West	X Authorized to be charged to deposit account					
	Enclosed					
	None required (government interest not affecting title					
	Traile reduited (government interest for amount grate)					
City: Westfield	8. Payment Information					
State: NJ Zip: 07090						
Phone Number: 908-654-5000						
Fax Number: 908-654-7866	Deposit Account Number 12-1095					
Email Address: mcormier@ldlkm.com	Authorized User Name Richard J. Botos					
9. Signature: Ridd Signature	July 12, 2013  Date					
Richard J. Botos - 32,016	Total number of pages including cover 5					
Name of Person Signing	sheet, attachments, and documents:					

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## EXHIBIT A

# CONFIRMATORY ASSIGNMENT OF PATENT RIGHTS

This Confirmatory Assignment of Patent Rights (the "Agreement") is entered into this 30th day of December, 2009 (the "Closing Date"), by and between Micron Technology, Inc., a Delaware corporation ("Assignor"), and Round Rock Research, LLC, a Delaware limited liability company ("Assigner").

WHEREAS, Assignor and Assignce are party to that certain Patent Sale and Transfer Agreement dated on or about the data hereof (as in effect from time to time, the "Patent Sale and Transfer Agreement"); capitalized terms used but not otherwise defined in this Agreement shall have the respective meaning assigned thereto in the Patent Sale and Transfer Agreement;

WHEREAS, pursuant to the Potent Sale and Transfer Agreement, Assignor has agreed to sell, convey, transfer, assign and deliver to Assignee all of Assignor's (and its Subsidiaries') right, title and interest, including Patent Rights, in and to the Patents listed on Schedule A hereto (collectively, the "Assigned Patents");

WHEREAS, Assignee is now desirous of acquiring the entire and exclusive right, title and interest in and to the Assigned Patents in the United States and throughout the world; and

WHEREAS, Assignor is now willing to assign to Assignee all rights, title and interest in end to the Assigned Patents in the United States and throughout the world; and

NOW, THEREFORE, for good and valuable consideration, recoipt of which is hereby acknowledged by Assignor, Assignor hereby assigns, transfers and conveys to Assignoe, its successors, logal representatives and assigns, and Assignee hereby accepts, all of Assignor's right, title and interest in the United States and throughout the world in and to the Assigned Patonts and may and all Letters Patent that are or may be granted thereon, and any legal equivalent thereof that may be granted in any country or countries foreign to the United States, in each case including without limitation any extensions, substitutes, continuations, continuationsin-part, divisions, reissues, reexaminations, and renowals thereof, or other equivalents thereof, and further, all rights and privileges pertaining to the Assigned Patents and any and all Letters Patent that are or may be granted thereon, and any legal equivalent thereof that may be granted in any country or countries foreign to the United States, including, without limitation, the right, if my, to perition, sue or otherwise seek and recover domagos, profits and any other remedy (monetary, injunctive, declaratory or other) in the United States and anywhere throughout the world for any past, present and future infringement thereof, conversion or misappropriation of, or other injury, offense, violation, breach of duty or wrong relating to, any of the Assigned Patents, or any license, agreement, contract or other matter relating thereto.

Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States of America and the appropriate officers of all other jurisdictions in which the Assigned Patents are or may be registered or in which applications included among the Assigned Patents are pending, to record the title of Assignee, its successors, legal representatives and assigns, as owner of all right, title and interest in and to the Assigned Patents, and to issue to Assignee, its successors, legal representatives and assigns, all Letters Patent and any legal

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equivalent thereof that may be granted in any country or countries foreign to the United States and recordations of patent rights resulting from any application included among the Assigned Patents, in accordance with the terms of this instrument.

Except to the extent that federal law preempts state law with respect to the matters covered hereby, this Agreement shall be governed by and construct in accordance with the laws of the State of Delaware, without giving effect to any of the principles of conflicts of laws thereof that would result in the application of the laws of another jurisdiction to this Agreement.

This Agreement shall be binding on, and shall inure to the benefit of, the parties hereto and their respective successors and assigns, and may be executed in two or more counterparts, each of which shall be deemed to be an original, but all of which shall constitute one and the same agreement. Each of the parties hereto agrees to accept and be bound by facsimile signatures hereto.

Each party represents that it has taken all necessary action to authorize the execution and delivery of this Agreement.

[The remainder of this page has been intentionally left blank.]

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> THE WITNESS WHEREOF, the parties boreto have caused this Agreement to be executed by their respective daily authorized officers, as of the date first written above.

#### ASSIGNOR:

MICRON TECHNOLOGY, INC.

Henry

Spens T\_ Appleton MUNICIPAR ENG ORG Tide:

#### ASSIGNEE:

ROUND ROCK RESEARCH, LLC

Name Town Desmarsis

On this 2-2rd day of Many Let 2009, before me appeared Struen R. Hopication, the person who signed this instrument, who acknowledged that he she signed it as a free act on bis/her own behalf or on behalf of the Assignor with authority to do so.

Smar of IDAHO County of 110

[Note that federal patent estigaments must also include a cover sheet. See 37 C.F.R. 3.28]

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### Schedule A Patents

				Scriedale .	Talems
MUEI- 0564.00/US	US	CONNECTED SUPPORT ENTITLEMENT SYSTEM AND 27-Aug-02 METHOD OF OPERATION	Issued	00 1 07 7400744	40,000,000
MUEI-		CONNECTED SUPPORT ENTITLEMENT SYSTEM METHOD	issueu	09-Jan-07 7162744	10/229,909
0564.01/US PBIT-	US	29-Sép-06 OF OPERATION INTERPOLATOR FOR A CMOS IMAGE SENSOR USING A	Issued	25-Dec-07 7313826	11/540,161
0004.00/US	UŞ	23-Feb-98 DIGITAL REGISTER	Issued	09-Mar-04 6704049	09/028,961
PBIT- 0004.01/US	US	INTERPOLATOR FOR A CMOS IMAGE SENSOR USING A 10-Feb-04 DIGITAL REGISTER	Pending		10/774,603
PBIT- 0010.00/US	US	23-Apr-99 DIGITAL EXPOSURE CIRCUIT FOR AN IMAGE SENSOR	lesued	14-Jun-05 6906745	09/298,306
PBIT- 0010.01/US	us	05-Máy-05 DIGITAL EXPÓSURE CIRCUIT FOR AN IMAGE SENSOR	Allowed		11/121,956
PBIT- 0015.00/US	US	Increasing Readout Speed in CMOS APS Sensors through Block 23-Mar-99 Readout	Issued	25-Jan-05 6847399	09/274,739
PBIT- 0015.01/US	US	Increasing Readout Speed in CMOS APS Sensors through Block 08-Ndv-04 Readout	Allowed		10/983,083
PBIT- 0015.02/US	US	Increasing Readout Speed in CMOS APS Sensors through Block 25-Sep-09 Readout	Pending	t.	
PBIT- 0015.03/US	US	Increasing Readout Speed in CMOS APS Sensors through Block 01-Oct-09 Readout	Pending		***
PBIT- 0027.00/RE	US				10404 700
PBIT-		24-Apr-03 PULSE-CONTROLLED LIGHT EMITTING DIODE SOURCE	Pending		10/421,780
0027.00/US PBIT-	US	04-Feb-99 PULSE-CONTROLLED LIGHT EMITTING DIODE SOURCE	Issued	24-Apr-01 6222172	09/246,013
0027.01/US PBIT-	US	21-Ndv-08 PULSE-CONTROLLED LIGHT EMITTING DIODE SOURCE REDUCING INTERNAL BUS SPEED IN A BUS SYSTEM	Pending		12/275,937
0033.00/US PBIT-	US	21-Jul-99 WITHOUT REDUCING READOUT RATE	Issued	12-Nov-02 6480921	09/359,068
0049.00/US pblt-	US	19-Aug-99 LOCK-IN PINNED PHOTODIODE PHOTO-DETECTOR PINNED PHOTODIODE PHOTO-DETECTOR WITH COMMON	Issued	29-May-01 6239456	09/378,565
0049.01/RE PBIT-	US	21-Sep-06 BUFFER TRANSISTOR AND BINNING CAPABILITY	Allowed		11/524,495
0049.01/US	US	29-May-01 LOCK-IN PINNED PHOTODIODE PHOTO-DETECTOR	Issued	21-Sep-04 6794214	09/867,846
pbit- 0049.02/RE	US	PINNED PHOTODIODE PHOTO-DETECTOR WITH COMMON 30-Meir-09 BUFFER TRANSISTOR AND BINNING CAPABILITY	Pending		12/413,626
PBIT- 0049.02/US	Ų\$	12-Jun-03 LOCK-IN PINNED PHOTODIODE PHOTO-DETECTOR	issued	15-Jun-04 6750485	10/459,595
PBIT- 0054.00/US	US	14-Odt-99 MICRO POWER MICRO-SIZED CMOS ACTIVE PIXEL	Issued	14-May-02 6368242	09/418,961
PBIT- 0054.01/US	US	14-May-02 MICRO POWER MICRO-SIZED CMOS ACTIVE PIXEL	Issued	10-May-05 6891145	10/146,667
PBIT- 0066,00/US	US	CONTOURED SURFACE COVER PLATE FOR IMAGE SENSOR 22-Deb-99 ARRAY	Issued	07-Nov-06 7133076	09/470,284
PBIT- 0066.01/US	US	CONTOURED SURFACE COVER PLATE FOR IMAGE SENSOR			. 1
PBIT-		19-Nov-03 ARRAY  CONTOURED SURFACE COVER PLATE FOR IMAGE SENSOR	Issued	03-Mar-09 7499093	10/715,555
0066.02/U\$ PBIT-	US	02-Feb-09 ARRAY	Pending		12/364,402
0089.00/RE PBIT-	ŲS	12-Jun-09 CMOS APS PIXEL SENSOR DYNAMIC RANGE INCREASE	Pending		12/484,039
0089.00/US PBIT-	US	31-Aug-00 CMOS APS PIXEL SENSOR DYNAMIC RANGE INCREASE	Issued	03-Oct-06 7116366	09/653,527
0089.01/US PBIT-	US	29-Jul-04 CMOS APS PIXEL SENSOR DYNAMIC RANGE INCREASE	Issued	12-Jun-07 7230645	10/901,114
0089.02/US	US	29-Jul-04 CMOS APS PIXEL SENSOR DYNAMIC RANGE INCREASE	Issued	03-Oct-06 7116368	10/901,178
PBIT-		HIGH-SPEED DIGITAL TO ANALOG CONVERTER USING MULTIPLE STAGGERED SUCCESSIVE APPROXIMATION			9 0 0 1
0133.00/US	US	25-Oct-01 CELLS HIGH-SPEED DIGITAL TO ANALOG CONVERTER USING	Issued	11-Nov-03 6646583	10/061,938
PBIT- 0133.01/RE	us .	MULTIPLE STAGGERED SUCCESSIVE APPROXIMATION	Pending	(	11/812,785
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