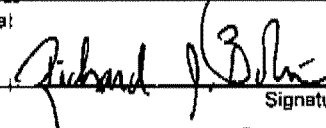


Form PTO-1595 (Rev. 03-11)  
OMB No. 0651-0027 (exp. 03/31/2015)

U.S. DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office

### RECORDATION FORM COVER SHEET PATENTS ONLY

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

<p><b>1. Name of conveying party(ies):</b> Micron Technology, Inc.</p> <p>Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>	<p><b>2. Name and address of receiving party(ies)</b></p> <p>Name: <u>Round Rock Research, LLC</u></p> <p>Internal Address: _____</p> <p>Street Address: _____</p> <p>Suite 2100 101 Hudson Street</p> <p>City: <u>Jersey City</u></p> <p>State: <u>New Jersey</u></p> <p>Country: <u>United States of America</u> Zip: <u>07302</u></p> <p>Additional name(s) &amp; address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>
<p><b>3. Nature of conveyance/Execution Date(s):</b></p> <p>Execution Date(s): <u>December 23, 2009</u></p> <p><input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Change of Name  <input type="checkbox"/> Security Agreement <input type="checkbox"/> Joint Research Agreement  <input type="checkbox"/> Government Interest Assignment  <input type="checkbox"/> Executive Order 9424, Confirmatory License  <input type="checkbox"/> Other _____</p>	<p><b>4. Application or patent number(s):</b> <input type="checkbox"/> This document is being filed together with a new application.</p> <p>A. Patent Application No.(s): <u>13/404,775</u></p> <p>B. Patent No.(s)</p> <p>Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>
<p><b>5. Name and address to whom correspondence concerning document should be mailed:</b></p> <p>Name: <u>Richard J. Botos</u> <u>LERNER, DAVID, LITTENBERG,</u> <u>KRUMHOLZ &amp; MENTLIK, LLP</u></p> <p>Internal Address: Atty. Dkt.: _____</p> <p>Street Address: <u>600 South Avenue West</u></p> <p>City: <u>Westfield</u></p> <p>State: <u>NJ</u> Zip: <u>07090</u></p> <p>Phone Number: <u>908-654-5000</u></p> <p>Fax Number: <u>908-654-7866</u></p> <p>Email Address: <u>mcormier@ldlkm.com</u></p>	<p><b>6. Total number of applications and patents involved:</b> <span style="border: 1px solid black; padding: 2px;">1</span></p> <p><b>7. Total fee (37 CFR 1.21(h) &amp; 3.41)</b> \$ <u>40.00</u></p> <p><input checked="" type="checkbox"/> Authorized to be charged to deposit account  <input type="checkbox"/> Enclosed  <input type="checkbox"/> None required (government interest not affecting title)</p> <p><b>8. Payment Information</b></p> <p>Deposit Account Number <u>12-1095</u>  Authorized User Name <u>Richard J. Botos</u></p>
<p><b>9. Signature:</b></p> <p style="text-align: center;"> Signature</p> <p style="text-align: right;">Date <u>July 15, 2013</u></p> <p style="text-align: center;"><u>Richard J. Botos - 32,016</u> Name of Person Signing</p> <p style="text-align: right;">Total number of pages including cover sheet, attachments, and documents: <span style="border: 1px solid black; padding: 2px;">5</span></p>	

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**EXHIBIT A****CONFIRMATORY ASSIGNMENT OF PATENT RIGHTS**

This Confirmatory Assignment of Patent Rights (the "Agreement") is entered into this 30th day of December, 2009 (the "Closing Date"), by and between Micron Technology, Inc., a Delaware corporation ("Assignor"), and Round Rock Research, LLC, a Delaware limited liability company ("Assignee").

WHEREAS, Assignor and Assignee are party to that certain Patent Sale and Transfer Agreement dated on or about the date hereof (as in effect from time to time, the "Patent Sale and Transfer Agreement"); capitalized terms used but not otherwise defined in this Agreement shall have the respective meaning assigned thereto in the Patent Sale and Transfer Agreement;

WHEREAS, pursuant to the Patent Sale and Transfer Agreement, Assignor has agreed to sell, convey, transfer, assign and deliver to Assignee all of Assignor's (and its Subsidiaries') right, title and interest, including Patent Rights, in and to the Patents listed on Schedule A hereto (collectively, the "Assigned Patents");

WHEREAS, Assignee is now desirous of acquiring the entire and exclusive right, title and interest in and to the Assigned Patents in the United States and throughout the world; and

WHEREAS, Assignor is now willing to assign to Assignee all rights, title and interest in and to the Assigned Patents in the United States and throughout the world; and

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged by Assignor, Assignor hereby assigns, transfers and conveys to Assignee, its successors, legal representatives and assigns, and Assignee hereby accepts, all of Assignor's right, title and interest in the United States and throughout the world in and to the Assigned Patents and any and all Letters Patent that are or may be granted thereon, and any legal equivalent thereof that may be granted in any country or countries foreign to the United States, in each case including without limitation any extensions, substitutes, continuations, continuations-in-part, divisions, reissues, reexaminations, and renewals thereof, or other equivalents thereof, and further, all rights and privileges pertaining to the Assigned Patents and any and all Letters Patent that are or may be granted thereon, and any legal equivalent thereof that may be granted in any country or countries foreign to the United States, including, without limitation, the right, if any, to petition, sue or otherwise seek and recover damages, profits and any other remedy (monetary, injunctive, declaratory or other) in the United States and anywhere throughout the world for any past, present and future infringement thereof, conversion or misappropriation of, or other injury, offense, violation, breach of duty or wrong relating to, any of the Assigned Patents, or any license, agreement, contract or other matter relating thereto.

Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States of America and the appropriate officers of all other jurisdictions in which the Assigned Patents are or may be registered or in which applications included among the Assigned Patents are pending, to record the title of Assignee, its successors, legal representatives and assigns, as owner of all right, title and interest in and to the Assigned Patents, and to issue to Assignee, its successors, legal representatives and assigns, all Letters Patent and any legal

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equivalent thereof that may be granted in any country or countries foreign to the United States and recordings of patent rights resulting from any application included among the Assigned Patents, in accordance with the terms of this instrument.

Except to the extent that federal law preempts state law with respect to the matters covered hereby, this Agreement shall be governed by and construed in accordance with the laws of the State of Delaware, without giving effect to any of the principles of conflicts of laws thereof that would result in the application of the laws of another jurisdiction to this Agreement.

This Agreement shall be binding on, and shall inure to the benefit of, the parties hereto and their respective successors and assigns, and may be executed in two or more counterparts, each of which shall be deemed to be an original, but all of which shall constitute one and the same agreement. Each of the parties hereto agrees to accept and be bound by facsimile signatures hereto.

Each party represents that it has taken all necessary action to authorize the execution and delivery of this Agreement.

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IN WITNESS WHEREOF, the parties hereto have caused this Agreement to be executed by their respective duly authorized officers, as of the date first written above.

ASSIGNOR:

MICRON TECHNOLOGY, INC.

By:

*[Signature]*  
Name: Steve R. Appleton  
Title: Chairman and CEO

ASSIGNEE:

ROUND ROCK RESEARCH, LLC

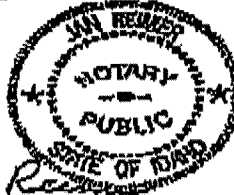
By:

*[Signature]*  
Name: John Desmarais  
Title: Manager

On this 23rd day of December, 2009, before me appeared Steven R. Appleton, the person who signed this instrument, who acknowledged that he/she signed it as a free act on his/her own behalf or on behalf of the Assignor with authority to do so.

State of IDAHO )

)

County of Ada )

[Note that federal patent assignments must also include a cover sheet. See 37 C.F.R. 3.28]

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## Schedule A Patents

2003-0454.02/US	US	25-Jul-06	APPARATUS AND METHOD FOR GENERATING A DELAYED CLOCK SIGNAL	Issued	11-Dec-07 7308594	11/493,433
2003-0454.03/US	US	25-Jul-06	APPARATUS AND METHOD FOR GENERATING A DELAYED CLOCK SIGNAL	Issued	02-Oct-07 7278045	11/493,481
2003-0454.04/US	US	25-Jul-06	APPARATUS AND METHOD FOR GENERATING A DELAYED CLOCK SIGNAL	Issued	25-Mar-08 7350093	11/493,421
2003-0454.05/US	US	25-Jul-06	APPARATUS AND METHOD FOR GENERATING A DELAYED CLOCK SIGNAL	Issued	27-Oct-09 7610502	11/493,422
2003-0454.06/US	US	25-Jul-06	APPARATUS AND METHOD FOR GENERATING A DELAYED CLOCK SIGNAL	Issued	27-Oct-09 7610503	11/493,427
2003-0454.07/US	US	28-Sep-09	APPARATUS AND METHOD FOR GENERATING A DELAYED CLOCK SIGNAL	Pending		12/568,532
2003-0456.00/CN	CN	29-Sep-04	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Pending		2004800296 43.3
2003-0456.00/DE	DE	29-Sep-04	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Issued	13-May-09 1 671 357	60 2004 021 124.9-08
2003-0456.00/EP	EP	29-Sep-04	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Issued	13-May-09 1671357	04789283.1
2003-0456.00/GB	GB	29-Sep-04	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Issued	13-May-09 1671357	04789283.1
2003-0456.00/JP	JP	29-Sep-04	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Pending		2006-534066
2003-0456.00/KR	KR	29-Sep-04	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Issued	09-Jun-09 10-903012	10-2006- 7009009
2003-0456.00/SG	SG	29-Sep-04	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Issued	29-Jun-07 120742	200602021-8
2003-0456.00/US	US	09-Oct-03	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Issued	13-Dec-05 6975556	10/684,123
2003-0456.01/US	US	18-Jul-05	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Issued	12-Sep-06 7106646	11/184,187
2003-0456.02/US	US	17-Aug-06	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Issued	20-Oct-09 7606101	11/506,238
2003-0456.03/US	US	28-Sep-09	CIRCUIT AND METHOD FOR CONTROLLING A CLOCK SYNCHRONIZING CIRCUIT FOR LOW POWER REFRESH OPERATION	Pending		12/568,507
2003-0468.00/US	US	20-Feb-04	METHODS OF FORMING OPENINGS, AND METHODS OF FORMING CONTAINER CAPACITORS	Issued	26-Dec-06 7153778	10/783,843
2003-0468.01/US	US	22-Jul-05	CAPACITOR STRUCTURES AND DRAM ARRAYS, METHODS OF FORMING OPENINGS, AND METHODS OF FORMING CONTAINER CAPACITORS	Issued	22-Jan-08 7321149	11/187,210
2003-0468.02/US	US	31-Aug-05	FORMING CONTAINER CAPACITORS	Issued	26-May-09 7538036	11/216,759
2003-0560.00/US	US	11-Jun-03	MEMORY MODULE AND METHOD HAVING IMPROVED SIGNAL ROUTING TOPOLOGY	Issued	17-Jul-07 7245145	10/460,588
2003-0560.01/US	US	01-Sep-04	MEMORY MODULE AND METHOD HAVING IMPROVED SIGNAL ROUTING TOPOLOGY	Issued	10-Jul-07 7242213	10/932,477
2003-0560.02/US	US	28-Sep-05	MEMORY MODULE AND METHOD HAVING IMPROVED SIGNAL ROUTING TOPOLOGY	Issued	16-Oct-07 7282947	11/238,900
2003-0560.03/US	US	08-Oct-07	MEMORY MODULE AND METHOD HAVING IMPROVED SIGNAL ROUTING TOPOLOGY	Issued	07-Jul-09 7557601	11/973,684
2003-0560.04/US	US	08-Jun-09	MEMORY MODULE AND METHOD HAVING IMPROVED SIGNAL ROUTING TOPOLOGY	Pending		12/480,589