

## PATENT ASSIGNMENT

Electronic Version v1.1  
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT														
NATURE OF CONVEYANCE:	ASSIGNMENT														
CONVEYING PARTY DATA															
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Yu-Chia Lai</td> <td>07/01/2013</td> </tr> <tr> <td>Hsien-Ming Tu</td> <td>07/01/2013</td> </tr> <tr> <td>Tung-Liang Shao</td> <td>07/01/2013</td> </tr> <tr> <td>Hsien-Wei Chen</td> <td>07/01/2013</td> </tr> <tr> <td>Chang-Pin Huang</td> <td>07/01/2013</td> </tr> <tr> <td>Ching-Jung Yang</td> <td>07/01/2013</td> </tr> </tbody> </table>		Name	Execution Date	Yu-Chia Lai	07/01/2013	Hsien-Ming Tu	07/01/2013	Tung-Liang Shao	07/01/2013	Hsien-Wei Chen	07/01/2013	Chang-Pin Huang	07/01/2013	Ching-Jung Yang	07/01/2013
Name	Execution Date														
Yu-Chia Lai	07/01/2013														
Hsien-Ming Tu	07/01/2013														
Tung-Liang Shao	07/01/2013														
Hsien-Wei Chen	07/01/2013														
Chang-Pin Huang	07/01/2013														
Ching-Jung Yang	07/01/2013														
RECEIVING PARTY DATA															
<table border="1"> <tr> <td>Name:</td> <td>Taiwan Semiconductor Manufacturing Co., LTD</td> </tr> <tr> <td>Street Address:</td> <td>No.8, Li-Hsin Rd.6, Science-Based Industrial Park</td> </tr> <tr> <td>City:</td> <td>Hsinchu</td> </tr> <tr> <td>State/Country:</td> <td>TAIWAN</td> </tr> <tr> <td>Postal Code:</td> <td>300</td> </tr> </table>		Name:	Taiwan Semiconductor Manufacturing Co., LTD	Street Address:	No.8, Li-Hsin Rd.6, Science-Based Industrial Park	City:	Hsinchu	State/Country:	TAIWAN	Postal Code:	300				
Name:	Taiwan Semiconductor Manufacturing Co., LTD														
Street Address:	No.8, Li-Hsin Rd.6, Science-Based Industrial Park														
City:	Hsinchu														
State/Country:	TAIWAN														
Postal Code:	300														
PROPERTY NUMBERS Total: 1															
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>13940626</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	13940626										
Property Type	Number														
Application Number:	13940626														
CORRESPONDENCE DATA															
Fax Number: 4804645692 <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i> Phone: 480-464-1111 Email: slaureanti@jacksonwhitelaw.com Correspondent Name: Steven J. Laureanti Address Line 1: 40 North Center Street, Suite 200 Address Line 4: Mesa, ARIZONA 85201															
ATTORNEY DOCKET NUMBER:	28503-007														
NAME OF SUBMITTER:	Steven J. Laureanti														

OP \$40.00 13940626

Signature:	/Steven J. Laureanti/
Date:	08/06/2013
Total Attachments: 2 source=13-07-01_007-executed_Assignment#page1.tif source=13-07-01_007-executed_Assignment#page2.tif	

NP-13873-US-B  
TSMC-2-13-0588

Inventor(s)-to-Assignee

## ASSIGNMENT

This assignment agreement is applicable to an invention entitled (Invention Title) \_\_\_\_\_  
Self-alignment Structure for Wafer Level Chip Scale Package

The PATENT RIGHTS referred to in this agreement are:

(check one) ☐ a patent application for this invention, executed by the ASSIGNOR(S) concurrently with this assignment.

☒ U.S. patent application Serial No. 13/940,626, filed July 12, 2013

☐ a U.S. patent application based on PCT International Application No. \_\_\_\_\_ filed on (date) \_\_\_\_\_ (U.S. patent application Serial No. \_\_\_\_\_, if known).

☐ U.S. patent No. \_\_\_\_\_, issued \_\_\_\_\_.

The PATENT RIGHTS also include all divisions, reissues, continuations and extensions of the patents and patent applications identified above.

The PATENT RIGHTS assigned under this agreement are:

(check one) ☒ U.S. patent rights only.

☐ Worldwide patent rights. In this case, the assignee shall have the right to claim the benefit of the filing date of any U.S. or foreign patent application for this invention.

The ASSIGNOR(S) referred to in this agreement is (or are) the inventor(s) whose signatures appear on page 2 of this Assignment and any Supplemental Sheet(s).

The ASSIGNEE referred to in this agreement is:

(Name of Assignee) TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.

(Address) NO.8, LI-HSIN RD.6, SCIENCE-BASED INDUSTRIAL PARK, HSINCHU, TAIWAN 300, R.O.C.

The ASSIGNEE is:

(check one) ☐ An individual.

☐ A Partnership.

☒ A Corporation of TAIWAN, R.O.C. (specify state or country)

☐ (other) \_\_\_\_\_

The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and valuable consideration, receipt of which is acknowledged, hereby assign(s) the following rights to the ASSIGNEE, its successors and assigns:

the full and exclusive right to the invention;

the entire right, title and interest in and to the PATENT RIGHTS;

the right to sue and recover for any past infringement; and

the right to claim priority under 35 USC 119, 35 USC 120, or any other applicable provisions, based on any earlier patent applications for this invention.

NP-13873-US-B  
TSMC-2-13-0588

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO  
ASSIGNEE: TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.  
INVENTION TITLE: Self-alignment Structure for Wafer Level Chip Scale Package

As to all U.S. patent applications assigned under this agreement, the ASSIGNOR(S) hereby authorize(s) and request(s) the Commissioner of Patents and Trademark to issue all Letters Patent to the ASSIGNEE as the assignee of the entire right, title and interest, for the sole use and enjoyment of said ASSIGNEE, its successors and assigns.

Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any fact known to the ASSIGNOR(S) with respect to said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause and all Letter Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

The ASSIGNOR(S) authorize(s) the attorneys and agents who have the power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

<u>Yu-Chia LAI</u>	<u>Yu-Chia LAI</u>	<u>2013/7/1</u>
Name of sole or first inventor	Signature	Date
<u>Hsien-Ming TU</u>	<u>Hsien-Ming Tu</u>	<u>2013/7/1</u>
Name of second inventor, if any	Signature	Date
<u>Tung-Liang SHAO</u>	<u>Tung-Liang Shao</u>	<u>2013/7/1</u>
Name of third inventor, if any	Signature	Date
<u>Hsien-Wei CHEN</u>	<u>Hsien-Wei Chen</u>	<u>2013.7.1</u>
Name of fourth inventor, if any	Signature	Date
<u>Chang-Pin HUANG</u>	<u>Chang-Pin HUANG</u>	<u>2013/7/1</u>
Name of fifth inventor, if any	Signature	Date
<u>Ching-Jung YANG</u>	<u>Ching-Jung Yang</u>	<u>2013/7/1</u>
Name of sixth inventor, if any	Signature	Date