

## PATENT ASSIGNMENT

Electronic Version v1.1  
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Conexant Systems, Inc.	08/08/2008
RECEIVING PARTY DATA	
Name:	NXP B.V.
Street Address:	High Tech Campus 60
City:	Eindhoven
State/Country:	NETHERLANDS
Postal Code:	5656 AG
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	8078129
CORRESPONDENCE DATA	
Fax Number:	4085185671
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	3474431592
Email:	david.schaeffer@nxp.com
Correspondent Name:	David L. Schaeffer
Address Line 1:	411 East Plumeria Drive, MS41
Address Line 2:	NXP Semiconductors, IP&L
Address Line 4:	San Jose, CALIFORNIA 95134
NAME OF SUBMITTER:	David L. Schaeffer
Signature:	/David L. Schaeffer/
Date:	08/13/2013
Total Attachments: 17	
source=8078129ParentConexantToNXP#page1.tif	
source=8078129ParentConexantToNXP#page2.tif	

CH \$40.00 8078129

source=8078129ParentConexantToNXP#page3.tif  
source=8078129ParentConexantToNXP#page4.tif  
source=8078129ParentConexantToNXP#page5.tif  
source=8078129ParentConexantToNXP#page6.tif  
source=8078129ParentConexantToNXP#page7.tif  
source=8078129ParentConexantToNXP#page8.tif  
source=8078129ParentConexantToNXP#page9.tif  
source=8078129ParentConexantToNXP#page10.tif  
source=8078129ParentConexantToNXP#page11.tif  
source=8078129ParentConexantToNXP#page12.tif  
source=8078129ParentConexantToNXP#page13.tif  
source=8078129ParentConexantToNXP#page14.tif  
source=8078129ParentConexantToNXP#page15.tif  
source=8078129ParentConexantToNXP#page16.tif  
source=8078129ParentConexantToNXP#page17.tif

## INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT

This INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT (this "Assignment") is effective as of August 8, 2008, among Conexant Systems, Inc., a Delaware corporation (the "Seller" or "Assignor"), and NXP B.V., a Dutch *besloten vennootschap* (the "Purchaser" or "Assignee"). Unless otherwise defined herein, capitalized terms shall have the meanings set forth in the Purchase Agreement (as defined below).

### RECITALS

WHEREAS, the Seller and the Purchaser have entered into that certain Asset Purchase Agreement, dated as of April 29, 2008 (the "Purchase Agreement"), pursuant to which, among other things, Assignee is acquiring certain Intellectual Property Rights on the terms and subject to the conditions set forth therein; and

WHEREAS, this Assignment is required to be executed and delivered by Assignor on or prior to the Closing Date, pursuant to Section 5.5 of the Purchase Agreement.

### ASSIGNMENT

NOW, THEREFORE, for good and valuable consideration, including that recited in the Purchase Agreement, the receipt and sufficiency of which is hereby acknowledged, the parties hereto agree as follows:

1. Assignment of Intellectual Property Rights. Assignor does hereby sell, transfer, and assign to Assignee all of the right, title, and interest of Assignor and its Subsidiaries in and to the Intellectual Property Rights that are included among the Transferred Assets, (the "Transferred IPR") including without limitation the specific items set forth below:

(a) all issued Patents and pending patent applications listed in Attachment I hereto, including all foreign and multinational counterparts, whether or not listed in Attachment I;

(b) all Trademark registrations and applications listed in Attachment II hereto, as well as the Internet domain name *amphion.com* and all worldwide trademark rights (whether registered or not) related to the *Amphion* name, and the goodwill associated therewith;

(c) all Copyrights included in the Transferred IPR, including the software listed in Attachment III hereto; and

(d) all mask works included in the Transferred IPR, including the mask work registrations listed in Attachment III hereto.

PATENT

REEL: 021531 FRAME: 0525

PATENT

REEL: 031001 FRAME: 0604

2. Assistance and Cooperation. This Assignment is effective between the parties on the date hereof. Assignor further agrees, subject to Section 3 (Perfection and Recordation), to perform (or cause to be performed) all such lawful acts and to execute (or cause to be executed) all such further assignments and other lawful documents as may reasonably be necessary to effectuate the Assignment and to perfect and record the Assignment in the various jurisdictions.

3. Perfection and Recordation. Assignee shall prepare all documents that are necessary to perfect and record the assignments of the Transferred IPR in the various jurisdictions, and Assignee shall be responsible for all of its own expenses, including recordation expenses, associated therewith.

4. Entire Agreement. This Assignment (including all Attachments hereto) (the Purchase Agreement including the all Schedules and Exhibits thereto), the Confidentiality Agreement (which remains in full force and effect) and the other Ancillary Agreements set forth the entire understanding of the parties and supersede all prior agreements and understandings, oral or written, between the parties relating to the subject matter hereof and thereof.

5. Binding Assignment. This Assignment shall be binding upon and inure to the benefit of the parties hereto and their respective successors and permitted assigns.

6. Governing Law. THIS ASSIGNMENT WILL BE CONSTRUED IN ACCORDANCE WITH, AND GOVERNED IN ALL RESPECTS BY, THE LAWS OF THE STATE OF NEW YORK (WITHOUT GIVING EFFECT TO PRINCIPLES OF CONFLICTS OF LAW).

7. Severability. In the event that any provision of this Assignment, or the application of such provision to any Person or set of circumstances, shall be determined to be invalid, unlawful, void or unenforceable to any extent, (a) a suitable and equitable provision shall be substituted therefor in order to carry out, so far as may be valid and enforceable, the intent and purpose of such invalid or unenforceable provision and (b) the remainder of this Assignment and the application of such provision to Persons or circumstances other than those as to which it is determined to be invalid, unlawful, void or unenforceable, will not be affected and will continue to be valid and enforceable to the fullest extent permitted by law.

8. Counterparts. This Assignment may be executed in several counterparts, each of which will constitute an original and all of which, when taken together, will constitute one and the same Assignment.

9. Headings. The section headings contained in this Assignment are inserted for reference purposes only and are not intended to be a part, nor should they affect the meaning or interpretation, of this Assignment.

10. Amendments. This Assignment may not be amended, modified, altered or supplemented except by means of a written instrument executed on behalf of all parties.

PATENT

REEL: 021531 FRAME: 0526

PATENT

REEL: 031001 FRAME: 0605

*(Signature Page Follows.)*

PATENT

REEL: 021531 FRAME: 0527

PATENT

REEL: 031001 FRAME: 0606

IN WITNESS WHEREOF, the parties have caused this Assignment to be executed as of the date first above written.

NXP BV

By: \_\_\_\_\_

Name: \_\_\_\_\_

Its: \_\_\_\_\_

By: \_\_\_\_\_

Name: \_\_\_\_\_

Its: \_\_\_\_\_

Conexant Systems, Inc.

By: \_\_\_\_\_

Name: \_\_\_\_\_

Its: \_\_\_\_\_

Seen by me, Judith Hester Elizabeth van Brussel, prospective civil-law notary, as substitute of Petra Maria de Jong, civil-law notary in Eindhoven, the Netherlands, for legalization of the signature of Mr. J.A.W. Schreurs, bearer of a Dutch passport with number NPP7F8CBO, valid until the 14th day of May 2012.

Signed in Eindhoven, the Netherlands,  
this 6th day of August 2008.



J.H.E. van Brussel

[Signature Page to Intellectual Property Assignment Agreement]

PATENT

REEL: 021531 FRAME: 0528

PATENT

REEL: 031001 FRAME: 0607

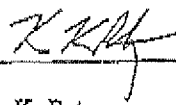
IN WITNESS WHEREOF, the parties have caused this Assignment to be executed as of the date first above written.

NXP BV

By: \_\_\_\_\_  
Name: \_\_\_\_\_  
Its: \_\_\_\_\_

By: \_\_\_\_\_  
Name: \_\_\_\_\_  
Its: \_\_\_\_\_

Conexant Systems, Inc.

By:  \_\_\_\_\_  
Name: \_\_\_\_\_  
Its: Kerry K. Petry  
Vice President and Treasurer

*[Signature Page to Intellectual Property Assignment Agreement]*

PATENT  
REEL: 02153 FRAME: 0529  
REEL: 031001 FRAME: 0608

STATE OF )  
 )SS.  
 COUNTY OF )

I, a notary public, in and for the county and state aforesaid, do hereby  
 certify that \_\_\_\_\_ personally known to me to be the  
 \_\_\_\_\_ of Conexant Systems, Inc., a Delaware corporation, and  
 \_\_\_\_\_ personally known to me to be the  
 \_\_\_\_\_ of NXP B.V., a Dutch *besloten vennootschap*, appeared before me  
 this day in person and acknowledged that each signed the above and foregoing instrument  
 as such person's free and voluntary act and as the free and voluntary act of the applicable  
 corporation pursuant to authority granted to such person by the board of directors of the  
 applicable corporation for the uses and purposes therein set forth.

IN WITNESS WHEREOF, I have hereunto set my hand and notarial seal  
 this day of \_\_\_\_\_, 2008.

\_\_\_\_\_  
 Notary Public

My commission expires: \_\_\_\_\_



Attachment I**Transferred Patents, Patent Applications and Innovation Disclosures**

Case No.	Cty	Patent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
95B021	US	A Perceptually Motivated Constrained Trellis Based Rate Control Method and Apparatus for Low Bit Rate Video Coding	ORD	Granted	08/802,042	2/18/1997	6,043,844	3/28/2000
97RSS106	US	Cable Modem Optimized for High-Speed Data Transmissions From the Home to the Cable Head	ORD	Granted	08/990,279	12/15/1997	5,986,691	11/16/1999
97RSS222	AU	Digital Controlled Phase Shifter	PCT	Granted	59630/94	12/22/1993	670862	02/03/1997
97RSS222	CA	Digital Controlled Phase Shifter	PCT	Granted	2,130,268	12/22/1993	2,130,268	05/16/2000
97RSS222	CN	Digital Controlled Phase Shifter	PCT	Granted	93119986.7	12/22/1993	93119986.7	07/28/2000
97RSS222	DE	Digital Controlled Phase Shifter	EPC	Granted	94905570.1	12/22/1993	69328084	03/15/2000
97RSS222	EP	Digital Controlled Phase Shifter	PCT	Granted	94905570.1	12/22/1993	0627137	03/15/2000
97RSS222	FR	Digital Controlled Phase Shifter	EPC	Granted	94905570.1	12/22/1993	0627137	03/15/2000
97RSS222	GB	Digital Controlled Phase Shifter	EPC	Granted	94905570.1	12/22/1993	0627137	03/15/2000
97RSS222	NL	Digital Controlled Phase Shifter	EPC	Granted	94905570.1	12/22/1993	0627137	03/15/2000
97RSS222	US	Digital Controlled Phase Shifter	ORD	Granted	994,931	12/22/1992	5,521,499	5/28/1996
97RSS240	US	Digital Video Converter Box for Subscriber/Home With Multiple Television Sets	ORD	Inactive	08/988,548	12/10/1997	5,936,660	8/10/1999
97RSS240	US	Digital Video Converter Box for Subscriber/Home With Multiple Television Sets	RBI	Granted	09/491,032	1/25/2000	RE39,202	7/18/2006
97RSS243	US	Finite Field Parallel Multiplier	ORD	Granted	07/990,524	12/15/1992	5,272,661	12/21/1993
97RSS245	US	L-Band Tuner With Quadrature Downconverter for PSK Data Applications	ORD	Granted	07/850,544	3/13/1992	5,325,401	6/18/1994

PATENT

REEL: 021531 FRAME: 0531

REEL: 031001 FRAME: 0610

Case No.	City	Patent Title	Case Type	Status	App. No.	Filing Date	Patent No.	Issue Date
97RSS247	US	Method and Apparatus for Performing Finite Field Division	ORD	Granted	938,109	8/31/1992	5,379,243	1/3/1995
97RSS249	US	Tuner with Quadrature Downconverter for Pulse Amplitude Modulated Data Applications	ORD	Granted	08/263,602	6/22/1994	5,528,633	6/18/1996
97RSS263	US	System for Detecting Voltage Pulses of a Particular Magnitude	CON	Granted	802,072	12/3/1991	5,198,703	3/30/1993
97RSS294	CA	Apparatus for, and Methods of, Providing a Universal Format of Pixels and for Scaling Fields in the Pixels	ORD	Granted	2,108,730	10/19/1993	2,108,730	10/12/1999
97RSS294	DE	Apparatus for, and Methods of, Providing a Universal Format of Pixels and for Scaling Fields in the Pixels	EPC	Granted	93119689.3	12/07/1993	0601535	03/26/2003
97RSS294	EP	Apparatus for, and Methods of, Providing a Universal Format of Pixels and for Scaling Fields in the Pixels	ORD	Granted	93119689.3	12/07/1993	0601535	03/26/2003
97RSS294	GB	Apparatus for, and Methods of, Providing a Universal Format of Pixels and for Scaling Fields in the Pixels	EPC	Granted	93119689.3	12/07/1993	0601535	03/26/2003
97RSS294	US	Apparatus for, and Methods of, Providing a Universal Format of Pixels and for Scaling Fields in the Pixels	CON	Granted	08/319,427	12/06/1994	5,542,041	07/30/1996
97RSS298	US	System for Determining the Time at Which an Analog Voltage Crosses a Voltage	ORD	Granted	006,823	1/21/1993	5,367,203	11/22/1994

PATENT

REEL: 021531 FRAME: 0532

REEL: 031001 FRAME: 0611

Case No.	City	Patent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
		Threshold						
97RSS573	US	Delay Line providing an Adjustable delay in Response to Binary Input Signals	CIP	Granted	382,677	2/2/1995	5,354,930	9/10/1996
98RSS130	US	Automatic Power Control for Cable Modem Upstream	ORD	Granted	09/272,632	03/18/1999	6,724,829	04/20/2004
98RSS131	US	Clock/Data Smoother for FEC Decoder and Full Rate DDS Clock Generation	ORD	Granted	09/272,760	3/19/1999	6,501,809	12/31/2002
98RSS409	US	System of and Method for Gamma Correction of Real-Time Video	RCE	Granted	09/450,643	12/16/2002	6,727,959	4/27/2004
99RSS107	DE	Method and Apparatus for Upstream Burst Transmission Synchronization in Cable Modems	PCT	Granted	00972062.4	10/09/2000	1222759	12/28/2005
99RSS107	EP	Method and Apparatus for Upstream Burst Transmission Synchronization in Cable Modems	PCT	Granted	00972062.4	10/09/2000	1222759	12/28/2005
99RSS107	FR	Method and Apparatus for Upstream Burst Transmission Synchronization in Cable Modems	PCT	Granted	00972062.4	10/09/2000	1222759	12/28/2005
99RSS107	GB	Method and Apparatus for Upstream Burst Transmission Synchronization in Cable Modems	PCT	Granted	00972062.4	10/09/2000	1222759	12/28/2005
99RSS107	US	Method and Apparatus for Upstream Burst Transmissions Synchronization in Cable Modems	DIV	Granted	09/909,044	07/19/2001	6,556,591	04/29/2003
99RSS107	US	Method and Apparatus for Upstream Burst Transmission Synchronization in Cable Modems	DIV	Granted	09/908,759	07/19/2001	6,553,040	04/29/2003
99RSS107	US	Method and	ORD	Granted	09/415,612	10/09/1999	6,526,070	02/25/2003

PATENT

REEL: 02153 PATENT: 0533

REEL: 031001 FRAME: 0612

Case No.	Cty	Patent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
		Apparatus for Upstream Burst Transmission Synchronization in Cable Modems						
99RSS148	US	Wideband Variable Gain Amplifier	ORD	Granted	09/480,831	1/10/2000	6,271,726	8/7/2001
99RSS163	US	SoftTrellis Slicer for Improving the Performance of a Decision-Directed Phase Tracker	ORD	Granted	09/668,231	9/22/2000	6,882,690	4/19/2005
99RSS197	US	Burst Reliability and Error Locator for Trellis Codes	ORD	Granted	10/350,303	1/22/2003	7,103,831	9/5/2006
99RSS198	US	Iterative Decoder Employing Multiple External Code Error Checks to Lower the Error Floor	CIP	Granted	09/865,958	05/25/2001	6,810,502	10/26/2004
99RSS198	US	Iterative Decoder Employing Multiple External Code Error Checks to Lower the Error Floor	CON	Granted	10/892,738	07/16/2004	7,310,768	12/18/2007
99RSS198	US	Iterative Decoder Employing Multiple External Code Error Checks to Lower the Error Floor	CON	Pending	11/944,320	11/21/2007		
99RSS198	US	Architecture and Method for the Improved Decoding of a Serially Concatenated Block and Convolutional Code	RCE	Granted	09/492,962	01/28/2000	6,606,724	08/12/2003
00CXT0009D	DE	Fully Integrated Broadband Tuner	EPC	Granted	01908776.6	01/31/2001	1256170	04/28/2004
00CXT0009D	FR	Fully Integrated Broadband Tuner	EPC	Granted	01908776.6	01/31/2001	1256170	04/28/2004
00CXT0009D	GB	Fully Integrated Broadband Tuner	EPC	Granted	01908776.6	01/31/2001	1256170	04/28/2004
00CXT0009D	US	Fully Integrated Broadband Tuner	DIV	Granted	10/786,921	2/25/2004	7,177,382	2/13/2007
00CXT0009D	US	Fully Integrated Broadband Tuner	ORD	Granted	09/497,717	2/4/2000	6,731,712	5/4/2004
00CXT0147N	US	Memory Transformation Apparatus and Method	ORD	Granted	07/371,167	6/26/1989	5,095,525	3/10/1992
00CXT0159D	US	Method and Apparatus for	ORD	Granted	07/364,058	06/09/1989	4,992,752	2/12/1991

PATENT

REEL: 0215 PATENT: 0534

REEL: 031001 FRAME: 0613

Case No.	City	Patent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
		Broadband Impedance Matching						
00CXT0292D	US	Method and Apparatus for a Demodulator Circuit	PCT	Granted	10/105,731	3/22/2002	7,106,810	9/12/2006
00CXT0294D	EP	Window Position Correction	PCT	Published	00969677.4	10/18/2000		
00CXT0294D	GB	Window Position Correction	ORD	Granted	0017132.2	07/12/2000	2364866	05/19/2004
00CXT0294D	US	Signal Derotating Receiver	PCT	Published	10/340,577	1/10/2003		
00CXT0294D	WO	Window Position Correction	ORD	Natl Phase	00/04001	10/18/2000		
00CXT0297D	EP	I/Q Phase and Gain Imbalance	PCT	Published	00923456.8	04/18/2000		
00CXT0297D	GB	I/Q Phase and Gain Imbalance	ORD	Granted	9908909.6	04/18/1999	2349285	02/12/2003
00CXT0297D	JP	I/Q Phase and Gain Imbalance	PCT	Pending	2000-613133	04/18/2000		
00CXT0297D	US	Method and Apparatus For Correcting Phase Imbalance In Received In-Phase and Quadrature Signals	CIP	Granted	09/698,567	10/26/2000	6,765,623	7/20/2004
00CXT0297D	WO	I/Q Phase and Gain Imbalance	ORD	Natl Phase	00/10351	04/18/2000		
00CXT0298D	DE	Guard Interval Size Detection	PCT	Granted	01954007.9	07/04/2001	60131407.7	11/14/2007
00CXT0298D	FR	Guard Interval Size Detection	PCT	Granted	01954007.9	07/04/2001	1302044	11/14/2007
00CXT0298D	GB	Guard Interval Size Detection	ORD	Granted	17131.4	07/12/2000	2364865	01/21/2004
00CXT0298D	IT	Guard Interval Size Detection	PCT	Granted	01954007.9	07/04/2001	1302044	11/14/2007
00CXT0298D	NL	Guard Interval Size Detection	PCT	Granted	01954007.9	07/04/2001	1302044	11/14/2007
00CXT0298D	US	Guard Interval Analysis Method and Apparatus	RCE	Appealed	10/340,854	1/10/2003		
00CXT0316D	EP	Cable Modem Having A Programmable Media Access Controller	PCT	Published	01910824.0	02/16/2001		
00CXT0316D	US	Cable Modem Having A Programmable Media Access Controller	ORD	Granted	09/785,035	2/16/2001	6,816,940	11/9/2004
00CXT0316D	WO	Cable Modem Having A Programmable Media Access Controller	ORD	Natl Phase	01/05028	02/16/2001		

PATENT

REEL: 021531 FRAME: 0535

REEL: 031001 FRAME: 0614

Case No.	Co.	Patent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
00CXT0330D	US	Programmable Pattern Match Engine	ORD	Granted	09/516,284	3/1/2000	7,010,802	3/7/2006
00CXT0348D	US	Interrupt Driven Interface Coupling a Programmable Media Access Controller and a Process Controller	ORD	Granted	09/516,465	3/1/2000	6,665,752	12/16/2003
00CXT0352D	US	Method and Apparatus for Flexibly Filtering Scheduling Messages in a Communication System	ORD	Granted	09/966,374	09/27/2001	7,206,321	17-Apr-2007
00CXT0361D	US	System for Carrier Phase Tracking of Coded Symbols Using Reliability Metrics for Symbol Estimates	ORD	Granted	09/715,877	11/17/2000	6,795,512	9/21/2004
00CXT0362D	US	Iterative Carrier Phase Tracking Decoding System	ORD	Granted	09/729,652	12/4/2000	6,856,656	2/15/2005
00CXT0364D	US	Symbol Reliability Determination and Symbol Pre-Selection Based on Reliability Criteria	ORD	Granted	10/368,017	2/14/2003	7,231,005	6/12/2007
00CXT0366D	US	Synchronization of a Communications System	CON	Granted	10/893,673	7/13/2004	7,065,703	7/20/2006
00CXT0366D	US	Synchronization of a Communications System	ORD	Granted	09/788,171	2/14/2001	6,769,093	7/27/2004
00CXT0369D	US	System of and Method for Decoding Trellis Codes	ORD	Granted	10/013,492	12/13/2001	6,865,711	3/8/2005
00CXT0370D	US	System of and Method for Decoding Trellis Codes	ORD	Granted	10/013,490	12/13/2001	6,973,615	12/6/2005
00CXT0371D	US	System for MPSK Symbol Hypothesis Testing	ORD	Granted	09/815,149	3/22/2001	6,922,438	7/26/2005
00CXT0374D	US	SNR-Related Parameter Estimation Method and System	ORD	Granted	09/870,926	5/30/2001	7,310,369	12/18/2007
00CXT0539D	US	Method and Apparatus for a Multicarrier Receiver Circuit and Guard Interval Size Detection	ORD	Granted	10/170,165	6/12/2002	7,206,361	4/17/2007

PATENT

REEL: 021531 FRAME: 0536

REEL: 031001 FRAME: 0615

Case No.	Cty	Patent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
00CXT0539D	US	Method and Apparatus for a Multicarrier Receiver Circuit and Guard Interval Size Detection	CON	Pending	11/685,196	03/12/2007		
00CXT0550D	US	METHOD AND APPARATUS FOR SCHEDULING UPSTREAM DATA PACKETS IN A BROADBAND COMMUNICATION SYSTEM	ORD	Granted	09/892,069	6/26/2001	7016376	3/21/2006
01CXT0228D	US	Integrated programmable tuner	ORD	Published	10/210,591	7/31/2002		
02CXT0017D	US	Fast Channel Scanning and Acquisition Algorithms for Cable Modem Applications	ORD	Published	10/677,110	10/1/2003		
02CXT0037D	US	Simultaneous multiple channel receiver	ORD	Published	10/882,083	6/29/2004		
02CXT0048D	US	Multi-Rate Encoding and Decoding System	ORD	Allowed	10/284,121	10/29/2002		
02CXT0061D	US	Noise Reduction Systems and Methods	CIP	Pending	11/013,963	12/16/2004		
02CXT0070D	US	Architecture for a Flexible and High-Performance Gateway Cable Modem	ORD	Published	10/675,566	9/30/2003		
02CXT0071D	US	Satellite Multi-Choice Switch System	CIP	Published	11/026,670	12/31/2004		
02CXT0077D	US	Satellite Transcoder	ORD	Pending	10/611,400	6/30/2003		
03CXT0005D	US	OPTIMUM RF VCO STRUCTURE	ORD	Granted	10/676,803	9/30/2003	6943635	9/13/2005
03CXT0014D	US	Procedure For BPSK Modulation with Reduced Envelope Peaking	ORD	Granted	10/611,325	6/30/2003	7,352,797	04/01/2008
03CXT0014D	US	Procedure For BPSK Modulation with Reduced Envelope Peaking	CIP	Pending	12/059,873	3/31/2008		
03CXT0033D	US	A Low-Cost High Performance Front-End Active Splitter for a System	ORD	Granted	10/750,989	12/31/2003	7,142,060	11/28/2006

PATENT

REEL: 021531 FRAME: 0537

REEL: 031001 FRAME: 0616

Case No.	Ct	Patent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
		Containing Multiple Reception Units						
04CXT0021D	US	Method and apparatus for efficient computation of check equations in periodical low density parity check (LDPC) codes	ORD	Published	10/867,355	6/14/2004		
04CXT0027D	US	Systems and method for a highly integrated, multi-mode tuner	ORD	Published	11/244,568	10/6/2005		
04CXT0027D	EP	Systems and method for a highly integrated, multi-mode tuner	ORD	Pending	6801693	8/16/2006		
04CXT0027D	TW	Systems and method for a highly integrated, multi-mode tuner	ORD	Published	95136966	10/4/2006		
04CXT0027D	JP	Systems and method for a highly integrated, multi-mode tuner	ORD	Authorized				
04CXT0031A	EP	Adaptive Image Data Compression	ORD	Pending	01113717.1	06/05/2001		
04CXT0031A	US	Adaptive Image Data Compression	ORD	Granted	09/588,266	5/6/2000	6,724,817	4/20/2004
04CXT0034A	US	Implementation of Wavelet Functions in Hardware	ORD	Granted	09/736,891	12/13/2000	6,785,700	8/31/2004
04CXT0074D	US	SYSTEMS AND METHOD FOR A DELAY LOCKED LOOP WITH FALSE-LOCK DETECTION	ORD	Granted	11/194,085	7/29/2005	7,301,379	11/27/2007
04CXT0083D	US	System and method for detecting known sequence in transmitted sequence	ORD	Published	11/194,107	7/28/2005		
05CXT0005D	TW	Systems and Methods For Accurate Analog Quadrature Signal Generation Using a Delay Locked Loop	ORD	Published	095111339	3/30/2006		
05CXT0005D	US	Systems and Method for Automatic Quadrature Phase Imbalance Compensation Using	CON	Granted	11/460,298	7/27/2006	7,298,222	11/20/2007

PATENT

REEL: 021531 FRAME: 0538

REEL: 031001 FRAME: 0617



Case No.	Cty	Parent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
		Delay Locked Loop						
05CXT0005D	US	Systems and Methods For Accurate Analog Quadrature Signal Generation Using a Delay Locked Loop	ORD	Granted	11/096,214	3/31/2005	7,123,103	10/17/2006
05CXT0006D	US	SYSTEMS AND METHOD FOR A HIGHLY LINEAR, LOW POWER MIXER	ORD	Published	11/096,217	3/31/2005		
05CXT0006D	EP	SYSTEMS AND METHOD FOR A HIGHLY LINEAR, LOW POWER MIXER	ORD	Published	6736121.2	2/27/2006		
05CXT0006D	TW	SYSTEMS AND METHOD FOR A HIGHLY LINEAR, LOW POWER MIXER	ORD	Published	95111342	3/30/2006		
05CXT0008A	US	H.264 Single Cycle Cabac Decoding	CON	Published	11/760,303	06/08/2007		
05CXT0008A	US	H.264 Single Cycle Cabac Decoding	ORD	Granted	11/287,630	11/28/2005	7,245,242	7/17/2007
05CXT0008A	WO	H.264 Single Cycle Cabac Decoding	ORD	Published	06/61303	11/28/2006		
05CXT00082D	US	Multipath Processing Systems and Methods	ORD	Pending	11/522,583	9/18/2006		
05CXT00091D	US	Efficient Decoders for LDPC Codes	ORD	Pending	11/303,449	12/16/2005		
05CXT00092D	US	De-blocking and De-ringing Systems and Methods	ORD	Published	11/233,674	9/23/2005		
05CXT00093D	US	Multi-channel LDPC decoder architecture	ORD	Published	11/303,876	12/16/2005		
05CXT00093D	EP	Multi-channel LDPC decoder architecture	ORD	Published	6773833.6	12/28/2007		
05CXT0167ST	US	Systems and Methods with Reduced Reference Spurs Using a Crystal Oscillator for Broadband Communications	ORD	Pending	11/612,666	12/19/2006		
05CXT0168CM	US	Erasures Assisted Block Code Decoder And Related Method	ORD	Published	11/595,546	11/10/2006		
05CXT0168CM	WO	Erasures Assisted	ORD	Published	07/009177	4/11/2007		

PATENT

REEL: 0215 PATENT: 0539

REEL: 031001 FRAME: 0618

Case No.	Cb	Patent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
		Block Code Decoder And Related Method						
06CXT0008MG	US	Multi-Format Stream Remultiplexer for Multi-Pass, Multi-Stream, Multiplexed Transport Stream Processing	PRO	Pending	60/946,122	6/25/2007		
06CXT0011ST	US	Systems Involving Temperature Compensation of Voltage Controlled Oscillators	ORD	Pending	11/613,216	12/20/2006		
06CXT0012MG	US	Encoded Digital Video Content Protection Between Transport Demultiplexer and Decoder	PRO	Pending	60/946,114	6/25/2007		
07CXT0021ST	US	Integrated Wideband RF Tracking Filter for RF Front End with Parallel Band Switched Tuned Amplifiers	ORD	Pending	12/098,122	4/4/2008		
07CXT0034PT	US	Time Domain Interpolation Scheme for Flash A/D Converters	ORD	Pending	12/002,153	12/13/2007		
07CXT0040CM		Receiver IQ Imbalance Calibration	IDS					
07CXT0064SD	US	Central Satellite Receiver Hub	PRO	Pending	61/036,935	3/15/2008		
07CXT0066NP	US	Method, System and Apparatus for Extended Rate/Range Communication Over a Communication Network	PRO	Pending	60/978,012	10/5/2007		
07CXT0068PT		Low PAPR Waveforms for OFDM Systems	IDS					
07CXT0073MG		A NOVEL INTRA COMPRESSION METHOD	IDS					
07CXT0080CV		A NOVEL LOW RESOLUTION CONTENT EXTRACTION METHOD	IDS					
07CXT0085PT		A NOVEL VIDEO COMPRESSION	IDS					

PATENT

REEL: 021531 FRAME: 0540

REEL: 031001 FRAME: 0619

Case No.	Cty	Patent Title	Case Type	Status	Appl. No.	Filing Date	Patent No.	Issue Date
		METHOD						
07CXT0096PT		New High Speed Fast Forward and Rewind Algorithm	IDS					
07CXT0097PT		A Novel PVR Trick Mode Method	IDS					
07CXT0105PT		Calibration Method for a Flash ADC	IDS					
08CXT0016MG	US	Method for Efficient Packet Framing in a Communication Network	PRO	Pending	61/042,586	4/4/2008		
08CXT0025ST		RF FILTER with ZERO-IF MIXING TUNING and CALIBRATION	IDS					
08CXT0026SD		A New Wide Range Symbol Rate and Carrier Frequency Offset Acquisition Algorithm	IDS					

RECORDED: 09/16/2008

RECORDED: 08/13/2013

PATENT  
REEL: 021531 FRAME: 0541PATENT  
REEL: 031001 FRAME: 0620