

## PATENT ASSIGNMENT

Electronic Version v1.1  
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
V CORP TECHNOLOGIES, INC.	08/26/2013
RECEIVING PARTY DATA	
Name:	Scott R. Velazquez
Street Address:	13460 El Presidio Trail
City:	San Diego
State/Country:	CALIFORNIA
Postal Code:	92130
PROPERTY NUMBERS Total: 14	
Property Type	Number
Patent Number:	6177893
Patent Number:	6198416
Patent Number:	6339390
Patent Number:	6344810
Patent Number:	6388594
Patent Number:	6424275
Patent Number:	6473013
Patent Number:	6570514
Patent Number:	7782235
Patent Number:	7940198
Patent Number:	8085175
Patent Number:	8164496
Application Number:	13455071
Application Number:	13915484
CORRESPONDENCE DATA	

502473256

PATENT  
 REEL: 031091 FRAME: 0375

CH \$560.00 6177893

Fax Number: 8587923447

*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*

Phone: 8587923446

Email: sympson@sandiegoiplaw.com

Correspondent Name: Trevor Coddington

Address Line 1: 12526 High Bluff Drive, Suite 300

Address Line 4: San Diego, CALIFORNIA 92130

ATTORNEY DOCKET NUMBER:

V CORP

NAME OF SUBMITTER:

Michelle Sympson

Signature:

/Michelle Sympson/

Date:

08/27/2013

Total Attachments: 3

source=V Corp Executed Assignment#page1.tif

source=V Corp Executed Assignment#page2.tif

source=V Corp Executed Assignment#page3.tif

## **ASSIGNMENT**

WHEREAS, V Corp Technologies, Inc., a corporation organized under the laws of Delaware, having a place of business located at 12526 High Bluff Drive, Suite 120, San Diego, CA 92130 USA (hereinafter "ASSIGNOR") is the owner of all rights to the patents and pending patent applications referred to in Schedule I ("the Patents"), which is attached hereto:

AND, WHEREAS, Scott R. Velazquez, an individual, having a residence located at 13460 El Presidio Trail, San Diego, CA 92130 USA (hereinafter "ASSIGNEE"), is desirous of acquiring all rights to the Patents;

NOW, THEREFORE, in consideration of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration, receipt of which is hereby acknowledged, ASSIGNOR does hereby sell, assign and transfer unto said ASSIGNEE, its successors, assigns and legal representatives, his entire right, title and interest in and throughout the United States of America (including its territories and dependencies) and all countries foreign thereto in and to the Patents, any other United States applications, including provisional, divisional, renewal, substitute, continuation, reexamination and reissue applications, based in whole or in part on the Patents or any parts thereof, and in and to any and all letters patent, including extensions thereof, of any country which have been or may be granted on any of the Patents or any parts thereof, and any and all past damages for infringement of the Patents;

AND ASSIGNOR hereby agrees for himself and his heirs, executors and administrators to execute without further consideration any further documents and instruments which may be necessary, lawful and proper in the prosecution of the Patents or in the preparation or prosecution of any continuing, substitute, divisional, renewal, reexamination or reissue application or in any amendments, extensions or interference proceedings, or other applications for patents of any region or country, that may be necessary to secure to ASSIGNEE its interest and title in and to the Patents or any parts thereof;

AND ASSIGNOR hereby covenants for himself and his legal representatives, and agrees with said ASSIGNEE, his successors and assigns, that ASSIGNOR has granted no right or license to make, use, sell or offer to sell the Patents or any parts thereof, to anyone except said ASSIGNEE, that prior to the execution of this deed, his right, title and interest in the Patents had not been otherwise encumbered, and that he has not and will not execute any instrument in conflict therewith;

AND ASSIGNOR does hereby authorize and request the United States Patent and Trademark Office, or any other authority of any other country, issue any and all letters patent which may be granted upon the Patents, or any parts thereof when granted, to said ASSIGNEE.

IN WITNESS WHEREOF, ASSIGNOR has hereunto set his hands and seals.

V CORP TECHNOLOGIES, INC.

08/26/2013  
Date

Scott R Velazquez  
Signature

Scott R. Velazquez

Name

State of California                    )  
County of San Diego                 )

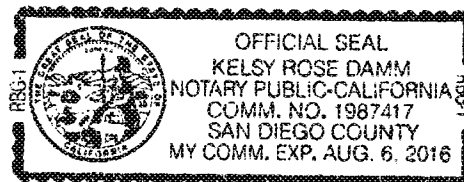
On 08/26/2013 before me, Kelsy Rose Dammm, Notary Public, personally appeared Scott R. Velazquez who proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacity, and that by his signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature

Kelsy Rose Dammm



NOTARY PUBLIC SEAL

### **Schedule I**

- 1) U.S. Patent No. 6,177,893, entitled "Parallel Processing Analog and Digital Converter," and issued January 23, 2001;
- 2) U.S. Patent No. 6,198,416, entitled "Linearity Error Compensator," and issued on March 6, 2011;
- 3) U.S. Patent No. 6,339,390, entitled "Adaptive Parallel Processing Analog and Digital Converter," and issued on January 15, 2002;
- 4) U.S. Patent No. 6,344,810, entitled "Linearity Error Compensator," and issued on February 5, 2002;
- 5) U.S. Patent No. 6,388,594, entitled "Method of Calibrating Analog and Digital Converters," and issued on May 14, 2002;
- 6) U.S. Patent No. 6,424,275, entitled "Linearity Error Compensator," and issued on July 23, 2002;
- 7) U.S. Patent No. 6,473,013, entitled "Parallel Processing Analog and Digital Converter," and issued on October 29, 2002;
- 8) U.S. Patent No. 6,570,514, entitled "Linearity Error Compensator," and issued on May 27, 2003;
- 9) U.S. Patent No. 7,782,235, entitled "Adaptive Mismatch Compensators and Methods for Mismatch Compensation," and issued on August 24, 2010;
- 10) U.S. Patent No. 7,940,198, entitled "Amplifier Linearizer," and issued on May 10, 2011;
- 11) U.S. Patent No. 8,085,175, entitled "Linearizer," and issued on December 27, 2011;
- 12) U.S. Patent No. 8,164,496, entitled "Mismatch Compensators and Methods for Mismatch Compensation," issued on April 24, 2012;
- 13) United States Patent Application No. 13/455,071, entitled "Adaptive Digital Receiver," and filed on April 24, 2012; and
- 14) United States Patent Application No. 13/915,484, entitled "Linearizer," and filed on June 11, 2013.